Bachelor of Science in $\,$ Computer Science and Engineering , AUST $\,$

Semester Wise Class Schedule, Spring, 2022

Year: 2, Semester: 1 (Section C)

Time/Day	08:00 AM- 08:50 AM	08:50 AM- 09:40 AM	09:40 AM- 10:30 AM	10:30 AM- 11:20 AM	11:20 AM- 12:10 PM	12:10 PM- 01:00 PM	01:00 PM- 01:50 PM	01:50 PM- 02:40 PM	02:40 PM- 03:30 PM	03:30 PM- 04:20 PM	04:20 PM- 05:10 PM	05:10 PM- 06:00 PM
SUN		EEE 2142(C1) 3B06 (TBA)		CSE 2103 7C07 (Shahariar)	MATH 2101 7C07 (TBA)	CSE 2105 7C07 (Hasan)		EEE 2142(C2) 4B05 (TBA)				
MON					CSE 2106(C2) 9A05 (Aftab, Hasan)		HUM 2109 7C06 (TBA)	MATH 2101 7C06 (TBA)	EEE 2141 7C06 (TBA)			
TUE					CSE 2100(C1/C2) 7B05 (Chowdhury)					EEE 2141 7A04 (TBA)	CSE 2103 7A04 (Shahariar)	CSE 2105 7A04 (Hasan)
WED					CSE 2106(C1) 9A05 (Hasan)		EEE 2141 7C06	CSE 2103 7C06	MATH 2101 7C06			
			CSE 2104(C2) 7B05 (Shahariar, Chowdhury)			(TBA)	(Shahariar)	(TBA)				
THU	HUM 2109 7A04 (TBA)	HUM 2109 7A04 (TBA)	CSE 2105 7A04 (Hasan)	CSE 2104(C1) 7B08 (Shahariar)								

Course No	Course Title	Credit Hour	Section	Course Teacher
CSE 2100	Software Development-II	0.75	C1/C2	Mr. Md. Reasad Zaman Chowdhury
CSE 2103	Data Structures	3.0	C	Mr. G. M. Shahariar
CSE 2104	Data Structures Lab	1.5	C1	Mr. G. M. Shahariar
CSE 2104	Data Structures Lab		C2	Mr. G. M. Shahariar, Ms. Moontaha Nishat Chowdhury
CSE 2105	Digital Logic Design	3.0	C	Ms. Syeda Shabnam Hasan
GGE 2106	Digital Lands Design Lab	1.5	C1	Ms. Syeda Shabnam Hasan
CSE 2106	Digital Logic Design Lab		C2	Ms. Anika Bintee Aftab, Ms. Syeda Shabnam Hasan
EEE 2141	Electronic Devices and Circuits	3.0	C	
EEE 2142		1.5	C1	
EEE 2142	Electronic Devices and Circuits Lab		C2	
HUM 2109	Society, Ethics and Technology	3.0	С	
MATH 2101	Mathematics-III	3.0	C	