Bachelor of Science in $\,$ Computer Science and Engineering , AUST $\,$

Semester Wise Class Schedule, Spring, 2022

Year: 2, Semester: 1 (Section A)

Time/Day	08:00 AM- 08:50 AM	08:50 AM- 09:40 AM	09:40 AM- 10:30 AM	10:30 AM- 11:20 AM	11:20 AM- 12:10 PM	12:10 PM- 01:00 PM	01:00 PM- 01:50 PM	01:50 PM- 02:40 PM	02:40 PM- 03:30 PM	03:30 PM- 04:20 PM	04:20 PM- 05:10 PM	05:10 PM- 06:00 PM
SUN	MATH 2101 7A03 (TBA)	CSE 2103 7A03 (Shahariar)	CSE 2105 7A03 (Hasan)		EEE 2142(A1) 3B06 (TBA)							
MON		CSE 2106(A1) 9A05 (Hasan)		EEE 2141 7C07 (TBA)	HUM 2109 7C07 (TBA)	MATH 2101 7C07 (TBA)		EEE 2142(A2) 3B06 (TBA)				
TUE								CSE 2100(A1/A2) 7B07 (Chowdhury)		CSE 2103 7A03 (Shahariar)	CSE 2105 7A03 (Hasan)	EEE 2141 7A03 (TBA)
WED		CSE 2106(A2) 9A05 (Hasan)					CSE 2103 7C07 (Shahariar)	MATH 2101 7C07 (TBA)	HUM 2109 7C07 (TBA)		CSE 2104(A1) 7B03 (EMAM HOSSAIN	V)
THU				EEE 2141 7A06 (TBA)	HUM 2109 7A06 (TBA)	CSE 2105 7A06 (Hasan)		CSE 2104(A2) 7B03 (Chowdhury)				

Course No	Course Title	Credit Hour	Section	Course Teacher		
CSE 2100	Software Development-II	0.75	A1/A2	Mr. Md. Reasad Zaman Chowdhury		
CSE 2103	Data Structures	3.0	A	Mr. G. M. Shahariar		
CSE 2104	Data Structures Lab	1.5	A1	Mr. Emam Hossain		
CSE 2104	Data Structures Lab		A2	Ms. Moontaha Nishat Chowdhury		
CSE 2105	Digital Logic Design	3.0	A	Ms. Syeda Shabnam Hasan		
CSE 2106	Digital Logic Design Lab	1.5	A1	Ms. Syeda Shabnam Hasan		
	Digital Logic Design Lab		A2	Ms. Syeda Shabnam Hasan		
EEE 2141	Electronic Devices and Circuits	3.0	A			
EEE 2142		1.5	A1			
EEE 2142	Electronic Devices and Circuits Lab		A2			
HUM 2109	Society, Ethics and Technology	3.0	A			
MATH 2101	MATH 2101 Mathematics-III		A			