



Note the original RaSCSi design calls the DIR pin "ATOB"
Enable Input ("G") is active low, so always grounded.

This card include bus transceiver logic to allow a Raspberry Pi to connect to a vintage Macintosh SCSi port. (It may work with other systems as well)

This design is based upon GIMONS's Target/Initiator design <http://retropc.net/gimons/rascsi/>

This is the "FULLSPEC" version of the board that can work as a SCSi target OR initiator

Thank you to everyone who has worked on this project!!

Sheet: /
File: rascsi_zero.kicad_sch

Title: PISCSi Zero

Size: USLetter Date: 10-Dec-2022

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