

REVISION HISTORY:

BCM943362WCD4 P100

- 1) Initial release.

BCM943362WCD4 P200

- 1) Added 10pF DC blocking capacitors on Pins 1, 3, and 5 of designator U3.

BCM943362WCD4_3 Rev01 (essentially P300)

- 1) Added 10k resistors R94, R95, R96 for identifying board revision.
2) Renamed "32K_PWM_OUT" net to "MCO_SLEEP_CLK" and moved from using PA11 to PA8.

(Note: When using WICED Powersave API, it is critical that an accurate 32.768kHz clock is used for the sleep clock input pin of the WLAN chip.

The MCO1 pin (PA8) can be used to route out the highly accurate 32.768kHz LSE clock and is now used as the source for the sleep clock into the WLAN chip.

The LSE clock is more accurate than generating a PWM signal out of PA11.)

