

REVISION HISTORY:

BCM943362WCD8\_1 Rev01

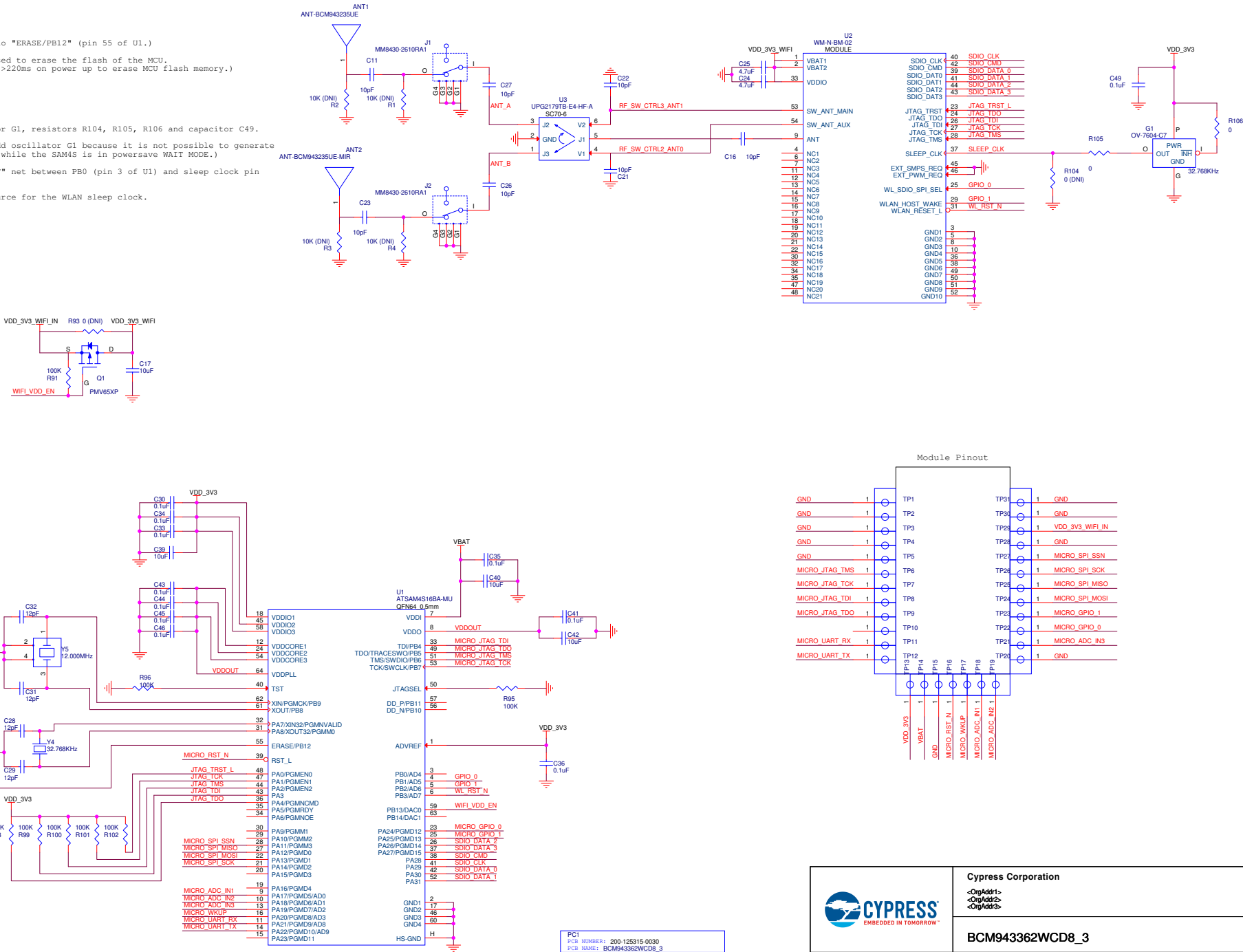
- 1) Initial release.

BCM943362WCD8\_2 Rev01

- 1) Added push button SW1 onto "ERASE/PB12" (pin 55 of U1.)  
(Note: This push button is used to erase the flash of the MCU.  
Push and hold SW1 for >220ms on power up to erase MCU flash memory.)
- 2) Changed R97 to 100k.

BCM943362WCD8\_3 Rev01

- 1) Added 32.768kHz oscillator G1, resistors R104, R105, R106 and capacitor C49.  
(Note: It was necessary to add oscillator G1 because it is not possible to generate a PWM signal from PB0 while the SAM4S is in powersave WAIT MODE.)
- 2) Disconnected "32K\_PWM\_OUT" net between PB0 (pin 3 of U1) and sleep clock pin of WLAN chip.
- G1 is now used as the source for the WLAN sleep clock.



Module Pinout		Module Pinout	
GND	1	TP1	TP31
GND	1	TP2	TP30
GND	1	TP3	TP29
GND	1	TP4	TP28
GND	1	TP5	TP27
MICRO_JTAG_TMS	1	TP6	TP26
MICRO_JTAG_TCK	1	TP7	TP25
MICRO_JTAG_TDI	1	TP8	TP24
MICRO_JTAG_TDO	1	TP9	TP23
MICRO_UART_RX	1	TP10	TP22
MICRO_UART_TX	1	TP11	TP21
VDD_3V3	1	TP12	TP20
VBAT	1	TP13	TP19
GND	1	TP14	TP18
MICRO_RST_N	1	TP15	TP17
MICRO_WKUP	1	TP16	TP16
MICRO_ADC_IN1	1	TP17	TP15
MICRO_ADC_IN2	1	TP18	TP14
MICRO_ADC_IN3	1	TP19	TP13
		TP20	TP12
		TP21	TP11
		TP22	TP10
		TP23	TP9
		TP24	TP8
		TP25	TP7
		TP26	TP6
		TP27	TP5
		TP28	TP4
		TP29	TP3
		TP30	TP2
		TP31	TP1

Cypress Corporation	
<OrigAddr1> <OrigAddr2> <OrigAddr3>	
BCM943362WCD8_3	
Size	DWG NO
C	824-125315-0030
Scale	Rev
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