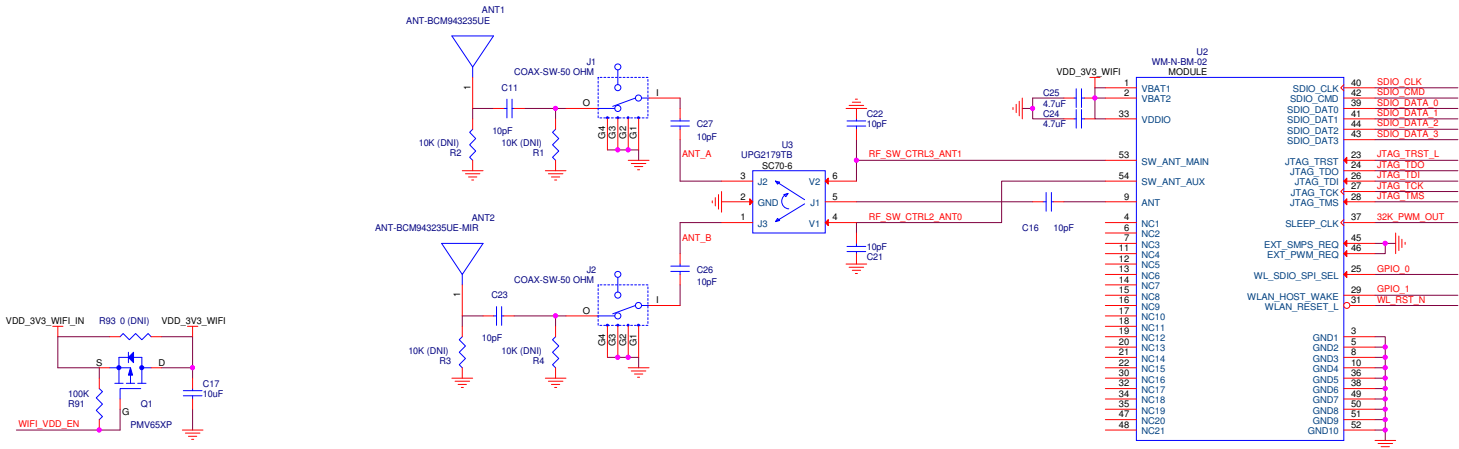


REVISION HISTORY:

BCM943362WCD6 Rev01

1) Initial release.

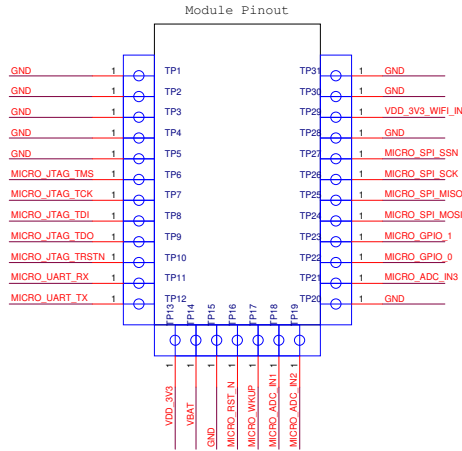
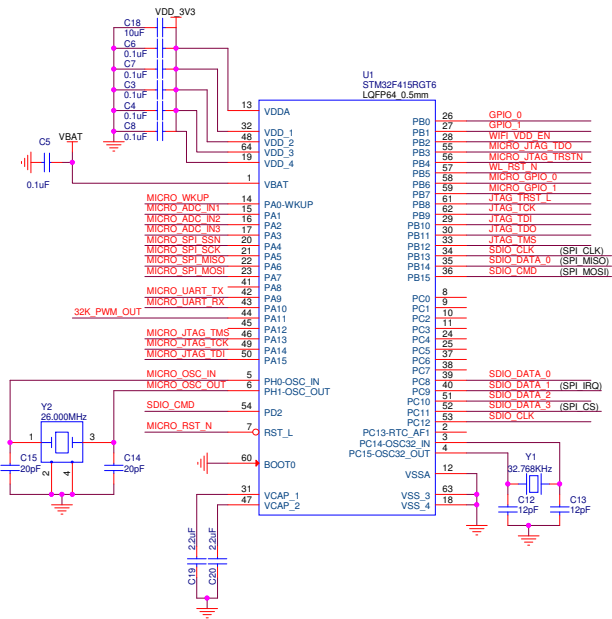



SDIO / SPI Interface Options:					
SDIO	STM32F205 Pin Number	STM32F205 Port/Pin	WM-N-BM-02 Pin Number	Function	
	53	PC12	<-->	40	SDIO_CLK
	54	PD2	<-->	42	SDIO_CMD
	39	PC8	<-->	39	SDIO_DATA_0
	40	PC9	<-->	41	SDIO_DATA_1
	51	PC10	<-->	42	SDIO_DATA_2
	52	PC11	<-->	43	SDIO_DATA_3
SPI	STM32F205 Pin Number	STM32F205 Port/Pin	WM-N-BM-02 Pin Number	Function	
	34	PB13	<-->	40	SPI_CLK
	36	PB15	<-->	42	SPI_MOSI
	35	PB14	<-->	39	SPI_MISO
	52	PC11	<-->	43	SPI_CS
	40	PC9	<-->	41	SPI_IRQ

Note: Only one set of communication lines are required to be connected between the microcontroller and wlan device (either SDIO or SPI).

Please select one set based on the table above.

It is NOT necessary to connect both SPI and SDIO.



		Cypress Corporation	
		<OrgAddr> <OrgAddr> <OrgAddr>	
		BCM943362WCD6	
		Size	Rev
C		824-125211-0000	Rev 01
Tuesday, July 02, 2013		Scale	Sheet 1 of 1

PC1  
PCB NUMBER: 200-125211-0000  
PCB NAME: BCM943362WCD6  
ZHT  
SCHEMATIC DIAGRAM