PRINTED CIRCUIT BOARD

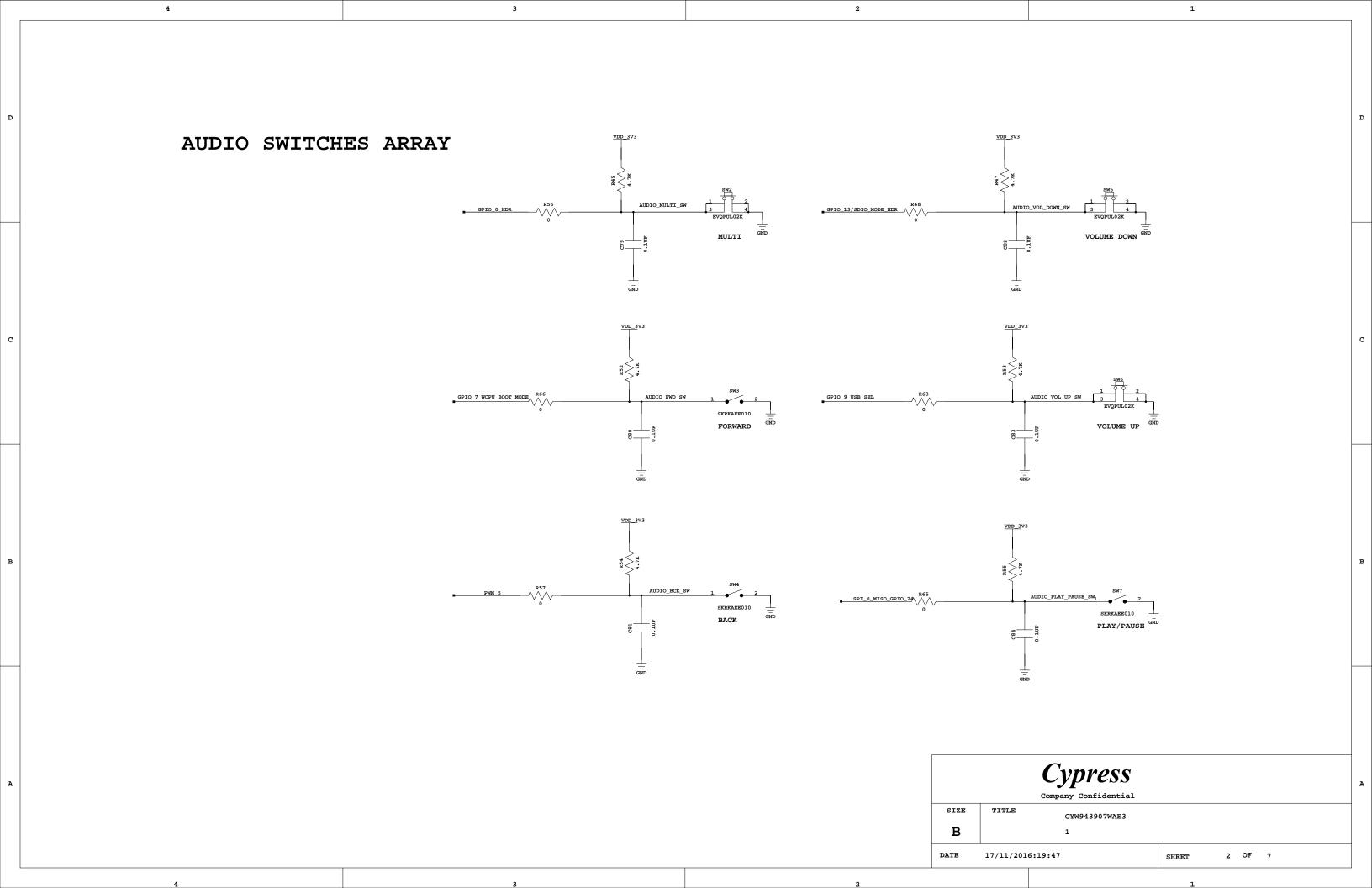
SCHEMATIC DIAGRAM

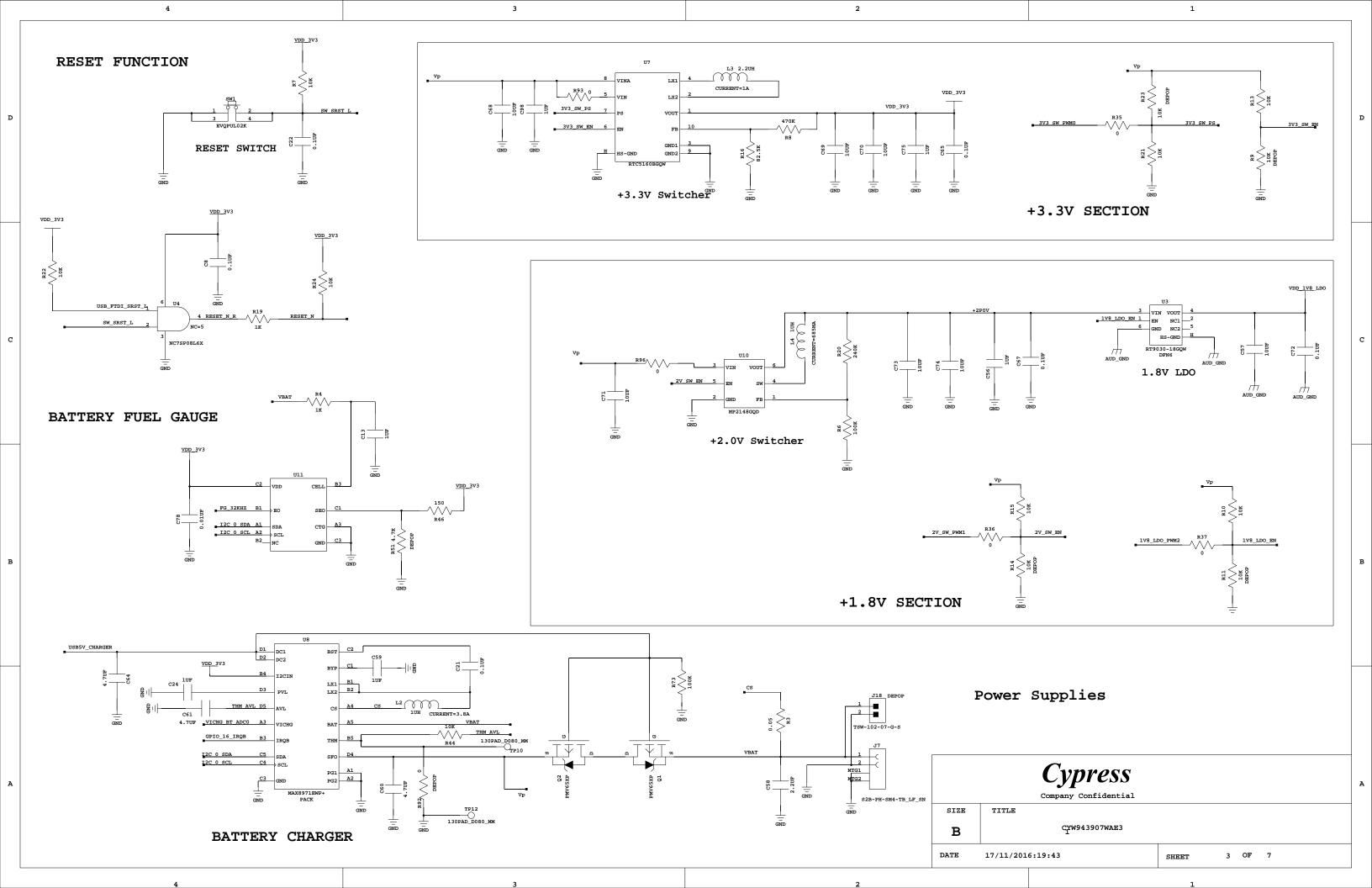
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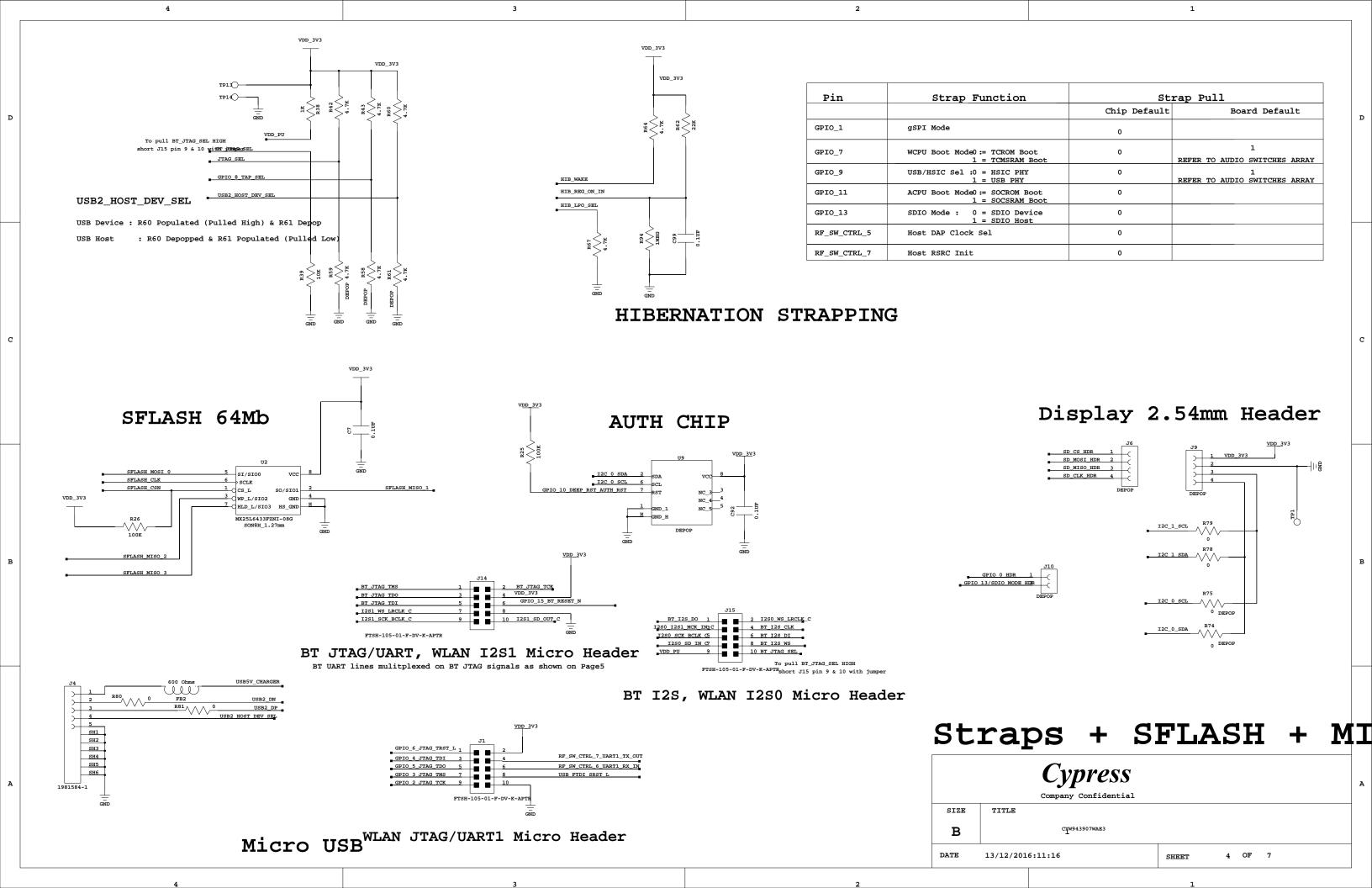
Actual PAD Names on BCM43907 WLCSP PKG	GPIO Definition on BCM43907 GCI Bus 2nd # is FUNCTION TABLE	Input or Output from BCM43907	Description of Functionality		
GPIO_0	GPIO_0 1	Input	AUDIO_MULTI_SW		
GPIO_1	GPIO_1 1	Output	43907 Wakes up BT IC (BT_DEV_WAKE)		
GPIO_2	GPIO_2 1*	NC - JTAG	Not Used JTAG_TCK - 4 Debug		
GPIO_3	GPIO_3 1*	NC - JTAG	Not Used JTAG_TMS - 4 Debug		
GPIO_4	GPIO_4 1*	NC - JTAG	Not Used JTAG_TDI - 4 Debug		
GPIO_5	GPIO_5 1*	NC - JTAG	Not Used JTAG_TDO - 4 Debug		
GPIO_6	GPIO_6 1*	NC - JTAG	Not Used JTAG_TRST_L - 4 Debug		
GPIO_7	GPIO_7 1	Input	AUDIO_FWD_SW		
GPIO_8	GPIO_8 1	Input	TAP_SEL		
GPIO_9	GPIO_9 1	Input	AUDIO_VOL_UP_SW		
GPIO_10	GPIO_10 1	Input	AUTH IC RESET		
GPIO_11	GPIO_11 1	NC	Not Used		
GPIO_12	GPIO_12 1	Input	CODEC PDN		
GPIO_13	GPIO_13 1	Input	AUDIO_VOL_DOWN_SW		
GPIO_14	GPIO_14 1	Input	BT IC Wakes up 43907 (BT_HOST_WAKE)		
GPIO_15	GPIO_15 1	Input	43907 Reset to BT IC (LOW = RESET BT 707)		
GPIO_16	GPIO_16 1	Output	Battery Charger Interrupt		
PWM_0	GPIO_18 3	Input	Enable +3.3V SW Regulator PSM mode (Power Save Mode)		
PWM_1	GPIO_19 3	Output	Enable +2.0V SW Regulator		
PWM_2	GPIO_20 3	Output	Enable +1.8V LDO		
PWM_3	GPIO_21 3	Input	Enable Green LED		
PWM_4	GPIO_22 3	Input	Enable Red LED		
PWM_5	GPIO_23 3	Input	AUDIO_BCK_SW		
SPI_0_CLK	GPIO_25 3	Input	Audio Interrupt		
SPI_0_MISO	GPIO_24 3	Input	AUDIO_PLAY_PAUSE_SW		
SPI_0_MOSI	GPIO_26 3	IN / OUT	TEST POINT		
SPI_0_CS	GPIO_27 3	IN / OUT	TEST POINT		
I2C0_SDATA	GPIO_28 3	IN / OUT	I2C0		
I2C0_CLK	GPIO_29 3	IN / OUT	12C0		

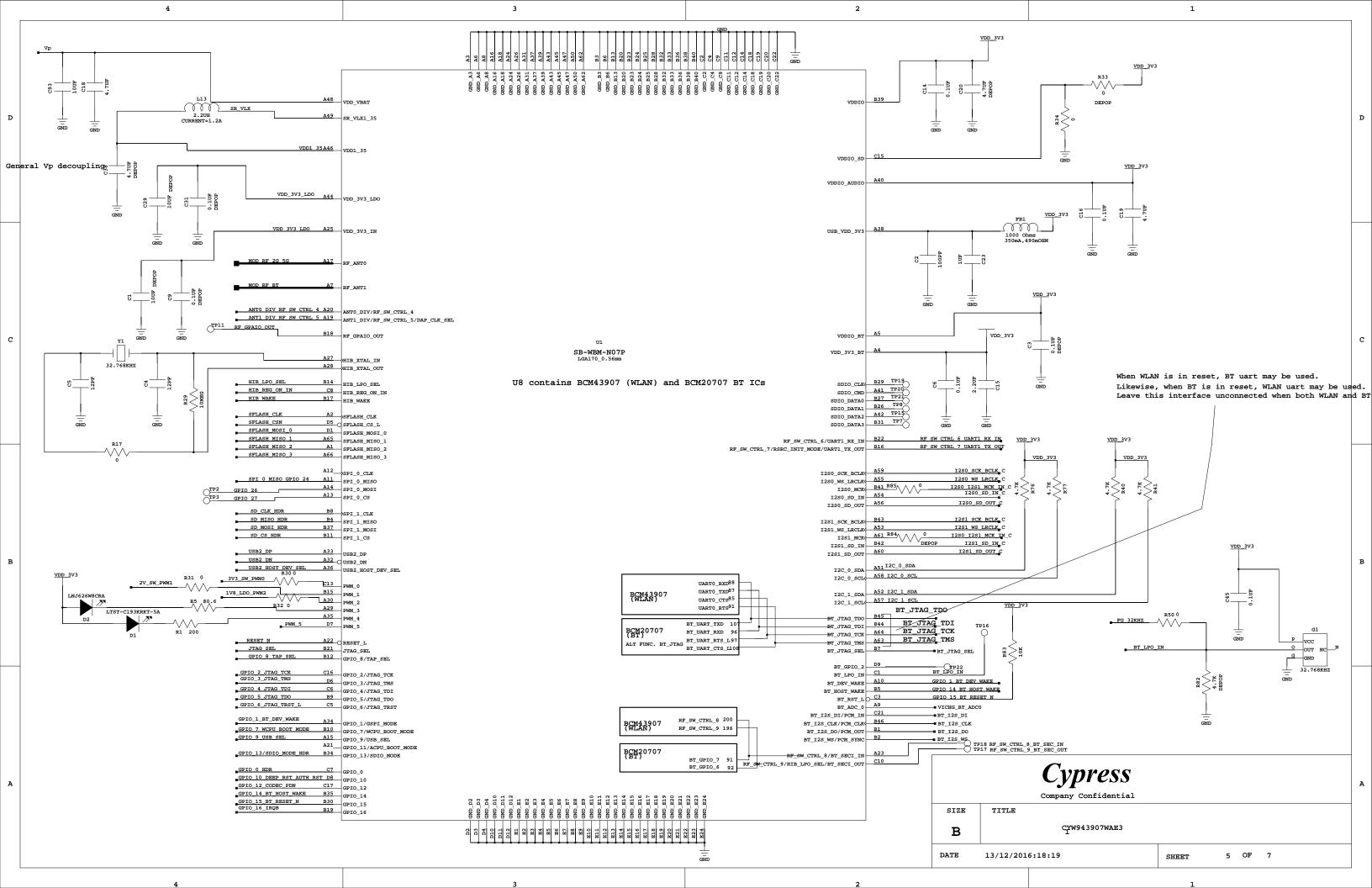
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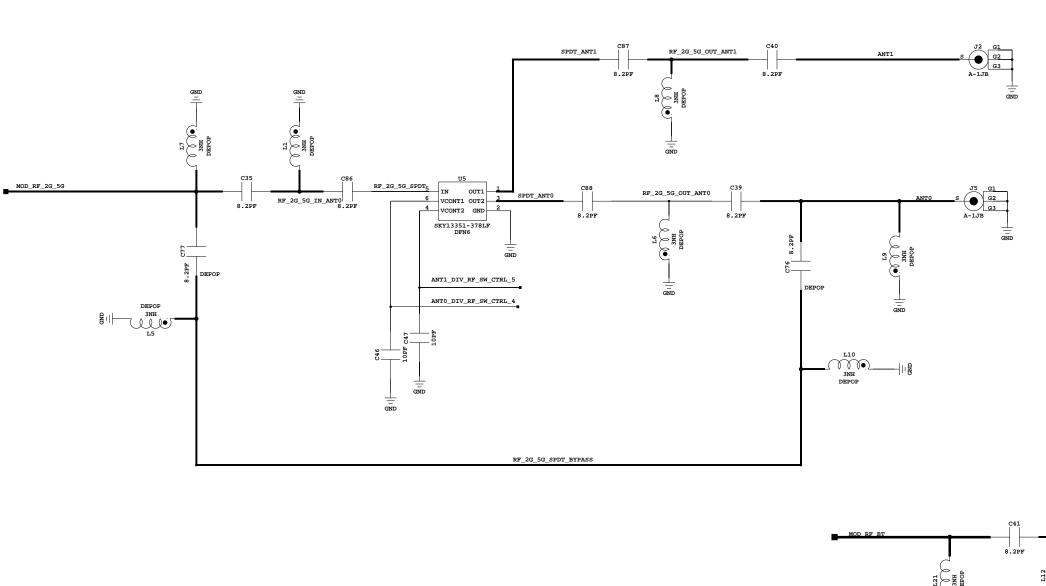
Approval	.s		Cyni	ross			
Designer	Date	Cypress Company Confidential					
Reviewer	Date	size B	CYW943907WAE3				
Reviewer	Date	Designer	Drawing Number 824-130995-0010		Version 1.0		
Reviewer	Date						
		DATE	15/12/2016:14:06		SHEET 1 OF	7	





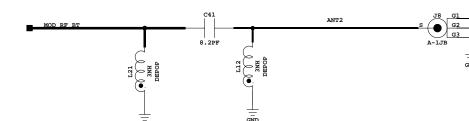






υ5	2G/5G SPDT Antenna Div	ersity Switch Control Table
MODE OF OPERATION Ant Diversity	ANTO_DIV_RF_SW_CTRL_4 SPDT (U5 VCONT1)	ANT1_DIV_RF_SW_CTRL_5 DPDT (U5 VCONT2)
RF_2G_5G to Ant0	1	0
RF_2G_5G to Ant1	0	1

ALL OTHER STATES UNDEFINED



ANTENNA DIVERSITY

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SIZE	TITLE				
В	CYW943907WAE3				
DATE	17/11/2016:19:43	SHEET	6	OF	7

