## REVISION HISTORY:

## BCM943362WCD6 Rev01

1) Initial release.

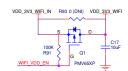
## BCM943362WCD6\_3 Rev01

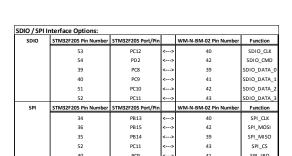
- 1) Added 10k resistors R94, R95, R96 for identifying board revision.
- 2) Renamed "32K\_PWM\_OUT" net to "MCO\_SLEEP\_CLK" and moved from using PA11 to PA8.

(Note: When using WICED Powersave API, it is critical that an accurate 32.768kHz clock is used for the sleep clock input pin of the WLAN chip.

The MCO1 pin (PA8) can be used to route out the highly accurate 32.768kHz LSE clock and is now used as the source for the sleep clock into the WLAN chip.

The LSE clock is more accurate than generating a PWM signal out of PA11.)





Note: Only one set of communication lines are required to be connected between the microcontroller and wlan device (either SDIO or SPI).

Please select one set based on the table above.

ANT1 ANT-BCM943235UE

ANT2 ANT-BCM943235UE-MIR

t is NOT necessary to connect both SPI and SDIO.

PC1
PCB NUMBER: 200-125211-0030
PCB NAME: BCM943362WCD6\_3

ZH1
SCHEMATIC DIAGRAM

VDD 3V3					Note: Only o microcontroll
C6 0.1uF					Please selec
C7 0.1uF C3 0.1uF	10	U1 STM32F415RGT6 LQFP64_0.5mm			It is NOT ned
O.TUF  MICRO WKUP  MICRO WKUP  MICRO WKUP  MICRO ZOR INI  MICRO SPI SSIN  MICR	13 VDDA 28 VDD 1 46 VDD 2 64 VDD 2 7 VDD 3 1 VBAT 14 PAO-WKUP 15 PA1 16 PA2 17 PA2 21 PA4 22 PA5 23 PA6 41 PA7	PB0 26 PB1 28 PB2 28 PB3 55 PB4 57 PB5 58 PB9 69 PB9 69 PB9 69 PB9 69 PB1 33 PB11 33 PB12 33 PB13 55 PB19 89 PB19	GPIO 0 GPIO 1 GPIO 1 WIFT VDD EN MICRO JTAG TOO MICRO JTAG TSSTN MICRO JTAG TSSTN MICRO GPIO 0 MICRO GPIO 1 JTAG TSST L JTAG TCK JTAG TCK JTAG TCK SID DATA 0 (SPI MSC)	VDD_3V3 A R06 INSTALL	L POSITION A
MICRO UART IX MICRO LATE TAX MICRO JARG TAX MICRO J	MICRO UART RX	POI 8 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	500 DATA 0 5 SSIO DATA 1 (SPI EO) 1 SSIO DATA 2 (SPI EO) 2 SSIO DATA 3 (SPI ES) 3 SDIO CIK	C STRING A VDD 3V3 A A R04K	L POSITION B

GND	1		TP	1						TP31		1	GND
GND	1	0	TP	2						TP30	0	1	GND
GND	1	ŏ	TP	3						TP29	-0	1	VDD_3V3_WIFI_II
GND	1	ŏ	TP	4						TP28	_	1	GND
GND	1	ŏ	TP	5						TP27	_	1	MICRO_SPI_SSN
MICRO_JTAG_TMS	1	ŏ	TP	6						TP26	-	1	MICRO_SPI_SCK
MICRO_JTAG_TCK	1	ŏ	TP	7						TP25	0	1	MICRO_SPI_MISO
MICRO_JTAG_TDI	1	ŏ	TP	8						TP24	_	1	MICRO_SPI_MOS
MICRO_JTAG_TDO	1	ŏ	TP	9						TP23	_	1	MICRO_GPIO_1
MICRO_JTAG_TRSTN	1	ŏ	TP	10						TP22	-	1	MICRO_GPIO_0
MICRO_UART_RX	1	ŏ	TP	11						TP21	-0	1	MICRO_ADC_IN3
MICRO_UART_TX	1	ŏ	TP	12_	10	"				TP20	-	1	GND
		Ľ	TP13 ==	Ě	TP15	TP16	191	TP 18	TP19		_		
				) (	0	þ	þ	<b>o</b>	) (	<b>b</b>			
			-	-	-	-	-	-	-				
			η.			z	<u>a</u>	ADC_IN1	ADC_IN2				
			VDD_3V3	A		MICRO_RST	MICRO WKUP	ADC	ADC				
			8	VBAT	۵	윤	윤	MICRO	MICRO				
					GND	M	M	MK	M				

Module Pinout

4 NC10
6 NC1
7 NC2
11 NC3
12 NC4
13 NC5
14 NC6
15 NC7
15 NC7
16 NC8
18 NC10
18 NC10
19 NC12
20 NC12
21 NC13
30 NC15
30 NC16

WL\_SDIO\_SPI\_SEL
WLAN\_HOST\_WAKE
WLAN\_RESET\_L

C16 10pF

