

# Energy Storage Elements (1): Capacitors

Lecture 10

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Jae W. Lee ([jaewlee@snu.ac.kr](mailto:jaewlee@snu.ac.kr))

Computer Science and Engineering

Seoul National University

***Slide credits: Prof. Anant Agarwal at MIT***

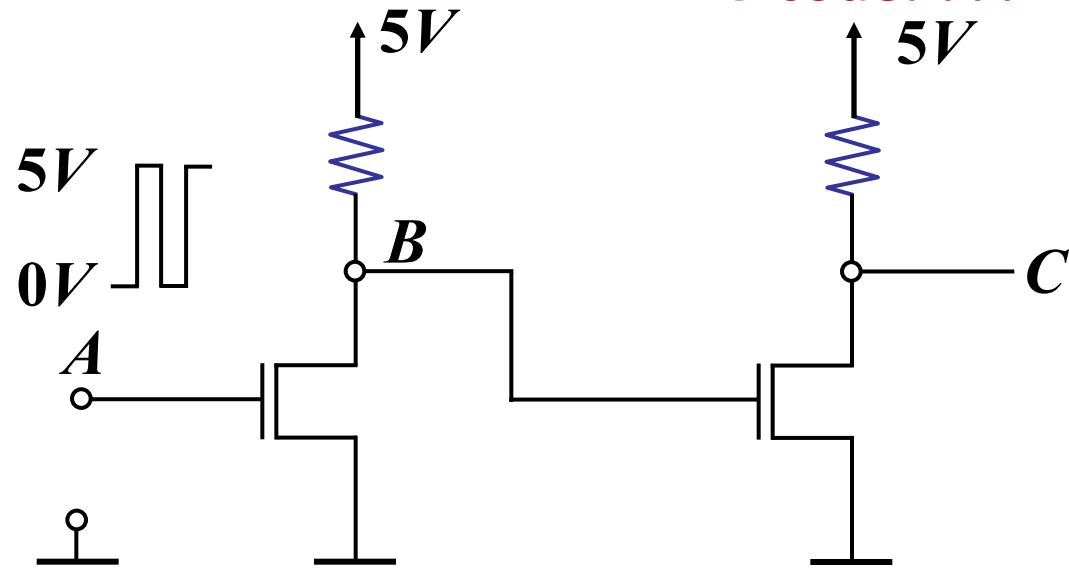
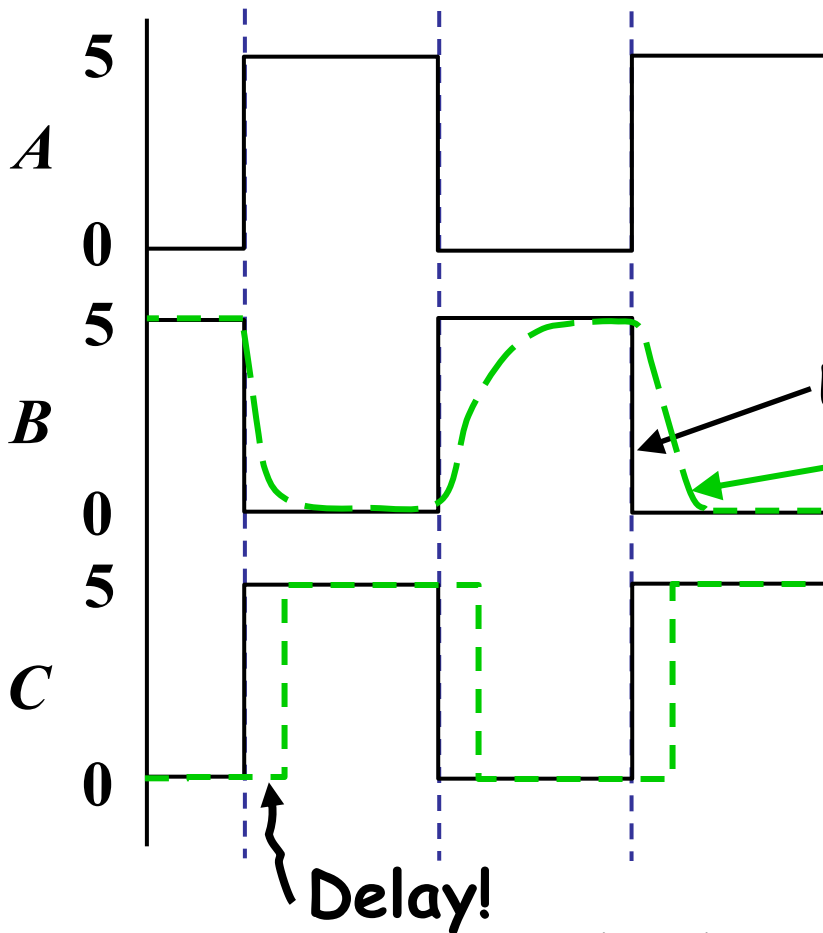
# Outline

**Textbook: 9.1, 9.2 (Capacitors only)**

- **Constitutive Laws**
- **Series and Parallel Connections**

# Motivation

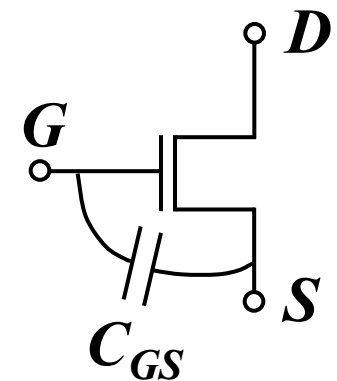
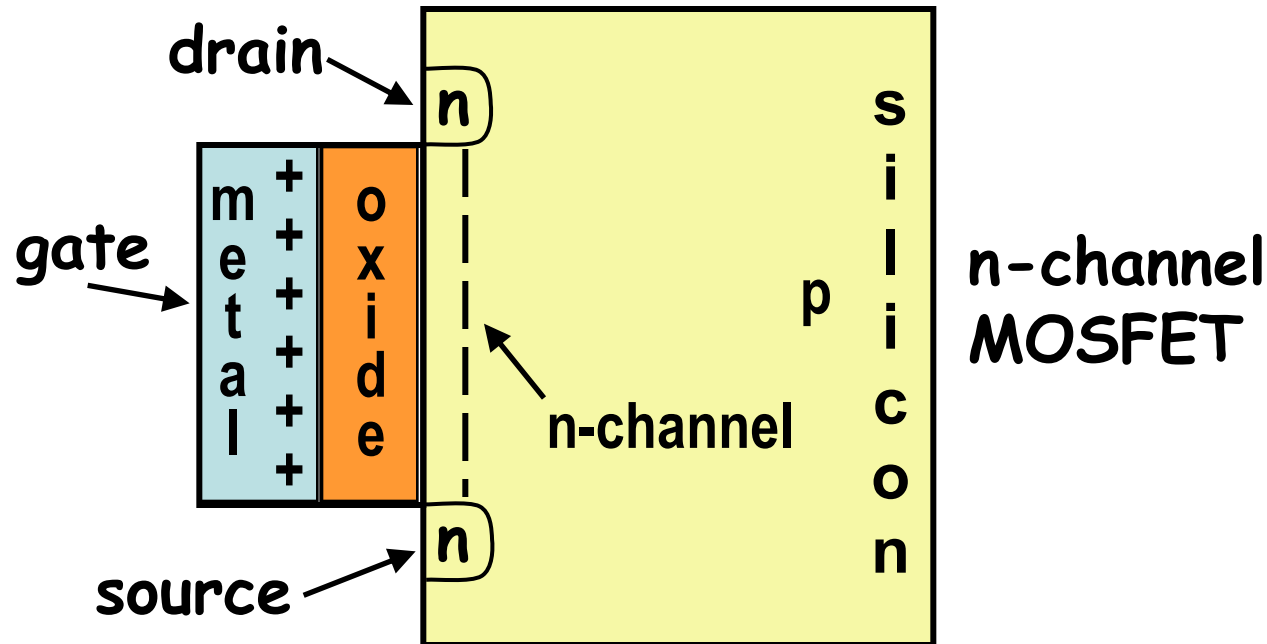
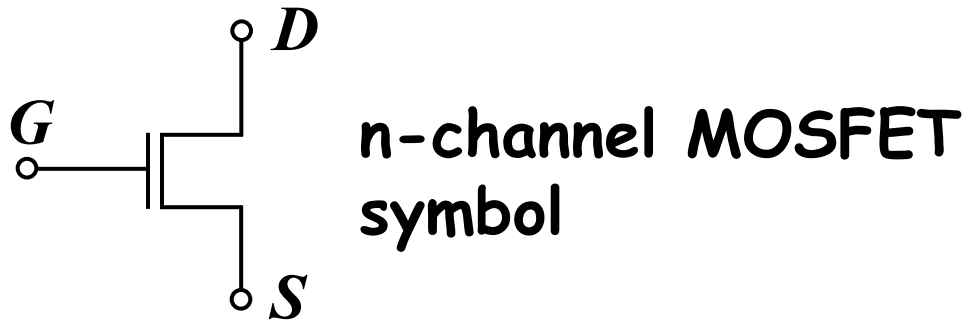
## ■ Example: Inverter delay



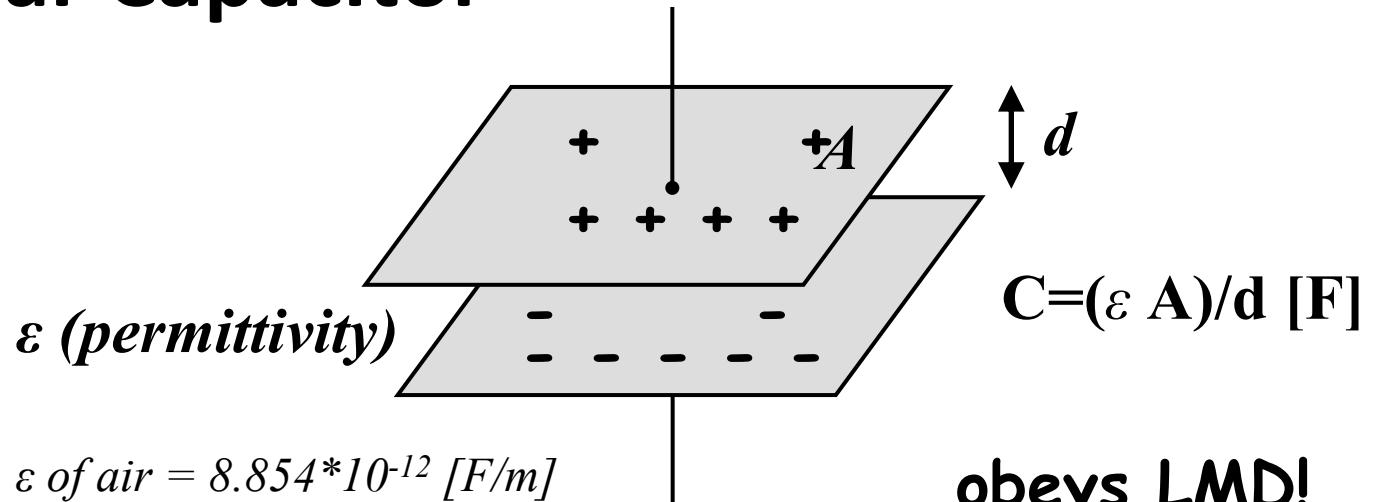
Expect this, right?  
But observe this!

Expected  
Observed

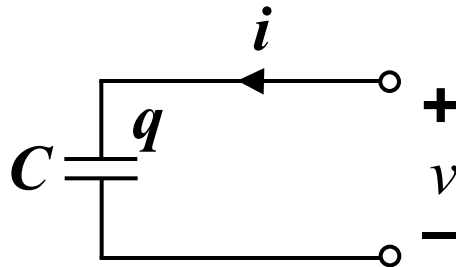
# MOSFET Modeling with Capacitor



# Ideal Linear Capacitor



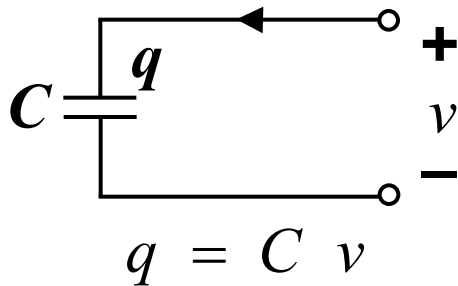
**obeys LMD!**  
**total charge on capacitor**  
 $= +q - q = 0$



$$q = C v$$

coulombs      farads      volts

# Ideal Linear Capacitor



$$v(t) =$$

$$\begin{aligned} i &= \frac{dq}{dt} \\ &= \frac{d(Cv)}{dt} \\ &= C \frac{dv}{dt} \end{aligned}$$

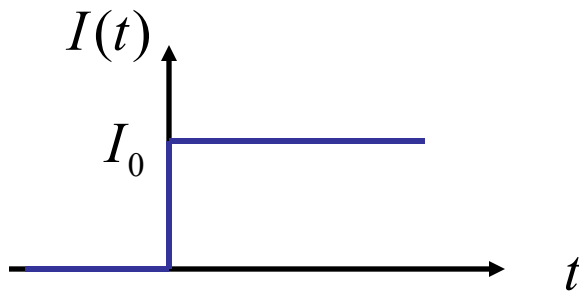
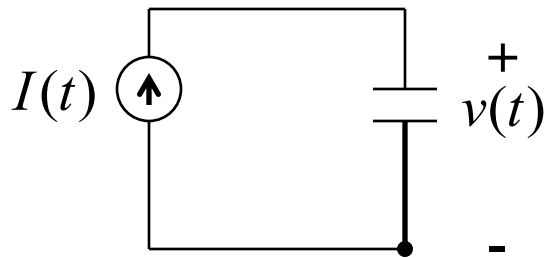
**A capacitor is a memory device  
→ history matters!**

# Ideal Linear Capacitor

- **Observations from  $i(t) = C dv/dt$** 
  - For DC voltage: Capacitor is an open circuit
  - For  $i$  to be finite,  $v(t)$  must be continuous
  - Typical units:  $\mu\text{F} \sim \text{pF}$

# Current Source and Capacitor

- What is  $v(t)$ ?

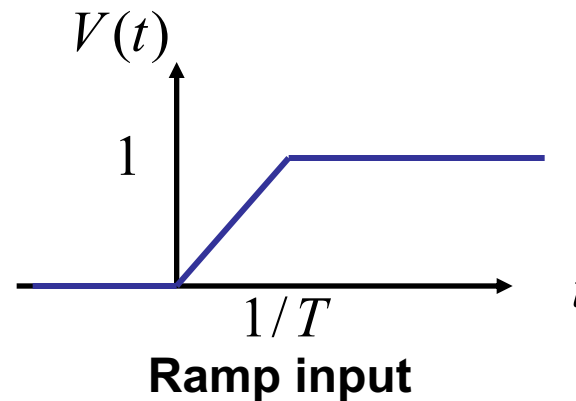
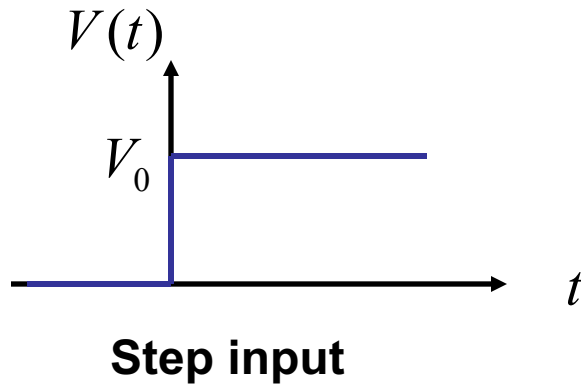
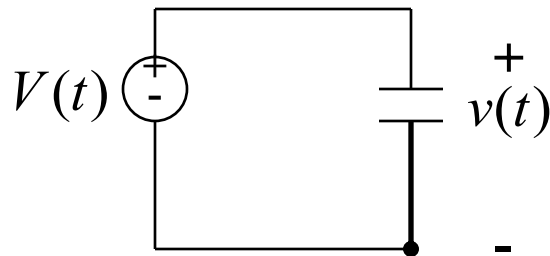


Step input

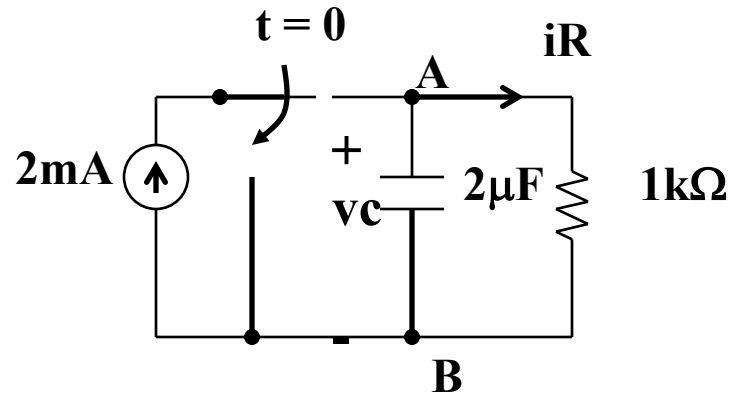


# Voltage Source and Capacitor

- Voltage across a capacitor must be continuous!



# Capacitor Voltage is Continuous

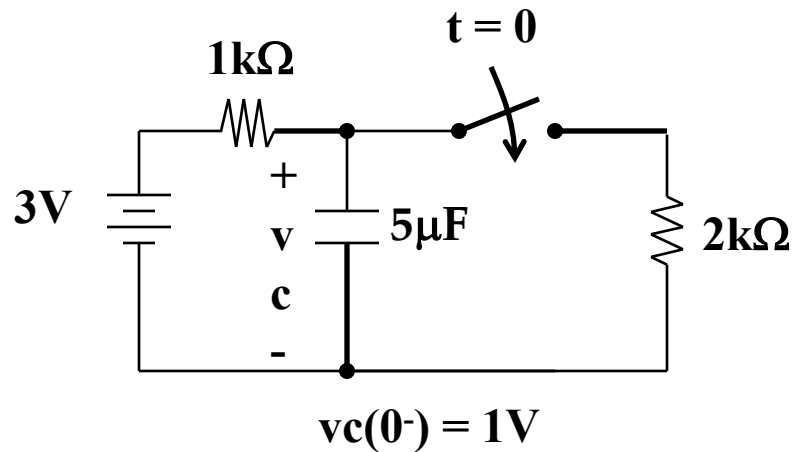


$$i_R(0-) = 1\text{mA}$$

$$\frac{dv_C}{dt}(0-) = ? \quad \frac{dv_C}{dt}(0+) = ?$$

# Exercise

- Switch is closed at  $t=0$ . Find  $\frac{dv_c}{dt}(0+)$ .



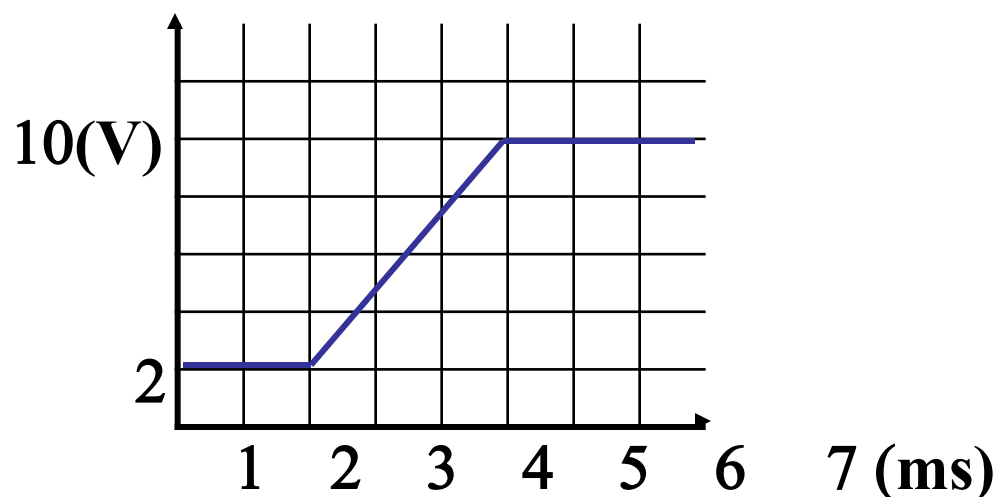
# Energy Storage-Memory Device

■ Stored energy =  $\omega_E(t)$

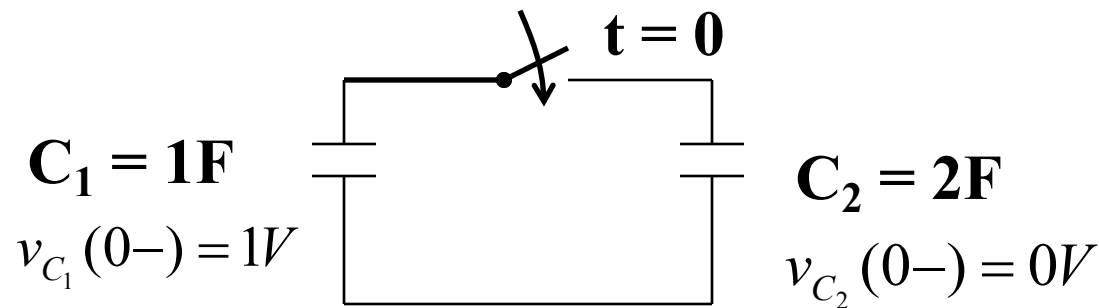
$$\frac{d\omega_E(t)}{dt} = i(t)v(t)$$

$$d\omega_E(t) = v(t)(i(t)dt) = v(t)dq(t)$$

$$\omega_E = \int_0^q v dx = \frac{q^2(t)}{2C} = \frac{Cv(t)^2}{2}$$



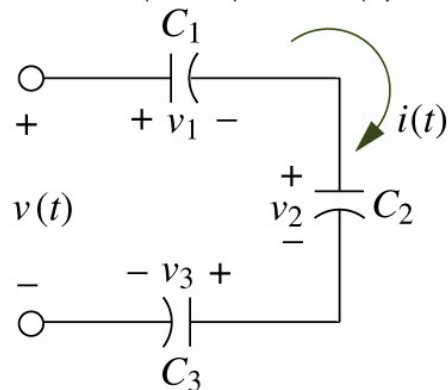
# Capacitor Loop: Charge Conservation



**Question: Is energy conserved?**

# Combining Capacitors in a Circuit

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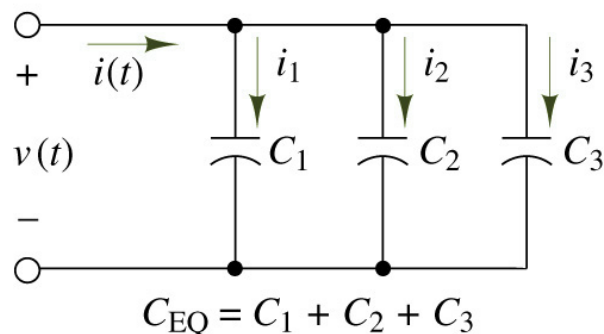


**Series connection:**

$$C_{EQ} = \frac{1}{\frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3}}$$

Capacitances in series combine  
like resistors in parallel

**Parallel connection:**



$$C_{EQ} = C_1 + C_2 + C_3$$

Capacitances in parallel add