

Sample and Hold Amplifiers Notes

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- The acquisition time is the minimum hold \Rightarrow sample time, and the settling time is the minimum sample \Rightarrow hold time.
- For high frequencies, open-loop configurations are generally used with diode bridge circuits.
- Generally, a buffer is required at the output for driving the load in order to prevent charge from leaking from the capacitor.
- For closed loop designs, output integrators can be used as the buffer in order to create a virtual ground at the input, which allows the switch to be switched from ground to virtual ground, eliminating leakage current issues.