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IT-228

B.E. IV Semester

Examination, June 2017

Choice Based Credit System (CBCS) Computer Architecture

Time: Three Hours

Maximum Marks: 60

Attempt any five questions. Note: i)

Total No. of Questions: 8]

- ii) All questions carry equal marks.
- Describe Von Neumann model with the help of diagram.
 - Using 8 bits 2's complement integer, show how to perform the subtraction operation 18-16.
- What is the decimal value of the following unsigned binary integers?
 - Explain hardwired micro-programmed control unit.
- What are different addressing modes? Explain each of them.
 - Write comparison between RISC and CISC.
- Explain DMA with the help of block diagram.
 - Explain how a stack is implemented in the memory system of a computer.

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- Explain about auxilliary and associative memory.
 - Describe the concept of virtual memory.

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- Write comparison between paging and segmentation.
 - Explain any page replacement algorithm with the help of example. www.rgpvonline.com
- What do you understand by multiprocessor? Write its characteristics.
 - Explain arithmetic pipelining.
- 8. Write a short note on:

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- Matrix multiplication
- Priority interrupt
- Instruction format

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