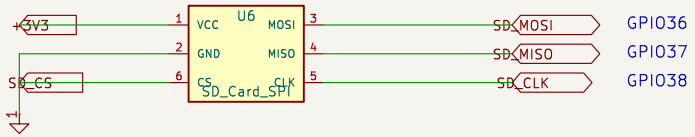
STORAGE — Micro SD Card

SPI interface for ROM storage (SNES ROMs up to 6MB)

SPI bus:

Chip Select:
GPIO39



Design Notes:

- SPI bus @ up to 40MHz (ESP32-S3 max for SD)
- SNES ROMs: 256KB to 6MB typical (HiROM/LoROM)
- FAT32 filesystem for easy ROM management via PC
- 3.3V logic: no level shifter needed (module has built-in)
- GPIO36-39 grouped for clean SPI trace routing on PCB

Generated by scripts/generate_schematics
ESP32 Emu Turbo — Handheld Retro Console

Sheet: /
File: 05-sd-card.kicad_sch

Title: Storage — SD Card SPI

Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.7		Id: 5/1