



The diagram shows a top-down view of a microprocessor die. It features a central horizontal bar labeled 'Shared L3 Cache**'. Above this bar are four identical rectangular blocks, each labeled 'Core'. To the left of the cache bar is a large rectangular block labeled 'Processor Graphics'. To the right of the cache bar is a vertical strip labeled 'System Agent, Display Engine & Memory Controller'. At the bottom of the die is a horizontal strip labeled 'Memory Controller I/O'. The die is color-coded: blue for logic blocks, yellow for cache and interconnects, orange for graphics, and green for I/O and peripheral blocks.

Processor Graphics

Core

Core

Core

Core

**System Agent,
Display Engine &
Memory Controller**

including
Display, PCIe*
and DMI I/Os

Shared L3 Cache**

Memory Controller I/O