| Task | Jan 13-19 | Jan 20-26 | Jan 27-Feb2 | Feb 3-9 | Feb 10-16 | Feb 17-23 | Feb 24-Mar 31 | Mar 2-8 | Mar 9-15 | Mar 16-22 | Mar 23-29 | Mar 30-Apr 5 | Apr 6-12 | Apr 13-19 | Apr 20-26 | Apr 27-May 3 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **Project Proposal** | 🟥 | | | | | | | | | | | | | | | |
| **Design document rev 1** | 🟥 | | | | | | | | | | | | | | | |
| **Solution proposal** | | 🟥 | | | | | | | | | | | | | | |
| Braille reader background research, finalize subsystem design specs, individual subsystem design | 🟥 | 🟥 | 🟥 | | | | | | | | | | | | | |
| Revision for Design Document | | | 🟥 | | | | | | | | | | | | | |
| **Design document rev 2** | | | 🟥 | | | | | | | | | | | | | |
| ADC algorithm research (tutorial) | | 🟥 | 🟥 | 🟥 | 🟩 | | | | | | | | | | | |
| Audio mapping algorithm research | | 🟥 | 🟥 | 🟥 | | | | | | | | | | | | |
| Update on subsystem diagram for audio mapping part | | | | 🟥 | 🟩 | | | | | | | | | | | |
| Install aeneas libraries, audio and ebook files for testing for audio mapping | | | | 🟥 | | | | | | | | | | | | |
| Build and test Audio Mapping on Python using aeneas library | | | | 🟥 | 🟩 | | | | | | | | | | | |
| Install System Workbench | | | | 🟥 | | | | | | | | | | | | |
| **Manager meeting** | | | | | 🟩 | | | | | | | | | | | |
| Use feedback from MM to revise subsystem design | | | | | 🟩 | 🟩 | | | | | | | | | | |
| Configuration of ADC (potentiometer, power) | | | | | 🟩 | 🟩 | | | | | | | | | | |
| Testing ADC output with board | | | | | | 🟩 | | | | | | | | | | |
| Configure prescaler factor and test on LED | | | | | | 🟩 | 🟩 | | | | | | | | | |
| Prepare for midterm demo (update on subsystem design document, run complete test on all subsystem parts with the board) | | | | | | | 🟩 | 🟩 | | | | | | | | |

**Team 17: 5 Blind Mice Project Timeline (Spring 2020)**

Week

| Task | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **Skills assessment (preliminary)** | | | | | | ■ | | | | | | | | | |
| **Midterm demo** | | | | | | | ■ | | | | | | | | |
| **Design document rev 3** | | | | | | | | ■ | | | | | | | |
| Integration of subsystems on microcontroller | | | | | | | ■ | ■ | ■ | ■ | ■ | | | | |
| Update subsystem schematic for PCB design | | | | | | | ■ | | | | | | | | |
| PCB design for the team (order, integration, soldering) | | | | | | | ■ | ■ | ■ | ■ | | | | | |
| **PCB review** | | | | | | | | | | ■ | | | | | |
| **Skills assessment (deadline)** | | | | | | | | | | | ■ | | | | |
| **PCB exception requests due** | | | | | | | | | | | | | | ■ | |
| Construct ebook reader and run test on the design | | | | | | | ■ | ■ | ■ | ■ | ■ | ■ | ■ | ■ | ■ |
| Prepare for midterm demo(finalize design document, prepare presentation slides) | | | | | | | | | | | | | | ■ | ■ |
| **Final demo** | | | | | | | | | | | | | | | ■ |
| **Final design review** | | | | | | | | | | | | | | | |
| **Design document - final** | | | | | | | | | | | | | | | ■ |
| **ABET** | | | | | | | | | | | | | | | ■ |

*Bolds are major deadlines
*Unbolds are specific tasks