Expected / Projected / Actual Class Progression

Week 1 - 2/1

- Syllabus
- What's already assigned
- Install
- Questions
- Recording

Week 2 - 2/6 2/8

- 1 - 1 - NA - NA - NA
- Apple Silicon
- Windows
- Intel Mac - get the distro, get QEMU, follow instructions for Windows except use your plain old terminal instead of WSL.
- Binary
- Powers of 2 up to 216
- Signed and Unsigned Integers
- 1's Complement and 2's Complement
- Registers
    - Integer Registers w & x
    - Why Have Registers
        * Speed of Processors Relative to RAM
    - *Up to this point was Tuesday 2/6. Thursday's class follows.*
    - Why Have Registers (Continued)
        * Steps Needed to Execute an Instruction
        * Pipelined Execution
    - Special Registers
        * Program Counter - pc
        * Stack Pointer - sp
        * Frame Pointer - x29
        * Link Register - x30

- Floating Point Registers $\boldsymbol{h}$, s, d, v & q
- Floating Point Construction

Week 3 - 2/13 2/15

- 0 / 0 / 0 / 0 / 0

Week 4 - 2/20 2/22

Week 5 - 2/27 2/29

Week 6 - 3/12 3/14

Week 7 - 3/19 3/21

Week 8 - 3/26 3/28

Week 9 - 4/2 4/4

Week 10 - 4/9 4/11

Week 11 - 4/16 4/18

Week 12 - 4/23 4/25

Week 13 - 4/30 5/2

Week 14 - 5/7 5/9