Component				
				stub: Pin/Parameter exists, but no add logic
				mocked: Pin/Parameter exists with simulation logic
Owner	Type	Dir	Name	
49	s32	OUT	hm2_7i76e.0.0.debug	
49	float	OUT	hm2_7i76e.0.7i76.0.0.analogin0	mocked
	float	IN	hm2_7i76e.0.7i76.0.0.analogin0-sim	mock: copy value to analogin<x>
49	float	RW	hm2_7i76e.0.7i76.0.0.analogin0-scalemax	stub
49	float	OUT	hm2_7i76e.0.7i76.0.0.analogin1	mocked
	float	IN	hm2_7i76e.0.7i76.0.0.analogin1-sim	mock: copy value to analogin<x>
49	float	RW	hm2_7i76e.0.7i76.0.0.analogin1-scalemax	stub
49	float	OUT	hm2_7i76e.0.7i76.0.0.analogin2	mocked
	float	IN	hm2_7i76e.0.7i76.0.0.analogin2-sim	mock: copy value to analogin<x>
49	float	RW	hm2_7i76e.0.7i76.0.0.analogin2-scalemax	stub
49	float	OUT	hm2_7i76e.0.7i76.0.0.analogin3	mocked
	float	IN	hm2_7i76e.0.7i76.0.0.analogin3-sim	mock: copy value to analogin<x>
49	float	RW	hm2_7i76e.0.7i76.0.0.analogin3-scalemax	stub
49	s32	OUT	hm2_7i76e.0.7i76.0.0.enc0.count	stub
49	u32	RW	hm2_7i76e.0.7i76.0.0.enc0.counts-per-rev	not yet implemented
49	bit	I/O	hm2_7i76e.0.7i76.0.0.enc0.index-enable	not yet implemented
49	float	OUT	hm2_7i76e.0.7i76.0.0.enc0.position	stub
49	s32	OUT	hm2_7i76e.0.7i76.0.0.enc0.rawcounts	not yet implemented
49	bit	I/O	hm2_7i76e.0.7i76.0.0.enc0.reset	not yet implemented
49	float	RW	hm2_7i76e.0.7i76.0.0.enc0.scale	not yet implemented
49	s32	OUT	hm2_7i76e.0.7i76.0.0.enc1.count	not yet implemented
49	u32	RW	hm2_7i76e.0.7i76.0.0.enc1.counts-per-rev	not yet implemented
49	bit	I/O	hm2_7i76e.0.7i76.0.0.enc1.index-enable	stub
49	float	OUT	hm2_7i76e.0.7i76.0.0.enc1.position	not yet implemented
49	s32	OUT	hm2_7i76e.0.7i76.0.0.enc1.rawcounts	not yet implemented
49	bit	I/O	hm2_7i76e.0.7i76.0.0.enc1.reset	not yet implemented
49	float	RW	hm2_7i76e.0.7i76.0.0.enc1.scale	not yet implemented
49	u32	RO	hm2_7i76e.0.7i76.0.0.encmode0	not yet implemented
49	u32	RO	hm2_7i76e.0.7i76.0.0.encmode1	not yet implemented
49	float	OUT	hm2_7i76e.0.7i76.0.0.fieldvoltage	mocked
	float	IN	hm2_7i76e.0.7i76.0.0.fieldvoltage-sim	mock: copy value to fieldvoltage
49	float	RW	hm2_7i76e.0.7i76.0.0.fieldvoltage-scalemax	not yet implemented
49	u32	RW	hm2_7i76e.0.7i76.0.0.hwrevision	not yet implemented
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-00	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-00-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-00-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-01	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-01-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-01-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-02	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-02-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-02-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-03	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-03-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-03-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-04	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-04-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-04-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-05	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-05-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-05-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-06	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-06-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-06-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-07	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-07-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-07-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-08	mocked

49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-08-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-08-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-09	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-09-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-10-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-10	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-10-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-10-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-11	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-11-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-11-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-12	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-12-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-12-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-13	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-13-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-13-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-14	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-14-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-14-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-15	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-15-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-15-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-16	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-16-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-16-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-17	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-17-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-17-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-18	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-18-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-18-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-19	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-19-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-19-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-20	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-20-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-20-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-21	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-21-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-21-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-22	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-22-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-22-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-23	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-23-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-23-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-24	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-24-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-24-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-25	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-25-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-25-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-26	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-26-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-26-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-27	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-27-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-27-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-28	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-28-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-28-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-29	mocked

49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-29-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-29-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-30	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-30-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-30-sim	mock: copy value to input-<xx>
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-31	mocked
49	bit	OUT	hm2_7i76e.0.7i76.0.0.input-31-not	mocked
	bit	IN	hm2_7i76e.0.7i76.0.0.input-31-sim	mock: copy value to input-<xx>
49	u32	RO	hm2_7i76e.0.7i76.0.0.nvencmode0	not yet implemented
49	u32	RO	hm2_7i76e.0.7i76.0.0.nvencmode1	not yet implemented
49	u32	RO	hm2_7i76e.0.7i76.0.0.nvunitnumber	not yet implemented
49	u32	RO	hm2_7i76e.0.7i76.0.0.nvwatchdogtimeout	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-00	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-00-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-01	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-01-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-02	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-02-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-03	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-03-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-04	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-04-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-05	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-05-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-06	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-06-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-07	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-07-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-08	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-08-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-09	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-09-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-10	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-10-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-11	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-11-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-12	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-12-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-13	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-13-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-14	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-14-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.output-15	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.output-15-invert	not yet implemented
49	bit	IN	hm2_7i76e.0.7i76.0.0.spindir	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.spindir-invert	stub
49	bit	IN	hm2_7i76e.0.7i76.0.0.spinena	stub
49	bit	RW	hm2_7i76e.0.7i76.0.0.spinena-invert	stub
49	float	IN	hm2_7i76e.0.7i76.0.0.spinout	stub
49	float	RW	hm2_7i76e.0.7i76.0.0.spinout-maxlim	stub
49	float	RW	hm2_7i76e.0.7i76.0.0.spinout-minlim	stub
49	float	RW	hm2_7i76e.0.7i76.0.0.spinout-scalemax	stub
49	u32	RW	hm2_7i76e.0.7i76.0.0.swrevision	not yet implemented
49	float	IN	hm2_7i76e.0.dpll.01.timer-us	not yet implemented
49	float	IN	hm2_7i76e.0.dpll.02.timer-us	not yet implemented
49	float	IN	hm2_7i76e.0.dpll.03.timer-us	not yet implemented
49	float	IN	hm2_7i76e.0.dpll.04.timer-us	not yet implemented
49	float	IN	hm2_7i76e.0.dpll.base-freq-khz	not yet implemented
49	u32	OUT	hm2_7i76e.0.dpll.ddsize	not yet implemented
49	float	OUT	hm2_7i76e.0.dpll.phase-error-us	not yet implemented
49	u32	IN	hm2_7i76e.0.dpll.plimit	not yet implemented
49	u32	OUT	hm2_7i76e.0.dpll.prescale	not yet implemented
49	u32	IN	hm2_7i76e.0.dpll.time-const	not yet implemented

49	s32	OUT	hm2_7i76e.0.encoder.00.count	stub
49	bit	RW	hm2_7i76e.0.encoder.00.counter-mode	not yet implemented
49	s32	OUT	hm2_7i76e.0.encoder.00.count-latched	not yet implemented
49	bit	RW	hm2_7i76e.0.encoder.00.filter	not yet implemented
49	bit	I/O	hm2_7i76e.0.encoder.00.index-enable	not yet implemented
49	bit	RW	hm2_7i76e.0.encoder.00.index-invert	not yet implemented
49	bit	RW	hm2_7i76e.0.encoder.00.index-mask	not yet implemented
49	bit	RW	hm2_7i76e.0.encoder.00.index-mask-invert	not yet implemented
49	bit	OUT	hm2_7i76e.0.encoder.00.input-a	not yet implemented
49	bit	OUT	hm2_7i76e.0.encoder.00.input-b	not yet implemented
49	bit	OUT	hm2_7i76e.0.encoder.00.input-index	not yet implemented
49	float	OUT	hm2_7i76e.0.encoder.00.position	stub
49	float	OUT	hm2_7i76e.0.encoder.00.position-latched	not yet implemented
49	bit	OUT	hm2_7i76e.0.encoder.00.quad-error	not yet implemented
49	bit	IN	hm2_7i76e.0.encoder.00.quad-error-enable	not yet implemented
49	s32	OUT	hm2_7i76e.0.encoder.00.rawcounts	not yet implemented
49	s32	OUT	hm2_7i76e.0.encoder.00.rawlatch	not yet implemented
49	bit	IN	hm2_7i76e.0.encoder.00.reset	not yet implemented
49	float	RW	hm2_7i76e.0.encoder.00.scale	not yet implemented
49	float	OUT	hm2_7i76e.0.encoder.00.velocity	not yet implemented
49	float	OUT	hm2_7i76e.0.encoder.00.velocity-rpm	not yet implemented
49	float	RW	hm2_7i76e.0.encoder.00.vel-timeout	not yet implemented
49	bit	IN	hm2_7i76e.0.encoder.hires-timestamp	not yet implemented
49	u32	IN	hm2_7i76e.0.encoder.sample-frequency	not yet implemented
49	s32	IN	hm2_7i76e.0.encoder.timer-number	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.000.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.000.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.001.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.001.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.002.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.002.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.003.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.003.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.004.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.004.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.005.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.005.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.006.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.006.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.006.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.006.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.006.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.006.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.007.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.007.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.007.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.007.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.007.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.007.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.008.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.008.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.009.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.009.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.010.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.010.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.011.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.011.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.012.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.012.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.012.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.012.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.012.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.012.out	not yet implemented

49	bit	OUT	hm2_7i76e.0.gpio.013.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.013.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.013.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.013.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.013.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.013.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.014.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.014.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.015.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.015.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.016.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.016.in_not	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.017.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.017.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.017.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.017.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.017.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.017.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.018.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.018.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.018.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.018.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.018.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.018.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.019.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.019.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.019.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.019.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.019.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.019.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.020.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.020.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.020.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.020.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.020.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.020.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.021.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.021.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.021.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.021.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.021.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.021.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.022.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.022.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.022.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.022.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.022.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.022.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.023.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.023.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.023.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.023.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.023.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.023.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.024.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.024.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.024.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.024.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.024.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.024.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.025.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.025.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.025.invert_output	not yet implemented

49	bit	RW	hm2_7i76e.0.gpio.025.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.025.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.025.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.026.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.026.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.026.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.026.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.026.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.026.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.027.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.027.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.027.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.027.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.027.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.027.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.028.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.028.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.028.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.028.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.028.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.028.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.029.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.029.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.029.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.029.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.029.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.029.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.030.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.030.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.030.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.030.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.030.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.030.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.031.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.031.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.031.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.031.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.031.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.031.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.032.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.032.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.032.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.032.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.032.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.032.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.033.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.033.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.033.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.033.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.033.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.033.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.034.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.034.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.034.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.034.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.034.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.034.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.035.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.035.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.035.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.035.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.035.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.035.out	not yet implemented

49	bit	OUT	hm2_7i76e.0.gpio.036.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.036.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.036.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.036.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.036.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.036.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.037.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.037.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.037.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.037.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.037.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.037.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.038.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.038.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.038.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.038.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.038.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.038.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.039.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.039.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.039.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.039.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.039.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.039.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.040.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.040.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.040.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.040.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.040.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.040.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.041.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.041.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.041.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.041.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.041.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.041.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.042.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.042.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.042.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.042.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.042.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.042.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.043.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.043.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.043.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.043.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.043.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.043.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.044.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.044.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.044.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.044.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.044.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.044.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.045.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.045.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.045.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.045.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.045.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.045.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.046.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.046.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.046.invert_output	not yet implemented

49	bit	RW	hm2_7i76e.0.gpio.046.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.046.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.046.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.047.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.047.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.047.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.047.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.047.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.047.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.048.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.048.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.048.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.048.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.048.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.048.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.049.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.049.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.049.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.049.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.049.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.049.out	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.050.in	not yet implemented
49	bit	OUT	hm2_7i76e.0.gpio.050.in_not	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.050.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.050.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.gpio.050.is_output	not yet implemented
49	bit	IN	hm2_7i76e.0.gpio.050.out	not yet implemented
49	bit	RW	hm2_7i76e.0.io_error	not yet implemented
49	bit	IN	hm2_7i76e.0.led.CR01	not yet implemented
49	bit	IN	hm2_7i76e.0.led.CR02	not yet implemented
49	bit	IN	hm2_7i76e.0.led.CR03	not yet implemented
49	bit	IN	hm2_7i76e.0.led.CR04	not yet implemented
49	bit	OUT	hm2_7i76e.0.packet-error	not yet implemented
49	s32	RO	hm2_7i76e.0.packet-error-decrement	not yet implemented
49	bit	OUT	hm2_7i76e.0.packet-error-exceeded	not yet implemented
49	s32	RW	hm2_7i76e.0.packet-error-increment	not yet implemented
49	s32	OUT	hm2_7i76e.0.packet-error-level	not yet implemented
49	s32	RW	hm2_7i76e.0.packet-error-limit	not yet implemented
49	u32	I/O	hm2_7i76e.0.packet-error-total	not yet implemented
49	s32	RW	hm2_7i76e.0.packet-read-timeout	not yet implemented
49	bit	IN	hm2_7i76e.0.pwmgen.00.enable	stub
49	bit	RW	hm2_7i76e.0.pwmgen.00.offset-mode	not yet implemented
49	bit	RW	hm2_7i76e.0.pwmgen.00.out0.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.pwmgen.00.out0.is_opendrain	not yet implemented
49	bit	RW	hm2_7i76e.0.pwmgen.00.out1.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.pwmgen.00.out1.is_opendrain	not yet implemented
49	s32	RW	hm2_7i76e.0.pwmgen.00.output-type	not yet implemented
49	float	RW	hm2_7i76e.0.pwmgen.00.scale	not yet implemented
49	float	IN	hm2_7i76e.0.pwmgen.00.value	stub
49	u32	RW	hm2_7i76e.0.pwmgen.pdm_frequency	stub
49	u32	RW	hm2_7i76e.0.pwmgen.pwm_frequency	stub
49	s32	OUT	hm2_7i76e.0.read.time	Not yet implemented
49	s32	RW	hm2_7i76e.0.read.tmax	Not yet implemented
49	bit	RO	hm2_7i76e.0.read.tmax-increased	Not yet implemented
49	s32	OUT	hm2_7i76e.0.read-request.time	Not yet implemented
49	s32	RW	hm2_7i76e.0.read-request.tmax	Not yet implemented
49	bit	RO	hm2_7i76e.0.read-request.tmax-increased	Not yet implemented
49	bit	RW	hm2_7i76e.0.sserial.00.tx0.invert_output	Not yet implemented
49	bit	RW	hm2_7i76e.0.sserial.00.tx0.is_opendrain	Not yet implemented
49	u32	OUT	hm2_7i76e.0.sserial.port-0.fault-count	Not yet implemented
49	u32	RW	hm2_7i76e.0.sserial.port-0.fault-dec	Not yet implemented
49	u32	RW	hm2_7i76e.0.sserial.port-0.fault-inc	Not yet implemented
49	u32	RW	hm2_7i76e.0.sserial.port-0.fault-lim	Not yet implemented

49	u32	OUT	hm2_7i76e.0.sserial.port-0.port_state	Not yet implemented
49	u32	OUT	hm2_7i76e.0.sserial.port-0.port_state2	Not yet implemented
49	u32	OUT	hm2_7i76e.0.sserial.port-0.port_state3	Not yet implemented
49	bit	IN	hm2_7i76e.0.sserial.port-0.run	Not yet implemented
49	bit	IN	hm2_7i76e.0.stepgen.00.control-type	stub
49	s32	OUT	hm2_7i76e.0.stepgen.00.counts	mocked
49	float	OUT	hm2_7i76e.0.stepgen.00.dbg_err_at_match	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.00.dbg_ff_vel	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.00.dbg_pos_minus_prev_cmd	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.00.dbg_s_to_match	Not yet implemented
49	s32	OUT	hm2_7i76e.0.stepgen.00.dbg_step_rate	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.00.dbg_vel_error	Not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.00.direction.invert_output	Not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.00.direction.is_opendrain	Not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.00.dirhold	stub
49	u32	RW	hm2_7i76e.0.stepgen.00.dirsetup	stub
49	bit	IN	hm2_7i76e.0.stepgen.00.enable	stub
49	float	RW	hm2_7i76e.0.stepgen.00.maxaccel	stub
49	float	RW	hm2_7i76e.0.stepgen.00.maxvel	stub
49	float	IN	hm2_7i76e.0.stepgen.00.position-cmd	mocked
49	float	OUT	hm2_7i76e.0.stepgen.00.position-fb	mocked
49	bit	IN	hm2_7i76e.0.stepgen.00.position-reset	Not yet implemented
49	float	RW	hm2_7i76e.0.stepgen.00.position-scale	stub
49	bit	RW	hm2_7i76e.0.stepgen.00.step.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.00.step.is_opendrain	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.00.step_type	mocked
49	u32	RW	hm2_7i76e.0.stepgen.00.steplen	stub
49	u32	RW	hm2_7i76e.0.stepgen.00.stepspace	stub
49	u32	RW	hm2_7i76e.0.stepgen.00.table-data-0	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.00.table-data-1	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.00.table-data-2	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.00.table-data-3	not yet implemented
49	float	IN	hm2_7i76e.0.stepgen.00.velocity-cmd	mocked
49	float	OUT	hm2_7i76e.0.stepgen.00.velocity-fb	mocked
49	bit	IN	hm2_7i76e.0.stepgen.01.control-type	stub
49	s32	OUT	hm2_7i76e.0.stepgen.01.counts	mocked
49	float	OUT	hm2_7i76e.0.stepgen.01.dbg_err_at_match	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.01.dbg_ff_vel	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.01.dbg_pos_minus_prev_cmd	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.01.dbg_s_to_match	Not yet implemented
49	s32	OUT	hm2_7i76e.0.stepgen.01.dbg_step_rate	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.01.dbg_vel_error	Not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.01.direction.invert_output	Not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.01.direction.is_opendrain	Not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.01.dirhold	stub
49	u32	RW	hm2_7i76e.0.stepgen.01.dirsetup	stub
49	bit	IN	hm2_7i76e.0.stepgen.01.enable	stub
49	float	RW	hm2_7i76e.0.stepgen.01.maxaccel	stub
49	float	RW	hm2_7i76e.0.stepgen.01.maxvel	stub
49	float	IN	hm2_7i76e.0.stepgen.01.position-cmd	mocked
49	float	OUT	hm2_7i76e.0.stepgen.01.position-fb	mocked
49	bit	IN	hm2_7i76e.0.stepgen.01.position-reset	Not yet implemented
49	float	RW	hm2_7i76e.0.stepgen.01.position-scale	stub
49	bit	RW	hm2_7i76e.0.stepgen.01.step.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.01.step.is_opendrain	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.01.step_type	mocked
49	u32	RW	hm2_7i76e.0.stepgen.01.steplen	stub
49	u32	RW	hm2_7i76e.0.stepgen.01.stepspace	stub
49	u32	RW	hm2_7i76e.0.stepgen.01.table-data-0	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.01.table-data-1	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.01.table-data-2	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.01.table-data-3	not yet implemented
49	float	IN	hm2_7i76e.0.stepgen.01.velocity-cmd	mocked

49	float	OUT	hm2_7i76e.0.stepgen.01.velocity-fb	mocked
49	bit	IN	hm2_7i76e.0.stepgen.02.control-type	mocked
49	s32	OUT	hm2_7i76e.0.stepgen.02.counts	mocked
49	float	OUT	hm2_7i76e.0.stepgen.02.dbg_err_at_match	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.02.dbg_ff_vel	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.02.dbg_pos_minus_prev_cmd	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.02.dbg_s_to_match	Not yet implemented
49	s32	OUT	hm2_7i76e.0.stepgen.02.dbg_step_rate	Not yet implemented
49	float	OUT	hm2_7i76e.0.stepgen.02.dbg_vel_error	Not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.02.direction.invert_output	Not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.02.direction.is_opendrain	Not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.02.dirhold	stub
49	u32	RW	hm2_7i76e.0.stepgen.02.dirsetup	stub
49	bit	IN	hm2_7i76e.0.stepgen.02.enable	stub
49	float	RW	hm2_7i76e.0.stepgen.02.maxaccel	stub
49	float	RW	hm2_7i76e.0.stepgen.02.maxvel	stub
49	float	IN	hm2_7i76e.0.stepgen.02.position-cmd	mocked
49	float	OUT	hm2_7i76e.0.stepgen.02.position-fb	mocked
49	bit	IN	hm2_7i76e.0.stepgen.02.position-reset	Not yet implemented
49	float	RW	hm2_7i76e.0.stepgen.02.position-scale	stub
49	bit	RW	hm2_7i76e.0.stepgen.02.step.invert_output	not yet implemented
49	bit	RW	hm2_7i76e.0.stepgen.02.step.is_opendrain	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.02.step_type	stub
49	u32	RW	hm2_7i76e.0.stepgen.02.steplen	stub
49	u32	RW	hm2_7i76e.0.stepgen.02.stepspace	stub
49	u32	RW	hm2_7i76e.0.stepgen.02.table-data-0	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.02.table-data-1	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.02.table-data-2	not yet implemented
49	u32	RW	hm2_7i76e.0.stepgen.02.table-data-3	not yet implemented
49	float	IN	hm2_7i76e.0.stepgen.02.velocity-cmd	mocked
49	float	OUT	hm2_7i76e.0.stepgen.02.velocity-fb	mocked
49	s32	IN	hm2_7i76e.0.stepgen.timer-number	stub
49	bit	I/O	hm2_7i76e.0.watchdog.has_bit	stub
49	u32	RW	hm2_7i76e.0.watchdog.timeout_ns	stub
49	s32	OUT	hm2_7i76e.0.write.time	not yet implemented
49	s32	RW	hm2_7i76e.0.write.tmax	not yet implemented
49	bit	RO	hm2_7i76e.0.write.tmax-increased	not yet implemented