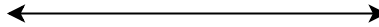


main memory
0x8000_0000 = 0x42

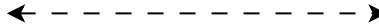
data kept in sync



l1d\$ line
0x8000_0000 = 0x42

main memory
0x8000_0000 = 0xFF

data out of sync



l1d\$ line
0x8000_0000 = 0x42

DMA
transfer



main memory
0x8000_0000 = 0xFF

data out of sync



l1d\$ line
0x8000_0000 = 0x42

CPU
access

