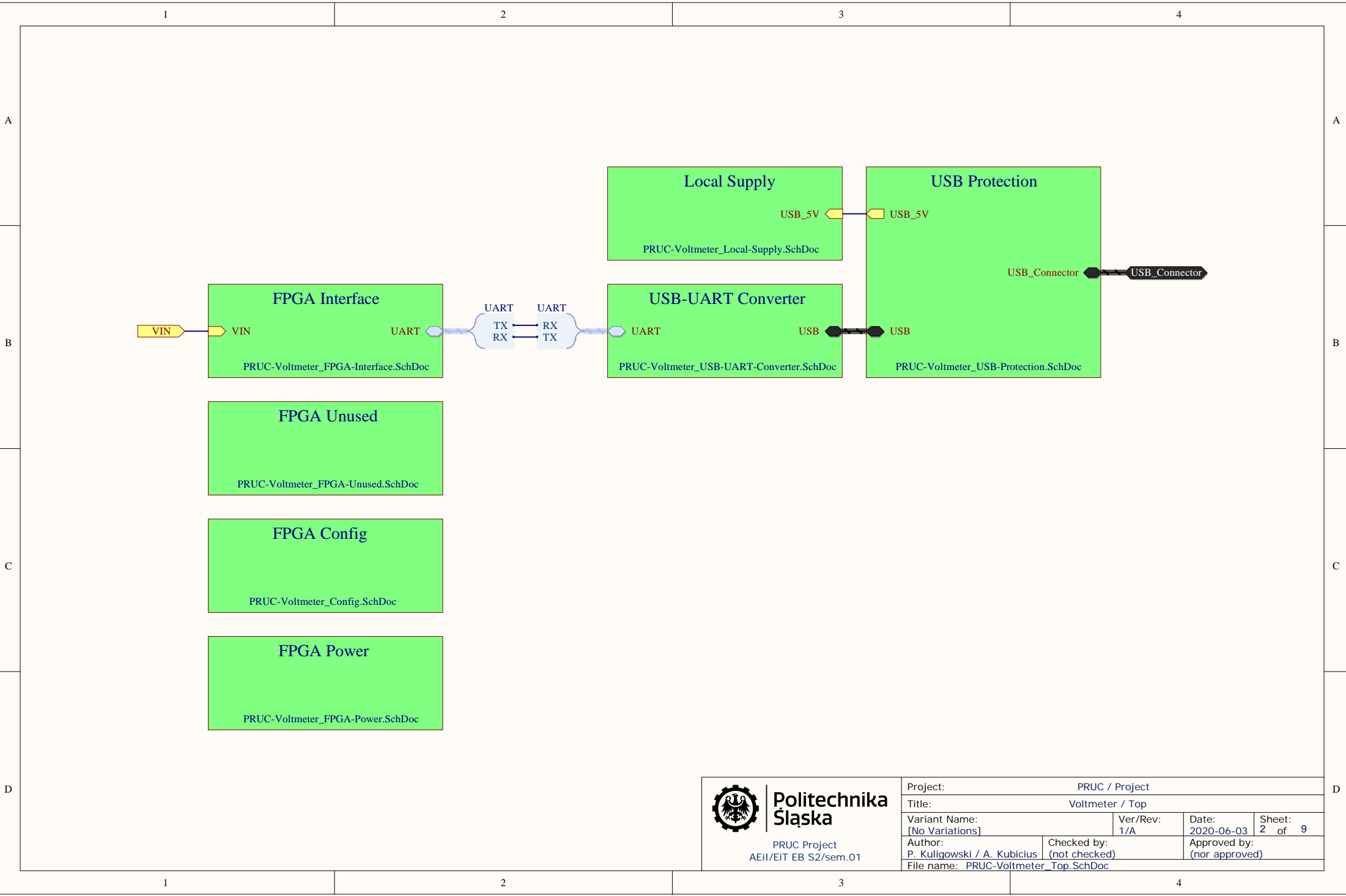


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PRUC Project
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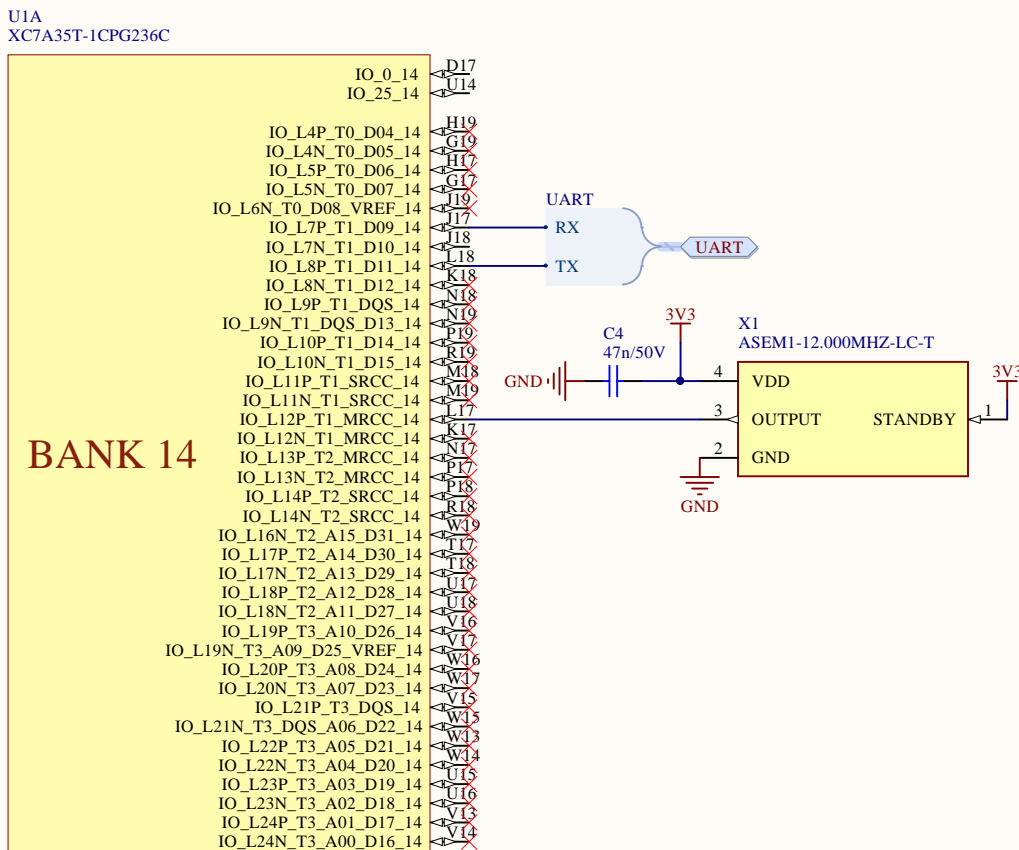
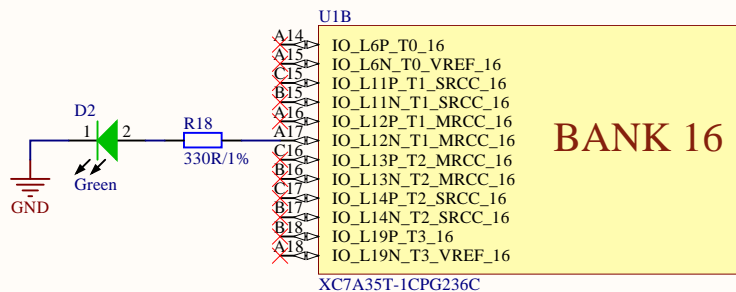
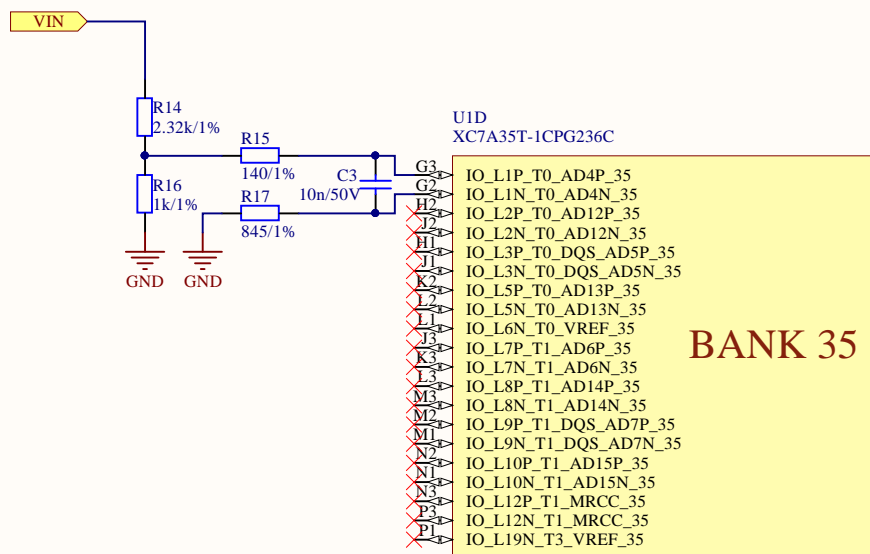
Project: PRUC / Project			
Title: *			
Variant Name: [No Variations]	Ver/Rev: 1/A	Date: 2020-06-03	Sheet: 1 of 9
Author: P. Kuligowski / A. Kubicius	Checked by: (not checked)	Approved by: (nor approved)	
File name: PRUC-Voltmeter_Connectors.SchDoc			



**Politechnika
Śląska**

PRUC Project
AEII/EIT EB S2/sem.01

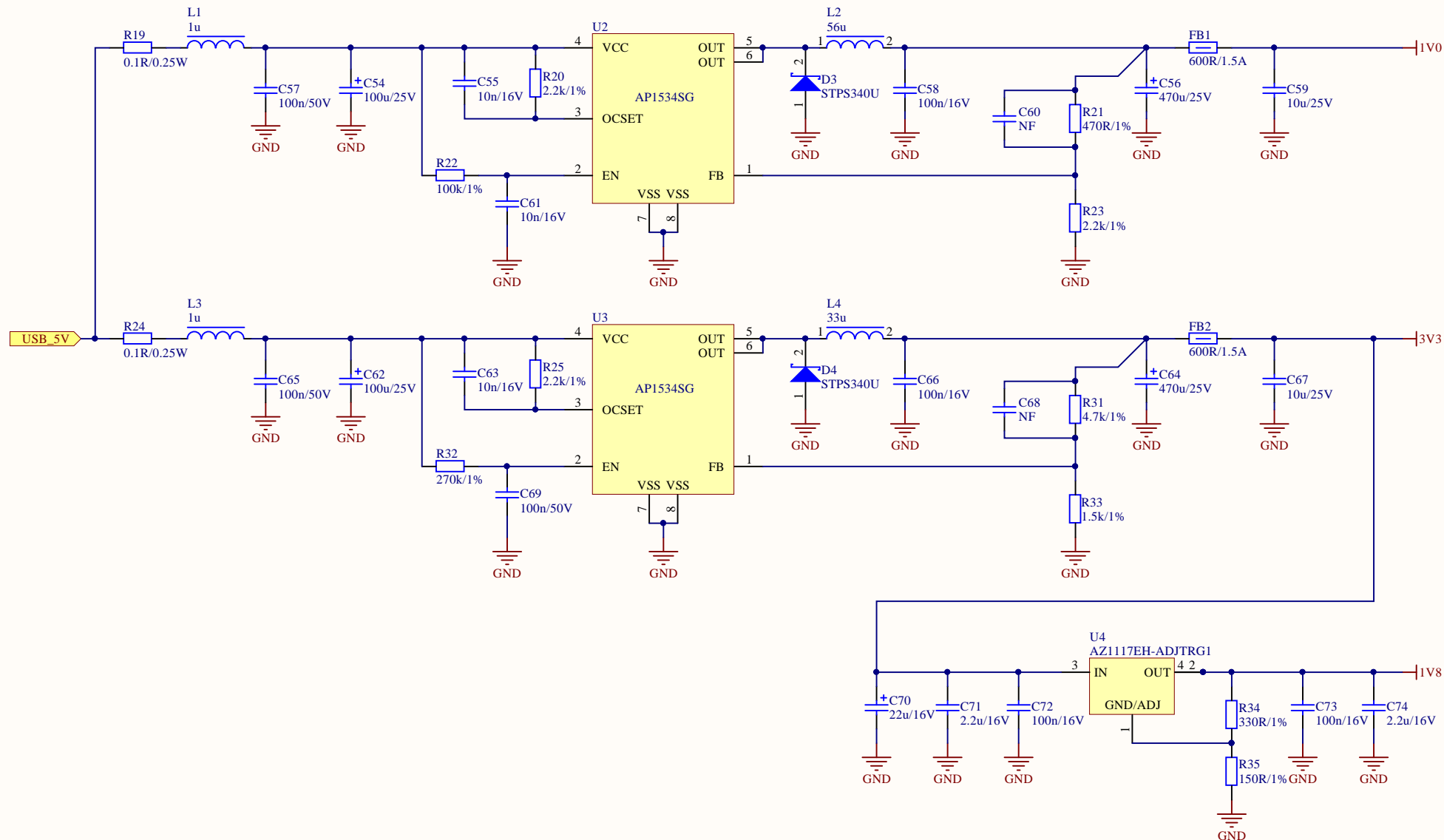
Project: PRUC / Project			
Title: Voltmeter / Top			
Variant Name: [No Variations]		Ver/Rev: 1/A	Date: 2020-06-03
Author: P. Kuligowski / A. Kubicius		Checked by: (not checked)	Sheet: 2 of 9
			Approved by: (nor approved)
File name: PRUC-Voltmeter_Top.SchDoc			



**Politechnika
Śląska**

PRUC Project
AEII/EIT EB S2/sem.01

Project: PRUC / Project				
Title: Voltmeter / FPGA XADC				
Variant Name: [No Variations]		Ver/Rev: 1/A	Date: 2020-06-03	Sheet: 3 of 9
Author: P. Kuligowski / A. Kubicius		Checked by: (not checked)	Approved by: (nor approved)	
File name: PRUC-Voltmeter FPGA-Interface.SchDoc				



**Politechnika
Śląska**

PRUC Project
AEII/EIT EB S2/sem.01

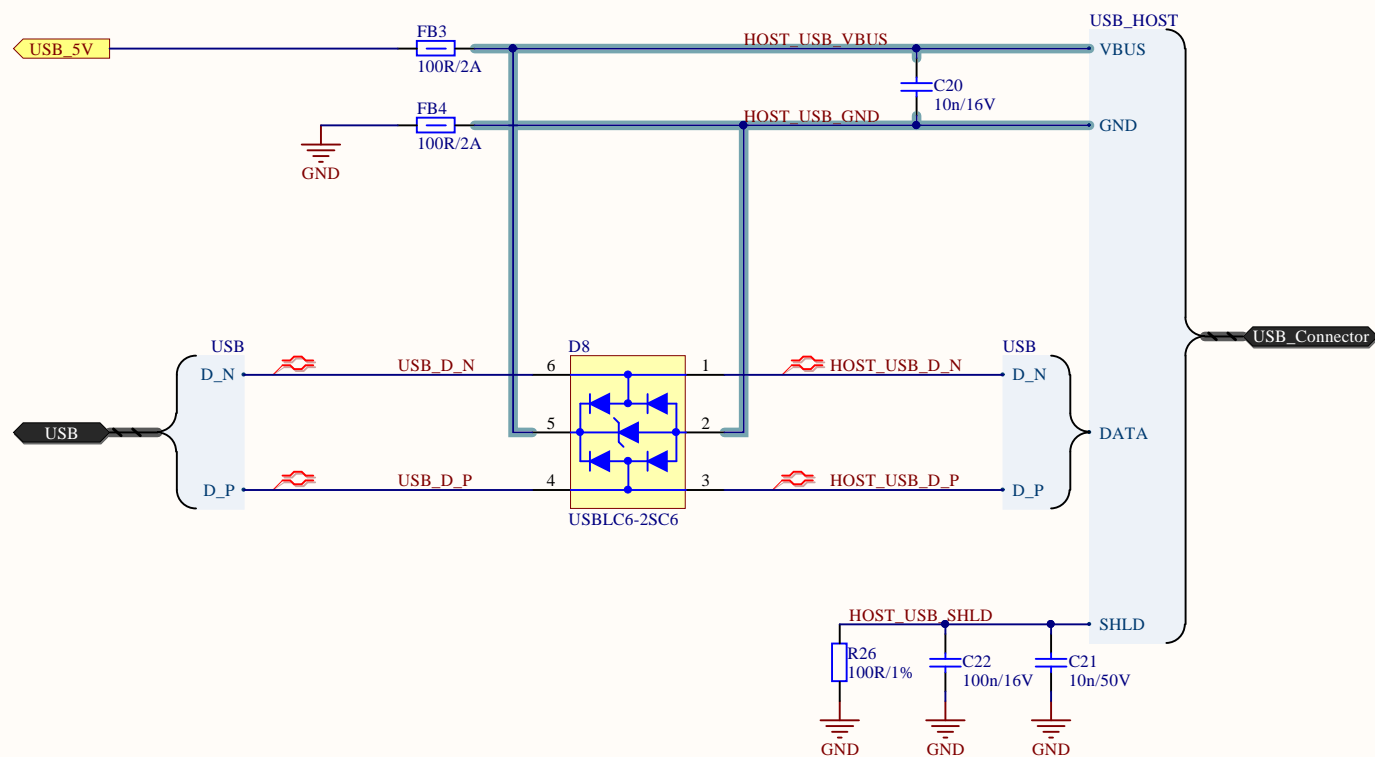
Project: PRUC / Project				
Title: Voltmeter / Local Supply				
Variant Name: [No Variations]		Ver/Rev: 1/A	Date: 2020-06-03	Sheet: 4 of 9
Author: P. Kuligowski / A. Kubicius		Checked by: (not checked)	Approved by: (nor approved)	
File name: PRUC-Voltmeter_Local-Supply.SchDoc				



**Politechnika
Śląska**

PRUC Project
AEII/EiT EB S2/sem.01

Project: PRUC / Project			
Title: USB-UART Converter			
Variant Name: [No Variations]		Ver/Rev: 1/A	Date: 2020-06-03 Sheet: 5 of 9
Author: P. Kuligowski / A. Kubicius	Checked by: (not checked)		Approved by: (not approved)
File name: PRUC-Voltmeter_USB-UART-Converter.SchDoc			



**Politechnika
Śląska**

PRUC Project
AEII/EIT EB S2/sem.01

Project: PRUC / Project				
Title: USB Protection				
Variant Name: [No Variations]		Ver/Rev: 1/A	Date: 2020-06-03	Sheet: 6 of 9
Author: P. Kuligowski / A. Kubicius		Checked by: (not checked)	Approved by: (nor approved)	
File name: PRUC-Voltmeter_USB-Protection.SchDoc				

U1C
XC7A35T-1CPG236C

BANK 34

IO_L1P_T0_34	R2
IO_L1N_T0_34	R3
IO_L2P_T0_34	R4
IO_L2N_T0_34	R5
IO_L3P_T0_DQS_34	R6
IO_L3N_T0_DQS_34	R7
IO_L5P_T0_34	W2
IO_L5N_T0_34	W3
IO_L6P_T0_34	W4
IO_L6N_T0_VREF_34	R3
IO_L9P_T1_DQS_34	R2
IO_L9N_T1_DQS_34	R3
IO_L11P_T1_SRCC_34	D4
IO_L11N_T1_SRCC_34	V4
IO_L12P_T1_MRCC_34	W4
IO_L12N_T1_MRCC_34	W5
IO_L13P_T2_MRCC_34	W6
IO_L13N_T2_MRCC_34	R8
IO_L14P_T2_SRCC_34	R8
IO_L14N_T2_SRCC_34	R8
IO_L16P_T2_34	U5
IO_L16N_T2_34	V5
IO_L19P_T3_34	U7
IO_L19N_T3_VREF_34	V7



**Politechnika
Śląska**

PRUC Project
AEII/EIT EB S2/sem.01

Project:		PRUC / Project	
Title:		Voltmeter / FPGA Unused	
Variant Name: [No Variations]	Ver/Rev: 1/A	Date: 2020-06-03	Sheet: 7 of 9
Author: P. Kuligowski / A. Kubicius	Checked by: (not checked)	Approved by: (nor approved)	
File name: PRUC-Voltmeter_FPGA-Unused.SchDoc			

