

EE517 : VLSI LAB-II

Experiment No. - 09

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Objective :

- Design Analysis of a Telescopic and Folded Cascode single-ended Operational Transconductance Amplifier for high Gain as shown below.
- All Transistors should be biased using current mirror approach.

Provided:

Supply voltage of 1.8V and load cap of 1pF, tsmc-180nm technology.

Specifications:

GBW of 15MHz, Reference golden current source of 40uA, slew rate=10v/us.

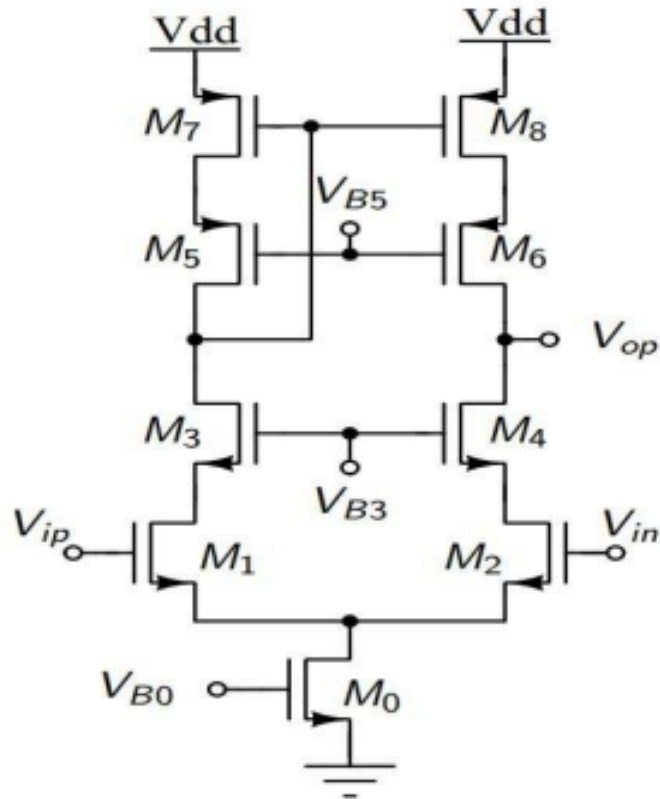
Tool used :

LTspice Simulator.

Theory :

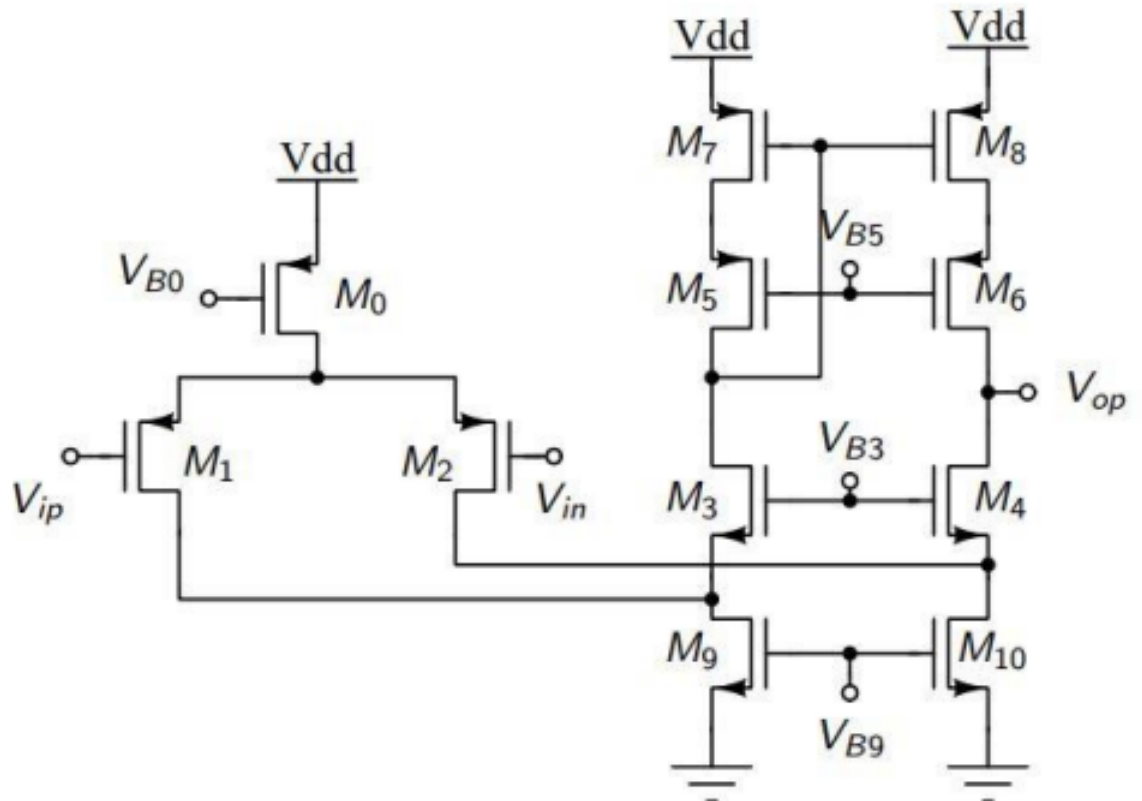
Telescopic cascode single-ended Operational Transconductance Amplifier :

Cascode configurations may be used to increase the voltage gain of CMOS transistor amplifier stages. This structure has been called a 'telescopic-cascode' op amp because the cascode are connected between the power supplies in series with the transistors in the differential pair, resulting in a structure in which the transistors in each branch are connected along a straight line. The main potential advantage of telescopic cascode op amps is that they can be designed so that the signal variations are entirely handled by the fastest-polarity transistors. In the first stage, we were simply looking for a configuration that allowed for high gain, low noise and minimal current since output swing is less critical. The folded cascode and the telescopic configurations were considered since we required at least one cascoded stage for a gain on the order of g_m^2 . A high swing configuration still needs to be used to that all the devices in this stage are in saturation. In comparing the two topologies, the folded cascode has more current legs and more devices in the signal path.



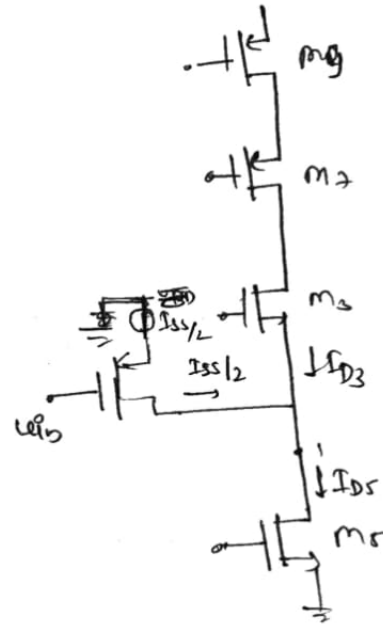
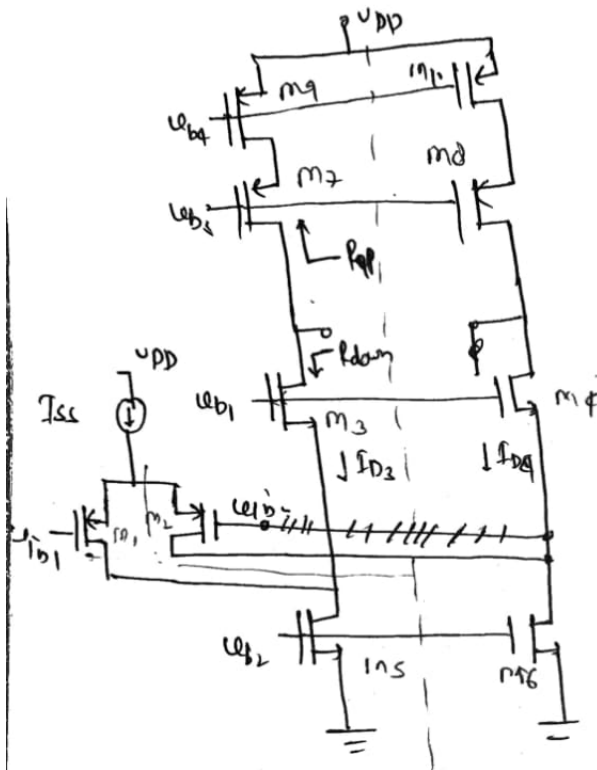
Flded cascode single-ended Operational Transconduc- tance Amplifier

Figure shows typical structure of a folded cascode op-amp. Folded cascode topology is called as 'folded cascode' because it comes from a folding down p-channel cascode active loads of a different-pair and changing the MOSFET to the n-channel. These topologies allow the input common-mode level of being close to the power supply voltage as well as providing a high output swing, wide input common-mode range and preferably steering in low voltage supply circuits. However, this topology has higher noise compare to the telescopic op-amp. Folded cascode amplifier is a single-pole operational amplifier with large output swing and has higher gain compared to the ordinary op-amp. It is very suitable for deep negative feedback because of its small signal gain that can be very large. Comparing to the ordinary telescopic amplifiers, folded cascode operational amplifiers have a larger output swing. Input and output can be short circuited to make it easier for the selection of input common-mode level due to its relatively large output swing. The input common-mode level can be close to the power supply voltage by using folded cascode op-amp.



Analysis of folded cascode :

Folded Cascode op-amp → Design approach



① max. differential swing $\Rightarrow 2(V_{DD} - (V_{ov3} + V_{ov5} + |V_{ov}|_2 + |V_{ov}|_5)$

② we can take V_{ov} for ^(m3, m2, m1) transistors = 0.2 volt.

→ In this case $m2$ & $m1$ transistors has to be larger size than $m3$ & $m5$ [due to less k_n than k_p]
To manage same amount of current

③ $I_{DS} = I_{D3} + \frac{I_{SS}}{2}$

in that case $m5$ can use can take larger V_{ov} as

$$(V_{ov})_5 = 0.3 \text{ volt}$$

& also can take larger $(\frac{W}{L})$ ratio.

④ let take power dissipation - up to the device
from there we can calculate how much current should be
flow through the transistor →

$$\begin{aligned}\text{Total current} &= I_{D5} + I_{D6} \\ &= \frac{I_{SS}}{2} + I_{D3} + \frac{I_{SS}}{2} + I_{D4} \\ &= (I_{SS} + I_{D3} + I_{D4})\end{aligned}$$

⑤ now we can calculate $(\frac{W}{L})$ ratios
as we know, from 4th eqn.

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_T)^2 \rightarrow \text{nmos}$$

$$\Rightarrow \left(\frac{W}{L}\right)_n = \frac{2 I_D}{\mu_n C_{ox} (V_{GS})^2} \rightarrow \text{nmos}$$

$$\Rightarrow \left(\frac{W}{L}\right)_p = \frac{2 I_D}{\mu_p C_{ox} (V_{GS})^2} \rightarrow \text{pmos}$$

⑦ Calculate bias voltage -

$$\underline{V_{b2}} \rightarrow V_{GS5} = V_{b2}$$

$$V_{DS5} = V_{GS5} - V_T = 0.3 \text{ Volt (assumption)}$$

$$V_{GS} = 0.3 + V_{th}$$

$$V_{b2} = (V_{DS})_5 + V_{GS}$$

$$\underline{V_{b1}} \rightarrow V_{GS3} = V_{GS} - V_{DS} = V_{b1} - (V_{DS})_5$$

$$V_{DS3} = V_{GS3} - V_{th} = 0.2$$

$$\Rightarrow V_{b1} - (V_{DS})_5 - V_{th} = 0.2$$

$$\begin{aligned}V_{b1} &= 0.2 + (V_{DS})_5 + V_{th} \\ &= V_{DS5} + V_{th} + V_{DS3}\end{aligned}$$

v_{b4} \rightarrow

$$v_{sg} = v_{sg} - v_{cg} = V_{DD} - v_{b4}$$

$$|v_{ov}|_q = 0.2 = (v_{sg} - |v_{tp}|)$$

$$v_{sg} = 0.2 + |v_{tp}|$$

$$V_{DD} - v_{b4} = 0.2 + |v_{tp}|$$

$$v_{b4} = V_{DD} - 0.2 - |v_{tp}|$$

$$v_{b4} = v_{dp} - |v_{ov}|_q - |v_{tp}|$$

v_{b3} \rightarrow

$$v_{sg7} = v_{sg7} - v_{cg7}$$

$$v_{sg7} = \overbrace{V_{DD} - |v_{ov}|_q} - v_{cg7}$$

$$|v_{ov}|_7 = v_{sg7} - |v_{tp}|$$

$$|v_{ov}|_7 = V_{DD} - |v_{ov}|_q - v_{cg7} - |v_{tp}|$$

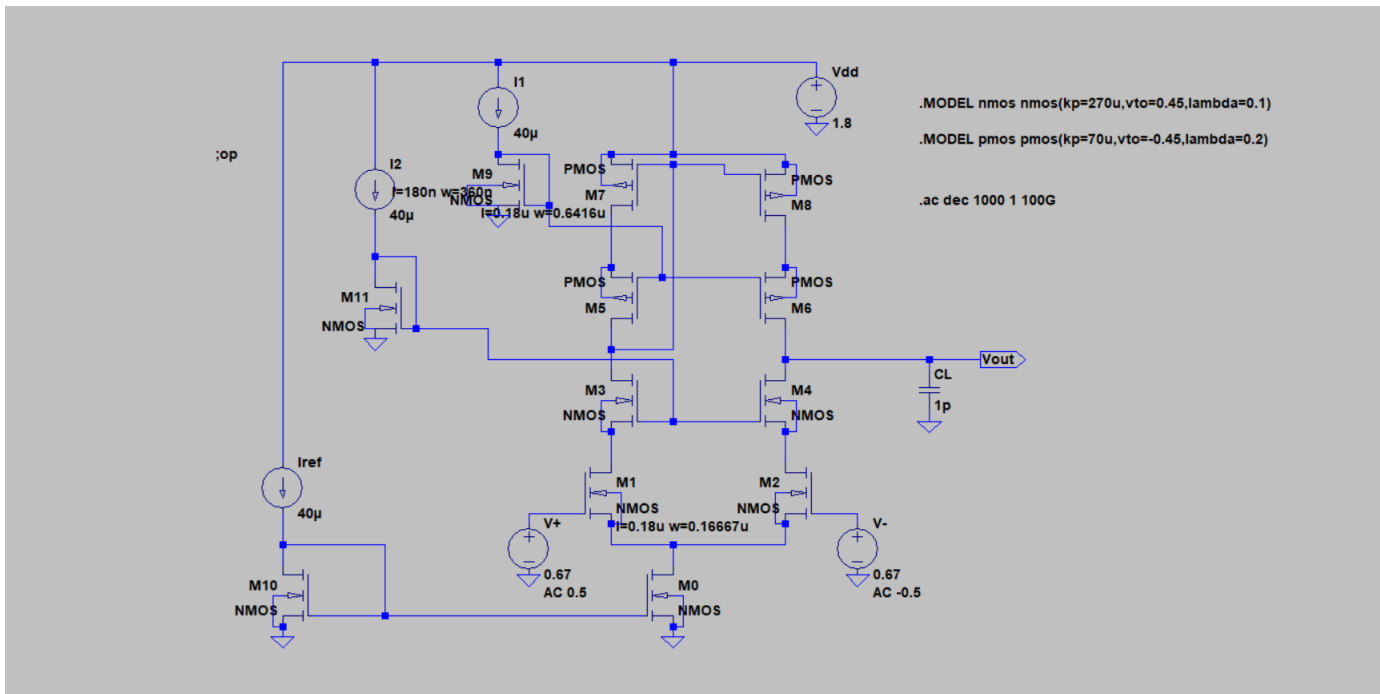
$$\Rightarrow v_{cg7} = V_{DD} - |v_{ov}|_q - |v_{ov}|_7 - |v_{tp}|$$

$$v_{b3} = V_{DD} - |v_{ov}|_q - |v_{ov}|_7 - |v_{tp}|$$

Observation and calculation :

Telescopic cascode single-ended Operational Transconductance Amplifier :

$$K_n = 270\mu A/V, K_p = 70\mu A/V, \lambda_n = 0.1V^{-1}, \lambda_p = 0.2V^{-1}, V_{tn} = 0.45V, V_{tp} = -0.45V$$



Schematic of telescopic cascode amplifier

Calculation :

Calculations & Design procedure

① m_1 to m_4 : $\ln G_{ox} = k_p = 270 \frac{\mu A}{V^2}$, $\mu_n = 0.1$, $\mu_p = 0.2$

Taking $I_D = 5 \mu A$

$V_{ov} = 0.2 \text{ V}$

$g_m = \frac{2 I_D}{V_{ov}} = 50 \mu A/V$

$\left(\frac{W}{L}\right) = \frac{g_m L}{2 I_D \ln G_{ox}} = 0.9259$

② m_5 to m_8

similarly $I_D = 5 \mu A$, $V_{ov} = 0.2 \text{ V}$

$\left(\frac{W}{L}\right) = \frac{g_m L}{2 I_D \ln G_{ox}} = 3.5647$

③ Gain:

$r_{on} = \frac{1}{0.1 \times 50} = 2 \text{ M}\Omega$

$r_{op} = \frac{1}{0.2 \times 50} = 1 \text{ M}\Omega$

$r_{out} = g_{m6}(r_{op})^2 \parallel g_{m4} r_{on} L$

$= (50 \times 10^6) \parallel (50 \times 2 \times 10^6)$

$= \frac{50 \times 100}{50 + 100} \times 10^6$

$r_{out} = 33.33 \text{ M}\Omega$

④ Gain \rightarrow

$A_v = g_{m1} (r_{out})$

$= 50 \times 33.33$

$A_v = 1667.66 \frac{V}{V} = 64.438 \text{ dB}$

$$GBW = \frac{g_m}{C_L} = \frac{50 \times 10^{-6}}{1 \times 10^{-12}}$$

$$GBW = 50 \times 10^6$$

$$BW = 25 \text{ kHz}$$

⑥ Input common mode →

$$V_{GS} - V_t > V_{ov}$$

$$V_G > V_{ov} + V_t + V_{DS}$$

$$V_G > 0.2 + 0.45 + 0.1$$

$$V_G > 0.75$$

⑦ Current should be maintain using the current mirror ckt. as shown in the schematic.

$$⑧ \left(\frac{W}{L}\right)_0 = 1.22$$

$$\left(\frac{W}{L}\right)_{10,11,12} = 2$$

ideal pden current source. = 9.14 A

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--- Operating Point ---

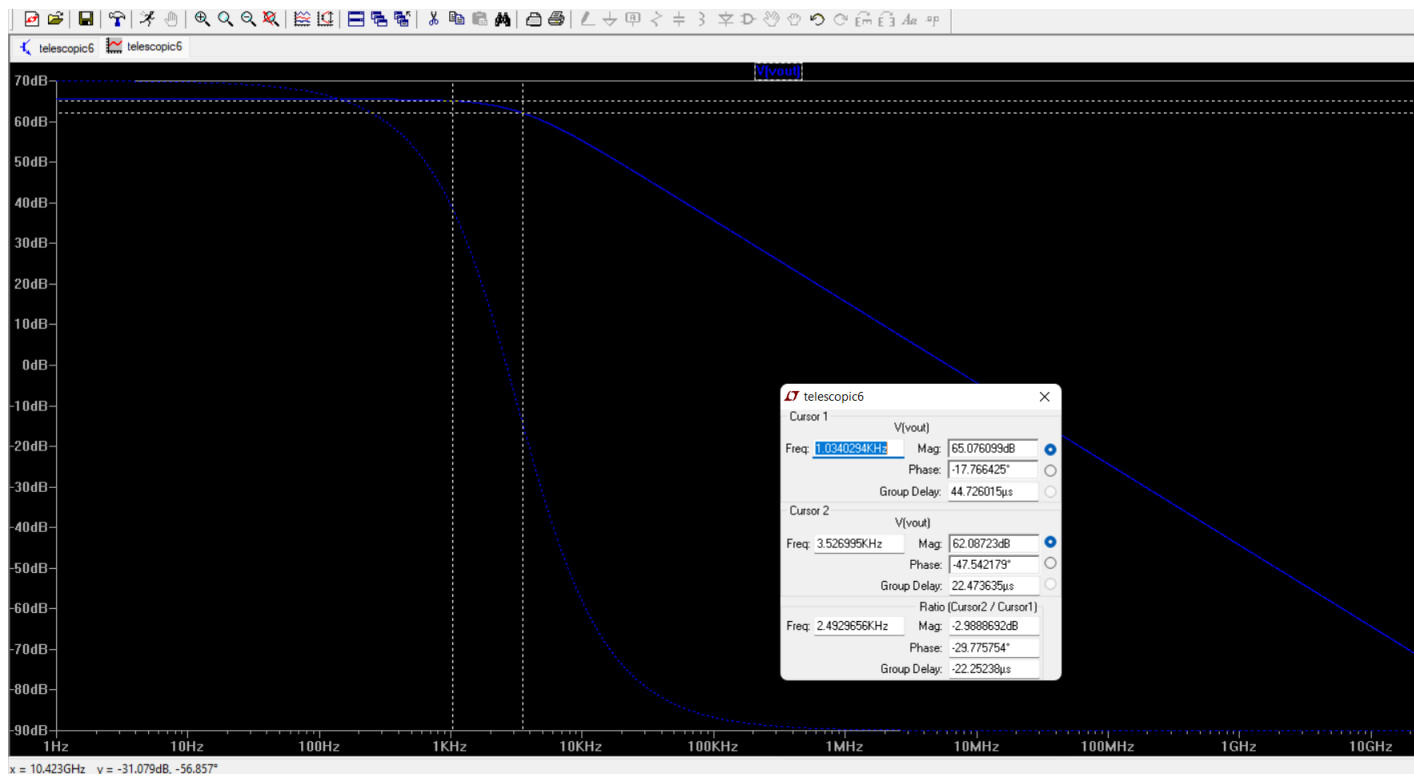
V(n009):	0.214764	voltage
V(n012):	0.67	voltage
V(n011):	0.0584916	voltage
V(n010):	0.214764	voltage
V(n013):	0.67	voltage
V(n003):	1.19288	voltage
V(n005):	0.820027	voltage
V(vout):	1.19288	voltage
V(n002):	0.820027	voltage
V(n006):	1.42915	voltage
V(n007):	1.42915	voltage
V(n001):	1.8	voltage
V(n014):	0.820027	voltage
V(n004):	0.102774	voltage
V(n008):	0.102774	voltage
Id(M8):	-3.30816e-006	device_current
Ig(M8):	0	device_current
Ib(M8):	3.80848e-013	device_current
Is(M8):	3.30816e-006	device_current
Id(M7):	-3.30816e-006	device_current
Ig(M7):	0	device_current
Ib(M7):	3.80848e-013	device_current
Is(M7):	3.30816e-006	device_current
Id(M6):	-3.30816e-006	device_current
Ig(M6):	0	device_current
Ib(M6):	2.46269e-013	device_current
Is(M6):	3.30816e-006	device_current
Id(M5):	-3.30816e-006	device_current
Ig(M5):	0	device_current
Ib(M5):	2.46269e-013	device_current
Is(M5):	3.30816e-006	device_current
Id(M11):	4e-005	device_current
Ig(M11):	0	device_current
Ib(M11):	-1.02774e-013	device_current
Is(M11):	-4e-005	device_current
Id(M9):	4e-005	device_current
Ig(M9):	0	device_current
Ib(M9):	-1.02774e-013	device_current
Is(M9):	-4e-005	device_current
Id(M10):	4e-005	device_current
Ig(M10):	0	device_current
Ib(M10):	-8.30027e-013	device_current
Is(M10):	-4e-005	device_current
Id(M0):	6.61632e-006	device_current

DC analysis of telescopic cascode

SPICE error log: C:\VLASI LAB II\lab 09\telescopic6.log

Cgb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Name:	m9	m10	m0	m4	m3
Model:	nmos	nmos	nmos	nmos	nmos
Id:	4.00e-05	4.00e-05	6.62e-06	3.31e-06	3.31e-06
Vgs:	8.20e-01	8.20e-01	8.20e-01	6.05e-01	6.05e-01
Vds:	8.20e-01	8.20e-01	5.85e-02	9.78e-01	9.78e-01
Vbs:	1.03e-01	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Vth:	4.50e-01	4.50e-01	4.50e-01	4.50e-01	4.50e-01
Vdsat:	3.70e-01	3.70e-01	3.70e-01	1.55e-01	1.55e-01
Gm:	2.16e-04	2.16e-04	1.94e-05	4.26e-05	4.26e-05
Gds:	3.70e-06	3.70e-06	1.04e-04	3.01e-07	3.01e-07
Gmb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cbd:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cbs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgsbv:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgdov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgbv:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgd:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Name:	m2	m1			
Model:	nmos	nmos			
Id:	3.31e-06	3.31e-06			
Vgs:	6.12e-01	6.12e-01			
Vds:	1.56e-01	1.56e-01			
Vbs:	0.00e+00	0.00e+00			
Vth:	4.50e-01	4.50e-01			
Vdsat:	1.62e-01	1.62e-01			
Gm:	3.97e-05	3.97e-05			
Gds:	1.66e-06	1.66e-06			
Gmb:	0.00e+00	0.00e+00			
Cbd:	0.00e+00	0.00e+00			
Cbs:	0.00e+00	0.00e+00			
Cgsbv:	0.00e+00	0.00e+00			
Cgdov:	0.00e+00	0.00e+00			
Cgbv:	0.00e+00	0.00e+00			
Cgs:	0.00e+00	0.00e+00			
Cgd:	0.00e+00	0.00e+00			
Cgb:	0.00e+00	0.00e+00			

Gm of telescopic cascode

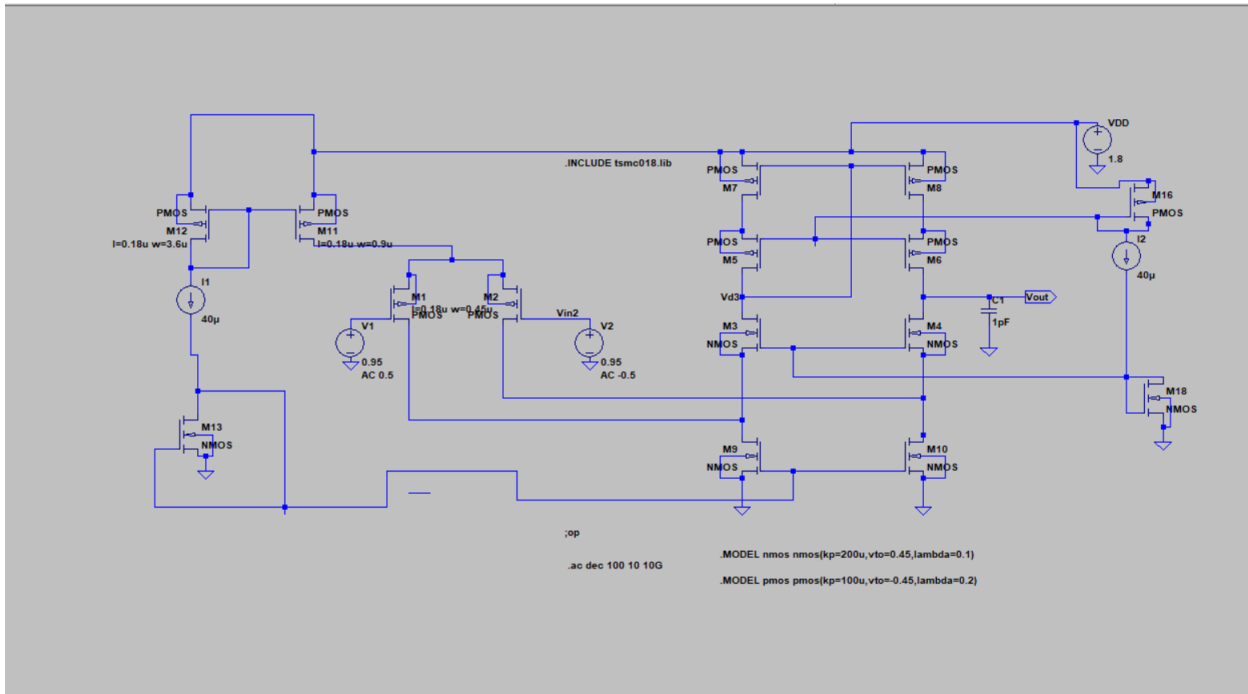


AC analysis of telescopic cascode

Gain = 65.07dB
B.W.=3.52KHz

Folded cascode single-ended Operational Transconductance Amplifier :

$$K_n = 200\mu A/V, K_p = 100\mu A/V, \lambda_n = 0.1V^{-1}, \lambda_p = 0.2V^{-1}, V_{tn} = 0.45V, V_{tp} = -0.45V$$



Schematic of Folded cascode amplifier

Calculation :

Calculation of folded cascode:

$$C_L = 1 \text{ pF}, \quad GBW = 15 \text{ MHz}$$

$$I_{ref} = 10 \mu\text{A}$$

$$\begin{aligned} k_n &= 200 \mu\text{A/V}^2 & V_{tn} &= 0.4 \text{ V} \\ k_p &= 100 \mu\text{A/V}^2 & V_{tp} &= -0.4 \text{ V} \\ \lambda_n &= 0.1 & & \\ \lambda_p &= 0.2 & & \end{aligned}$$

from schematic

$$SR = 10$$

$$I_{D11} = 10 \mu\text{A}$$

$$[I_D = SR C_L]$$

Taking $|V_{ov}| = 0.2 \text{ V}$ for both $n\text{mos}$ & $p\text{mos}$

$$\textcircled{1} \quad I_3 = I_5 = I_7 = I_9 = I_6 = I_8 = 5 \mu\text{A}$$

$$I_{11} = I_{12} = 5 \mu\text{A}$$

$$g_{m1} = \frac{2I_1}{V_{ov}} = 50 \mu\text{A/V} \quad \text{for } [m_1, m_2, m_3, m_4, m_5, m_6, m_7, m_8]$$

$$\left(\frac{W}{L}\right) = \frac{g_{m1}^2}{2\mu_n k_p} = \frac{(50 \times 10^{-6})^2}{2 \times 5 \times 10^{-6} \times 100 \mu\text{A/V}^2}$$

$$\boxed{\left(\frac{W}{L}\right) = 2.5} \quad \text{for } [m_1, m_2, m_3, m_4, m_5, m_6, m_7, m_8]$$

for m_3 & m_4

$$I_D = 5 \mu\text{A}, \quad V_{ov} = 0.2 \text{ V}$$

$$\left(\frac{W}{L}\right) = \frac{2I_D}{k_n(V_{ov})^2} = \frac{2}{0.04} = 1.25$$

m_9 & m_{10}

$$I_D = 10 \mu\text{A}, \quad V_{ov} = 0.2$$

$$\boxed{\frac{W}{L} = 2.5}$$

$$R_{out} = \left[g_{m6} (r_{o6})^2 \right] \parallel \left[g_{m4} r_{o4} (r_{o1} \parallel r_{o10}) \right]$$

$$r_{o6} = \frac{1}{\lambda_{n6}} = \frac{1}{0.1 \times 10^{-3}} = 10 \text{ k}\Omega$$

$$r_{o4} = \frac{1}{\lambda_{n4}} = \frac{1}{0.1 \times 10^{-3}} = 10 \text{ k}\Omega$$

$$r_{o6} = r_{o8} = \frac{1}{\lambda_{p6}} = \frac{1}{0.2 \times 10^{-3}} = 5 \text{ k}\Omega$$

$$r_{o10} = \frac{1}{\lambda_{n10}} = \frac{1}{0.1 \times 10^{-3}} = 10 \text{ k}\Omega$$

$$r_{o1} = \frac{1}{\lambda_{p1}} = \frac{1}{0.2 \times 10^{-3}} = 5 \text{ k}\Omega$$

$$g_{m6} = g_{m4} = \frac{2 \text{ mV}}{V_T} = \frac{2 \times 50 \mu\text{A}}{0.2 \text{ V}} = 500 \mu\text{A/V}$$

$$R_{out} = \left[500 \mu\text{A/V} \left[10^4 \times 10^4 \right] \right] \parallel \left[500 \mu\text{A/V} \times 2 \times 10^4 \left[\frac{1}{2} \times 10^4 \right] \right]$$

$$= 50 \text{ M}\Omega \parallel 50 \text{ M}\Omega$$

$$= 25 \text{ M}\Omega$$

2

$$A_{v2} = g_{m1} R_{out} = 500 \mu\text{A/V} \times 25 \text{ M}\Omega$$

$$\boxed{A_{v2} \approx 12.5} \quad \text{or} \quad \underline{61.93 \text{ dB}}$$

C:\Users\pawan\Desktop\FOLDED CASCODE1.asc

--- Operating Point ---

V(n001):	1.8	voltage
V(vout):	1.30451	voltage
V(n007):	0.95	voltage
V(vin2):	0.95	voltage
V(n006):	1.56927	voltage
V(n008):	0.0938619	voltage
V(n009):	0.0938619	voltage
V(n002):	1.16166	voltage
V(n011):	0.600468	voltage
V(n003):	1.16166	voltage
V(n010):	0.587442	voltage
V(n005):	1.65626	voltage
V(vd3):	1.30451	voltage
V(n004):	1.65626	voltage
Id(M10):	4.90468e-006	device_current
Ig(M10):	0	device_current
Ib(M10):	-1.03596e-013	device_current
Is(M10):	-4.90468e-006	device_current
Id(M9):	4.90468e-006	device_current
Ig(M9):	0	device_current
Ib(M9):	-1.03596e-013	device_current
Is(M9):	-4.90468e-006	device_current
Id(M3):	2.66147e-007	device_current
Ig(M3):	0	device_current
Ib(M3):	-1.22064e-012	device_current
Is(M3):	-2.66146e-007	device_current
Id(M4):	2.66147e-007	device_current
Ig(M4):	0	device_current
Ib(M4):	-1.22064e-012	device_current
Is(M4):	-2.66146e-007	device_current
Id(M18):	4e-005	device_current
Ig(M18):	0	device_current
Ib(M18):	-5.97442e-013	device_current
Is(M18):	-4e-005	device_current
Id(M13):	4e-005	device_current
Ig(M13):	0	device_current
Ib(M13):	-6.10468e-013	device_current
Is(M13):	-4e-005	device_current
Id(M7):	-2.66147e-007	device_current
Ig(M7):	0	device_current
Ib(M7):	1.53699e-013	device_current
Is(M7):	2.66147e-007	device_current
Id(M8):	-2.66147e-007	device_current
Ig(M8):	0	device_current

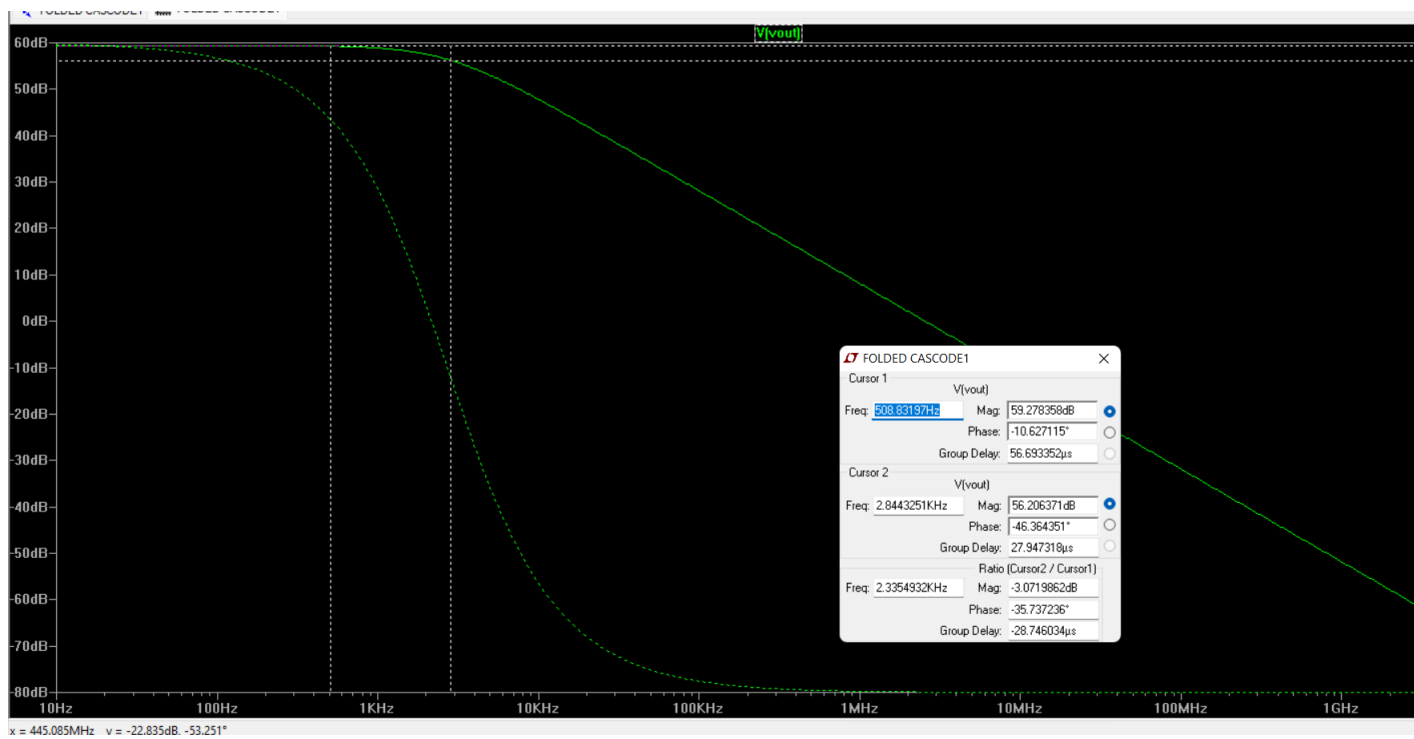
DC analysis of Folded cascode

SPICE Error Log: C:\Users\pawan\Desktop\FOLDED CASCODE1.log

Cgb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Name:	m13	m7	m8	m5	m6
Model:	nmos	pmos	pmos	pmos	pmos
Id:	4.00e-05	-2.66e-07	-2.66e-07	-2.66e-07	-2.66e-07
Vgs:	6.00e-01	-4.95e-01	-4.95e-01	-4.95e-01	-4.95e-01
Vds:	6.00e-01	-1.44e-01	-1.44e-01	-3.52e-01	-3.52e-01
Vbs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Vth:	4.50e-01	-4.50e-01	-4.50e-01	-4.50e-01	-4.50e-01
Vdsat:	1.50e-01	-4.55e-02	-4.55e-02	-4.46e-02	-4.46e-02
Gm:	5.32e-04	1.17e-05	1.17e-05	1.19e-05	1.19e-05
Gds:	3.77e-06	5.17e-08	5.17e-08	4.97e-08	4.97e-08
Gmb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cbd:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cbs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgsov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgdov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgbov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgd:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Name:	m16	m12	m11	m2	m1
Model:	pmos	pmos	pmos	pmos	pmos
Id:	4.00e-05	-4.00e-05	-9.28e-06	4.64e-06	4.64e-06
Vgs:	0.00e+00	-6.38e-01	-6.38e-01	8.56e-01	8.56e-01
Vds:	6.38e-01	-6.38e-01	-2.31e-01	1.48e+00	1.48e+00
Vbs:	6.38e-01	0.00e+00	0.00e+00	1.48e+00	1.48e+00
Vth:	-4.50e-01	-4.50e-01	-4.50e-01	-4.50e-01	-4.50e-01
Vdsat:	-1.88e-01	-1.88e-01	-1.88e-01	-1.69e-01	-1.69e-01
Gm:	4.25e-04	4.25e-04	9.85e-05	5.48e-05	5.48e-05
Gds:	7.09e-06	7.09e-06	1.77e-06	7.16e-07	7.16e-07
Gmb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cbd:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cbs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgsov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgdov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgbov:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgs:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgd:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00
Cgb:	0.00e+00	0.00e+00	0.00e+00	0.00e+00	0.00e+00

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Gm of Folded cascode



AC analysis of Folded cascode

Gain = 59.27dB
B.W.=2.84KHz

Results :

Telescopic cascode single-ended Operational Transconductance Amplifier :

- W/L (of M1,M2,M3,M4) = 0.9259
- W/L (of M5,M6,M7,M8) = 3.5647
- W/L Of M0 = 1.22
- W/L (of M10,M11,M12) = 2
- $R_{out} = 33.33M\Omega$
- Gain = 65.07 dB
- Band Width 3.52KHz

Folded cascode single-ended Operational Transconductance Amplifier :

- W/L (of M1,M2,M5,M6,M7,M8) = 2.5
- W/L (of M3 , M4) = 1.25
- W/L Of M9 ,M10) = 2.5
- $R_{out} = 25M\Omega$
- Gain = 59.27 dB
- Band Width = 2.84KHz

Conclusion :

- Successfully implemented both of the design having high gain and high output impedance.
- Telescopic cascode has more gain and output impedance because of having a parallel small signal resistances in folded reduces the R_{out} .
- Experimental B.W. is very low than that of calculated b.w. because of transconductance of driving transistor is lower than the value which is taken in the calculation.
- Output swing is high in folded cascode in compare with telescopic.
- Folded topology has higher noise than telescopic.