

# **UDAYA** ASSISTANT PROFESSOR

## CONTACT

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## **CAREER OBJECTIVE**

To secure a position where my smart work is used.

## **EDUCATION**

2013 ME vlsi design

Srinivasan engineering college

7.35

2009 BE ece

Accet 72

## **TECHNICAL SKILLS**

Actively participated in international conferences.

## **PROJECTS**

# Dynamic power management

#### One year

Power reduction s important nowadays. In my project power is reduced by online algorithm. My project doesnt follow offline method.

Optical communication sender /receiver

Six months

**Metal detector** 

Mini project

# **PERSONAL PROFILE**

25/07/87 **Date of Birth Nationality** Indian **Marital Status** Married **Known Languages** English tamil Reading novels, craft

Hobby works.

### **ACHIEVEMENTS & AWARDS**

Received rajyapuraskar award for guides.

Actively participated in nss.

Actively participated in assembly language programming.

Actively participated in microprocessor test.

#### INPLANT TRAINING

Underwent an inplant training at thermal power station tuticorin.

## **INTERESTS**

Low power management Analog communication Linear integrated circuits Digital communication

## **INDUSTRIAL EXPOSURE**

Actively went for industrial visit at kodai fm, astro physics, pothigai TV.

## **DECLARATION**

I hereby declare that all the details above are true to the best of my knowledge.

Place: Chennai Date: 3/1/2020