



LOGESH K

:+91-7200570060 | :logeshara@gmail.com

Deepakam Greens , 12th Cross Street, Pallikaranai , Chennai-600100

OBJECTIVE

A Highly exuberant Electrical Design Engineer with 8 Years of hands on Experience in the field of electrical proposal & Designing LV Switch-gear Industry with extensive knowledge in electrical Design single line diagram, schematic diagram in AutoCAD.

WORK EXPERIENCE

November 2018 - Present

Organization : HCL Technologies, Chennai.

Designation : Lead Engineer

Projects : Wafer Inspection & Review Tool for Lotus Exige ESL20.

- Designing Electrical schematic circuits using OrCAD.
- Extensive Knowledge in ECO,ECN,industrial standards(IEC,ISO,UL), lifecycle management(PLM), electronic spec sheet, design database and managing large assemblies.
- Preparation of Bill of Materials (BOM),Manuals & Documentation for Trouble shooting.
- Co-ordination with suppliers on raw materials specifications, dimensions and approvals.

June 2016 - October 2018

Organization : VKA POWERMASTER Private Ltd, Bangalore.

Designation : Electrical Design Engineer (LV Switch-gear)

Projects : Basement Ventilation Panels for Prestige Builders, VFD Panels.

- Leading the Design Team to achieve well planned Design and Execution of LV Switch-gear panels.
- Attended Pre -Tender Meetings.
- Designing Control panels using AutoCAD. Developing electrical Designs, Proposals & Estimation of the LV Switch-gear panels for residential / Commercial Complexes & Sub Stations.
- Preparing Bill of Materials (BOM) panel wise for procurement and Production.
- Maintaining ISO Documentation Process of Design Department with Team Coordination.

October 2015 - June 2016

Organization : SKYTECH INFINTE PLATFORM PVT LTD, BANGALORE

Designation : Electrical Proposal Engineer.

Projects : MCC Panels for GE Water Supply, Bangalore.

- Solving Technical queries for ambiguities in drawings, specifications and BOQ
- Assisted Drafting team in Preparation of Electrical LV Panels, MCC Control Panel Drawing.

June 2014 - October 2015

Organization : V V SYSTEMS & POWER PANELS PVT LTD, BANGALORE

Designation : Junior Design Engineer

Projects : Low Tension Panels for Aster CMH Hospital, M/s. BHEL

- Designing the schematic diagrams(SLD - PDB, LDB, MCC & PCC Panels) for L.T Power Control panels.
- Checking up to control panels physically as per GA After panel Fabricated.
- Following up with customer/contractor to obtain quotation in time, clarifying all their inquiries and meetings with the supplier/subcontractor when required.

TECHNICAL SKILLS

- OrCAD
- ENOVIA(For Product Life Cycle Management)
- Visio
- AutoCAD
- EplanP8
- Machine testing & Troubleshooting
- PLC Programming(Ladder logic)

PROFESSIONAL SKILLS

- Positive attitude towards the work.
- Self Motivated & ability to work as a team and also individual.
- Adaptable to the environment.
- Complex Problem solving
- Decision Making

LANGUAGES KNOWN

- English
- Tamil
- Kannada

EDUCATION

- MBA - Technology Management. (Distance Education)
(6.00 CGPA) 2016-2018
- BE - Electrical & Electronic Engineering.
(7.98CGPA) 2010-2014
- Higher Secondary School.
(82.41%) 2009-2010

DECLARATION

I, hereby declare that every minute of the document is correct and accurate to the best of my knowledge and beliefs.

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