

MADHESH N

13th DEC 1996,

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DESIGN ENGINEER:

- To seek a challenging job where I can implement all my Technical, Analytical and Functional skills to serve the best of my abilities in contributing towards Organizational as well as my growth and set an example.
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SUMMARY:

- **6+ Years** of experience in design and development of **Printed Circuit Boards**.
 - Good knowledge of IPC specifications, PCB manufacturing and assembly process.
 - Research and suggest tools for improving quality and efficiency of PCB design
 - Excellent problem solving skills and interpersonal skills.
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KEY SKILLS:

- **Allegro, OrCAD Capture (16.6 & 17.2)**
 - **Altium 17, 18.2 & 19**
 - **3D Part mechanical design Tool (Solid works)**
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PROFESSIONAL EXPERIENCE

VVDN Technologies Private Limited.

JAN.2016 – Present

Designation – Sr. Component Engineer

Key Responsibilities handled:

- Knowledge of schematics, PCB layouts of electronic designs by using OrCAD, Allegro and Altium Designer.
- To Create/Modify schematic symbols for hardware schematic capture.
- To Create/Modify PCB footprint for PCB layout design.
- To Reworking the schematic symbols and PCB Footprints.
- To check footprints against manufacturer recommendations and IPC specifications.
- Maintain, update and synchronize part databases in ECAD (Allegro and Altium)
- Create 3D parts using mechanical design software (solid works)
- I have done the script to automatically generate Gerber files.
- Create footprint of Standard Mouse Bite for breaking Printed Circuit Board (IPC 2221).
- performing library part creation and maintain a part library by integrating existing and newly build components
- PCB and IPC design standards for surface mount and through hole designs.

- Strong knowledge of Allegro/altium tools to develop common library database
 - Collecting electronics components, part creation, decal creation, ERP Processing, JIRA tool maintaining.
 - Reviewing Footprint and Logical symbols.
 - Searching for new or replacement parts (if part is EOL or Non-RoHS or any other issues), technical information, second sources, etc. using industry standard and internal tools.
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EDUCATIONAL CREDENTIALS:

- **Bachelor of Electronic and Communication Engineer** (Part time) in SRM Institute of Technology, Chennai, Tamil Nadu, during 2017-2020, Secured 7.4 CGPA (under Anna University, Chennai).
 - **DEEE**(Electrical and Electronics Engineering), SRI Ragavendra Polytechnic College, Tamilnadu, during 2012-20015, Secured 88%.
 - **S.S.L.C.** from Vidyaa Vikas Boys Higher Secondary School, Tiruchengode, Tamil Nadu, during 2011-2012, Secured second 66%.
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ACHIEVEMENTS:

- Got Star of the Month and Luminary award for Appreciation by closing all challenges at VVDN Technology Private Limited.
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DECLARATION:

I hereby declare that all the above information is true and correct to the best of my knowledge and I would render my best for the satisfactions of my employer.

Place : Chennai,

Date :

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