



## **RTL9310 Series Layer3 Managed Switch Controller Register Reference File**

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April 18, 2018

## Revision History

Revision	Date	Description
V1.0	2018/04/18	First Release

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## CHAPTER 1

# General

The chapter describes the general components

SECTION 1.1	<b>CHIP INFORMATION</b>
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Chip information module

### **MODEL\_NAME\_INFO**

REGISTER ADDRESS : 0xBB000004

DEFAULT VALUE : 0x93110000

Specify the model name information.

Bits	Field	Description	Type	Default
31:16	RTL_ID	RTL number Note: This register can be overwritten by E-fuse.	RW	0x9311
15:11	MODEL_CHAR_1ST	First English character of model name 0x0: NULL character 0x1-0x1A: character 'A' - 'Z' 0x1B-0x1F: invalid Default value is changed by bonding set. Note: This register can be overwritten by E-fuse.	RW	0x0
10:6	MODEL_CHAR_2ND	Second English character of model name 0x0: NULL character 0x1-0x1A: character 'A' - 'Z' 0x1B-0x1F: invalid Default value is changed by bonding set. Note: This register can be overwritten by E-fuse.	RW	0x0
5:4	MODEL_CHAR_3RD	Reserved for third English character of model name . Note: This register can be overwritten by E-fuse.	RW	0x0
3:0	RTL_VID	RTL number Version ID. Record the RTL Version ID. Counting start from 0. So 0,1, 2, 3... usually map to version A,B,C,D... Note: This register can be overwritten by E-fuse.	RW	0x0

SECTION 1.2	<b>RESET</b>
-------------	--------------

Reset module

## RST\_GLB\_CTRL

REGISTER ADDRESS : 0xBB000400

DEFAULT VALUE : 0xA60

Global reset control registers.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19	BYP_NIC_ACK_TIMEOUT	Bypass NIC ACK timeout. 0b0: disable 0b1: enable	RW	0x0
18:17	REG_POR_PULL_CTRL		RW	0x0
16	REG_POR_LVDTCT		RW	0x0
15:8	RST_PULSE_WIDTH	Control the pulse width of reset I/O pin (active low signal). 1 unit = 1ms. Pulse width = 0ms ~ 255ms Default = 10ms	RW	0x0A
7	MDX_GLB_RST	Software reset for all MDX circuit. 0b0: normal 0b1: reset Note: write 0b1 and self-clear to 0b0	RWAC	0x0
6	CHIP_RST_EN	Control reset I/O pin if can send reset low signal when trigger software chip reset. 0b0: disable 0b1: enable	RW	0x1
5	WDOG_RST_EN	Control reset I/O pin if can send reset low signal when trigger watch dog reset. 0b0: disable 0b1: enable	RW	0x1
4	CPU_MEM_RST	Software reset for CPU/Memory controller/Peripheral/LXB/NIC. 0b0: normal 0b1: reset Note: write 0b1 and self-clear to 0b0	RWAC	0x0
3	SW_SERDES_RST	Software reset for SERDES. 0b0: normal 0b1: reset Note: write 0b1 and self-clear to 0b0	RWAC	0x0
2	SW_NIC_RST	Software reset for switch NIC interface. 0b0: normal 0b1: reset Note: write 0b1 and self-clear to 0b0	RWAC	0x0
1	SW_Q_RST	Software reset for switch Queue/Packet SERDES/GMAC/QM/TX Buffer/ALE. 0b0: normal 0b1: reset Note: write 0b1 and self-clear to 0b0	RWAC	0x0
0	SW_CHIP_RST	Software reset for whole chip. 0b0: normal 0b1: reset Note: write 0b1 and self-clear to 0b0	RWAC	0x0

## **CHIP\_RST\_STS**

REGISTER ADDRESS : 0xBB000404

DEFAULT VALUE : 0x0

Chip reset status.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	CHIP_RST_STS	Chip reset status (cold restart or warm restart). 0b0: cold restart. 0b1: warm restart. Note: 1. Cold restart: power on reset, and HW pin reset. 2. Warm restart: software chip reset. 3. Only cold restart and watchdog reset can clear this bit.	RW	0x0

## **WDOG\_RST\_STS**

REGISTER ADDRESS : 0xBB000408

DEFAULT VALUE : 0x0

Watch dog reset status.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	WDOG_RST_STS	Watch dog reset status (cold restart or warm restart) . . 0b0: cold restart. 0b1: warm restart. Note: 1. Cold restart: power on reset, and HW pin reset. 2. Warm restart: watchdog reset. 3. Only cold restart and software chip reset can clear this bit.	RW	0x0

## **BONDING\_STS**

REGISTER ADDRESS : 0xBB00040C

DEFAULT VALUE : 0x1F0

Reflect the bonding result.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	RESERVED			

## SECTION 1.3

**INTERFACE**

Module for chip interface

**I2C\_SLV\_CTRL**

REGISTER ADDRESS : 0xBB001000

DEFAULT VALUE : 0x0

Data endian control for I2C slave mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:7	RESERVED			
6:1	CFG_SDA_DLY	Delay SDA for N system clock. 0 is disable	RW	0x0
0	DATA_ENDIAN_SEL	Select data byte endian for i2c slave mode. 0b0: Low-byte data first. 0b1: High-byte data first.	RW	0x0

**I2C\_MST\_IF\_CTRL**

REGISTER ADDRESS : 0xBB001004

DEFAULT VALUE : 0x0

wait mode and open-drain mode selection

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:28	RESERVED			
27	CFG_DATA_HOLD_TIME_2	Select data hold time for I2C Master#2 controller. 0b0: 100ns 0b1: T/4 (T is SCK period)	RW	0x0
26	CFG_DATA_HOLD_TIME_1	Select data hold time for I2C Master#1 controller. 0b0: 100ns 0b1: T/4 (T is SCK period)	RW	0x0
25:22	CFG_SCK_I_DLY_2	Configure SCK input delay for I2C Master#2 controller. Use 250MHz to delay, 1 unit = 4ns. 0x0: no delay 0x1~0xF: 1~15 unit delay	RW	0x0
21:18	CFG_SCK_I_DLY_1	Configure SCK input delay for I2C Master#1 controller. Use 250MHz to delay, 1 unit = 4ns. 0x0: no delay 0x1~0xF: 1~15 unit delay	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
17:16	CFG_WAIT_SCK_MODE_2	For I2C Master2 controller. 0x0: do not wait for SCK=1 or not 0x1: wait for SCK=1 after ACK, and then launch the next command 0x2: wait for SCK=1 after each SCK cycle, and then launch the next command 0x3: reserved	RW	0x0
15:14	CFG_WAIT_SCK_MODE_1	For I2C Master1 controller. 0x0: do not wait for SCK=1 or not 0x1: wait for SCK=1 after ACK, and then launch the next command 0x2: wait for SCK=1 after each SCK cycle, and then launch the next command 0x3: reserved	RW	0x0
13	I2C_OPEN_DRN_SCK_2	For SCK of I2C Maserter1 controller 0b0: always output enable 0b1: use open-drain style	RW	0x0
12	I2C_OPEN_DRN_SCK_1	For SCK of I2C Maserter1 controller 0b0: always output enable 0b1: use open-drain style	RW	0x0
11:0	I2C_OPEN_DRN_SDA	BitMask control for SDA11 ~ SDA0 0b0: always output enable 0b1: use open-drain style	RW	0x0

## I2C\_MST\_IF\_SEL

REGISTER ADDRESS : 0xBB001008

DEFAULT VALUE : 0x0

Select GPIO pins work as SDA/SCL or normal GPIO.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13:12	GPIO_SCL_SEL	Bit mask[1:0] for selecting GPIO1~GPIO0 works as GPIO or SCL. 0b0: GPIO 0b1: SCL Note: 1. GPIO0 is mapping to SCL0 of controller#1. 2. GPIO1 is mapping to SCL1 of controller#2.	RW	0x0
11:0	GPIO_SDA_SEL	Bit mask[1:0] for selecting GPIO13~GPIO2 work as GPIO or SDA. 0b0: GPIO 0b1: SDA Note: GPIO2~GPIO13 is mapping to SDA0~SDA11.	RW	0x0

## I2C\_MST1\_CTRL

REGISTER ADDRESS : 0xBB00100C

DEFAULT VALUE : 0x28200

I2C master#1 control registers.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	SCL_FREQ	SCL clock rate selection for I2C master mode. 0x0: 50KHz 0x1: 100KHz 0x2: 400KHz 0x3: 2.5MHz	RW	0x0
29:26	CHK_ACK_DLY	Delay time for I2C Master to check the replying ACK of slave device. ACK check delay = Check_Ack_Delay *unit 1 unit = 1/(SCL_FREQ) 0 means no delay, max delay time is 15 clock cycle.	RW	0x0
25:22	DRV_ACK_DLY	Delay time for I2C Master to driving ACK bit. Delay time = Drive_ACK_Delay *unit 1 unit = 1/(SCL_FREQ) 0 means no delay, max delay time is 15 clock cycle.	RW	0x0
21:18	SDA_OUT_SEL	Select SDA output from SDA0~SDA11. 0x0: select SDA0 as SDA output. 0x1: select SDA1 as SDA output. 0x2: select SDA2 as SDA output. 0x3: select SDA3 as SDA output. 0x4: select SDA4 as SDA output. 0x5: select SDA5 as SDA output. 0x6: select SDA6 as SDA output. 0x7: select SDA7 as SDA output. 0x8: select SDA8 as SDA output. 0x9: select SDA9 as SDA output. 0xA: select SDA10 as SDA output. 0xB: select SDA11 as SDA output. Others: Reserved. Note: before setting SDA_OUT_SEL, CPU should first set GPIO pin as SDA function; otherwise, SDA_OUT_SEL will not work as expected; if SDA[x] is not selected as SDA output, it should output a high-impedance to avoid any mistake.	RW	0x0
17:11	DEV_ADDR	Select device type id & address of external I2C slave device. Note: SFP/SFP+ device address is fixed at 7'b1010000	RW	0x50
10:9	MEM_ADDR_WIDTH	Select memory address width of external I2C slave device. 0b0: 0-byte 0b1: 1-byte 0b2: 2-byte 0b3: 3-byte	RW	0x1
8:5	DATA_WIDTH	Configure I2C read/write width=DATA_WIDTH + 1. 0x0: 1-byte 0x1: 2-byte 0x2: 3-byte : 0xF: 16-byte	data	RW
				0x0

Bits	Field	Description	Type	Default
4	READ_MODE	I2C read command mode. 0b0: standard mode. 0b1: old mode.  Note: 1. Standard mode read command is Write + Address + Read+ Data 2. Old mode read command is Read + Address + Data	RW	0x0
3	RST	I2C Reset Command before each start command. 0b0: do not add I2C reset command before each start command 0b1: add I2C reset command before each start command	RW	0x0
2	RWOP	Read/write operation. 0b0: read 0b1: write	RW	0x0
1	FAIL	I2C indirect access fail. 0b0: normal 0b1: fail, self cleared at each I2C trigger operation.  Note: 1. Access fail means the slave device has no ack response while access.	RO	0x0
0	TRIG	Trigger I2C master command. 0b0: complete access. 0b1: execute access.  Note: self clear to 0 after complete access.	RWAC	0x0

### I2C\_MST1\_MEMADDR\_CTRL

REGISTER ADDRESS : 0xBB001010

DEFAULT VALUE : 0x0

Select memory address for I2C master#1.

Bits	Field	Description	Type	Default
31:24	RESERVED			
23:0	MEM_ADDR	Select memory address of external I2C slave device to access.  Note: If I2C_MemAddr_Width selectin is 1-byte, only Mem_Addr [7:0] is valid, bit[23:8] is invalid.	RW	0x0

### I2C\_MST1\_DATA\_CTRL

BASE ADDRESS : 0xBB001014

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 8 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (8 bits per field)

Data read or write for I2C master#1.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
7:0	DATA	Data for read or write access.	RW	0x0

## I2C\_MST2\_CTRL

REGISTER ADDRESS : 0xBB001024

DEFAULT VALUE : 0x28200

I2C master#2 control registers.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	SCL_FREQ	SCL clock rate selection for I2C master mode. 0x0: 50KHz 0x1: 100KHz 0x2: 400KHz 0x3: 2.5MHz	RW	0x0
29:26	CHK_ACK_DLY	Delay time for I2C Master to check the replying ACK of slave device. ACK check delay = Check_Ack_Delay *unit 1 unit = 1/(SCL_FREQ) 0 means no delay, max delay time is 15 clock cycle.	RW	0x0
25:22	DRV_ACK_DLY	Delay time for I2C Master to driving ACK bit. Delay time = Drive_ACK_Delay *unit 1 unit = 1/(SCL_FREQ) 0 means no delay, max delay time is 15 clock cycle.	RW	0x0
21:18	SDA_OUT_SEL	Select SDA output from SDA0~SDA11. 0x0: select SDA0 as SDA output. 0x1: select SDA1 as SDA output. 0x2: select SDA2 as SDA output 0x3: select SDA3 as SDA output 0x4: select SDA4 as SDA output 0x5: select SDA5 as SDA output 0x6: select SDA6 as SDA output 0x7: select SDA7 as SDA output 0x8: select SDA8 as SDA output 0x9: select SDA9 as SDA output 0xA: select SDA10 as SDA output 0xB: select SDA11 as SDA output Others: Reserved. Note: before setting SDA_OUT_SEL, CPU should first set GPIO pin as SDA function; otherwise, SDA_OUT_SEL will not work as expected; if SDA[x] is not selected as SDA output, it should output a high-impedance to avoid any mistake.	RW	0x0
17:11	DEV_ADDR	Select device type id & address of external I2C slave device. Note: SFP/SFP+ device address is fixed at 7'b1010000	RW	0x50

Bits	Field	Description	Type	Default
10:9	MEM_ADDR_WIDTH	Select memory address width of external I2C slave device. 0b0: 0-byte 0b1: 1-byte 0b2: 2-byte 0b3: 3-byte	RW	0x1
8:5	DATA_WIDTH	Configure I2C read/write width=DATA_WIDTH + 1. 0x0: 1-byte 0x1: 2-byte 0x2: 3-byte : 0xF: 16-byte	data	RW
4	READ_MODE	I2C read command mode. 0b0: standard mode. 0b1: old mode. Note: 1. Standard mode read command is Write + Address + Read+ Data 2. Old mode read command is Read + Address + Data	RW	0x0
3	RST	I2C Reset Command before each start command. 0b0: do not add I2C reset command before each start command 0b1: add I2C reset command before each start command	RW	0x0
2	RWOP	Read/write operation. 0b0: read 0b1: write	RW	0x0
1	FAIL	I2C indirect access fail. 0b0: normal 0b1: fail, self cleared at each I2C trigger operation. Note: 1. Access fail means the slave device has no ack response while access.	RO	0x0
0	TRIG	Trigger I2C master command. 0b0: complete access. 0b1: execute access. Note: self clear to 0 after complete access.	RWAC	0x0

## I2C\_MST2\_MEMADDR\_CTRL

REGISTER ADDRESS : 0xBB001028

DEFAULT VALUE : 0x0

Select memory address for I2C master#2.

Bits	Field	Description	Type	Default
31:24	RESERVED			
23:0	MEM_ADDR	Select memory address of external I2C slave device to access. Note: If I2C_MemAddr_Width selectin is 1-byte, only Mem_Addr [7:0] is valid, bit[23:8] is invalid.	RW	0x0

Bits	Field	Description	Type	Default
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## I2C\_MST2\_DATA\_CTRL

BASE ADDRESS : 0xBB00102C  
 ARRAY INDEX : 0 - 15  
 ARRAY OFFSET : 8 bits  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (8 bits per field)

Data read or write for I2C master#2.

Bits	Field	Description	Type	Default
7:0	DATA	Data for read or write access.	RW	0x0

## SPI\_CTRL0

REGISTER ADDRESS : 0xBB00103C  
 DEFAULT VALUE : 0x68024

Bits	Field	Description	Type	Default
31:26	RESERVED			
25:17	DATA_WIDTH	SPI Command read/write data width = RW (Data_Width+1) byte. Maximum support 512 bytes.	RW	0x003
16:14	ADDR_WIDTH	External SPI Slave device memory address width. 0x0: 1-Byte 0x1: 2-Byte 0x2: 3-Byte 0x3: 4-Byte 0x4: No Address Byte Others reserved	RW	0x2
13:11	SPI_CLK_SEL	SPI operating clock rate selection. The value defines the divisor to generate SPI clock. SPI Clock = Interface clock(250MHz)/ (SPI_CLK_DIV) 0x0: DIV=4 0x1: DIV=6 0x2: DIV=8 0x3: DIV=16 0x4: DIV=125 0x5: DIV=160 0x6: DIV=200 0x7: DIV=250	RW	0x0

Bits	Field	Description	Type	Default
10	GPIO_SPI_SEL	Select GPIO8-10 works as GPIO or SCK/ MISO/ MOSI. 0b0: works as GPIO8-10; 0b1: GPIO8 works as SCK, GPIO9 works as MISO, GPIO10 works as MOSI.	RW	0x0
9	GPIO12_CSB1_SEL	Select GPIO12 works as GPIO or CS#1. 0b0: GPIO12; 0b1: CS#1	RW	0x0
8	GPIO11_CSB0_SEL	Select GPIO11 works as GPIO or CS#0. 0b0: GPIO11; 0b1: CS#0	RW	0x0
7	SPI_CPHA	Clock phase control bit; CPHA parameter is used to shift the sampling phase 0b0: the data are sampled on the leading (first) clock edge 0b1: the data are sampled on the trailing (second) clock edge	RW	0x0
6	SPI_CPOL	Clock phase control bit; SPI clock may be inverted or non-inverted transmitting 0b0: SCLK low while idle 0b1: SCLK high while idle	RW	0x0
5:3	SPI_TSLCH	SPI CS# active setup time adjustment(relative to SCK) Basic unit = 16ns(Interface clock(250MHz)/4) 0x0 means 1 unit, 0x1 means 2 units, etc.	RW	0x4
2:0	SPI_TCHSH	SPI CS# active hold time adjustment. Basic unit = 16ns(Interface clock(250MHz)/4) 0x0 means 1 unit, 0x1 means 2 units, etc.	RW	0x4

## SPI\_CTRL1

REGISTER ADDRESS : 0xBB001040

DEFAULT VALUE : 0x0

Bits	Field	Description	Type	Default
31:9	RESERVED			
8:6	SPI_RX_DLY	SPI RX data delay control Basic unit = 4ns (Interface clock 250MHz) 0x0 means no delay , 0x1 means 1 unit, 0x2 means 2 units, etc.	RW	0x0
5:3	SPI_CLK_DLY	SPI clock delay control Basic unit = 4ns (Interface clock 250MHz) 0x0 means no delay , 0x1 means 1 unit, 0x2 means 2 units, etc.	RW	0x0
2:0	SPI_TX_DLY	SPI TX data delay control Basic unit = 4ns (Interface clock 250MHz) 0x0 means no delay , 0x1 means 1 unit, 0x2 means 2 units, etc.	RW	0x0

## SPI\_CTRL2

REGISTER ADDRESS : 0xBB001044

DEFAULT VALUE : 0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:11	RESERVED			
10	CSB_OUT_SEL	Select CS# output from which pin; 0b0: CS#0 0b1: CS#1  Note: before setting CSB_OUT_SEL, CPU should first set GPIO pin as CS# function; otherwise, CSB_OUT_SEL will not work as expected; if CS#[x] is not selected as CSB output, it should output a high to avoid any mistake.	RW	0x0
9	SPI_CMD_TYPE	Select read command: After read command output, SPI master must store the data from SPI slave output.  Select write command: SPI master just only output write command.  0b0: Read 0b1: Write	RW	0x0
8:1	SPI_CMD	SPI command code of a command transaction. Ex: 'Read data' is 0x3, 'write data' is 0x2	RW	0x0
0	SPI_TRIG	Trigger SPI Master command. 0: idle; 1: trigger, self cleared to 0 after executed.	RWAC	0x0

### **SPI\_DATA**

BASE ADDRESS : 0xBB001048

ARRAY INDEX : 0 - 127

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	SPI_DATA	For read operation, data is read from slave SPI device;  For write operation, data will be write to slave SPI device.  Note: only the bits assigned at Data_Width field are valid. For example, set Data_Width to 5, so only bit[48:0] are valid.	RW	0x0

### **SPI\_ADDR**

REGISTER ADDRESS : 0xBB001248

DEFAULT VALUE : 0x0

Bits	Field	Description	Type	Default
31:0	SPI_ADDR	<p>External SPI slave device memory address for access</p> <p>Note:</p> <p>When "ADDR_WIDTH selected as 1-Byte mode, bit [7: 0] is valid, bit [31: 8] is invalid.</p> <p>When "ADDR_WIDTH selected as 2-Byte mode, bit [15: 0] is valid, bit [31: 16] is invalid.</p> <p>When "ADDR_WIDTH selected as 3-Byte mode, bit [23: 0] is valid, bit [31: 24] is invalid.</p>	RW	0x0

## MAC\_SLV\_TIMEOUT

REGISTER ADDRESS : 0xBB00124C

DEFAULT VALUE : 0x7FFE

Specify the slave spi timeout option for unexpected break of spi command.

Bits	Field	Description	Type	Default
31:15	RESERVED			
14:1	SPI_TIMEOUT_SET	<p>SPI slave timeout timing setting;</p> <p>0x0: no timeout;</p> <p>Others: timeout after SPI_TIMEOUT_SET * 100ns;</p> <p>So max timeout time is about 1.64ms.</p> <p>Note: Can not be set to 0, it is recommended to set the minimum value of 2</p>	RW	0x3FFF
0	SPI_TIMEOUT_FLAG	<p>SPI slave timeout indication;</p> <p>0b0: no timeout;</p> <p>0b1: timeout;</p> <p>This bit should latch high after clear to 0b0 after read.</p>	RC	0x0

### SECTION 1.4

## LED

LED module

## LED\_GLB\_CTRL

REGISTER ADDRESS : 0xBB000600

DEFAULT VALUE : 0x429654

Specify LED global control register

Bits	Field	Description	Type	Default
31:25	RESERVED			

Bits	Field	Description	Type	Default
24	LED_STREAM_XCHG	Enable serial copper and fiber LED signal stream exchange function 0b0: disable (copper stream first) 0b1: enable (fiber stream first) Note: this register only for serial LED mode	RW	0x0
23	LEDIC_RESTART	Whether or not restart RTL8231 every time; (write RTL8231 reg 0 bit 1 to 0) 0b0: not restart 0b1: restart	RW	0x0
22	LEDIC_PAGE_ACCESS	Whether or not change RTL8231 page to page 0x1E every time; (write RTL8231 reg 31 to 0x1E) 0b0: not change; 0b1: change	RW	0x1
21	LED_ACTIVE	Select serial LED is high active or low active. 0b0: Low active 0b1: High active Note: this register only for serial LED mode, this register must be set to 1 (High active) in scan LED mode.	RW	0x0
20	LED_SIGNAL_INVERT	Enable LED signal stream invert function 0b0: disable 0b1: enable Note: this register only for serial LED mode	RW	0x0
19:17	BLINK_TIME_SEL	Select the LED blinking time. 0x0:Reserved 0x1: 32ms low and 32ms high toggle. 0x2: 64ms low and 64ms high toggle. 0x3: 128 ms low and 128ms high toggle. 0x4: 256 ms low and 512ms high toggle. 0x5: 512 ms low and 256ms high toggle. 0x6: 1024 ms low and 1024ms high toggle. 0x7: reserved.	RW	0x1
16:15	LED_CLK_SEL	Select LED_CLK and LED_MDC period time. 0x0: 800ns 0x1: 400ns 0x2: 200ns 0x3: 100ns Note: these bits are valid both in serial LED mode and scan LED mode	RW	0x1
14	RESERVED			
13:12	SYS_LED_MODE	System LED mode configuration When SYS_LED_EN=1, The system LED can be configured as following: 0x0: off 0x1: Blinking 64ms 0x2: Blinking 1024ms 0x3: Light	RW	0x1
11:8	STP2_PWR_ON_LED	Select power on blinking LED in step 2 power on duration. "1" = LED on, "0" = LED off.	RW	0x6
7:4	STP1_PWR_ON_LED	Select power on blinking LED in step 1 power on duration. "1" = LED on, "0" = LED off.	RW	0x5

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:2	PWR_ON_BLINK_SEL	Select power on blinking time (T). 0x0: disable 0x1: 400ms 0x2: 800ms 0x3: 1.6s	RW	0x1
1:0	LED_MOD	LED MODE selection. 0x0: Disable LED. 0x1: Enable LED, and display LED in serial mode; 0x2: Enable LED, and display LED in single color scan mode; 0x3: Enable LED, and display LED in bicolor scan mode;	RW	0x0

### **LED\_PORT\_NUM\_CTRL**

BASE ADDRESS : 0xBB000604

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x2

This is a One-Dimension Register Field Array. (2 bits per field)

Specify per-port LED number

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	LED_NUM_SEL	Select LED number for each port. 0x0: 1 LEDs for each port. Pn_LED[0]. 0x1: 2 LEDs for each port. Pn_LED[1:0]. 0x2: 3 LEDs for each port. Pn_LED[2:0]. 0x3: 4 LEDs for each port. Pn_LED[2:0]. Note: This register must be select 3-LEDs in Bi-color scan LED mode.	RW	0x2

### **LED\_SET\_3\_CTRL1**

REGISTER ADDRESS : 0xBB000614

DEFAULT VALUE : 0xA400A80

Specify LED set3 control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET3_LED3_SEL	Select set3 LED3 mode configuration.	RW	0x0A40
15:0	SET3_LED2_SEL	Select set3 LED2 mode configuration. Default is 100M Link/Act	RW	0x0A80

## **LED\_SET\_3\_CTRL0**

REGISTER ADDRESS : 0xBB000618

DEFAULT VALUE : 0xA200A01

Specify LED set3 control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET3_LED1_SEL	Select set3 LED1 mode configuration. Default is 1000M Link/Act.	RW	0x0A20
15:0	SET3_LED0_SEL	Select set3 LED0 mode configuration. Default is 10G Link/Act.	RW	0x0A01

## **LED\_SET\_2\_CTRL1**

REGISTER ADDRESS : 0xBB00061C

DEFAULT VALUE : 0x2010BFF

Specify LED set2 control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET2_LED3_SEL	Select set2 LED3 mode configuration. Default is 10G Link.	RW	0x0201
15:0	SET2_LED2_SEL	Select set2 LED2 mode configuration. Default is Link/Act.	RW	0x0BFF

## **LED\_SET\_2\_CTRL0**

REGISTER ADDRESS : 0xBB000620

DEFAULT VALUE : 0x2200280

Specify LED set2 control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET2_LED1_SEL	Select set2 LED1 mode configuration. Default is 1000M link.	RW	0x0220
15:0	SET2_LED0_SEL	Select set2 LED0 mode configuration. Default is 100M link.	RW	0x0280

## **LED\_SET\_1\_CTRL1**

REGISTER ADDRESS : 0xBB000624

DEFAULT VALUE : 0xC0008000

Specify LED set1 control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET1_LED3_SEL	Select set1 LED3 mode configuration. Default is Col/Full duplex.	RW	0xC000
15:0	SET1_LED2_SEL	Select set1 LED2 mode configuration. Default is Full duplex.	RW	0x8000

### **LED\_SET\_1\_CTRL0**

REGISTER ADDRESS : 0xBB000628

DEFAULT VALUE : 0xA800B00

Specify LED set1 control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET1_LED1_SEL	Select set1 LED1 mode configuration. Default is 100M Link/Act.	RW	0x0A80
15:0	SET1_LED0_SEL	Select set1 LED0 mode configuration. Default is 10M Link/Act.	RW	0x0B00

### **LED\_SET\_0\_CTRL1**

REGISTER ADDRESS : 0xBB00062C

DEFAULT VALUE : 0xA408000

Specify LED set0 control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET0_LED3_SEL	Select set0 LED3 mode configuration.	RW	0x0A40
15:0	SET0_LED2_SEL	Select set0 copper LED2 mode configuration. Default is Full duplex.	RW	0x8000

### **LED\_SET\_0\_CTRL0**

REGISTER ADDRESS : 0xBB000630

DEFAULT VALUE : 0xA200B80

Specify LED set0 control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	SET0_LED1_SEL	Select set0 copper LED1 mode configuration. Default is 1000M Link/Act.	RW	0x0A20
15:0	SET0_LED0_SEL	Select set0 LED0 mode configuration. Default is 10M/100M Link/Act.	RW	0x0B80

### **LED\_PORT\_COPR\_SET\_SEL\_CTRL**

BASE ADDRESS : 0xBB000634

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Specify per port copper LED set selection

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	LED_COPR_SET_PSEL	Select per port copper LED definition is set0 or set1 or set2 or set3. 0x0: set0 0x1: set1 0x2: set2 0x3: set3	RW	0x0

### **LED\_PORT\_FIB\_SET\_SEL\_CTRL**

BASE ADDRESS : 0xBB000644

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Specify per port fiber LED set selection

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	LED_FIB_SET_PSEL	Select per port fiber LED definition is set0 or set1 or set2 or set3. 0x0: set0 0x1: set1 0x2: set2 0x3: set3	RW	0x0

### **LED\_PORT\_COPR\_MASK\_CTRL**

BASE ADDRESS : 0xBB000654

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify copper LED port mask control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	LED_COPR_PMASK	Copper LED port mask, default value is decide by chip mode	RW	0x0

### **LED\_PORT\_FIB\_MASK\_CTRL**

BASE ADDRESS : 0xBB00065C

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify fiber LED port mask control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	LED_FIB_PMASK	fiber LED port mask, default value is decide by chip mode	RW	0x0

### **LED\_PORT\_COMBO\_MASK\_CTRL**

BASE ADDRESS : 0xBB000664

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify combo LED port mask control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	LED_COMBO_PMASK	LED combo port selection. Mapping to physical port 51 ~ port 0. 0b0: Copper status display on copper LED, fiber status display on fiber LED. 0b1: Copper and fiber status display on same LED. default value is decide by chip mode	RW	0x0

## **SW\_LED\_LOAD**

REGISTER ADDRESS : 0xBB00066C

DEFAULT VALUE : 0x0

Specify software control LED enable register and software control LED load action register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	SW_LED_LOAD	In software control LED mode, since software LED control registers distribute in different register sets, they need one bit for all software control registers synchronization. 0b0: normal 0b1: load all software control LED registers. Self-clear to "0".	RWAC	0x0

## **LED\_PORT\_SW\_EN\_CTRL**

BASE ADDRESS : 0xBB000670

PORT INDEX : 0 - 55

PORT OFFSET : 4 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (4 bits per field)

Specify software control LED enable register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:0	SW_CTRL_LED_EN	Enable software control LED3-LED0 0x0: LED3-LED0 Disable 0x1: LED3-LED1 disable, LED0 Enable 0x2: LED3-LED2 and LED1 disable, LED1 enable ... 0xF: LED3-LED0 enable	RW	0x0

## **LED\_PORT\_SW\_CTRL**

BASE ADDRESS : 0xBB00068C

PORT INDEX: 0 - 55

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Specify LED mode configuration when SW\_CTRL\_LED\_EN=1.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:24	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23:21	SW_FIB_LED3_MODE	Fiber LED3 mode configuration for port. When SW_CTRL_LED_EN=1, The LED3 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0
20:18	SW_FIB_LED2_MODE	Fiber LED2 mode configuration for port. When SW_CTRL_LED_EN=1, The LED2 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0
17:15	SW_FIB_LED1_MODE	Fiber LED1 mode configuration for port. When SW_CTRL_LED_EN=1, The LED1 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0
14:12	SW_FIB_LED0_MODE	Fiber LED0 mode configuration for port. When SW_CTRL_LED_EN=1, The LED0 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0
11:9	SW_COPR_LED3_MODE	Copper LED3 mode configuration for port. When SW_CTRL_LED_EN=1, The LED3 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
8:6	SW_COPR_LED2_MODE	Copper LED2 mode configuration for port. When SW_CTRL_LED_EN=1, The LED2 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0
5:3	SW_COPR_LED1_MODE	Copper LED1 mode configuration for port. When SW_CTRL_LED_EN=1, The LED1 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0
2:0	SW_COPR_LED0_MODE	Copper LED0 mode configuration for port. When SW_CTRL_LED_EN=1, The LED0 of port can be configured as following: 0x0: off 0x1: Blinking 32ms 0x2: Blinking 64ms 0x3: Blinking 128ms 0x4: Blinking 256ms 0x5: Blinking 512ms 0x6: Blinking 1024ms 0x7: Light	RW	0x0

### LED\_INDRT\_ACCESS\_CTRL

REGISTER ADDRESS : 0xBB00076C

DEFAULT VALUE : 0x0

Specify LED indirect access control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:28	RESERVED			
27:12	DATA	Input parameters. If RWOP=0 (read), then REG_DATA [15:0]=input data [15:0] If RWOP=1 (write), then REG_DATA [15:0]=output data [15:0]	RW	0x0
11:7	REG	Select register number to access. 0x0: access register 0 0x1: access register 1 0x2: access register 2 ... 0x1f: access register 31	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
6:2	PHYADDR	Select RTL8231's PHY address to access	RW	0x0
1	RWOP	Read/write operation. 0b0: read 0b1: Write	RW	0x0
0	CMD	Request MAC to access RTL8231 Reg. 0b0: complete access 0b1: execute access Note: When MAC completes access, it will clear this bit.	RWAC	0x0

## EXT\_GPIO\_GLB\_CTRL

REGISTER ADDRESS : 0xBB0007D4

DEFAULT VALUE : 0xC

Specify extended gpio control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20:15	EXT_GPIO_MDX4_READY	Ready bits of 8231_MDX controller (4), read from 8231(0) reg1[9:4]	RO	0x0
14:9	EXT_GPIO_MDX5_READY	Ready bits of 8231_MDX controller (5), read from 8231(1) reg1[9:4]	RO	0x0
8	EXT_GPIO_EN	Enable or Disable extend GPIO Function; this bit will assigned to control GPIO1 and GPIO2 pad; 0b0: disable, GPIO[2:1] will act as GPIO 0b1: enable, GPIO[1] will act as GPIO_MDC and GPIO[2] act as GPIO_MDIO	RW	0x0
7:6	EXT_GPIO_MDIO_PREMBL	Select MDIO preamble bits: 0x0: 32bits 0x1:16bits 0x2: 8bits 0x3: 1bit the first cmd of the cmd sequence that must be 32bits preamble	RW	0x0
5:4	EXT_GPIO_MDC_PERIOD	MDC period for extend GPIO function; 0x0: 800ns 0x1: 400ns 0x2: 200ns 0x3: 100ns	RW	0x0
3	EXT_GPIO_MDX4_ACCESS_MODE	config 8231_MDX controller (4) accessing mode 0b0 : trigger mode 0b1 : continuous mode	RW	0x1
2	EXT_GPIO_MDX5_ACCESS_MODE	config 8231_MDX controller (5) accessing mode 0b0 : trigger mode 0b1 : continuous mode	RW	0x1
1	EXT_GPIO_MDX4_EN	Enable 8231_MDX controller (4) 0b0 : disable 0b1 : enable	RW	0x0
0	EXT_GPIO_MDX5_EN	Enable 8231_MDX controller (5) 0b0 : disable 0b1 : enable	RW	0x0

Bits	Field	Description	Type	Default
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## EXT\_GPIO\_TRIG

REGISTER ADDRESS : 0xBB0007D8

DEFAULT VALUE : 0x0

Specify trigger accessing extended gpio when access mode is trigger mode

Bits	Field	Description	Type	Default
31:2	RESERVED			
1	EXT_GPIO_MDX4_TRIG	Trigger 8231_MDX controller (4) in trigger mode 0b0 : Complete 0b1 : Trigger	RW	0x0
0	EXT_GPIO_MDX5_TRIG	Trigger 8231_MDX controller (5) in trigger mode 0b0 : Complete 0b1 : Trigger	RW	0x0

## EXT\_GPIO\_DIR\_CTRL\_1

REGISTER ADDRESS : 0xBB0007DC

DEFAULT VALUE : 0x0

Specify extended gpio direction control register1

Bits	Field	Description	Type	Default
31:0	EXT_GPIO_31_0_DIR	External GPIO [31:0] direction. 0b0: input 0b1: output	RW	0x0

## EXT\_GPIO\_DIR\_CTRL\_2

REGISTER ADDRESS : 0xEB0007E0

DEFAULT VALUE : 0x0

Specify extended gpio direction control register2

Bits	Field	Description	Type	Default
31:0	EXT_GPIO_63_32_DIR	External GPIO [63:32] direction. 0b0: input 0b1: output	RW	0x0

### **EXT\_GPIO\_DIR\_CTRL\_3**

REGISTER ADDRESS : 0xBB0007E4

DEFAULT VALUE : 0x0

Specify extended gpio direction control register3

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:10	RESERVED			
9:0	EXT_GPIO_73_64_DIR	External GPIO [73:64] direction. 0b0: input 0b1: output	RW	0x0

### **EXT\_GPIO\_DATA\_CTRL\_1**

REGISTER ADDRESS : 0xBB0007E8

DEFAULT VALUE : 0x0

Specify extended gpio data register1

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	EXT_GPIO_31_DATA	External GPIO [31] data. If EXT_GPIO_DIR=0 (input direction), then EXT_GPIO_31_0_DATA =input data [31:0] If EXT_GPIO_DIR=1 (output direction), then EXT_GPIO_31_0_DATA =output data [31:0]	RW	0x0
30	EXT_GPIO_30_DATA	External GPIO [30] data.	RW	0x0
29	EXT_GPIO_29_DATA	External GPIO [29] data.	RW	0x0
28	EXT_GPIO_28_DATA	External GPIO [28] data.	RW	0x0
27	EXT_GPIO_27_DATA	External GPIO [27] data.	RW	0x0
26	EXT_GPIO_26_DATA	External GPIO [26] data.	RW	0x0
25	EXT_GPIO_25_DATA	External GPIO [25] data.	RW	0x0
24	EXT_GPIO_24_DATA	External GPIO [24] data.	RW	0x0
23	EXT_GPIO_23_DATA	External GPIO [23] data.	RW	0x0
22	EXT_GPIO_22_DATA	External GPIO [22] data.	RW	0x0
21	EXT_GPIO_21_DATA	External GPIO [21] data.	RW	0x0
20	EXT_GPIO_20_DATA	External GPIO [20] data.	RW	0x0
19	EXT_GPIO_19_DATA	External GPIO [19] data.	RW	0x0
18	EXT_GPIO_18_DATA	External GPIO [18] data.	RW	0x0
17	EXT_GPIO_17_DATA	External GPIO [17] data.	RW	0x0
16	EXT_GPIO_16_DATA	External GPIO [16] data.	RW	0x0
15	EXT_GPIO_15_DATA	External GPIO [15] data.	RW	0x0
14	EXT_GPIO_14_DATA	External GPIO [14] data.	RW	0x0
13	EXT_GPIO_13_DATA	External GPIO [13] data.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
12	EXT_GPIO_12_DATA	External GPIO [12] data.	RW	0x0
11	EXT_GPIO_11_DATA	External GPIO [11] data.	RW	0x0
10	EXT_GPIO_10_DATA	External GPIO [10] data.	RW	0x0
9	EXT_GPIO_9_DATA	External GPIO [9] data.	RW	0x0
8	EXT_GPIO_8_DATA	External GPIO [8] data.	RW	0x0
7	EXT_GPIO_7_DATA	External GPIO [7] data.	RW	0x0
6	EXT_GPIO_6_DATA	External GPIO [6] data.	RW	0x0
5	EXT_GPIO_5_DATA	External GPIO [5] data.	RW	0x0
4	EXT_GPIO_4_DATA	External GPIO [4] data.	RW	0x0
3	EXT_GPIO_3_DATA	External GPIO [3] data.	RW	0x0
2	EXT_GPIO_2_DATA	External GPIO [2] data.	RW	0x0
1	EXT_GPIO_1_DATA	External GPIO [1] data.	RW	0x0
0	EXT_GPIO_0_DATA	External GPIO [0] data.	RW	0x0

## **EXT\_GPIO\_DATA\_CTRL\_2**

REGISTER ADDRESS : 0xBB0007EC

DEFAULT VALUE : 0x0

Specify extended gpio data register2

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	EXT_GPIO_63_DATA	External GPIO [63] data. If EXT_GPIO_DIR=0 (input direction), then EXT_GPIO_63_32_DATA =input data [63:32] If EXT_GPIO_DIR=1 (output direction), then EXT_GPIO_63_32_DATA =output data [63:32]	RW	0x0
30	EXT_GPIO_62_DATA	External GPIO [62] data.	RW	0x0
29	EXT_GPIO_61_DATA	External GPIO [61] data.	RW	0x0
28	EXT_GPIO_60_DATA	External GPIO [60] data.	RW	0x0
27	EXT_GPIO_59_DATA	External GPIO [59] data.	RW	0x0
26	EXT_GPIO_58_DATA	External GPIO [58] data.	RW	0x0
25	EXT_GPIO_57_DATA	External GPIO [57] data.	RW	0x0
24	EXT_GPIO_56_DATA	External GPIO [56] data.	RW	0x0
23	EXT_GPIO_55_DATA	External GPIO [55] data.	RW	0x0
22	EXT_GPIO_54_DATA	External GPIO [54] data.	RW	0x0
21	EXT_GPIO_53_DATA	External GPIO [53] data.	RW	0x0
20	EXT_GPIO_52_DATA	External GPIO [52] data.	RW	0x0
19	EXT_GPIO_51_DATA	External GPIO [51] data.	RW	0x0
18	EXT_GPIO_50_DATA	External GPIO [50] data.	RW	0x0
17	EXT_GPIO_49_DATA	External GPIO [49] data.	RW	0x0
16	EXT_GPIO_48_DATA	External GPIO [48] data.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15	EXT_GPIO_47_DATA	External GPIO [47] data.	RW	0x0
14	EXT_GPIO_46_DATA	External GPIO [46] data.	RW	0x0
13	EXT_GPIO_45_DATA	External GPIO [45] data.	RW	0x0
12	EXT_GPIO_44_DATA	External GPIO [44] data.	RW	0x0
11	EXT_GPIO_43_DATA	External GPIO [43] data.	RW	0x0
10	EXT_GPIO_42_DATA	External GPIO [42] data.	RW	0x0
9	EXT_GPIO_41_DATA	External GPIO [41] data.	RW	0x0
8	EXT_GPIO_40_DATA	External GPIO [40] data.	RW	0x0
7	EXT_GPIO_39_DATA	External GPIO [39] data.	RW	0x0
6	EXT_GPIO_38_DATA	External GPIO [38] data.	RW	0x0
5	EXT_GPIO_37_DATA	External GPIO [37] data.	RW	0x0
4	EXT_GPIO_36_DATA	External GPIO [36] data.	RW	0x0
3	EXT_GPIO_35_DATA	External GPIO [35] data.	RW	0x0
2	EXT_GPIO_34_DATA	External GPIO [34] data.	RW	0x0
1	EXT_GPIO_33_DATA	External GPIO [33] data.	RW	0x0
0	EXT_GPIO_32_DATA	External GPIO [32] data.	RW	0x0

### **EXT\_GPIO\_DATA\_CTRL\_3**

REGISTER ADDRESS : 0xBB0007F0

DEFAULT VALUE : 0x0

Specify extended gpio data register3

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:10	RESERVED			
9	EXT_GPIO_73_DATA	External GPIO [73] data. If EXT_GPIO_DIR=0 (input direction), then EXT_GPIO_31_0_DATA =input data [73:64] If EXT_GPIO_DIR=1 (output direction), then EXT_GPIO_31_0_DATA =output data [73:64]	RW	0x0
8	EXT_GPIO_72_DATA	External GPIO [72] data.	RW	0x0
7	EXT_GPIO_71_DATA	External GPIO [71] data.	RW	0x0
6	EXT_GPIO_70_DATA	External GPIO [70] data.	RW	0x0
5	EXT_GPIO_69_DATA	External GPIO [69] data.	RW	0x0
4	EXT_GPIO_68_DATA	External GPIO [68] data.	RW	0x0
3	EXT_GPIO_67_DATA	External GPIO [67] data.	RW	0x0
2	EXT_GPIO_66_DATA	External GPIO [66] data.	RW	0x0
1	EXT_GPIO_65_DATA	External GPIO [65] data.	RW	0x0
0	EXT_GPIO_64_DATA	External GPIO [64] data.	RW	0x0

## **EXT\_GPIO\_INDRT\_ACCESS\_CTRL**

REGISTER ADDRESS : 0xBB0007F4

DEFAULT VALUE : 0x0

Specify extended GPIO indirect access control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28	GPIO_RCMD_FAIL	When RTL9310 trigger reading command ( GPIO_RWOP=1 & GPIO_CMD=1 ), this bit clear to '0'; When reading command fail to detect turn round bit, this bit is set to '1'. Note: GPIO_RCMD_FAIL will be clear to "0" when next GPIO_CMD executing.	RO	0x0
27:12	GPIO_DATA	Input parameters: If RWOP=0 (read), then REG_DATA [15:0]=input data [15:0] If RWOP=1 (write), then REG_DATA [15:0]=output data [15:0]	RW	0x0
11:7	GPIO_REG	Select register number to access. 0x0: access register 0 0x1: access register 1 0x2: access register 2 ::: 0x1f: access register 31	RW	0x0
6:2	GPIO_PHYADDR	Select RTL8231's PHY address to access	RW	0x0
1	GPIO_RWOP	Read/write operation. 0b0: read 0b1: Write	RW	0x0
0	GPIO_CMD	Request MAC to access RTL8231 Reg. 0b0: complete access 0b1: execute access Note: When MAC completes access, it will clear this bit.	RWAC	0x0

### SECTION 1.5

## **INTERRUPT**

Interrupt module

## **IMR\_PORT\_LINK\_STS\_CHG**

BASE ADDRESS : 0xBB00126C

PORT INDEX : 0 - 55

PORT\_OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt mask control for per-port link status change.

Bits	Field	Description	Type	Default
0	IMR_PORT_LINK_STS_CH G	Interrupt mask control for port link status change. Allow per-port to decide if send interrupt to CPU when link status change. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	RW	0x0

## IMR\_SERDES\_ERR

BASE ADDRESS : 0xBB001274  
ARRAY INDEX : 0 - 13  
ARRAY OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt mask control for per serdes.

Bits	Field	Description	Type	Default
0	IMR_SERDES_ERR	Interrupt mask control for Serdes 0 ~ Serdes 23 error. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	RW	0x0

## IMR\_REMOTE\_INTR\_STS\_UPD

REGISTER ADDRESS : 0xBB001278  
DEFAULT VALUE : 0x0

Remote interrupt mask control for device0~device15.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	IMR_RMT_INTR_STS_UPD	Remote interrupt mask control for device 0 ~ device 15. 0b0: Does not allow any interrupt 0b1: Allow interrupt	RW	0x0

## IMR\_MISC

REGISTER ADDRESS : 0xBB00127C  
DEFAULT VALUE : 0x0

Miscellaneous interrupt mask control.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18	IMR_STAT_TRIGGER	Interrupt mask control for based(ACL/OpenFlow) counter. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	flow-RW	0x0
17	IMR_ETHDM	Interrupt mask control for ETH-DM. ASIC should generate an interrupt if ETH-DM RX timestamp table is full. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	RW	0x0
16	IMR_OAM_DYGASP	Interrupt mask control for OAM Dying gasp. 0b0: Does not allow any interrupt. 0b1: Allow interrupt	RW	0x0
15:0	IMR_CCM	Interrupt mask bit. If one of the interrupt mask bits is disabled then the interrupt generated due to the corresponding ERF instance counters expiry will be disabled. 0b0: disable, mask interrupt. 0b1: enable, allow interrupt.	RW	0x0

## **IMR\_EXT\_GPIO**

BASE ADDRESS : 0xBB001280  
ARRAY INDEX : 0 - 36  
ARRAY OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt mask control for external GPIO

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	IMR_EXT_GPIO	Interrupt mask control for GPIO of external RTL8231 status change. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	RW	0x0

## **IMR\_AUTO\_RECOVERY**

REGISTER ADDRESS : 0xBB00128C  
DEFAULT VALUE : 0x0

Interrupt mask control for auto recovery.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:7	RESERVED			

Bits	Field	Description	Type	Default
6	IMR_AUTO_REC	Interrupt mask control for auto recovery. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	RW	0x0
5	IMR_TX_CRC_CNTR	Interrupt mask control for TX CRC. 0b0: normal. 0b1: interrupt pending. TX_CRC_ CNTR include TX_etherStatsFragments, TX_etherStatsCRCAlignErrors, and TX_etherStatsJabbers MIB counters.	RW	0x0
4:0	IMR_SMI_CHECK_REG	Interrupt mask control for PHY register check result [4:0]. 0b0: Does not allow any interrupt. 0b1: Allow interrupt.	RW	0x0

### **IMR\_SERDES\_UPD\_PHYSTS\_0**

BASE ADDRESS : 0xBB001290

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-port interrupt mask control of serdes update PHY status.

Bits	Field	Description	Type	Default
0	IMR_SDS_UPD_PHYSTS	Per-port interrupt mask control of serdes update PHY status. 0b0: Does not allow any interrupt 0b1: Allow interrupt	RW	0x0

### **IMR\_RLFD**

BASE ADDRESS : 0xBB001298

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-port interrupt mask control of rapid link fault detection.

Bits	Field	Description	Type	Default
0	IMR_RLFD	Per-port interrupt mask control for rapid link fault detection. 0b0: Does not allow any interrupt 0b1: Allow interrupt	RW	0x0

## **IMR\_SERDES\_RXIDLE**

REGISTER ADDRESS : 0xBB0012A0

DEFAULT VALUE : 0x0

Interrupt mask control for Serdes#13~Serdes#2 rx detect signal

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:12	RESERVED			
11:0	IMR_SERDES_RXIDLE	Interrupt mask control for Serdes#13~Serdes#2 which RXIDLE signal from high go low. 0b0: Does not allow any interrupt 0b1: Allow interrupt	RW	0x0

## **IMR\_DBGO**

REGISTER ADDRESS : 0xEB0012A4

DEFAULT VALUE : 0x0

Interrupt mask control for debug\_out[31:0]

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	IMR_DBGO	Interrupt mask control for debug_out[31:0] 0b0: Does not allow any interrupt 0b1: Allow interrupt	RW	0x0

## **DBG\_CTRL0**

REGISTER ADDRESS : 0xBB0012A8

DEFAULT VALUE : 0x0

Select interrupt source from debug\_out[31:0]

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	CFG_DBGO_MSK	Select the interrupt source is from which bits of debug_out[31:0].	RW	0x0

## **DBG\_CTRL1**

REGISTER ADDRESS : 0xBB0012AC

DEFAULT VALUE : 0x0

Set compare values to check debug\_out[31:0]

Bits	Field	Description	Type	Default
31:0	CFG_DBGO_CMP_VAL	Set compare values to check debug_out[31:0]	RW	0x0

## DBG\_CTRL2

REGISTER ADDRESS : 0xBB0012B0

DEFAULT VALUE : 0x0

Set interrupt trigger condition for debug\_out[31:0]

Bits	Field	Description	Type	Default
31:3	RESERVED			
2	CFG_DBGO_MG	Select interrupt trigger condition When CFG_DBGO_TYPE=0x1 or 0x2. 0b0: all enable bit of debug_out must satisfy trigger type 0b1: one of enable bit of debug_out satisfy trigger type	RW	0x0
1:0	CFG_DBGO_TYPE	Select interrupt trigger condition for debug_out[31:0]. 0x0: meet CFG_DBGO_CMP_VAL 0x1: rising edge 0x2: failing edge	RW	0x0

## ISR\_GLB\_SRC\_STS

REGISTER ADDRESS : 0xBB0012B4

DEFAULT VALUE : 0x0

Interrupt status for global source

Bits	Field	Description	Type	Default
31:17	RESERVED			
16	ISR_GLB_DBGO	Global interrupt pending flag for debug_out[31:0] 0b0: normal 0b1: interrupt pending	RO	0x0
15	ISR_GLB_SERDES_RXIDLE	Global interrupt pending flag for Serdes#13~Serdes#2 which RXIDLE signal from high go low. 0b0: normal 0b1: interrupt pending	RO	0x0
14	ISR_GLB_RLFD	Global interrupt pending flag for RLFD.	RO	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
13	ISR_GLB_STAT_TRIGGER	Global interrupt pending flag for flow-based(ACL/OpenFlow) counter. 0b0: normal 0b1: interrupt pending	RO	0x0
12	ISR_GLB_RMT_INTR_STS_UPD	Global interrupt pending flag for remote interrupt. 0b0: normal 0b1: interrupt pending	RO	0x0
11	ISR_GLB_AUTO_REC	Global interrupt pending flag for auto recovery. 0b0: normal. 0b1: interrupt pending.	RO	0x0
10	ISR_GLB_TX_CRC_CNTR	Global interrupt pending flag for TX_CRC_CTR. 0b0: normal. 0b1: interrupt pending.	RO	0x0
9	ISR_GLB_SMI_CHECK	Global interrupt pending flag for PHY register check result. 0b0: normal. 0b1: interrupt pending.	RO	0x0
8	ISR_GLB_SERDES_UPDPHYSTS	Global interrupt pending flag for serdes update PHY status. 0b0: normal. 0b1: interrupt pending.	RO	0x0
7	ISR_GLB_TM	Global interrupt pending flag for thermal meter 0&1 detect high or low temperature. 0b0: normal. 0b1: interrupt pending.	RO	0x0
6	ISR_GLB_EXT_GPIO	Global interrupt pending flag for GPIO0~36 of external RTL8231 status change. 0b0: normal. 0b1: interrupt pending.	RO	0x0
5	ISR_GLB_ETHDM	The logical AND result of ISR_ETHDM and IMR_ETHDM. 0b0: normal. 0b1: interrupt pending.	RO	0x0
4	ISR_GLB_OAM_DYGASP	The logical AND result of ISR_OAM_DYGASP and IMR_OAM_DYGASP. 0b0: normal. 0b1: interrupt pending.	RO	0x0
3	ISR_GLB_CCM	Global interrupt pending flag of CCM[15:0]. 0b0: normal. 0b1: interrupt pending.	RO	0x0
2	ISR_GLB_TIMESTAMP_LATCH	Global interrupt pending flag for 32 PTP modules. 0b0: normal 0b1: interrupt pending	RO	0x0
1	ISR_GLB_SERDES	Global interrupt pending flag for serdes0~13 happen error. 0b0: normal. 0b1: interrupt pending.	RO	0x0
0	ISR_GLB_LINK_CHG	Global interrupt pending flag for port0~51 link status change. 0b0: normal. 0b1: interrupt pending.	RO	0x0

## **ISR\_PORT\_LINK\_STS\_CHG**

BASE ADDRESS : 0xBB0012B8

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt status for per-port link status change.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	ISR_PORT_LINK_STS_CH G	Interrupt pending flag for per-port link status change. 0b0: normal. 0b1: interrupt pending. Note: Write 0b1 to clear pending flag.	RW1C	0x0

## **ISR\_SERDES\_ERR**

BASE ADDRESS : 0xBE0012C0

ARRAY INDEX : 0 - 13

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt status for per SERDES error.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	ISR_SERDES_ERR	Interrupt pending flag for Serdes 0 ~ Serdes 23 happen error. 0b0: normal. 0b1: interrupt pending. Note: 1. Write 0b1 to clear pending flag. 2. Serdes error includes serdes link fail & serdes rx symbol error.	RW1C	0x0

## **ISR\_REMOTE\_INTR\_STS\_UPD**

REGISTER ADDRESS : 0xBB0012C4

DEFAULT VALUE : 0x0

Remote interrupt status for device0~device15.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15:0	ISR_RMT_INTR_STS_UPD	Remote interrupt pending flag for device 0 ~ device 15. 0b0: normal 0b1: interrupt pending Note: Write 0b1 to clear pending flag.	RW1C	0x0

## **ISR\_MISC**

REGISTER ADDRESS : 0xBB0012C8

DEFAULT VALUE : 0x0

Misllaneous interrupt status.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:18	RESERVED			
17	ISR_ETHDM	Interrupt pending flags for ETH-DM due to the RX timestamp table full. 0b0: normal. 0b1: interrupt pending. Note: Write 0b1 to clear pending flag.	RW1C	0x0
16	ISR_OAM_DYGASP	Interrupt pending flag for OAM Dying gasp. 0b0: normal. 0b1: interrupt pending. Note: Write 0b1 to clear pending flag.	RW1C	0x0
15:0	ISR_CCM	Interrupt status bitmask of ports participated in ERP instances. Bit0: INS0_PORT0 Bit1: INS0_PORT1 Bit2: INS1_PORT0 Bit3: INS1_PORT1 Bit4: INS2_PORT0 Bit5: INS2_PORT1 Bit6: INS3_PORT0 Bit7: INS3_PORT1 Bit8: INS4_PORT0 Bit9: INS4_PORT1 Bit10: INS5_PORT0 Bit11: INS5_PORT1 Bit12: INS6_PORT0 Bit13: INS6_PORT1 Bit14: INS7_PORT0 Bit15: INS7_PORT1	RW1C	0x0

## **ISR\_EXT\_GPIO**

BASE ADDRESS : 0xBB0012CC

ARRAY INDEX : 0 - 36

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt status for external GPIO0~36.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	ISR_EXT_GPIO	Interrupt pending flag for GPIO of external RTL8231 status change. 0b0: normal. 0b1: interrupt pending. Note: Write 0b1 to clear pending flag.	RW1C	0x0

## EXT\_GPIO\_INTR\_MODE

BASE ADDRESS : 0xBB0012D4

ARRAY INDEX : 0 - 36

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x3

This is a One-Dimension Register Field Array. (2 bits per field)

Select the interrupt trigger conditions for external GPIO.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	EXT_GPIO_INTR_MODE	Each GPIO of external RTL8231 has two bits to decide GPIO's interrupt mode. 0x0: reserved. 0x1: falling edge trigger interrupt. 0x2: rising edge trigger interrupt. 0x3: falling or rising edge trigger interrupt.	RW	0x3

## ISR\_AUTO\_RECOVERY

REGISTER ADDRESS : 0xBB0012E4

DEFAULT VALUE : 0x0

Interrupt status for auto recovery, tx crc, and phy reg check.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:7	RESERVED			
6	ISR_AUTO_REC	Interrupt pending flag for auto recovery. 0b0: normal. 0b1: interrupt pending. Note: Write 0b1 to clear pending flag.	RW1C	0x0

Bits	Field	Description	Type	Default
5	ISR_TX_CRC_CNTR	<p>Interrupt pending flag for TX CRC.</p> <p>0b0: normal. 0b1: interrupt pending.</p> <p>Note: Write 0b1 to clear pending flag.</p> <p style="text-align: right;">TX_CRC_CTR include TX_etherStatsFragments, TX_etherStatsCRCAlignErrors, and TX_etherStatsJabbers MIB counters.</p>	RW1C	0x0
4:0	ISR_SMI_REG_CHECK4_0	<p>Interrupt pending flag for PHY register check0~4 result of port#0~55.</p> <p>Bit.0: flag for SMI_PHY_REG_CHK0_RESULT. Bit.1: flag for SMI_PHY_REG_CHK1_RESULT. Bit.2: flag for SMI_PHY_REG_CHK2_RESULT. Bit.3: flag for SMI_PHY_REG_CHK3_RESULT. Bit.4: flag for SMI_PHY_REG_CHK4_RESULT.</p> <p>0b0: normal. 0b1: interrupt pending.</p> <p>Note: Write 0b1 to clear pending flag.</p>	RW1C	0x0

### ISR\_SERDES\_UPD\_PHYSTS

BASE ADDRESS : 0xBB0012E8

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-port interrupt status of serdes update PHY status.

Bits	Field	Description	Type	Default
0	ISR_SDS_UPD_PHYSTS	<p>Per-port interrupt pending flag of serdes update PHY status.</p> <p>0b0: normal 0b1: interrupt pending</p> <p>Note: Write 0b1 to clear pending flag</p>	RW1C	0x0

### ISR\_STAT\_TRIGGER

BASE ADDRESS : 0xBB001800

ARRAY INDEX : 0 - 4095

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Interrupt status for flow-based(ACL/OpenFlow) counter.

Bits	Field	Description	Type	Default
0	ISR_STAT_TRIGGER	Interrupt pending flag for flow-based(ACL/OpenFlow) counter. 0b0: normal. 0b1: interrupt pending. Note: Write 0b1 to clear pending flag.	RW1C	0x0

## **ISR\_RLFD**

BASE ADDRESS : 0xBB0012F0  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-port interrupt status of rapid link fault detection.

Bits	Field	Description	Type	Default
0	ISR_RLFD	Per-port interrupt pending flag for rapid link fault detection. 0b0: normal 0b1: interrupt pending	RW1C	0x0

## **ISR\_SERDES\_RXIDLE**

REGISTER ADDRESS : 0xBB0012F8  
DEFAULT VALUE : 0x0

Interrupt status for Serdes#13~Serdes#2 rx detect signal

Bits	Field	Description	Type	Default
31:12	RESERVED			
11:0	ISR_SERDES_RXIDLE	Interrupt pending flag for Serdes#13 ~ Serdes#2 which RXIDLE signal from high go low . 0b0: normal 0b1: interrupt pending	RW1C	0x0

## **ISR\_DBGO**

REGISTER ADDRESS : 0xBB0012FC  
DEFAULT VALUE : 0x0

Interrupt status for debug\_out[31:0]

Bits	Field	Description	Type	Default
31:1	RESERVED			
0	ISR_DBGO	Interrupt pending flag for debug_out[31:0] 0b0: normal 0b1: interrupt pending	RW1C	0x0

SECTION 1.6

**HW MISC.**

HW Misc. module

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## CHAPTER 2

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# CPU Relative

The chapter describes features related to CPU relative

SECTION 2.1	<b>NIC &amp; DMA</b>
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This section describes the control registers which are related to sending/receiving packet to/from CPU.

### **DMA\_IF\_RX\_BASE\_DESC\_ADDR\_CTRL**

BASE ADDRESS : 0xBB000800

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Get/Set the first pointer address of Rx pktbuf descriptor ring.

Bits	Field	Description	Type	Default
31:0	ADDR	Rx pktbuf descriptor first pointer address.	RW	0x0

### **DMA\_IF\_RX\_CUR\_DESC\_ADDR\_CTRL**

BASE ADDRESS : 0xBB000880

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Get the current pointer address of Rx pktbuf descriptor ring.

Bits	Field	Description	Type	Default
31:0	ADDR	Rx pktbuf current descriptor pointer address.	RO	0x0

## DMA\_IF\_TX\_BASE\_DESC\_ADDR\_CTRL

BASE ADDRESS : 0xBB000900

ARRAY INDEX : 0 - 1

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Get/Set the first pointer address of Tx pktbuf descriptor ring.

Bits	Field	Description	Type	Default
31:0	ADDR	Tx pktbuf descriptor first pointer address.	RW	0x0

## DMA\_IF\_TX\_CUR\_DESC\_ADDR\_CTRL

BASE ADDRESS : 0xBB000908

ARRAY INDEX : 0 - 1

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Get the current pointer address of Tx pktbuf descriptor ring.

Bits	Field	Description	Type	Default
31:0	ADDR	Tx pktbuf current descriptor pointer address.	RO	0x0

## DMA\_IF\_INTR\_RX\_RUNOUT\_MSK

REGISTER ADDRESS : 0xBB000910

DEFAULT VALUE : 0x0

Enable rx runout interrupt signal of CPU interface.

Bits	Field	Description	Type	Default
31:0	RX_RUN_OUT	Enable interrupt for running out of pktbuf descriptors. Each bit represents 1 Rx descriptor ring setting. 0b0: disable 0b1: enable	RW	0x0

## DMA\_IF\_INTR\_RX\_DONE\_MSK

REGISTER ADDRESS : 0xBB000914

DEFAULT VALUE : 0x0

Enable rx done interrupt signal of CPU interface.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	RX_DONE	Enable interrupt for successful receive of each packet. Each bit represents 1 Rx descriptor ring setting. 0b0: disable 0b1: enable	RW	0x0

### **DMA\_IF\_INTR\_RX\_DONE\_MSK**

REGISTER ADDRESS : 0xBB000918

DEFAULT VALUE : 0x0

Enable tx done interrupt signal of CPU interface.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:4	RESERVED			
3:2	TX_ALL_DONE	Enable interrupt for successful transmission of all packets in the Tx descriptor ring (all descriptor are owned by the CPU). Each bit represents 1 Tx descriptor ring setting. 0b0: disable 0b1: enable	RW	0x0
1:0	TX_DONE	Enable interrupt for successful transmission of each packet. Each bit represents 1 Tx descriptor ring setting. 0b0: disable 0b1: enable	RW	0x0

### **DMA\_IF\_INTR\_RX\_RUNOUT\_STS**

REGISTER ADDRESS : 0xBB00091C

DEFAULT VALUE : 0x0

Get the rx runout pending status of CPU interface.  
(Write ‘1’ to clear)

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	RX_RUN_OUT	Pending status of the RX_RUN_OUT interrupt. Each bit represents 1 Rx descriptor ring status.	RW1C	0x0

## DMA\_IF\_INTR\_RX\_DONE\_STS

REGISTER ADDRESS : 0xBB000920

DEFAULT VALUE : 0x0

Get the rx done pending status of CPU interface.  
(Write ‘1‘ to clear)

Bits	Field	Description	Type	Default
31:0	RX_DONE	Pending status of the RX_DONE interrupt. Each bit represents 1 Rx descriptor ring status.	RW1C	0x0

## DMA\_IF\_INTR\_TX\_DONE\_STS

REGISTER ADDRESS : 0xBB000924

DEFAULT VALUE : 0x0

Get the tx done pending status of CPU interface.  
(Write ‘1‘ to clear)

Bits	Field	Description	Type	Default
31:4	RESERVED			
3:2	TX_ALL_DONE	Pending status of the TX_ALL_DONE interrupt. Each bit represents 1 Tx descriptor ring status.	RW1C	0x0
1:0	TX_DONE	Pending status of the TX_DONE interrupt. Each bit represents 1 Tx descriptor ring status.	RW1C	0x0

## DMA\_IF\_CTRL

REGISTER ADDRESS : 0xBB000928

DEFAULT VALUE : 0x64000000

The register controls the packet TX/RX DMA.

Bits	Field	Description	Type	Default
31:30	RESERVED			
29:16	RX_TRUNCATE_LEN	The length should be truncated in the buffer.	RW	0x0640
15:7	RESERVED			
6	RX_TRUNCATE_EN	Enable Rx truncate jumbo frame. 0b0: disable 0b1: enable	RW	0x0
5	TX_EN	Enable Tx DMA process. 0b0: disabled 0b1: enabled	RW	0x0
4	RX_EN	Enable Rx DMA process. 0b0: disabled 0b1: enabled	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3	TX_HIGH_FETCH	Tx High ring descriptor fetch notify. (Set this bit to trigger the packet send) Note: Auto clear to 0 by asic.	RWAC	0x0
2	TX_LOW_FETCH	Tx low ring descriptor fetch notify. (Set this bit to trigger the packet send) Note: Auto clear to 0 by asic.	RWAC	0x0
1	TX_HIGH_BUSY	TX High Ring DMA status. (for debugging) 0b0: idle 0b1: busy	RO	0x0
0	TX_LOW_BUSY	TX Low Ring DMA status. (for debugging) 0b0: idle 0b1: busy	RO	0x0

## DMA\_IF\_PKT\_CTRL

REGISTER ADDRESS : 0xBB004858

DEFAULT VALUE : 0x18

The register controls the packet format for CPU port, and operating mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:7	RESERVED			
6	TRAP_ETAG_PKT_FMT	Specify the ECID tag format trapped/copied to CPU. 0b0: original packet 0b1: modified packet	RW	0x0
5	FWD_ETAG_PKT_FMT	Specify the ECID tag format forwarded to CPU. 0b0: original packet 0b1: modified packet	RW	0x0
4	TRAP_PKT_TAG	When EXT_CPU_EN=0b1, this bit indicates whether insert the CPU tag into the packets which is trapped to CPU port. 0b0: without CPU tag 0b1: with CPU tag	RW	0x1
3	FWD_PKT_TAG	When EXT_CPU_EN=0b1, this bit indicates whether insert the CPU tag into the packets which is forwarded to CPU port. 0b0: without CPU tag 0b1: with CPU tag	RW	0x1
2	TRAP_PKT_FMT	Specify the packet format trapped/copied to CPU. 0b0: original packet 0b1: modified packet	RW	0x0
1	FWD_PKT_FMT	Specify the packet format forwarded to CPU. 0b0: original packet 0b1: modified packet	RW	0x0
0	PKT_VLAN_FMT	Specify the packet's VLAN tag to CPU. 0b0: original packet 0b1: modified packet	RW	0x0

## DMA\_IF\_RX\_RING\_SIZE

BASE ADDRESS : 0xBB002080

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 10 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (10 bits per field)

Set the limit of ring size for each queue to avoid HOL issue.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
9:0	SIZE	The Rx pktbuf descriptor ring size for each queue. 0b00000000: disable HOL 0b00000001 – 0b11111111: the number of pktbuf descriptor	RW	0x0

## DMA\_IF\_RX\_RING\_CNTR

BASE ADDRESS : 0xBB0020AC

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 10 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (10 bits per field)

Get/Set the used nubmer of descriptor for each queue.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
9:0	CNTR	The Rx pktbuf descriptor ring counter for each queue. For read: get the number of used descriptors For write: to subtract the value to the counter	RW	0x0

## DMA\_IF\_PHYSICAL\_ADDR\_MSK

REGISTER ADDRESS : 0xEB00092C

DEFAULT VALUE : 0xFFFFFFFF

NIC The mask register to convert the virtual address to physical.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	MSK	The mask that is used to convert the virtual to physical by and operation.(Example: 0x90080000 (Virtual address) AND 0x1FFFFFFF (MSK) = 0x10080000 (Physical address).)	RW	0x1FFFFFFF

## MBIST\_NIC\_W

REGISTER ADDRESS : 0xBB000930

DEFAULT VALUE : 0x0

mbist\_nic\_w

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:4	RESERVED			
3	CFG_DRF_TEST_RESUME_02		RW	0x0
2	CFG_DRF_TEST_RESUME_01		RW	0x0
1	CFG_BIST_RSTN_02		RW	0x0
0	CFG_BIST_RSTN_01		RW	0x0

## MBIST\_NIC\_R

REGISTER ADDRESS : 0xBB000934

DEFAULT VALUE : 0x0

mbist\_nic\_r

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:15	RESERVED			
14	NIC_DRF_START_PAUSE_02		RO	0x0
13	NIC_DRF_BIST_FAIL_02		RO	0x0
12	NIC_BIST_FAIL_02		RO	0x0
11	NIC_DRF_BIST_DONE_02		RO	0x0
10	NIC_BIST_DONE_02		RO	0x0
9	NIC_DRF_START_PAUSE_01		RO	0x0
8	NIC_DRF_BIST_FAIL_01		RO	0x0
7	NIC_BIST_FAIL_01		RO	0x0
6	NIC_DRF_BIST_DONE_01		RO	0x0
5	NIC_BIST_DONE_01		RO	0x0
4	NIC_DRF_START_PAUSE		RO	0x0
3	NIC_DRF_BIST_FAIL		RO	0x0
2	NIC_BIST_FAIL		RO	0x0
1	NIC_DRF_BIST_DONE		RO	0x0
0	NIC_BIST_DONE		RO	0x0

## SECTION 2.2

## APPLICATION TRAP

Application Trap module

### **TRAP\_CTRL**

REGISTER ADDRESS : 0xBB008C00

DEFAULT VALUE : 0x1

trap global control register.

Bits	Field	Description	Type	Default
31:3	RESERVED			
2	CPU_SEL	Select packet trap to local or master CPU 0b0: trap to local CPU 0b1: trap to master CPU	RW	0x0
1:0	L2_CRC_ERR_ACT	Specify action for L2 CRC error packets. 0x0: forward 0x1: drop 0x2: trap 0x3: reserved	RW	0x1

### **TRAP\_ARP\_GRAT\_PORT\_ACT**

BASE ADDRESS : 0xBB008C04

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Gratuitous ARP Action Per-Port register.

Determine the action (Forward/Drop/Trap/Copy) of Gratuitous ARP packets for each port.

Bits	Field	Description	Type	Default
1:0	GARP_ACT	Action for Gratuitous ARP packets. 0b00: Forward 0b01: Drop 0b10: Trap 0b11: Copy to CPU	RW	0x0

## SECTION 2.3

## TABLE ACCESS

Table access control registers including flexible table format configuration and indirect table read/write registers

## **TBL\_ACCESS\_CTRL\_0**

REGISTER ADDRESS : 0xBB008500

DEFAULT VALUE : 0x0

Indirect table access control for table group 0.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20	EXEC	Trigger hardware to execute indirect table access. 0b0: completed 0b1: executing (Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit.)	RWAC	0x0
19	CMD	Table access operation. 0b0: read 0b1: write	RW	0x0
18:15	TBL	Access table type 0: L2/OpenFlow table (SRAM) 1: L2/OpenFlow table (Binary CAM) 2: Multicast portmask table 3: VLAN table 4: IVC table 5: MSTI table 6: MAC binding table (SRAM) 7: MAC binding table (TCAM) 8: Tunnel terminate table 9: Tunnel start table/VxLAN 10: Flexible table 0 (SRAM) (BSSID/ MPLS de-cap/ MAC binding) 11: Flexible table 0 (BCAM) 12: Flexible table 1 (BSSID list/ MPLS next hop) 13: Source port to trunk ID mapping table 14: Port egress queue counter table Other: Reserved	RW	0x0

Bits	Field	Description	Type	Default
14:0	ADDR	<p>Select access address of the table</p> <p>Table type:</p> <ul style="list-style-type: none"> <li>L2/OpenFlow (SRAM): ADDR [14:0]={TBL_IDX[0:0], Index [11:0], EntrySel [1:0] }</li> <li>L2/OpenFlow (BCAM): ADDR [14:0]={ 00000000, Index [6:0] }</li> <li>Multicast Portmask: ADDR [14:0]={ 000, Index [11:0] }</li> <li>VLAN: ADDR [14:0]={000, VID [11:0]}</li> <li>IVC: ADDR [14:0]={ 00000 , Index [9:0] }</li> <li>MSI: ADDR [14:0]={00000000, Index [6:0]}</li> <li>MAC Binding(SRAM): ADDR [14:0] = { 00000 , Index [7:0], EntrySel [1:0] }</li> <li>MAC Binding(BCAM): ADDR [14:0] = { 00000000 , Index [5:0] }</li> <li>Tunnel Terminate: ADDR [14:0] = { 000000 , EntrySel[1:0], Index [6:0] }</li> <li>Tunnel Start/VxLAN: ADDR [14:0] = { 000000 , EntrySel[1:0], Index [6:0] }</li> <li>Flexible table 0 (BSSID/MPLS decap/ MAC binding) (SRAM): ADDR [14:0]={ 0000, Index [10:0] }</li> <li>Flexible table 0 (BSSID/MPLS decap/ MAC binding) (BCAM): ADDR [14:0]={ 00000000, Index [5:0] }</li> <li>Flexible table 1 (BSSID List/ MPLS next hop): ADDR [14:0]={ 00, Index [12:0] }</li> <li>Source port to trunk ID mapping table: ADDR [14:0]={ 00000, Index [9:0] }</li> <li>Port egress queue counter table: ADDR [14:0]={ 00000000, Index [5:0] }</li> </ul>	RW	0x0

### TBL\_ACCESS\_L2\_METHOD\_CTRL

REGISTER ADDRESS : 0xBB008504

DEFAULT VALUE : 0x0

This register takes effect when doing L2 (SRAM) read to decide the read method.

Bits	Field	Description	Type	Default
31:19	RESERVED			
18:16	METHOD	<p>L2 table (SRAM) look up method</p> <p>0x0: with specified lut address</p> <p>0x1: with specified next lut address (L2 UC entry)</p> <p>0x2: with specified next lut address (L2 ASIC Learn UC entry)</p> <p>0x3: with specified next lut address (L2 Software Add UC entry)</p> <p>0x4: with specified next lut address (L2 UC Next-Hot entry)</p> <p>0x5: with specified next lut address (L2 MC entry)</p> <p>0x6: with specified next lut address (L2 MC Next-Hot entry)</p>	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15	HIT	If L2 table (SRAM) get next look up hit 0b0: next entry does not exist 0b1: found next entry	RO	0x0
14:0	HIT_ADDR	The address of hit entry found by L2 get next method.	RO	0x0

### TBL\_ACCESS\_DATA\_0

BASE ADDRESS : 0xBB008508  
ARRAY INDEX : 0 - 7  
ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Indirect table access data of table group 0.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	DATA	Data content of the entry for the specified table.	RW	0x0

### TBL\_ACCESS\_CTRL\_1

REGISTER ADDRESS : 0xBB0040C0  
DEFAULT VALUE : 0x0

Indirect table access control for table group 1.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:18	RESERVED			
17	EXEC	Trigger hardware to execute indirect table access. 0b0: completed 0b1: executing (Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit.)	RWAC	0x0
16	CMD	Table access operation. 0b0: read 0b1: write	RW	0x0
15:14	TBL	Access table type 0: ACL/Full match table 1: Meter table 2: Flow counter table 3: Reserved	RW	0x0

Bits	Field	Description	Type	Default
13:0	ADDR	<p>Select access address of the table</p> <p>Table type:</p> <ul style="list-style-type: none"> <li>ACL: ADDR [13:0]={ 00, Index [11:0] }</li> <li>Meter: ADDR [13:0]={ 00000 , Index [8:0] }</li> <li>Flow Counter (Read): ADDR [13:0]={ 00, Index [11:0] }</li> <li>Flow Counter (Write): ADDR [13:0]={PKTC-NTR[0:0], BYTECNTR[0,0], Index [11:0] }</li> </ul> <p>Note: The Write command of Flow Counter table would always write CNTR_MODE field. PKTC-NTR[0:0] and BYTECNTR[0:0] in ADDR decide if PKT_CNTR or BYTE_CNTR fields would be written.</p>	RW	0x0

### TBL\_ACCESS\_DATA\_1

**BASE ADDRESS :** 0xBB0040C4  
**ARRAY INDEX :** 0 - 21  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Common Register Array.

Indirect table access data of table group 1.

Bits	Field	Description	Type	Default
31:0	DATA	Data content of the entry for the specified table.	RW	0x0

### TBL\_ACCESS\_CTRL\_2

**REGISTER ADDRESS :** 0xBB008528  
**DEFAULT VALUE :** 0x0

Indirect table access control for table group 2.

Bits	Field	Description	Type	Default
31:20	RESERVED			
19	EXEC	<p>Trigger hardware to execute indirect table access.</p> <p>0b0: completed 0b1: executing</p> <p>(Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit.)</p>	RWAC	0x0
18	CMD	<p>Table access operation.</p> <p>0b0: read 0b1: write</p>	RW	0x0

Bits	Field	Description	Type	Default
17:14	TBL	Access table type 0: Link Aggregation table 1: Port Isolation table 2: L3 Router MAC table 3: Host Routing/OpenFlow table 4: Prefix Routing/OpenFlow table 5: L3 ECMP table 6: L3 Next Hop table 7: L3 IGR Interface table 8: L3 EGR Interface table 9: L3 OIL table 10: OpenFlow group table 11: OpenFlow action bucket table 12: L3 Ingress Interface MIB Counter Table 13: L3 Egress Interface MIB Counter Table 14: L3 Ingress Tunnel Interface MIB Counter Table 15: L3 Egress Tunnel Interface MIB Counter Table	RW	0x0
13:0	ADDR	Select access address of the table Table type: Link Aggregation: ADDR [13:0] = { 0000000 , Index [6:0] } Port Isolation: ADDR [13:0] = { 000 , Index [10:0] } L3 Router MAC: ADDR [13:0]={0000, Index [9:0] } Host Routing/OpenFlow: ADDR [13:0]={ Tbl_ID[0], Index [9:0], Sel[2:0] } Prefix Routing/OpenFlow: ADDR [13:0] = { Index [10:0], Sel[2:0] } L3 ECMP: ADDR [13:0] = { 000000 , Index [7:0] } L3 Next Hop: ADDR [13:0] = { 0, Index [12:0] } L3 IGR Interface: ADDR [13:0] = { 0000 , Index [9:0] } L3 EGR Interface: ADDR [13:0] = { 0000 , Index [9:0] } L3 OIL: ADDR [13:0] = { Index [13:0] } OpenFlow group: ADDR [13:0] = {000, Index [10:0] } OpenFlow action bucket: ADDR [13:0] = {0, Index [12:0] } L3 Ingress Interface MIB Counter: ADDR [13:0] = { 0000 , Index [9:0] } L3 Egress Interface MIB Counter: ADDR [13:0] = { 0000 , Index [9:0] } L3 Ingress Tunnel Interface MIB Counter: ADDR [13:0] = { 00000 , Index [8:0] } L3 Egress Tunnel Interface MIB Counter: ADDR [13:0] = { 00000 , Index [8:0] }	RW	0x0

## TBL\_ACCESS\_DATA\_2

BASE ADDRESS : 0xBB00852C  
 ARRAY INDEX : 0 - 5  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Indirect table access data of table group 2.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	DATA	Data content of the entry for the specified table.	RW	0x0

### TBL\_ACCESS\_CTRL\_3

REGISTER ADDRESS : 0xBB000200

DEFAULT VALUE : 0x0

Indirect table access control for table group 3.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:17	RESERVED			
16	EXEC	Trigger hardware to execute indirect table access. 0b0: completed 0b1: executing (Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit.)	RWAC	0x0
15	CMD	Table access operation. 0b0: read 0b1: write	RW	0x0
14:12	TBL	Access table type 0x0: VLAN Untag table 0x1: EVC table 0x2: MPLS Enacap table 0x3: BSSID Tunnel Start table 0x4: Egress Remarking table 0x5: Tunnel start table/VxLAN for Encapsulation	RW	0x0
11:0	ADDR	Select access address of the table Table type: VLAN Untag: ADDR [11:0]={ VID [11:0] } EVC: ADDR [11:0]={ 00 , Index [9:0] } MPLS Encap: ADDR [11:0]={ 0, VID [10:0] } BSSID Tunnel Start table: ADDR [11:0]={ Index [11:0] } Egress Remarking table: ADDR [11:0]={ 00, Index [9:0] } Tunnel Start/VxLAN: ADDR [11:0] = { 000 , EntrySel[1:0], Index [6:0] }	RW	0x0

### TBL\_ACCESS\_DATA\_3

BASE ADDRESS : 0xBB000204

ARRAY INDEX : 0 - 8

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Indirect table access data of table group 3.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	DATA	Data content of the entry for the specified table.	RW	0x0

### **TBL\_ACCESS\_CTRL\_4**

REGISTER ADDRESS : 0xBB0020DC

DEFAULT VALUE : 0x0

Indirect table access control for table group 4.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:9	RESERVED			
8	EXEC	Trigger hardware to execute indirect table access. 0b0: completed 0b1: executing (Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit.)	RWAC	0x0
7	CMD	Table access operation. 0b0: read 0b1: write	RW	0x0
6	TBL	Access table type 0b0: Egress bandwidth control table 0b1: Output queue table	RW	0x0
5:0	ADDR	Select access address of the table Table type: Egress bandwidth control table: ADDR [5:0]={ Index [5:0] } Output queue table: ADDR [5:0]={ Index [5:0] }	RW	0x0

### **TBL\_ACCESS\_DATA\_4**

BASE ADDRESS : 0xBB0020E0

ARRAY INDEX : 0 - 28

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Indirect table access data of table group 4.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	DATA	Data content of the entry for the specified table.	RW	0x0

### **TBL\_ACCESS\_CTRL\_5**

REGISTER ADDRESS : 0xBB007E1C

DEFAULT VALUE : 0x0

Indirect table access control for table group 5.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:10	RESERVED			
9	EXEC	Trigger hardware to execute indirect table access. 0b0: completed 0b1: executing (Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit.)	RWAC	0x0
8	CMD	Table access operation. 0b0: read 0b1: write	RW	0x0
7:6	TBL	Access table type 0x0: Port MIB counter table 0x1: Private MIB counter table 0x2: Auto recovery tx error MIB counter table	RW	0x0
5:0	ADDR	Select access address of the table Table type: Port MIB Counter: ADDR [5:0]={ Index [5:0] } Private MIB counter table: ADDR [5:0]={Index [5:0] } Auto recovery tx error MIB counter table: ADDR [5:0]={Index [5:0] }	RW	0x0

### **TBL\_ACCESS\_DATA\_5**

BASE ADDRESS : 0xBB007E20

ARRAY INDEX : 0 - 52

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Indirect table access data of table group 5.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	DATA	Data content of the entry for the specified table.	RW	0x0

### **FLEX\_TBL\_CTRL**

REGISTER ADDRESS : 0xBB008544

DEFAULT VALUE : 0x2

Flexible table format selection control register.

Bits	Field	Description	Type	Default
31:2	RESERVED			
1:0	FLEX_TBL_FMT	Flexible table 0 and 1 format. 0x0: Flexible-Table 0 = BSSID, Flexible-Table 1 = BSSID List 0x1: Flexible-Table 0 = MPLS decap, Flexible Table 1 = MPLS Next Hop 0x2: Flexible-Table 0 = IP-MAC binding, Flexible-Table 1 = unused (disabled) 0x3: Flexible-Table 0 = ECID_PVID, Flexible-Table 1 = ECID List.	RW	0x2

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## CHAPTER 3

# MAC & PHY

The chapter describes features related to MAC & PHY

SECTION 3.1

### MAC CONTROL

The module contains MAC polling configuration, Indirect PHY access and generic MAC control functions.

#### **MAC\_L2\_GLOBAL\_CTRL1**

REGISTER ADDRESS : 0xBB005548

DEFAULT VALUE : 0x4D41A14A

Specify MAC L2 Misc global control register.

Bits	Field	Description	Type	Default
31:30	FIB_UNIDIR_WIN_LOW	Select the timing of UNIDIR_WIN=0. 0x0: 100ms 0x1: tx idle 0x2: 500ms 0x3: 1s	RW	0x1
29	IPG_5G_4NBYTE_COMPS_EN	Enable 5G port 4N byte IPG compensation function. 0b0: disable 0b1: enable	RW	0x0
28	FWD_PAUSE_TAG_EN	0b0: drop the pause frame with tag 0b1: forward the pause frame with tag	RW	0x0
27	LINK_INTR_EN	Enable MAC recognize 10GBASE-T Link Interruption Fault and pause MAC TX 0b0: Disable 0b1: Enable	RW	0x1
26	FIB_UNIDIR_ONLY_CPUTX_EN	Only forwarding packet which from CPU port when fiber unidirectional is enable. 0b0: Disable. 0b1: Enable.	RW	0x1
25	IPG_10G_4NBYTE_COMPS_EN	Enable 10G port 4N byte IPG compensation function. 0b0: disable 0b1: enable	RW	0x0
24	IPG_5G_COMPS_EN	Enable 5G port IPG compensation function. 0b0: disable 0b1: enable	RW	0x1

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23	IPG_5G_SEL	Select 5G port IPG compensation function. Change some 96-bit time IPG to 88-bit time IPG. 0b0: 65ppm compensation. 0b1: 90ppm compensation.	RW	0x0
22	IPG_2P5G_COMPS_EN	Enable 2.5G port IPG compensation function. 0b0: disable 0b1: enable	RW	0x1
21	IPG_2P5G_SEL	Select 2.5G port IPG compensation function. Change some 96-bit time IPG to 88-bit time IPG. 0b0: 65ppm compensation. 0b1: 90ppm compensation.	RW	0x0
20	FWD_PAUSE_EN	0b0:drop valid pause frame as normal pause action 0b1:forward valid pause frame and regard it as a normal packet(no pause action)	RW	0x0
19	FWD_INVLD_MAC_CTRL_EN	0b0:drop invalid MAC control frame 0b1:forward invalid MAC control frame and regard it as a normal packet	RW	0x0
18	FWD_UNKN_OPCODE_EN	0b0:drop unknown opcode frame 0b1:forward unknown opcode frame and regard it as a normal packet	RW	0x0
17:16	LIMIT_PAUSE_EN	Enable limited TX pause packet number. After switch continuous transmit 128 or 32 pause frames in the same port, switch will stop to transmit pause frame. However, after the congestion lifting, re-enter congestion state, switch will continue to send pause frame. 0x0: disable. 0x1: 128 pause enable. 0x2: 32 pause enable 0x3: reserve	RW	0x1
15	IPG_1G_100M_10M_COMP_S_EN	Enable Giga/100M/10M port IPG compensation function. 0b0: disable 0b1: enable	RW	0x1
14	IPG_1G_100M_10M_SEL	Select Giga/100M/10M port IPG compensation function. Change some 96-bit time IPG to 88-bit time IPG. 0b0: 65ppm compensation. 0b1: 90ppm compensation.	RW	0x0
13	IPG_10G_COMPS_EN	Enable 10G port IPG compensation function. 0b0: disable 0b1: enable	RW	0x1
12	IPG_10G_SEL	Select 10G port IPG compensation function. Change some 96-bit time IPG to 88-bit time IPG. 0b0: 65ppm compensation. 0b1: 90ppm compensation.	RW	0x0
11	IOL_LEN_ERR_EN	Enable IOL test function. Option to drop the packets with error length value in LENGTH field. 0b0= Disable IOL mode of maximum packet length with error value discard. 0b1=Enable IOL mode of MAC function test.Packets with error length value should be discarded.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
10	IOL_MAX_LEN_EN	<p>Enable IOL test function. Option to check maximum packet length for untagged packet in 1518-Byte and tagged packet in 1522-Byte.</p> <p>0b0= Disable IOL mode to meet max length require.</p> <p>0b1= Enable IOL MAC function test mode, maximum packet length without VLAN tag should be 1518. And maximum packet length with VLAN tag should be 1522.</p> <p>Note: "MAX_LEN_100M_10M_SEL" and "MAX_LEN_1G_2P5G_5G_10G_SEL" are ineffective when IOL_MAX_LEN_EN=1.</p>	RW	0x0
9:5	LIMIT_IPG_CFG_1G_2P5_G	Limit ipg+preamble size for RX when link speed is 1G or 2.5G, Min value is 10. Default : 10 bytes.	RW	0x0A
4:0	LIMIT_IPG_CFG_10M_100M	Limit ipg+preamble size for RX when link speed is 10M/100M, Min value is 10. Default : 10 bytes.	RW	0x0A

## MAC\_L2\_GLOBAL\_CTRL2

REGISTER ADDRESS : 0xBB001358

DEFAULT VALUE : 0xE208108

Specify MAC L2 Misc global control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:27	RESERVED			
26	IFU_PATCH_EN	<p>Enable iA patch updated.</p> <p>Pin strapping</p> <p>0b0: Disable</p> <p>0b1: Enable</p>	RW	0x1
25	HW_RESET_EN	<p>Triggering hardware reset signal or not before booting from SPI flash.</p> <p>Pin strapping</p> <p>0b0: Do not trigger hardware reset signal</p> <p>0b1: Trigger hardware reset signal</p>	RW	0x1
24	DDR_DATA_BUS_WIDTH_SEL	<p>Select DDR data buswidth.</p> <p>Pin strapping</p> <p>0b0: DQ16</p> <p>0b1: DQ32</p>	RW	0x0
23	BYPASS_OC0_PLL	<p>Bypass SoC clock and input from RO</p> <p>PAD_EXTAL_CLK pin.</p> <p>Pin strapping</p> <p>0b0: use PLL</p> <p>0b1: bypass PLL</p>	RO	0x0
22	BYPASS_INITIAL	<p>Bypassing initial step.</p> <p>Pin strapping</p> <p>0b0: Normal initial step</p> <p>0b1: Bypass initial step</p>	RO	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
21	RST_CMD_DIS	Indicated whether issuing 0x66, 0x99 SPI flash commands or not when before booting from SPI flash. Pin strapping 0b0: Issue 0x66, 0x99 SPI flash commands. 0b1: Issue no reset command.	RW	0x1
20:19	RESERVED			
18	OUTPUT_125M_SEL	Select GPIO[28] or 125M output  0b0: GPIO[28] 0b1: 125MHz output for Serdes Converter PTP used	RW	0x0
17	SMI_SET3_SEL	Select GPIO or SMI_SET3 function GPIO[29]=>SMI_SET3_MDC GPIO[30]=>SMI_SET3_MDIO  0b0: GPIO 0b1: SMI_SET3	RW	0x0
16	LED_SYNC_SEL	Select GPIO[31] or LED_SYNC function  0b0: GPIO[31] 0b1: LED_SYNC	RW	0x0
15	JTAG_SEL	Select GPIO or E-JTAG function GPIO[3]=>JTAG_TMS GPIO[4]=>JTAG_TCK GPIO[5]=>JTAG_TRST# GPIO[6]=>JTAG_TDI GPIO[7]=>JTAG_TDO  0b0: GPIO 0b1: E-JTAG	RW	0x1
14:13	SYS_CLK_SEL	Select system clock. 0x0: 650MHz 0x1: 325MHz 0x2: 175MHz only for 48FE+4G mode 0x3: Reserved	RW	0x0
12	SET3_MDC_EN	Enable SET3 MDC clock ouput. 0b0: disable SET3 MDC clock output.(Default) 0b1: enable SET3 MDC clock output.	RW	0x0
11	SET2_MDC_EN	Enable SET2 MDC clock ouput. 0b0: disable SET2 MDC clock output.(Default) 0b1: enable SET2 MDC clock output.	RW	0x0
10	SET1_MDC_EN	Enable SET1 MDC clock ouput. 0b0: disable SET1 MDC clock output.(Default) 0b1: enable SET1 MDC clock output.	RW	0x0
9	SET0_MDC_EN	Enable SET0 MDC clock ouput. 0b0: disable SET0 MDC clock output.(Default) 0b1: enable SET0 MDC clock output.	RW	0x0
8	SYS_LED_EN	When enable System LED, the GPIO[0] change to system LED pin. 0b0: Disable system LED 0b1: Enable system LED	RW	0x1
7	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
6	BOOT_SEL	Boot selection Pin strapping 0b0: SPI-NOR 0b1: ROM (SPL-NAND)	RW	0x0
5:4	DDR_TYPE_SEL	DDR type selection. Pin strapping 0x0: DDR3 0x1: DDR4 0x2: LPDDR3 0x3: Reserved	RW	0x0
3	POR_SEL	Select BG PORcircuit. Pin strapping 0b0: Old BG POR 0b1: New BG POR	RO	0x1
2	EXT_CPU_EN	Enable external CPU. Pin strapping 0b0: Disable 0b1: Enable	RW	0x0
1	REG_IF_SEL	Select switch register access interface. Pin strapping 0b0: I2C Slave interface 0b1: SPI Slave interface	RW	0x0
0	RESERVED			

## MAC\_L2\_PORT\_CTRL

BASE ADDRESS : 0xBB006000

PORT INDEX : 0 - 56

PORT OFFSET : 0x80

DEFAULT VALUE : 0x193

This is a One-Dimension Port Register Array.

Specify MAC L2 Misc per port control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:22	PER_PORT_MAC_ECO	PER_PORT_MAC ECO register	RW	0x0
21	STK_1G_PKT_FMT	This option is valid for stacking port only. Configure 1G stacking port packet format as follows 0x1: 11 bytes IPG (Line Rate) + 3 bytes PREAMBLE + 1 byte SFD + Payload + 1 byte CRC 0x0: 6 bytes IPG (Line Rate) + 1 byte PREAMBLE + 1 byte SFD + Payload + 1 byte CRC	RW	0x0
20:5	TX_IPG	Configure TX line rate IPG, the permitted range is 0x5 to 0xFFFF	RW	0x000C
4	PADDING_UND_SIZE_EN	Enable padding the packet length to 64-Byte for under size packets. 0b0: disable. 0b1: enable.	RW	0x1

Bits	Field	Description	Type	Default
3	PASS_ALL_MODE_EN	Pass all packets mode exclude crc error packet. Other normal and error packets will be forwarded, exclude the PAUSE frame/invalid pause frame/known opcode frame. 0b0: disable 0b1: enable	RW	0x0
2	BYP_TX_CRC	if pkt is modified, TX recalculating CRC is bypass. 0b0:disable 0b1:enable	RW	0x0
1	TX_EN	Enable MAC port TX. 0b0: disable MAC Port TX. 0b1: enable MAC Port TX.	RW	0x1
0	RX_EN	Enable MAC port RX. 0b0: disable MAC Port RX. 0b1: enable MAC Port RX.	RW	0x1

### MAC\_L2\_PADDING\_SEL

REGISTER ADDRESS : 0xBB00554C

DEFAULT VALUE : 0x0

Under size packets padding content register.

Bits	Field	Description	Type	Default
31:8	RESERVED			
7:0	PADDING_SEL	The padding content when packet length less than 64 bytes after tag removed.	RW	0x0

### MAC\_L2\_ADDR\_CTRL

REGISTER ADDRESS : 0xBB00135C

DEFAULT VALUE : 0x4C000000, 0xE0

Specify the switch MAC address.

Bits	Field	Description	Type	Default
63:48	RESERVED			
47:32	SW_MAC_ADDR_47_32	Switch MAC address byte 5,4.	RW	0x00E0
31:0	SW_MAC_ADDR_31_0	Switch MAC address byte 3,2,1,0. The switch MAC address used in following situation: 1. When DA of packet is switch MAC address and EtherType=0x8808, Op=0x0001, this packet should be treated as pause frame. 2. When switch send out pause frame, the SA of pause frame should be switch MAC address.	RW	0x4C000000

## MAC\_L2\_CPU\_CRC

REGISTER ADDRESS : 0xBB00485C

DEFAULT VALUE : 0x1

Recalculate packet's CRC when transmitting if packet is received from cpu port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:2	RESERVED			
1:0	CRC_CPU_RC_EN	Enable egress port CRC recalculate if packet's source port is cpu port 0x0: disable CRC recalculate 0x1: enable CRC recalculate 0x2: CRC recalculate only for packets modified by ASIC. (If packets' content are not be modified, system doesn't recalculate CRC). 0x3: Reserved	RW	0x1

## SMI\_INDRT\_ACCESS\_CTRL\_0

REGISTER ADDRESS : 0xBB000C00

DEFAULT VALUE : 0xFFFF800

SMI indirect access control.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	ACCESS	0: mdx 1: inband	RW	0x0
23:11	MAIN_PAGE	Select main page number to access. 0x00: main page 0 0x01: main page 1 0x02: main page 2 : 0x1FFF: do not change.	RW	0x1FFF
10:6	REG	Select register number to access. 0x0: register 0 0x1: register 1 0x2: register 2 : 0x1F: register 31	RW	0x0
5	BROADCAST	Enable broadcast for PHY registers write command. 0b0: disable 0b1: enable	RW	0x0
4	RWOP	Read/write operation. 0b0: Read 0b1: Write	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:2	TYPE	Select PHY register type to access. 0x0: 1G normal register 0x1: 1G MMD register 0x2: 10G MMD register 0x3: reserved  Note: 1. If select 1G and 10G MMD register type, registers EXT PAGE, MAIN PAGE and REG settings are don't care.	RW	0x0
1	FAIL	PHY access fail. 0b0: normal 0b1: fail  Note: 1. It will auto clear to 0 when next command trigger, or write 0b1 to clear this bit.	RW1C	0x0
0	CMD	Request ASIC to access PHY MII Reg. 0b0: Normal (finish) 0b1: Execute  Note: When ASIC completes access, it will clear this bit.	RWAC	0x0

### **SMI\_INDRT\_ACCESS\_CTRL\_1**

REGISTER ADDRESS : 0xBB000C04

DEFAULT VALUE : 0x1FF

Extend page control for SMI indirect access.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:9	RESERVED			
8:0	EXT_PAGE	Select extension page number to access. 0x000: page 0 0x001: page 1 0x002: page 2 : 0x1FF: do not change.	RW	0x1FF

### **SMI\_INDRT\_ACCESS\_CTRL\_2**

BASE ADDRESS : 0xBB000C08

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Select port mask for indirect write command.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	PORT_MSK	Bitmask for port#0~port#51 selection. (not PHY ID) 0b0: disable 0b1: enable Note: This register is only for indirect write command.	RW	0x0

### **SMI\_INDRT\_ACCESS\_CTRL\_3**

REGISTER ADDRESS : 0xBB000C10

DEFAULT VALUE : 0x0

Port id selection for read & data for read/write control.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	READ_DATA	Data content for read/write access.	RO	0x0
15:0	WRTIE_DATA	Data content for read/write access.	RW	0x0

### **SMI\_INDRT\_ACCESS\_BC\_PHYID\_CTRL**

REGISTER ADDRESS : 0xBB000C14

DEFAULT VALUE : 0x1F

Select broadcast PHY ID for indirect access.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:11	RESERVED			
10:5	PORT_ID	Select port id for indirect read access.	RW	0x0
4:0	BC_PHYID	Select broadcast PHY-ID. Note: This register is only for broadcast PHY write command.(BROADCAST='1' and RWOP='1')	RW	0x1F

### **SMI\_INDRT\_ACCESS\_MMD\_CTRL**

REGISTER ADDRESS : 0xBB000C18

DEFAULT VALUE : 0x0

Select MMD device address and register for indirect access.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20:16	MMD_DEVAD	MMD device address.	RW	0x0

Bits	Field	Description	Type	Default
15:0	MMD_REG	MMD register number.	RW	0x0

## MAC\_GLB\_CTRL

REGISTER ADDRESS : 0xBB005550

DEFAULT VALUE : 0x2312

Specify the system-wise control register in the MAC.

Bits	Field	Description	Type	Default
31:14	RESERVED			
13	MAC_48PASS1_DROP_EN	48 pass one MAC drop function enable.  0b0: disable 0b1: enable  Note: If 48PASS1_MAC_DROP_EN=0b0, per port flow control ability should be force disable.	RW	0x1
12	ORIGINAL_CRS	Select original CRS signal. 0b0: CRS come from RXDV 0b1: Original CRS	RW	0x0
11	ORIGINAL_COL	Select original COL signal. 0b0: COL come from RXDV&TXEN 0b1: Original COL	RW	0x0
10	DEFER_PKT_CONT_SEL	0b0: 0x55 0b1: 0xaa	RW	0x0
9	MAX_RETX_SEL	The late collision max retransmission threshold selection.  0b0: 95-bytes 0b1: 110-bytes	RW	0x1
8	LATE_COLI_DROP_EN	Enable IOL test function. Option to drop or re-transmit the packets with late collision.  0b0: When late collision occurs, stop transmitting current packet and re-transmit the packet. 0b1: When late collision occurs, stop transmitting current packet and drop the packet	RW	0x1
7	IOL_MAX_RETRY_EN	Enable IOL test function. Option to drop the packet after consecutive 16 collisions.  0b0: Disable IOL MAC function test mode, always retry after consecutive 16 collision. 0b1: Enable IOL MAC function test mode, drop the packet after consecutive 16 collision.	RW	0x0
6	BKOFF_SPDUP	Speed up backoff 0b0: Normal backoff in 512 bit time. 0b1: Speed up backoff to 8 bit time.	RW	0x0

Bits	Field	Description	Type	Default
5:4	BKOFF_SEL	Select the back off time: 0x0: always Normal backoff, backofflimit = 9; 0x1: Enable Aggressive backoff during back pressure backoffLimit=3;normal back off, backofflimit=9 0x2: Enable Aggressive backoff during back pressure backoffLimit=3;normal backoff, backofflimit=6 0x3: IOL mode, always Normal backoff, backoffLimit= 10. Note: jam mode and defer mod both support aggressive back-off.	RW	0x1
3	HALF_48PASS1_EN	Enable 48 Pass 1 function in half duplex. 0b0: disable. 0b1: enable. Note: 48 include normal collision and force collision.	RW	0x0
2	BKPRES_MTHD_SEL	Select half duplex backpressure method. 0b0: Collision-based backpressure (Jam mode). 0b1: Carrier-based backpressure (Defer mode).	RW	0x0
1:0	DEFER_IPG_SEL	Select Defer IPG when half duplex backpressure. 0x0: 56 bit time. 0x1: 64 bit time. 0x2: 88 bit time. 0x3: 96 bit time.	RW	0x2

## MAC\_PORT\_CTRL

BASE ADDRESS : 0xBB006004

PORT INDEX : 0 - 56

PORT OFFSET : 0x80

DEFAULT VALUE : 0x3

This is a One-Dimension Port Register Array.

Specify per port control register in MAC.

Bits	Field	Description	Type	Default
31:5	RESERVED			
4:3	PRECOLLAT_SEL	Threshold of PortRx Search to solve Late Collision Issue. Use following setting as start position to collide: 0x0: Only from the start of CRS; 0x1: From the start of Preamble to 20 bytes; 0x2: From the first bytes after SFD(0xD5) to 20 bytes; 0x3: Reserved	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:1	LATE_COLI_THR	<p>Late collision threshold. The ASIC depend on this threshold to decide late collision occurrence.</p> <p>0x0: 64 bytes  0x1: 72 bytes  0x2: 88 bytes  0x3: 104 bytes</p> <p>Note: 64-bytes include 8-bytes preamble (8+56=64-bytes)</p> <p>Note: If LATE_COLI_THR set to 0(64Byte), it means collision at 64-th byte is late collision.</p>	RW	0x1
0	BKPRES_EN	<p>Enable the half duplex Backpressure.</p> <p>0b0: disable.  0b1: enable.</p>	RW	0x1

### **MAC\_CPU\_TAG\_ID\_CTRL**

REGISTER ADDRESS : 0xBB001364

DEFAULT VALUE : 0x8899

Specify the CPU tag ID.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	CPU_TAG_ID	CPU tag ID	RW	0x8899

### **HALF\_CHG\_CTRL**

BASE ADDRESS : 0xBB006008

PORT INDEX : 0 - 55

PORT OFFSET : 0x80

DEFAULT VALUE : 0x400

This is a One-Dimension Port Register Array.

Specify whether and how half duplex mode auto switch to full duplex mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13	HALF_TO_FULL_PAUSE	Select tx/rx pause ability,when force change to full duplex mode.  0b0: disable 0b1: enable	RW	0x0
12	REF_RX_CONGEST	When change to full duplex, refer to RXDV 0b0: not refer to RXDV, only refer to TXEN 0b1: refer RXDV and TXEN(just only count normal collision)	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
11:7	CHG_DUP_THR	Change half duplex to full duplex collision threshold.  Note: Can not be set to 0, it is recommended to set the minimum value of 8	RW	0x08
6	FULL_DET_EN	When link partner in force full mode, RTL9310 can detect whether change from half duplex to full duplex mode. 0b0: disable; 0b1: enable.	RW	0x0
5	MAC_CHG_DUP	Indicate that MAC force change to full duplex mode. 0b0: not force change to full duplex; 0b1: force change to full duplex (self cleared when link status changed).	RO	0x0
4:0	COL_CUR_CNT	Current collision counter.	RO	0x0

### **SMI\_PORT\_ADDR\_CTRL**

BASE ADDRESS : 0xBB000C74

PORT INDEX : 0 - 55

PORT OFFSET : 5 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (5 bits per field)

Specify the PHY Address of port n.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
4:0	PORT_ADDR	Port n SMI Address. Software patch by model application.	RW	0x0

### **SMI\_PORT\_POLLING\_SEL**

BASE ADDRESS : 0xBB000C9C

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Specify the POLLING SELECT of portn.

Bits	Field	Description	Type	Default
1:0	PORT_POLLING_SEL	Port nSMI polling set select 0x0: SMI SET 0 0x1: SMI SET 1 0x2: SMI SET 2 0x3: SMI SET 3 Default Value port0~port23 is mapping to SMI SET 0; port24~port47 is mapping to SMI SET 1; port48~port55 is mapping to SMI SET 2.	RW	0x0

### **SMI\_PHY\_ABLTY\_GET\_SEL**

BASE ADDRESS : 0xBB000CAC

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Specify per-port PHY Ability get select

Bits	Field	Description	Type	Default
1:0	PHY_ABLTY_GET_SEL	Select how MAC gets PHY ability. 0x0: outband MDC/MDIO polling 0x1: inband serdes polling 0x2: serdes ability bus 0x3: reserved Note: 10G copper always use outband MDC/MDIO polling	RW	0x0

### **SMI\_GLB\_CTRL1**

REGISTER ADDRESS : 0xBB000CBC

DEFAULT VALUE : 0x40000

Specif SMI Control Register

Bits	Field	Description	Type	Default
31:19	RESERVED			
18	SMI_GATING_SEL	When polling PHY link's register data is 0xFFFF, select link status update or not. 0b0: Disable, it means update link status. 0b1: Enable, it means does not update link status.	RW	0x1
17	SMI_SET3_SMPLEDG_SEL	Select clock posedge or negedge to sample data. 0b0: Rising Edge 0b1: Falling Edge	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
16	SMI_SET2_SMPLEDG_SEL	Select clock posedge or negedge to sample data. 0b0: Rising Edge 0b1: Falling Edge	RW	0x0
15	SMI_SET1_SMPLEDG_SEL	Select clock posedge or negedge to sample data. 0b0: Rising Edge 0b1: Falling Edge	RW	0x0
14	SMI_SET0_SMPLEDG_SEL	Select clock posedge or negedge to sample data. 0b0: Rising Edge 0b1: Falling Edge	RW	0x0
13	ABORT_ACT	Abort define is 1. Mdc/mdio no turn around cycle 2. Sds inband timeout) 0b0: not keep current link status when abort 0b1: keep current link status when abort	RW	0x0
12	TMOUT_ACT	0b0: not keep current link status when timeout 0b1: keep current link status when timeout	RW	0x0
11	SMI_SET3_WRITE_PHY_P AGE0	Select SMI SET3 whether write-in 0x0 at PHY Reg31 at polling procedure. 0b0: do not write-in 0x0 at PHY Reg31. 0b1: write-in 0x0 at PHY Reg31. Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
10	SMI_SET2_WRITE_PHY_P AGE0	Select SMI SET2 whether write-in 0x0 at PHY Reg31 at polling procedure. 0b0: do not write-in 0x0 at PHY Reg31. 0b1: write-in 0x0 at PHY Reg31. Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
9	SMI_SET1_WRITE_PHY_P AGE0	Select SMI SET1 whether write-in 0x0 at PHY Reg31 at polling procedure. 0b0: do not write-in 0x0 at PHY Reg31. 0b1: write-in 0x0 at PHY Reg31. Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
8	SMI_SET0_WRITE_PHY_P AGE0	Select SMI SET0 whether write-in 0x0 at PHY Reg31 at polling procedure. 0b0: do not write-in 0x0 at PHY Reg31. 0b1: write-in 0x0 at PHY Reg31. Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
7:6	SMI_SET3_FMT_SEL	Select SET3 of SMI polling format 0x0: Clause 22 standard reg.MDIO format 0x1: Clause 22 proprietary reg. MDIO format 0x2: Clause 45 standard MDIO format 0x3: Clause 45 proprietary MDIO format Note:When a SMI SET polling format is selected , it can not polling alternate between Clause 22 and Clause 45 format at the same SMI set.	RW	0x0
5:4	SMI_SET2_FMT_SEL	Select SET2 of SMI polling format 0x0: Clause 22 standard reg.MDIO format 0x1: Clause 22 proprietary reg. MDIO format 0x2: Clause 45 standard MDIO format 0x3: Clause 45 proprietary MDIO format Note:When a SMI SET polling format is selected , it can not polling alternate between Clause 22 and Clause 45 format at the same SMI set.	RW	0x0

Bits	Field	Description	Type	Default
3:2	SMI_SET1_FMT_SEL	Select SET1 of SMI polling format 0x0: Clause 22 standard reg.MDIO format 0x1: Clause 22 proprietary reg. MDIO format 0x2: Clause 45 standard MDIO format 0x3: Clause 45 proprietary MDIO format Note:When a SMI SET polling format is selected , it can not polling alternate between Clause 22 and Clause 45 format at the same SMI set.	RW	0x0
1:0	SMI_SET0_FMT_SEL	Select SET0 of SMI polling format 0x0: Clause 22 standard reg.MDIO format 0x1: Clause 22 proprietary reg. MDIO format 0x2: Clause 45 standard MDIO format 0x3: Clause 45 proprietary MDIO format Note:When a SMI SET polling format is selected , it can not polling alternate between Clause 22 and Clause 45 format at the same SMI set.	RW	0x0

## SMI\_GLB\_CTRL0

REGISTER ADDRESS : 0xBB000CC0

DEFAULT VALUE : 0x550

Specif SMI Control Register

Bits	Field	Description	Type	Default
31:28	RESERVED			
27	SMI_SET3_PRVTE1_POLLING	Select whether to polling 10G proprietary1 related registers for SET3 SMI.. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: For SMI_SETx_FMT_SEL=0x2 or 0x3, clause 45 format only.	RW	0x0
26	SMI_SET2_PRVTE1_POLLING	Select whether to polling 10G proprietary1 related registers for SET2 SMI.. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: For SMI_SETx_FMT_SEL=0x2 or 0x3, clause 45 format only.	RW	0x0
25	SMI_SET1_PRVTE1_POLLING	Select whether to polling 10G proprietary1 related registers for SET1 SMI.. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: For SMI_SETx_FMT_SEL=0x2 or 0x3, clause 45 format only.	RW	0x0
24	SMI_SET0_PRVTE1_POLLING	Select whether to polling 10G proprietary1 related registers for SET0 SMI.. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: For SMI_SETx_FMT_SEL=0x2 or 0x3, clause 45 format only.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23	SMI_SET3_PRVTE0_POLLING	Select whether to polling proprietary related registers for SET3 SMI. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: SMI_SETx_FMT_SEL=0x2 or 0x3,clause 45 format, polling 1.25G, 2.5G and 5G.	RW	0x0
22	SMI_SET2_PRVTE0_POLLING	Select whether to polling proprietary related registers for SET2 SMI. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: SMI_SETx_FMT_SEL=0x2 or 0x3,clause 45 format, polling 1.25G, 2.5G and 5G.	RW	0x0
21	SMI_SET1_PRVTE0_POLLING	Select whether to polling proprietary related registers for SET1 SMI. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: SMI_SETx_FMT_SEL=0x2 or 0x3,clause 45 format, polling 1.25G, 2.5G and 5G.	RW	0x0
20	SMI_SET0_PRVTE0_POLLING	Select whether to polling proprietary related registers for SET0 SMI. 0b0: not polling proprietary related register; 0b1: polling proprietary related registers; Note: SMI_SETx_FMT_SEL=0x2 or 0x3,clause 45 format, polling 1.25G, 2.5G and 5G.	RW	0x0
19	SMI_SET3_POLLING_PAR_K_SEL	Select SMI SET3 whether park page at polling procedure. 0b0: do not park; 0b1: park; Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
18	SMI_SET2_POLLING_PAR_K_SEL	Select SMI SET2 whether park page at polling procedure. 0b0: do not park; 0b1: park; Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
17	SMI_SET1_POLLING_PAR_K_SEL	Select SMI SET1 whether park page at polling procedure. 0b0: do not park; 0b1: park; Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
16	SMI_SET0_POLLING_PAR_K_SEL	Select SMI SET0 whether park page at polling procedure. 0b0: do not park; 0b1: park; Note: valid for FEPHY and GPHY, not valid for 10G PHY. (Hardware support)	RW	0x0
15:12	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
11:10	SMI_SET3_FREQ_SEL	Select MDC period to PHY register polling for GPHY SMI interface. 0x0: 800ns(1.25MHz); 0x1: 400ns(2.5MHz); 0x2: 200ns(5MHz); 0x3: RESERVED	RW	0x1
9:8	SMI_SET2_FREQ_SEL	Select MDC period to PHY register polling for GPHY SMI interface. 0x0: 800ns(1.25MHz); 0x1: 400ns(2.5MHz); 0x2: 200ns(5MHz); 0x3: RESERVED	RW	0x1
7:6	SMI_SET1_FREQ_SEL	Select MDC period to PHY register polling for GPHY SMI interface. 0x0: 800ns(1.25MHz); 0x1: 400ns(2.5MHz); 0x2: 200ns(5MHz); 0x3: RESERVED	RW	0x1
5:4	SMI_SET0_FREQ_SEL	Select MDC period to PHY register polling for GPHY SMI interface. 0x0: 800ns(1.25MHz); 0x1: 400ns(2.5MHz); 0x2: 200ns(5MHz); 0x3: RESERVED	RW	0x1
3	SMI_SET3_PREAMBLE_SEL	Select MDIO preamble bits for SMI interface: 0b0: 32bits, each cmd need 32bits preamble; 0b1: 1bit, the first cmd of the cmd sequence that accesses one PHY must be 32bits preamble, the others cmds only need 1bit preamble.	RW	0x0
2	SMI_SET2_PREAMBLE_SEL	Select MDIO preamble bits for SMI interface: 0b0: 32bits, each cmd need 32bits preamble; 0b1: 1bit, the first cmd of the cmd sequence that accesses one PHY must be 32bits preamble, the others cmds only need 1bit preamble.	RW	0x0
1	SMI_SET1_PREAMBLE_SEL	Select MDIO preamble bits for SMI interface: 0b0: 32bits, each cmd need 32bits preamble; 0b1: 1bit, the first cmd of the cmd sequence that accesses one PHY must be 32bits preamble, the others cmds only need 1bit preamble.	RW	0x0
0	SMI_SET0_PREAMBLE_SEL	Select MDIO preamble bits for SMI interface: 0b0: 32bits, each cmd need 32bits preamble; 0b1: 1bit, the first cmd of the cmd sequence that accesses one PHY must be 32bits preamble, the others cmds only need 1bit preamble.	RW	0x0

### **SMI\_BYPASS\_ABILITY\_LOCK\_CTRL**

BASE ADDRESS : 0xBB000CC4

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Select Bypass MAC ability IP.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	BYPASS_ABLTY_LOCK	Bypass MAC ability IP (for debug MAC ability IP).  0b0: No bypass 0b1: Bypass MAC ability IP	RW	0x0

## **SMI\_PORT\_POLLING\_CTRL**

BASE ADDRESS : 0xBB000CCC

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Select PHY for SMI polling.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	SMI_POLLING_PMSK	Enable polling PHY for the 4 set of SMI bus.  0b0: disable 0b1: enable  Note: 1. SMI doesn't polling external CPU port, it was configured by MAC force mode. 2. The default value depend on the chip mode.	RW	0x0

## **LINK\_DELAY\_CTRL**

REGISTER ADDRESS : 0xBB000CD8

DEFAULT VALUE : 0x56

Specify delay update link up status fot tx protection.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18:17	DBG_SEL_ABLTY_RSL		RW	0x0
16	LNKDN_FRC_DIS		RW	0x0
15:8	LINK_DOWN_TIME		RW	0x0
7	LINK_DOWN_TIME_EN		RW	0x0
6	LINKUP_DELAY_EN		RW	0x1

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:4	LINKUP_DELAY_10G_5G	The delay time between MAC get PHY link up info to enable packet Rx/Tx when speed is 10G. 0x0: No delay 0x1: 1ms 0x2: 5ms 0x3: 10ms	RW	0x1
3:2	LINKUP_DELAY_2P5G_100M_100M	The delay time between MAC get PHY link up info to enable packet Rx/Tx when speed is 2.5G, 1000M or 100M. 0x0: No delay 0x1: 10ms (default) 0x2: 20ms 0x3: 30ms	RW	0x1
1:0	LINKUP_DELAY_10M	The delay time between MAC get PHY link up info to enable packet Rx/Tx when speed is 10M. 0x0: 50ms 0x1: 150ms 0x2: 250ms 0x3: 350ms others: no delay	RW	0x2

### **MDIO\_FREE\_CNT\_CTRL**

REGISTER ADDRESS : 0xBB000CDC

DEFAULT VALUE : 0x259

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13	MDIO_FREE_CNT_RDY	MDIO free counter delay ready. 0b0: Not ready 0b1: Ready	RO	0x0
12:1	MDIO_FREE_CNT_SEL	MDIO free counter selection. (unit is 100ns)	RW	0x12C
0	MDIO_FREE_CNT_EN	Enable MDIO free counter. 0b0: Disable 0b1: Enable	RW	0x1

### **SMI\_GPHY\_RLFD\_POLLING\_SEL**

REGISTER ADDRESS : 0xBB000CE0

DEFAULT VALUE : 0xA470015

Specify the page address and reg address that polling G-PHY RLFDregister.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RLFD_PAGE_POLLING_GP HY	Select the page address that RTL9310 Polling G-PHY RLFD register.	RW	0x0A47
15:0	RLFD_REG_POLLING_GPHY	Select the register address that RTL9310 Polling G-PHY RLFD register.	RW	0x0015

Bits	Field	Description	Type	Default
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### **SMI\_RLFD\_POLLING\_BIT\_SEL**

REGISTER ADDRESS : 0xBB000CE4

DEFAULT VALUE : 0x0

Specify the bit address that RLFD polling for GPHY and 10G PHY RLFD register.

Bits	Field	Description	Type	Default
31:8	RESERVED			
7:4	RLFD_BIT_POLLING_10G PHY	Select the bit address that RTL9310 Polling 10G-PHY RLFD register. Note:0x0 means bit0, 0xf means bit15.	RW	0x0
3:0	RLFD_BIT_POLLING_GPHY	Select the bit address that RTL9310 Polling G-PHY RLFD register. Note:0x0 means bit0, 0xf means bit15.	RW	0x0

### **SMI\_10GPHY\_RLFD\_POLLING\_SEL**

REGISTER ADDRESS : 0xBB000CE8

DEFAULT VALUE : 0x0

Specify the device address and reg address that RLFD polling 10G PHY RLFD register.

Bits	Field	Description	Type	Default
31:21	RESERVED			
20:16	RLFD_DEV_POLLING_10G PHY	Select the device address that RTL9310 Polling 10G-PHY RLFD register.	RW	0x0
15:0	RLFD_REG_POLLING_10G PHY	Select the register address that RTL9310 Polling 10G-PHY RLFD register.	RW	0x0

### **SMI\_GPHY\_POLLING\_SEL**

REGISTER ADDRESS : 0xBB000CEC

DEFAULT VALUE : 0x1A

Specify the page address and reg address that polling G-PHY proprietary register.

Bits	Field	Description	Type	Default
31:16	INTPAGE_POLLING_GPHY	Select the page address that RTL9310 Polling G-PHY proprietary register.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15:0	INTREG_POLLING_GPHY	Select the register address that RTL9310 Polling G-PHY proprietary register.	RW	0x001A

### **SMI\_10GPHY\_POLLING\_SEL0**

REGISTER ADDRESS : 0xBB000CF0

DEFAULT VALUE : 0x1FA434

Specify the device address and reg address that polling 10G PHY proprietary0 register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20:16	INTDEV0_POLLING_10GP HY	Select the device address that RTL9310 Polling 10G PHY internal resolution register.	RW	0x1F
15:0	INTREG0_POLLING_10GP HY	Select the register address that RTL9310 Polling 10G PHY internal resolution register.	RW	0xA434

### **SMI\_10GPHY\_POLLING\_SEL1**

REGISTER ADDRESS : 0xBB000CF4

DEFAULT VALUE : 0x1FA435

Specify the device address and reg address that polling 10G PHY proprietary1 register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20:16	INTDEV1_POLLING_10GP HY	Select the device address that RTL9310 Polling 10G PHY internal resolution register.	RW	0x1F
15:0	INTREG1_POLLING_10GP HY	Select the register address that RTL9310 Polling 10G PHY internal resolution register.	RW	0xA435

### **SMI\_10GPHY\_POLLING\_SEL2**

REGISTER ADDRESS : 0xBB000CF8

DEFAULT VALUE : 0x11FA400

Specify the device address , reg and bit address that polling 10G PHY register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
24:21	REG0_BIT_POLLING_10G PHY	Select the bit address that RTL9310 Polling 10G PHY local duplex ability register for PHY force mod.  Note:0x0 means bit0, 0xf means bit15	RW	0x8
20:16	REG0_DEV_POLLING_10G PHY	Select the device address that RTL9310 Polling 10G PHY local duplex ability register for PHY force mode.	RW	0x1F
15:0	REG0_REG_POLLING_10G PHY	Select the register address that RTL9310 Polling 10G PHY local duplex ability register for PHY force mod.	RW	0xA400

### **SMI\_10GPHY\_POLLING\_SEL3**

REGISTER ADDRESS : 0xBB000CFC

DEFAULT VALUE : 0x13FA412

Specify the device address , reg and bit address that polling 10G PHY register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24:21	REG9_BIT_POLLING_10G PHY	Select the bit address that RTL9310 Polling 10G PHY local 1000Mbps ability register.  Note:0x0 means bit0, 0xf means bit15	RW	0x9
20:16	REG9_DEV_POLLING_10G PHY	Select the device address that RTL9310 Polling 10G PHY local 1000Mbps ability register.	RW	0x1F
15:0	REG9_REG_POLLING_10G PHY	Select the register address that RTL9310 Polling 10G PHY local 1000Mbps ability register.	RW	0xA412

### **SMI\_10GPHY\_POLLING\_SEL4**

REGISTER ADDRESS : 0xBB000D00

DEFAULT VALUE : 0x17FA414

Specify the device address , reg and bit address that polling 10G PHY register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24:21	REG10_BIT_POLLING_10 GPHY	Select the bit address that RTL9310 Polling 10G PHY link partner 1000Mbps ability register.  Note:0x0 means bit0, 0xf means bit15.	RW	0xB
20:16	REG10_DEV_POLLING_10 GPHY	Select the device address that RTL9310 Polling 10G PHY link partner 1000Mbps ability register.	RW	0x1F
15:0	REG10_REG_POLLING_10 GPHY	Select the register address that RTL9310 Polling 10G PHY link partner 1000Mbps ability register.	RW	0xA414

## **MAC\_FORCE\_MODE\_CTRL**

**BASE ADDRESS :** 0xBB000DCC

**PORT INDEX :** 0 - 56

**PORT OFFSET :** 0x4

**DEFAULT VALUE :** 0x32800

This is a One-Dimension Port Register Array.

Specify per port link, speed, pause and duplex registers in MAC force mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:26	RESERVED			
25	SMI_FEFI_SEL	When SMI_FORCE_FEFI_EN=0b1, this bit indicates FEFI status of port. 0b0:Manual configure as Remote Fault indication from link partner has not been detected. 0b1:Manual configure as Remote Fault indication from link partner has been detected	RW	0x0
24	FORCE_MSTR_SLV	When SMI_FORCE_MSTR_SLV_EN=0b1, this bit indicates MASTER/ SLAVE status of port. 0b0:Manual configure as SLAVE 0b1:Manual configure as MASTER	RW	0x0
23	EEE_10G_EN	Enable EEE capability for 10GBASE-T. In normal mode(SMI_FORCE_EEE_EN=0b0), the MAC EEE enable state should also consider PHY EEE enable state. 10G MAC EEE is enabled only when EEE_10G_EN is enable and PHY EEE is enable. When SMI_FORCE_EEE_EN=0b1 (EEE force mode), this bit directly decides that if MAC 10G EEE is enable. This is per port configuration. 0b0:Disable 0b1:Enable	RW	0x0
22	EEE_5G_EN	Enable EEE capability for 5G. In normal mode(SMI_FORCE_EEE_EN=0b0), the MAC EEE enable state should also consider PHY EEE enable state. 5G MAC EEE is enabled only when EEE_5G_EN is enable and PHY EEE is enable. When SMI_FORCE_EEE_EN=0b1 (EEE force mode), this bit directly decides that if MAC 5G EEE is enable. This is per port configuration. 0b0:Disable 0b1:Enable	RW	0x0
21	EEE_2P5G_EN	Enable EEE capability for 2.5G. In normal mode (SMI_FORCE_EEE_EN=0b0), the MAC EEE enable state should also consider PHY EEE enable state. 2.5G MAC EEE is enabled only when EEE_2P5G_EN is enable and PHY EEE is enable. When SMI_FORCE_EEE_EN=0b1 (EEE force mode), this bit directly decides that if MAC 2.5G EEE is enable. This is per port configuration. 0b0:Disable 0b1:Enable	RW	0x0

Bits	Field	Description	Type	Default
20	EEE_1000M_EN	<p>Enable EEE capability for 1000BASE-T. In normal mode(SMI_FORCE_EEE_EN=0b0), the MAC EEE enable state should also consider PHY EEE enable state. Giga MAC EEE is enabled only when EEE_GIGA_EN is enable and PHY EEE is enable.</p> <p>When SMI_FORCE_EEE_EN=0b1 (EEE force mode), this bit directly decides that if MAC 1000M EEE is enable. This is per port configuration.</p> <p>0b0:Disable 0b1:Enable</p>	RW	0x0
19	RESERVED			
18	EEE_100M_EN	<p>Enable EEE capability for 100BASE-TX. In normal mode(SMI_FORCE_EEE_EN=0b0), the MAC EEE enable state should also consider PHY EEE enable state. 100M MAC EEE is enabled only when EEE_100M_EN is enable and PHY EEE is enable.</p> <p>When SMI_FORCE_EEE_EN=0b1 (EEE force mode), this bit directly decides that if MAC 100M EEE is enable. This is per port configuration.</p> <p>0b0:Disable 0b1:Enable</p>	RW	0x0
17	SMI_RX_PAUSE_EN	<p>When SMI_FORCE_FC_EN =0b1, this bit indicates RX pause status of port.</p> <p>0b0:Disable RX pause 0b1:Enable RX pause</p>	RW	0x1
16	SMI_TX_PAUSE_EN	<p>When SMI_FORCE_FC_EN=0b1, this bit indicates TX pause status of port.</p> <p>0b0:Disable TX pause 0b1:Enable TX pause</p>	RW	0x1
15:12	SMI_SPD_SEL	<p>When SMI_FORCE_SPD_EN=0b1, this bit indicates speed status of port.</p> <p>0x0:Port N is 10Mbps. 0x1:Port N is 100Mbps. 0x2:Port N is 1000Mbps. (default) 0x3:Reserved 0x4:Port N is 10Gbps. 0x5:Port N is 2.5Gbps. 0x6:Port N is 5Gbps. 0x7:Reserved 0x8:Reserved Others reserved.</p>	RW	0x2
11	SMI_DUP_SEL	<p>When SMI_FORCE_DUP_EN=0b1, this bit indicates duplex status of port.</p> <p>0b0:Half duplex. 0b1:Full duplex. (default)</p>	RW	0x1
10	SMI_FORCE_MEDIA	<p>When SMI_FORCE_MEDIA_EN=0b1, this bit indicates media status of port.</p> <p>0b0:copper. 0b1:fiber.</p>	RW	0x0
9	SMI_FORCE_LINK	<p>When SMI_FORCE_LINK_EN=0b1, this bit indicates link status of port.</p> <p>0b0:link down. (default) 0b1:link up.</p>	RW	0x0
8	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
7	SMI_FORCE_FEFI_EN	Enable the FEFI force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in FEFI force mode.	RW	0x0
6	SMI_FORCE_MSTR_SLV_E_N	Enable the Master/Slave force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in Master/Slave force mode.	RW	0x0
5	SMI_FORCE_EEE_EN	Enable the EEE force mode. 0b0:Port N is in PHY NWay mode or PHY force mode (EEE normal mode). (default) 0b1:Port N is in EEE force mode(EEE force mode).	RW	0x0
4	SMI_FORCE_FC_EN	Enable the flow control force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in flow control force mode.	RW	0x0
3	SMI_FORCE_SPD_EN	Enable the speed force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in speed force mode.	RW	0x0
2	SMI_FORCE_DUP_EN	Enable the duplex force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in duplex force mode.	RW	0x0
1	SMI_FORCE_MEDIA_EN	Enable the media force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in media force mode.	RW	0x0
0	SMI_FORCE_LINK_EN	Enable the link force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in link force mode.	RW	0x0

## MAC\_RLFD\_EN

BASE ADDRESS : 0xBB000EB0  
 PORT INDEX : 0 - 55  
 PORT OFFSET : 1 bit  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port RLFD enable control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RLFD_EN	Select whether process link fault detection. 0b0:do not process Rapid Link Fault Detection; 0b1: process Rapid Link Fault Detection;	RW	0x0

## MAC\_RLFD\_STS

**BASE ADDRESS :** 0xBB000EB8  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port RLFD status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RLFD_STS	Link fault detection status polling from phy. 0b0: link fault has not been detected; 0b1: link fault been detected.	RC	0x0

## MAC\_LINK\_STS

**BASE ADDRESS :** 0xBB000EC0  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port link status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	LINK_STS	Link status of port n.(n=0~55 for normal port, n=56 for cpu port) 0b0: link down. 0b1: link up.  Note: When link status change, this register must latch the last time link down status (latch low), that must read register twice to get actual value.	RC	0x0

## MAC\_LINK\_MEDIA\_STS

**BASE ADDRESS :** 0xBB000EC8  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port link on media status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	MEDIA_STS	Link on media status of port n.(n=0~55 for normal port, n=56 for cpu port) 0b0: copper. 0b1: fiber.	RO	0x0

## MAC\_LINK\_SPD\_STS

BASE ADDRESS : 0xBB000ED0  
PORT INDEX : 0 - 56  
PORT OFFSET : 4 bits  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (4 bits per field)

The port link on speed status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:0	SPD_STS	0x0:Port N is 10Mbps. 0x1:Port N is 100Mbps. 0x2:Port N is 1000Mbps. (default) 0x3:Reserved 0x4:Port N is 10Gbps. 0x5:Port N is 2.5Gbps. 0x6:Port N is 5Gbps. 0x7:Reserved 0x8:Reserved Others reserved.	RO	0x0

## MAC\_LINK\_DUP\_STS

BASE ADDRESS : 0xBB000EF0  
PORT INDEX : 0 - 56  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port link on duplex status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	DUP_STS	Link on duplex status of port n.(n=0~55 for normal port, n=56 for cpu port) 0b0: half duplex 0b1: full duplex	RO	0x0

## MAC\_TX\_PAUSE\_STS

**BASE ADDRESS :** 0xBB000EF8  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port TX pause status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	TX_PAUSE_STS	Link on TX pause status of port n.(n=0~55 for normal port, n=56 for cpu port) 0b0: Port N does not have TX pause ability. 0b1: Port N has TX pause ability.	RO	0x0

### **MAC\_RX\_PAUSE\_STS**

**BASE ADDRESS :** 0xBB000F00  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port RX pause status register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RX_PAUSE_STS	Link on RX pause status of port n.(n=0~55 for normal port, n=56 for cpu port) 0b0: Port N does not have RX pause ability. 0b1: Port N has RX pause ability.	RO	0x0

### **MAC\_EEE\_ABLTY**

**BASE ADDRESS :** 0xBB000F08  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port EEE ability register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EEE_ABLTY	EEE ability of port n.(n=0~55 for normal port, n=56 for cpu port) 0b0: EEE Disable 0b1: EEE Enable	RO	0x0

## **MAC\_MSTR\_SLV\_STS**

**BASE ADDRESS :** 0xBB000F10

**PORT INDEX :** 0 - 56

**PORT OFFSET :** 1 bit

**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port master/slave configuration resolution.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	MSTR_SLV_STS	Master/Slave configuration resolution for port n.(n=0~55 for normal port, n=56 for cpu port) 0b1 = Local PHY configuration resolved to MASTER 0b0 = Local PHY configuration resolved to SLAVE	RO	0x0

## **FEFI\_STS**

**BASE ADDRESS :** 0xBB000F18

**PORT INDEX :** 0 - 56

**PORT OFFSET :** 1 bit

**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

The port FEFI manual configuration indication.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	FEFI_STS	Port FEFI status. Note: When FEFI occurs, this register must latch the last FEFI down status (latch high), that must read register twice to get actual value.	RC	0x0

## **FORCE\_MAC\_SPD**

**BASE ADDRESS :** 0xBB000F20

**PORT INDEX :** 0 - 56

**PORT OFFSET :** 1 bit

**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Enable force MAC speed mode.

Bits	Field	Description	Type	Default
0	FORCE_MAC_SPD	Enable MAC speed force mode. 0b0:Port N is in PHY NWay mode or PHY force mode. (default) 0b1:Port N is in speed force mode.	RW	0x0

## SPD\_FORCE\_VAL

BASE ADDRESS : 0xBB000F28

PORT INDEX : 0 - 56

PORT OFFSET : 4 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (4 bits per field)

Specify per port MAC speed force MAC speed.

Bits	Field	Description	Type	Default
3:0	SPD_FORCE_VAL	When FORCE_MAC_SPD=0b1, this bit indicates speed status of port. 0x0: Port N is 10Mbps. 0x1: Port N is 100Mbps. 0x2: Port N is 1000Mbps. (default) 0x3: Reserved 0x4: Port N is 10Gbps. 0x5: Port N is 2.5Gbps. 0x6: Port N is 5Gbps. 0x7: Reserved 0x8: Reserved Others reserved.	RW	0x0

## MAC\_L2\_PORT\_MAX\_LEN\_CTRL

BASE ADDRESS : 0xBB005554

PORT INDEX : 0 - 55

PORT OFFSET : 0x4

DEFAULT VALUE : 0xC003000

This is a One-Dimension Port Register Array.

Specify the switch MAC can rx the maxlen of packet when speed is 10G and 1G

Bits	Field	Description	Type	Default
31:29	RESERVED			
28	MAX_LEN_TAG_INC	MAXLEN include the tag in RX 0b0: tag will not counted into the packet length to compare with the MAXLEN 0b1: tag will counted into the packet length to compare with the MAXLEN	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
27:14	MAX_LEN_100M_10M_SEL	In 100M/10M, the packet received should be limited to be no more than the specified length in bytes. (This length including FCS)	RW	0x3000
13:0	MAX_LEN_1G_2P5G_5G_10G_SEL	In 1G/2.5G/5G/10G, the packet received should be limited to be no more than the specified length in bytes. (This length including FCS)	RW	0x3000

## MAC\_L2\_CPU\_MAX\_LEN\_CTRL

REGISTER ADDRESS : 0xBB001368

DEFAULT VALUE : 0x18F863E

Specify the max length of CPU MAC that can be rx and tx.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:28	RESERVED			
27:14	CPU_PORT_RX_MAX_LEN	The packet (MAC receive from CPU ) should be limited to be no more than the specified length in bytes. (This length including FCS).	RW	0x063E
13:0	CPU_PORT_TX_MAX_LEN	The packet (MAC transmit to CPU ) should be limited to be no more than the specified length in bytes. (This length including FCS).	RW	0x063E

## MAC\_SERDES\_MODE\_CTR

BASE ADDRESS : 0xBB00136C

ARRAY INDEX : 0 - 13

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0xFFFF

This is a One-Dimension Common Register Array.

Specify serdes 0-23 interface in different MAC bandwidth

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:12	RESERVED			
11:9	MAC10G_SERDES_MODE	Select serdes mode of 10G MAC when disable serdes force mode (SERDES_IF[7]=0).  0x0: SGMII (1.25G) 0x1: HISGMII(3.125G) 0x2: R-XAUI lite (6.125Gx1) 0x3: RESERVED 0x4: 10G BASE-R (10.3125G) 0x5: RESERVED 0x6: RESERVED 0x7: Serdes OFF	RW	0x7

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
8:6	MAC5G_SERDES_MODE	Select serdes mode of 10G MAC when disable serdes force mode (SERDES_IF[7]=0).  0x0: SGMII (1.25G) 0x1: HISGMII(3.125G) 0x2: R-XAUI lite (6.125Gx1) 0x3: RESERVED 0x4: 10G BASE-R (10.3125G) 0x5: RESERVED 0x6: RESERVED 0x7: Serdes OFF	RW	0x7
5:3	MAC2P5G_SERDES_MODE	Select serdes mode of 10G MAC when disable serdes force mode (SERDES_IF[7]=0).  0x0: SGMII (1.25G) 0x1: HISGMII(3.125G) 0x2: R-XAUI lite (6.125Gx1) 0x3: RESERVED 0x4: 10G BASE-R (10.3125G) 0x5: RESERVED 0x6: RESERVED 0x7: Serdes OFF	RW	0x7
2:0	MAC1G_SERDES_MODE	Select serdes mode of 10G MAC when disable serdes force mode (SERDES_IF[7]=0).  0x0: SGMII (1.25G) 0x1: HISGMII(3.125G) 0x2: R-XAUI lite (6.125Gx1) 0x3: RESERVED 0x4: 10G BASE-R (10.3125G) 0x5: RESERVED 0x6: RESERVED 0x7: Serdes OFF	RW	0x7

### **PROT\_SERDSE\_MUX\_CTRL\_0**

REGISTER ADDRESS : 0xBB0013BC

DEFAULT VALUE : 0x0

Specify the MAC port mux selection

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	P47_GMII_SEL	Port 47 GMII selection. 0b0: Serdes 7_7 0b1: Serdes 9_3	RW	0x0
30	P46_GMII_SEL	Port 46 GMII selection. 0b0: Serdes 7_6 0b1: Serdes 9_2	RW	0x0
29	P45_GMII_SEL	Port 45 GMII selection. 0b0: Serdes 7_5 0b1: Serdes 7_3	RW	0x0
28	P44_GMII_SEL	Port 44 GMII selection. 0b0: Serdes 7_4 0b1: Serdes 7_2	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
27	P43_GMII_SEL	Port 43 GMII selection. 0b0: Serdes 7_3 0b1: Serdes 9_1	RW	0x0
26	P42_GMII_SEL	Port 42 GMII selection. 0b0: Serdes 7_2 0b1: Serdes 9_0	RW	0x0
25	P37_GMII_SEL	Port 37 GMII selection. 0b0: Serdes 6_5 0b1: Serdes 6_3	RW	0x0
24	P36_GMII_SEL	Port 36 GMII selection. 0b0: Serdes 6_4 0b1: Serdes 6_2	RW	0x0
23	P29_GMII_SEL	Port 29 GMII selection. 0b0: Serdes 5_5 0b1: Serdes 5_3	RW	0x0
22	P28_GMII_SEL	Port 28 GMII selection. 0b0: Serdes 5_4 0b1: Serdes 5_2	RW	0x0
21	P21_GMII_SEL	Port 21 GMII selection. 0b0: Serdes 4_5 0b1: Serdes 4_3	RW	0x0
20	P20_GMII_SEL	Port 20 GMII selection. 0b0: Serdes 4_4 0b1: Serdes 4_2	RW	0x0
19	P15_GMII_SEL	Port 15 GMII selection. 0b0: Serdes 3_7 0b1: Serdes 3_3	RW	0x0
18	P14_GMII_SEL	Port 14 GMII selection. 0b0: Serdes 3_6 0b1: Serdes 3_2	RW	0x0
17:16	P13_GMII_SEL	Port 13 GMII selection. 0x0: Serdes 3_5 0x1: Serdes 3_1 0x2: Serdes 3_3 0x3: RSERVED	RW	0x0
15:14	P12_GMII_SEL	Port 12 GMII selection. 0x0: Serdes 3_4 0x1: Serdes 3_0 0x2: Serdes 3_2 0x3: RSERVED	RW	0x0
13	P11_GMII_SEL	Port 11 GMII selection. 0b0: Serdes 3_3 0b1: Serdes 2_3	RW	0x0
12	P10_GMII_SEL	Port 10 GMII selection. 0b0: Serdes 3_2 0b1: Serdes 2_2	RW	0x0
11	P9_GMII_SEL	Port 9 GMII selection. 0b0: Serdes 3_1 0b1: Serdes 2_1	RW	0x0
10	P8_GMII_SEL	Port 8 GMII selection. 0b0: Serdes 3_0 0b1: Serdes 2_0	RW	0x0
9	P7_GMII_SEL	Port 7 GMII selection. 0b0: Serdes 2_7 0b1: Serdes 1_3	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
8	P6_GMII_SEL	Port 6 GMII selection. 0b0: Serdes 2_6 0b1: Serdes 1_2	RW	0x0
7:6	P5_GMII_SEL	Port 5 GMII selection. 0x0: Serdes 2_5 0x1: Serdes 1_1 0x2: Serdes 2_3 0x3: RSERVED	RW	0x0
5:4	P4_GMII_SEL	Port 4 GMII selection. 0x0: Serdes 2_4 0x1: Serdes 1_0 0x2: Serdes 2_2 0x3: RSERVED	RW	0x0
3	P3_GMII_SEL	Port 3 GMII selection. 0b0: Serdes 2_3 0b1: Serdes 0_3	RW	0x0
2	P2_GMII_SEL	Port 2 GMII selection. 0b0: Serdes 2_2 0b1: Serdes 0_2	RW	0x0
1	P1_GMII_SEL	Port 1 GMII selection. 0b0: Serdes 2_1 0b1: Serdes 0_1	RW	0x0
0	P0_GMII_SEL	Port 0 GMII selection. 0b0: Serdes 2_0 0b1: Serdes 0_0	RW	0x0

### **PROT\_SERDSE\_MUX\_CTRL\_1**

REGISTER ADDRESS : 0xB0013C0

DEFAULT VALUE : 0x0

Specify the serdes port mux selection

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:22	RESERVED			
21	SERDES9_0_GMII_SEL	Serdes 9 channel 0 GMII selection. 0b0: Port 50 0b1: Port 42	RW	0x0
20	SERDES7_3_GMII_SEL	Serdes 7 channel 3 GMII selection. 0b0: Port 43 0b1: Port 45	RW	0x0
19	SERDES7_2_GMII_SEL	Serdes 7 channel 2 GMII selection. 0b0: Port 42 0b1: Port 44	RW	0x0
18	SERDES6_3_GMII_SEL	Serdes 6 channel 3 GMII selection. 0b0: Port 35 0b1: Port 37	RW	0x0
17	SERDES6_2_GMII_SEL	Serdes 6 channel 2 GMII selection. 0b0: Port 34 0b1: Port 36	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
16	SERDES5_3_GMII_SEL	Serdes 5 channel 3 GMII selection. 0b0: Port 27 0b1: Port 29	RW	0x0
15	SERDES5_2_GMII_SEL	Serdes 5 channel 2 GMII selection. 0b0: Port 26 0b1: Port 28	RW	0x0
14	SERDES4_3_GMII_SEL	Serdes 4 channel 3 GMII selection. 0b0: Port 19 0b1: Port 21	RW	0x0
13	SERDES4_2_GMII_SEL	Serdes 4 channel 2 GMII selection. 0b0: Port 18 0b1: Port 20	RW	0x0
12:11	SERDES3_3_GMII_SEL	Serdes 3 channel 3 GMII selection. 0x0: Port 11 0x1: Port 15 0x2: Port 13 0x3: RESERVED	RW	0x0
10:9	SERDES3_2_GMII_SEL	Serdes 3 channel 2 GMII selection. 0x0: Port 10 0x1: Port 14 0x2: Port 12 0x3: RESERVED	RW	0x0
8	SERDES3_1_GMII_SEL	Serdes 3 channel 1 GMII selection. 0b0: Port 9 0b1: Port 13	RW	0x0
7	SERDES3_0_GMII_SEL	Serdes 3 channel 0 GMII selection. 0b0: Port 8 0b1: Port 12	RW	0x0
6:5	SERDES2_3_GMII_SEL	Serdes 2 channel 3 GMII selection. 0x0: Port 3 0x1: Port 11 0x2: Port 5 0x3: RESERVED	RW	0x0
4:3	SERDES2_2_GMII_SEL	Serdes 2 channel 2 GMII selection. 0x0: Port 2 0x1: Port 10 0x2: Port 4 0x3: RESERVED	RW	0x0
2	SERDES2_1_GMII_SEL	Serdes 2 channel 1 GMII selection. 0b0: Port 1 0b1: Port 9	RW	0x0
1	SERDES2_0_GMII_SEL	Serdes 2 channel 0 GMII selection. 0b0: Port 0 0b1: Port 8	RW	0x0
0	P50_GMII_SEL	Port 50 GMII selection. 0b0: Serdes 9_0 0b1: Serdes 8_2	RW	0x0

## FIB\_UNIDIR\_CTRL

BASE ADDRESS : 0xBB0013C4  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify fiber unidirectional control register.

Bits	Field	Description	Type	Default
0	FIB_UNIDIR_EN	Enable fiber unidirectional function. 0b0: Disable. 0b1: Enable.	RW	0x0

SECTION 3.2

## PHY & SERDES

PHY & Serdes module

### SERDES\_MODE\_CTRL

BASE ADDRESS : 0xBB0013CC

ARRAY INDEX : 0 - 13

ARRAY OFFSET : 8 bits

DEFAULT VALUE : 0x9F

This is a One-Dimension Register Field Array. (8 bits per field)

SERDES0~13 interface selection.

Bits	Field	Description	Type	Default
7:0	SERDES_IF	<p>bits [4:0]: Select serdes speed when enable serdes force mode (bit[7]=1).</p> <p>0x02: SGMII (1.25G)  0x04: 1000Base-X (1.25G)  0x05: 100Base-FX (125M)  0x06: QSGMII (5G w/o FEC, 6G w/i FEC)  0x07: 1000Base-X/100Base-FX auto-detection  0x09: XSMII (2.5G)  0x0D: USXGMII(10.3125G w/o FEC, 10.3125G w/i FEC)  0x10: XSGMII (10.3125G)  0x11: QHSGMII (10.3125G)  0x12: HISGMII (3.125G)  0x14: Dual-HISGMII (6.25G)  0x16: 2500Base-X (3.125G)  0x17: R-XAU1 lite (6.25G)  0x1A: 10GBase-R(10.3125G w/o FEC, 10.3125G w/i FEC)  0x1B: 10GBase-R/SGMII auto-detection  0x1C: 10GBase-R/1000Base-X auto-detection  0x1F: Serdes OFF  Others: Reserved</p> <p>bit [5]: Enable/disable serdes FEC (Forward Error Collection) function.  0b0: FEC mode is OFF  0b1: FEC mode is ON  Note: Only QSGMII, 10GBase-R, and USXGMII support FEC function, and default is disabled.</p> <p>bit [6]: Speed up 10GBase-R from 10.3125G to 10.9375G.  0b0: disable speed up (10.3125G)  0b1: enable speed up (10.9375G)  Note: 10GBase-R default is 10.3125G</p> <p>bit [7]: Enable serdes force mode  0b0: Disable  0b1: Enable</p>	RW	0x9F

### SERDES\_INDRT\_ACCESS\_CTRL

REGISTER ADDRESS : 0xBB005638

DEFAULT VALUE : 0x0

SERDES indirect access control register.

Bits	Field	Description	Type	Default
31:18	RESERVED			
17:13	REG	Select serdes register number to access. 0x0: access register 0 0x1: access register 1 0x2: access register 2 : 0x1F: access register 31	RW	0x0

Bits	Field	Description	Type	Default
12:7	PAGE	Select serdes page to access. Digital Serdes Register: 0x0: SDS_PAGE 0x1: SDS_EXT_PAGE 0x2: FIB_PAGE 0x3: FIB_EXT_PAGE 0x4: TGR_STD_0_PAGE 0x5: TGR_STD_1_PAGE 0x6: TGR_PRO_0_PAGE 0x7: TGR_PRO_1_PAGE 0x8: TGX_STD_0_PAGE 0x9: TGX_STD_1_PAGE 0xA: TGX_PRO_0_PAGE 0xB: TGX_PRO_1_PAGE 0xC~0x1E: reserved 0x1F: WDIG_PAGE Analog Serdes Register: 0x20: analog register misc (ex: force mode) 0x21: analog register - common 0x22: analog register - speed 0x23: analog register - speed ext 0x24: analog register - 1.25G speed 0x25: analog register - 1.25G speed extend 0x26: analog register - 2.5G speed 0x27: analog register - 2.5G speed extend 0x28: analog register - 3.125G speed 0x29: analog register - 3.125G speed extend 0x2A: analog register - 5G speed (also for 6G) R/W 0x0 0x2B: analog register - 5G speed extend (also for 6G) 0x2C: analog register - 6.25G speed 0x2D: analog register - 6.25G speed extend 0x2E: analog register - 10G speed 0x2F: analog register - 10G speed extend 0x30: analog register - GPON speed 0x31: analog register - GPON speed extend 0x32: analog register - EPON speed 0x33: analog register - EPON speed extend 0x34: analog register - 6G speed 0x35: analog register - 6G speed extend 0x36~0x3F: reserved	RW	0x0
6:2	INDEX	Select serdes index to access. 0x0: serdes index 0 0x1: serdes index 1 : 0x1f: serdes index 31	RW	0x0
1	RWOP	Read/write operation. 0b0: read 0b1: Write	RW	0x0
0	CMD	Request indirect access to serdes register. 0b0: complete access 0b1: execute access Note: when indirect access completes, it is self-cleared	RWAC	0x0

## SERDES\_INDRT\_DATA\_CTRL

REGISTER ADDRESS : 0xBB00563C

DEFAULT VALUE : 0x0

Data read/write for SERDES indirect access.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	DATA	If RWOP=0x0, then SDS_DATA = read data If RWOP=0x1, then SDS_DATA = write data	RW	0x0

## SERDES\_BC\_CTRL

REGISTER ADDRESS : 0xBB005640

DEFAULT VALUE : 0x1F

Specify serdes broadcast registers

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	RESERVED			
30:5	SERDES_BC_EN	Enable serdes to receive broadcast data. Bit mask is mapping to serdes index25 ~ index0. 0b0: Disable 0b1: Enable	RW	0x0
4:0	SERDES_BC_ID	Assign serdes broadcast ID. Note: If enable serdes broadcast function and the value of INDEX[4:0] field in SERDES_INDRT_ACCESS_CTRL equal to SERDES_BC_ID that will perform broadcast function.	RW	0x1F

## SERDES\_UNIDIR\_CTRL

REGISTER ADDRESS : 0xBB0013DC

DEFAULT VALUE : 0x0

serdes unidirection control

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:26	RESERVED			
25:12	SDS_UNIDIR_TX_ABLE	Enable serdes unidirection ability for not TGX/TGR mode. Bit mask [13:0] is mapping to Serdes13 ~ Serdes0. 0b0: Disable 0b1: Enable	RW	0x0

Bits	Field	Description	Type	Default
11:0	CFG_UNIDIR_EN_TGXR	Enable unidirection for serdes TGX/TGR mode. Bit mask [11:0] is mapping to Serdes13 ~ Serdes2. 0b0: Disable 0b1: Enable	RW	0x0

## SERDES\_MISC\_CTRL0

REGISTER ADDRESS : 0xBB0013E0

DEFAULT VALUE : 0x0

Bits	Field	Description	Type	Default
31:28	RESERVED			
27:14	SDS_FRC_LD	Force serdes APHY power down. Bit mask [13:0] is mapping to Serdes13 ~ Serdes0. 0b0: Disable 0b1: Enable	RW	0x0
13:0	SDS_ISO_ON	Enable power isolation to separate serdes APHY and DPHY. Bit mask [13:0] is mapping to Serdes13 ~ Serdes0. 0b0: Disable 0b1: Enable	RW	0x0

## SERDES\_MISC\_CTRL1

REGISTER ADDRESS : 0xBB0013E4

DEFAULT VALUE : 0x0

Bits	Field	Description	Type	Default
31:12	RESERVED			
11:0	SEL_ORI_312P5	Select clock frequency for serdes PCS at QHSG-MII mode. Bit mask [11:0] is mapping to Serdes13 ~ Serdes2. 0b0: 322MHz 0b1: 312.5MHz	RW	0x0

## USXGMII\_SUBMODE\_CTRL

BASE ADDRESS : 0xBB0013E8

ARRAY INDEX : 2 - 13

ARRAY OFFSET : 5 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (5 bits per field)

Only for Serdes 2~13 when operate at USXGMII mode

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
4:0	USXGMII_SUBMODE	Select USXGMII submode when enable serdes force mode. (SERDES_IF[7]=1). 0x0: 10G-SXGMII (10G / 1-port 10G) 0x1: 10G-DXGMII (10G / 2-port 5G) 0x2: 10G-QXGMII (10G / 4-port 2.5G) 0x3: 5G-SXGMII (5G / 1-port 5G) 0x4: 5G DXGMII (5G / 2-port 2.5G) 0x5: 2.5G-SXGMII (2.5G / 1-port 2.5G)	RW	0x0

SECTION 3.3

## **POWER SAVING**

Power Saving(Green Ethernet/EEE/EEE+) module

### **EEE\_PORT\_TX\_EN**

BASE ADDRESS : 0xBB005644  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Per port enable or disable EEE TX function.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EEE_PORT_TX_EN	Enable port TX EEE capability. 0b0: Disable the port EEE function for TX. 0b1: Enable the port EEE function for TX.	RW	0x1

### **EEE\_PORT\_RX\_EN**

BASE ADDRESS : 0xBB00564C  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Per port enable or disable EEE RX function.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EEE_PORT_RX_EN	Enable port RX EEE capability. 0b0: Disable the port EEE function for RX. 0b1: Enable the port EEE function for RX.	RW	0x1

## EEE\_MISC\_CTRL0

REGISTER ADDRESS : 0xBB005654

DEFAULT VALUE : 0x8

Configure EEE TX wake up decision selection, delay after link up

Bits	Field	Description	Type	Default
31:5	RESERVED			
4	EEE_EN_FC_EFCT	Enable flow control effect while EEE wake up decision maker. 0b0: disable 0b1: enable	RW	0x0
3	EEE_REF_RXLPI	Determin whether "TX_PAUSE_WAKE_TIMER_XXX" should reference rx lpi state. 0b0: pause wake not reference rx LPI state. 0b1: pause wake timer only works when rx is not in LPI state.	RW	0x1
2:1	EEE_LINK_UP_DELAY	Select the delay time that LPI TX state machine starts to run after link up. 0x0: 1024 ms 0x1: 256 ms 0x2: 0 ms 0x3: 2048ms	RW	0x0
0	EEE_TX_WAKE_SEL	Select LPI TX wake up decision mode for EEE. 0b0: wake up when any packet is going to TX. 0b1: wake up when a high priority packet is going to TX or queued enough low priority packets (more than LOW_Q_THR) or low priority packets waiting enough time (more than LOW_Q_TX_DELAY_XXX) in queue.	RW	0x0

## EEE\_MISC\_CTRL1

REGISTER ADDRESS : 0xBB002154

DEFAULT VALUE : 0x2000

Configure EEE TX high queue definition and low queue threshold.

Bits	Field	Description	Type	Default
31:20	RESERVED			
19:8	EEE_LOW_Q THR	Set the threshold for TX low queue in QoS based LPI decision maker to wake up. Unit: Page.	RW	0x020
7:0	EEE_HIGH_Q	Define the high priority queues for eight queues in LPI mode. 0b0: Low priority 0b1: High priority Ex: If set bit 7:0= 0b1110001, queue 0, 4, 5, 6 are high priority queue	RW	0x0

## **EEE\_TX\_MINIFG\_CTRL0**

REGISTER ADDRESS : 0xBB005658

DEFAULT VALUE : 0x200014

Configure EEE TX min IFG for GE-lite and FE port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	EEE_TX_LPI_MINIPG_GE LITE	To define the min IPG between active mode and LPI mode for GE-lite port.  In other words, to set TXEN falling edge to TXER raising edge min delay, unit is bytes.  For GE-lite port, 1 byte equals 16 ns. Default should be above 0.5 us, this it is set to 32 in default.	RW	0x0020
15:0	EEE_TX_LPI_MINIPG_FE	To define the min IPG between active mode and LPI mode for FE port.  In other words, to set TXEN falling edge to TXER raising edge min delay, unit is bytes.  For 100M port, 1 byte equals 80 ns. Default should be above 1.5 us, thus it is set to 20 in default.	RW	0x0014

## **EEE\_TX\_MINIFG\_CTRL1**

REGISTER ADDRESS : 0xBB00565C

DEFAULT VALUE : 0x271003F

Configure EEE TX min IFG for 10G and GE port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	EEE_TX_LPI_MINIPG_10 G	To define the min IPG between active mode and LPI mode for 10G port.  In other words, to set TXEN falling edge to TXER raising edge min delay, unit is bytes.  For 10G port, 1 byte equals 0.8 ns. Default should be above 0.5 us, this it is set to 625 in default.	RW	0x0271
15:0	EEE_TX_LPI_MINIPG_GE	To define the min IPG between active mode and LPI mode for GE port.  In other words, to set TXEN falling edge to TXER raising edge min delay, unit is bytes.  For 1G port, 1 byte equals 8 ns. Default should be above 0.5 us, this it is set to 63 in default.	RW	0x003F

## **EEE\_TX\_MINIFG\_CTRL2**

REGISTER ADDRESS : 0xBB005660

DEFAULT VALUE : 0x139009C

Configure EEE TX min IFG for 2.5G and 5G port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	EEE_TX_LPI_MINIPG_5G	To define the min IPG between active mode and LPI mode for 5G port. In other words, to set TXEN falling edge to TXER raising edge min delay, unit is bytes. For 5G port, 1 byte equals 1.6 ns. Default should be above 0.5 us, this it is set to 313 in default.	RW	0x0139
15:0	EEE_TX_LPI_MINIPG_2P 5G	To define the min IPG between active mode and LPI mode for 2.5G port. In other words, to set TXEN falling edge to TXER raising edge min delay, unit is bytes. For 2.5G port, 1 byte equals 3.2 ns. Default should be above 0.5 us, this it is set to 156 in default.	RW	0x009C

### **EEE\_WAIT\_RX\_INACTIVE\_CTRL0**

REGISTER ADDRESS : 0xBB005664

DEFAULT VALUE : 0x33A3A3A

Configure EEE TX wait RX inactive timers and control.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:26	RESERVED			
25	EEE_WAIT_RX_INACTIVE _GE	Decide if TX can enter LPI only when RX is not active (no RX packets) for a period of time and TX would leave LPI immediately when RX is active for GE ports.	RW	0x1
24	EEE_WAIT_RX_INACTIVE _GELITE	Decide if TX can enter LPI only when RX is not active (no RX packets) for a period of time and TX would leave LPI immediately when RX is active for GE-LITE ports	RW	0x1
23:16	RESERVED			
15:8	EEE_WAIT_RX_INACTIVE _TIMER_GE	Works when WAIT_RX_INACTIVE_GE is true to decide the period of time that RX should be not active (no RX packets) for TX to enter LPI mode for GE ports. Unit is 1 us.	RW	0x3A
7:0	EEE_WAIT_RX_INACTIVE _TIMER_GELITE	Works when WAIT_RX_INACTIVE_GELITE is true to decide the period of time that RX should be not active (no RX packets) for TX to enter LPI mode for GE-LITE ports. Unit is 2 us.	RW	0x3A

### **EEE\_MULTIWAKE\_CTRL**

REGISTER ADDRESS : 0xBB00566C

DEFAULT VALUE : 0x61

Configure EEE multiple wake up port numbers and timer.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:8	RESERVED			
7:5	EEE_MULTIWAKE_PORTS	The number of ports of a wakeup group. 0x0: 2 ports 0x1: 4 ports 0x2: 6 ports 0x3: 8 ports 0x4: 10 ports 0x5: 12 ports 0x6: 14 ports 0x7: 16 ports	RW	0x3
4:3	EEE_MULTIWAKE_INTLV	Indicate the time interval between each MAC Group can quit LPI mode when enable multi-ports wakeup sequentially Algorithms. 0x0: 1-time unit 0x1: 2-time unit 0x2: 3-time unit 0x3: 4-time unit	RW	0x0
2:1	EEE_MULTIWAKE_TIME_UNIT	Configure time unit for multiwake up function. 0x0: 1us 0x1: 4us 0x2: 10us 0x3: 15us	RW	0x0
0	EEE_MULTIWAKE_EN	0b0: Disable multi-ports wakeup sequentially Algorithms. 0b1: Enable multi-ports wakeup sequentially Algorithms.	RW	0x1

### **EEE\_TX\_TIMER\_100M\_CTRL**

REGISTER ADDRESS : 0xB005670

DEFAULT VALUE : 0x12C1E

Configure MAC 100M EEE timers containing delay timer for low queue packets to TX and wake up delay timer for leaving TX LPI mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:8	EEE_LOW_Q_TX_DELAY_FE	Timer value in EEE function to delay low queue packets to TX for FE port. A timer starts to count when a low queue packet reaches. When the timer is larger than LOW_Q_TX_DELAY_FE, the queued packets of the port would start to TX. Unit: us Default value is 300 us for FE port.	RW	0x12C
7:0	EEE_TX_WAKE_TIMER_FE	Timer value in EEE function to wait before wake from LPI mode for 100M. Value from 0~255. Unit: us Default value is 30 us for FE port.	RW	0x1E

## **EEE\_TX\_TIMER\_GELITE\_CTRL**

REGISTER ADDRESS : 0xBB005674

DEFAULT VALUE : 0x512C22

Configure MAC GE-lite EEE timers containing delay timer for low queue packets to TX, wake up delay timer for leaving TX LPI mode, and TX pause wake up timer.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:28	RESERVED			
27:20	EEE_TX_PAUSE_WAKE_TIMER_GELITE	Timer value in EEE function to wait before wake from LPI mode for TX pause frame for GE-lite port. Value from 0~255. Unit: us Default value is 5us for Giga-lite.	RW	0x05
19:8	EEE_LOW_Q_TX_DELAY_GELITE	Timer value in EEE function to delay low queue packets to TX for GE-lite port. A timer starts to count when a low queue packet reaches. When the timer is larger than LOW_Q_TX_DELAY_GELITE, the queued packets of the port would start to TX. Unit: us. Default value is 300 us for GE-lite port.	RW	0x12C
7:0	EEE_TX_WAKE_TIMER_GELITE	Timer value in EEE function to wait before wake from LPI mode for GE-lite port. Value from 0~255. Unit: us Default value is 34 us for GE-lite port.	RW	0x22

## **EEE\_TX\_TIMER\_GIGA\_CTRL**

REGISTER ADDRESS : 0xBB005678

DEFAULT VALUE : 0x512C11

Configure MAC GIGA EEE timers containing delay timer for low queue packets to TX, wake up delay timer for leaving TX LPI mode, and TX pause wake up timer.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:28	RESERVED			
27:20	EEE_TX_PAUSE_WAKE_TIMER_GE	Timer value in EEE function to wait before wake from LPI mode for TX pause frame for GE port. Value from 0~255. Unit: us Default value is 5 us for Giga.	RW	0x05
19:8	EEE_LOW_Q_TX_DELAY_GE	Timer value in EEE function to delay low queue packets to TX for GE port. A timer starts to count when a low queue packet reaches. When the timer is larger than LOW_Q_TX_DELAY_GE, the queued packets of the port would start to TX. Unit: us Default value is 300 us for GE port.	RW	0x12C

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
7:0	TX_WAKE_TIMER_GE	Timer value in EEE function to wait before wake from LPI mode for GE port. Value from 0~255. Unit: us Default value is 17 us for GE port.	RW	0x11

## **EEE\_TX\_TIMER\_2P5G\_CTRL**

REGISTER ADDRESS : 0xBB00567C

DEFAULT VALUE : 0xC81E

Configure MAC 2.5G EEE timers containing delay timer for low queue packets to TX, wake up delay timer for leaving TX LPI mode, and TX pause wake up timer.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:8	EEE_LOW_Q_TX_DELAY_2P5G	Timer value in EEE function to delay low queue packets to TX for 2.5G port. A timer starts to count when a low queue packet reaches. When the timer is larger than TX_DELAY_TIMER_2P5G, the queued packets of the port would start to TX. Unit: us Default value is 200 us for 2.5G port.	RW	0x0C8
7:0	TX_WAKE_TIMER_2P5G	Timer value in EEE function to wait before wake from LPI mode for 2.5G port. Value from 0~255. Unit: us Default value is 30 us for 2.5G port.	RW	0x1E

## **EEE\_TX\_TIMER\_5G\_CTRL**

REGISTER ADDRESS : 0xBB005680

DEFAULT VALUE : 0xC80F

Configure MAC 5G EEE timers containing delay timer for low queue packets to TX, wake up delay timer for leaving TX LPI mode, and TX pause wake up timer.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:8	EEE_LOW_Q_TX_DELAY_5G	Timer value in EEE function to delay low queue packets to TX for 5G port. A timer starts to count when a low queue packet reaches. When the timer is larger than TX_DELAY_TIMER_5G, the queued packets of the port would start to TX. Unit: us Default value is 200 us for 5G port.	RW	0x0C8

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
7:0	TX_WAKE_TIMER_5G	Timer value in EEE function to wait before wake from LPI mode for 5G port. Value from 0~255. Unit: us Default value is 15 us for 5G port.	RW	0x0F

## EEE\_TX\_TIMER\_10G\_CTRL

REGISTER ADDRESS : 0xBB005684

DEFAULT VALUE : 0xC808

Configure MAC 10G EEE timers containing delay timer for low queue packets to TX, wake up delay timer for leaving TX LPI mode, and TX pause wake up timer.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:8	EEE_LOW_Q_TX_DELAY_10G	Timer value in EEE function to delay low queue packets to TX for 10 Giga port. A timer starts to count when a low queue packet reaches. When the timer is larger than TX_DELAY_TIMER_10G, the queued packets of the port would start to TX. Unit: us Default value is 200 us for 10 Giga port.	RW	0x0C8
7:0	EEE_TX_WAKE_TIMER_10G	Timer value in EEE function to wait before wake from LPI mode for 10 Giga port. Value from 0~255. Unit: us Default value is 8 us for 10 Giga port.	RW	0x08

## EEE\_PORT\_TX\_STS

BASE ADDRESS : 0xBB005688

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per port EEE TX status showing that if a port is in TX LPI mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EEE_EEEP_TX_STS	Indicate the per port EEE or EEEP TX LPI status (read to clear). 0b0: the port TX is in normal state. 0b1: the port TX is in LPI state.	RC	0x0

## **EEE\_PORT\_RX\_STS**

BASE ADDRESS : 0xBB005690

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per port EEE RX status showing that if a port is in RX LPI mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EEE_EEEP_RX_STS	Indicate the per port EEE or EEEP RX LPI status (read to clear). 0b0: the port RX is in normal state. 0b1: the port RX is in LPI state.	RC	0x0

SECTION 3.4

### **CABLE DOCTOR**

Cable Doctor module

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## CHAPTER 4

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# VLAN

The chapter describes features related to 802.1Q VLAN & QinQ

SECTION 4.1	<b>802.1Q VLAN &amp; QINQ</b>
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802.1Q VLAN & QinQ module

### **VLAN\_TAG\_TPID\_CTRL**

BASE ADDRESS : 0xBB0013FC

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x88A88100

This is a One-Dimension Common Register Array.

Specify TPID(Tag Protocol Identifier) value for outer and inner tag. Up to four outer and inner TPIDs can be specified.

Bits	Field	Description	Type	Default
31:16	OTPID	Outer tag TPID value	RW	0x88A8
15:0	ITPID	Inner tag TPID value	RW	0x8100

### **VLAN\_ETAG\_TPID\_CTRL**

REGISTER ADDRESS : 0xBB00051C

DEFAULT VALUE : 0x8100

Specify TPID(Tag Protocol Identifier) value for extra tag. Only one extra TPID can be specified.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	ETPID	Extra tag TPID value	RW	0x8100

## **VLAN\_PORT\_OTAG\_TPID\_CMP\_MSK**

BASE ADDRESS : 0xBB00600C

PORT INDEX : 0 - 56

PORT OFFSET : 0x80

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Specify which outer TPID to compare for each port. Up to four TPID can be compared. A packet is deemed to be outer-tagged if it hit one of the outer TPID value.

Bits	Field	Description	Type	Default
31:4	RESERVED			
3:0	OTPID_CMP_MSK	Bitmask to indicate which TPID to compare. OTPID_CMP_MSK=0b1111 means comparing all the TPIDs.	RW	0x0

## **VLAN\_PORT\_ITAG\_TPID\_CMP\_MSK**

BASE ADDRESS : 0xBE006010

PORT INDEX : 0 - 56

PORT OFFSET : 0x80

DEFAULT VALUE : 0x1

This is a One-Dimension Port Register Array.

Specify which inner TPID to compare for each port. Up to four TPID can be compared. A packet is deemed to be inner-tagged if it hit one of the inner TPID value.

Bits	Field	Description	Type	Default
31:4	RESERVED			
3:0	ITPID_CMP_MSK	Bitmask to indicate which TPID to compare. ITPID_CMP_MSK=0b1111 means comparing all the TPIDs.	RW	0x1

## **VLAN\_PORT\_ETAG\_TPID\_CMP**

BASE ADDRESS : 0xBB000520

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify whether to compare extra TPID for each port. A packet is deemed to be extra-tagged if it hit the extra TPID value. If extra tag is parsed, parser just skip it and continue parsing the remaining payload.

Bits	Field	Description	Type	Default
0	ETPID_CMP	Indicate whether to compare the extra TPID.	RW	0x0

## VLAN\_PORT\_AFT

BASE ADDRESS : 0xBB009400

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0xF

This is a One-Dimension Port Register Array.

VLAN accept frame type configuration.

Bits	Field	Description	Type	Default
31:4	RESERVED			
3	OTAG_UNTAG_ACCEPT	Permit outer VLAN untagged and priority-tagged frame.	RW	0x1
2	OTAG_TAG_ACCEPT	Permit outer VLAN tagged frame.	RW	0x1
1	ITAG_UNTAG_ACCEPT	Permit inner VLAN untagged and priority-tagged frame.	RW	0x1
0	ITAG_TAG_ACCEPT	Permit inner VLAN tagged frame.	RW	0x1

## VLAN\_CTRL

REGISTER ADDRESS : 0xBB0094E4

DEFAULT VALUE : 0x0

VLAN global configuration.

Bits	Field	Description	Type	Default
31	RESERVED			
30	STP_LEAK_EN	Enable spanning tree egress leaking when known multicast packet is leaked (LEAK_EN = 1). System provides the configuration in case ingress port and egress ports are in different MSTI. 0: Disable 1: Enable	RW	0x0
29	LEAK_EN	Enable leaking known L2(IP6)/IP multicast packet if the dest. port doesn't belong to the given VLAN of the packet 0: Disable 1: Enable	RW	0x0
28	SVL_FID_SRC	FID source selection of SVL mode: 0b0: FID = UC_SVL_FID / MC_SVL_FID according to the packet type. 0b1: FID = VLAN_ENTRY.SVL_FID (share the same field with FLOOD_BSSID_LIST_IDX).	RW	0x0

Bits	Field	Description	Type	Default
27:16	MC_SVL_FID	To indicate the FID of the SVL VLANs for multi-cast packets. Note: may be referred when SVL_FID_SRC = 0b0.	RW	0x0
15:4	UC_SVL_FID	To indicate the FID of the SVL VLANs for unicast packets. Note: may be referred when SVL_FID_SRC = 0b0.	RW	0x0
3:2	OCFI_ACT	Action to take if outer CFI=1. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
1:0	ICFI_ACT	Action to take if inner CFI=1. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0

### VLAN\_PORT\_IGR\_CTRL

BASE ADDRESS : 0xBB0094E8

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x8006001

This is a One-Dimension Port Register Array.

VLAN ingress port configuration.

Bits	Field	Description	Type	Default
31:30	RESERVED			
29	SA_LRN_MODE	SMAC learning mode 0b0: it is able to learn the SMAC, even the packet is not the member of incoming VLAN. 0b1: only learn while the Rx packet is the member of incoming VLAN.	RW	0x0
28	PRIVATE_VLAN_EN	Enabit bit of Private VLAN	RW	0x0
27:26	OPVID_FMT	Apply outer Port-based VLAN configuration on, 0x0: outer untagged and outer priority tagged packet 0x1: outer untagged packet 0x2: all packet(outer untagged, outer priority-tagged, outer tagged) 0x3: reserved	RW	0x2
25:14	OPVID	Outer Port-based VLAN ID.	RW	0x001
13:12	IPVID_FMT	Apply inner Port-based VLAN configuration on, 0x0: inner untagged and inner priority tagged packet 0x1: inner untagged packet 0x2: all packet(inner untagged, inner priority-tagged, inner tagged) 0x3: reserved	RW	0x2

Bits	Field	Description	Type	Default
11:0	IPVID	Inner Port-based VLAN ID.	RW	0x001

## VLAN\_PORT\_FWD\_CTRL

BASE ADDRESS : 0xBB0095CC

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0xC

This is a One-Dimension Port Register Array.

VLAN port-based configuration

Bits	Field	Description	Type	Default
31:4	RESERVED			
3	DBL_TAG	For double (outer + inner) tagged packets, learning & forwarding & filtering base on inner VID or outer VID 0b0: inner VID 0b1: outer VID	RW	0x1
2	OTAG	For single outer packets, learning & forwarding & filtering base on inner VID or outer VID 0b0: inner VID 0b1: outer VID	RW	0x1
1	ITAG	For single inner packets, learning & forwarding & filtering base on inner VID or outer VID 0b0: inner VID 0b1: outer VID	RW	0x0
0	UNTAG	For untagged packets, learning & forwarding & filtering base on inner VID or outer VID 0b0: inner VID 0b1: outer VID	RW	0x0

## VLAN\_APP\_PKT\_CTRL

REGISTER ADDRESS : 0xBB0096B0

DEFAULT VALUE : 0x0

Per-System Application Packet Control

Bits	Field	Description	Type	Default
31:18	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
17:15	ARP REP ACT	Select the action for ARP reply packet while VLAN_GROUP_MASK[7] of the forwarding VLAN is set. 0x0: Null operation 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5~0x7: Reserved	RW	0x0
14:12	ARP REQ ACT	Select the action for ARP request packet while VLAN_GROUP_MASK[6] of the forwarding VLAN is set. 0x0: Null operation 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5~0x7: Reserved	RW	0x0
11:9	DHCP6 ACT	Select the action for DHCPv6 Snooping packet while VLAN_GROUP_MASK[5] of the forwarding VLAN is set. 0x0: Null operation 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5~0x7: Reserved	RW	0x0
8:6	DHCP ACT	Select the action for DHCP Snooping packet while VLAN_GROUP_MASK[4] of the forwarding VLAN is set. 0x0: Null operation 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5~0x7: Reserved	RW	0x0
5:3	MLD ACT	Select the action for MLD Snooping packet while VLAN GROUP_MASK[3] of the forwarding VLAN is set. 0x0: Null operation 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5~0x7: Reserved	RW	0x0
2:0	IGMP ACT	Select the action for IGMP Snooping packet while VLAN_GROUP_MASK[2] of the forwarding VLAN is set. 0x0: Null operation 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5~0x7: Reserved	RW	0x0

## VLAN\_PORT\_IGR\_FLTR

BASE ADDRESS : 0xBB0096B4

PORT INDEX : 0 - 56

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (2 bits per field)

Per ingress port specifies VLAN ingress filtering action. A packet is deemed to violate ingress VLAN filtering if its ingress port isn't in the corresponding VLAN member set.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	IGR_FLTR_ACT	Action to take for a packet that violate the VLAN ingress filtering. 0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU	RW	0x1

### **VLAN\_PORT\_EGR\_FLTR**

BASE ADDRESS : 0xBB0096C4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Per egress port enables VLAN egress filtering. The packet's outgoing ports which enable the egress VLAN filtering are filtered(doing AND operation) by VLAN member set.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EGR_FLTR_EN	Enable VLAN egress filtering. 0x0:disable 0x1:enable	RW	0x1

### **VLAN\_PORT\_TAG\_CTRL**

BASE ADDRESS : 0xBB004860

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x3C9

This is a One-Dimension Port Register Array.

VLAN port-based tag status configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
13:12	EGR_OTAG_STS	To indicate whether keep incoming outer tag status on egress port. 0x0: follow the current tag status. 0x1: force to untag packet 0x2: force to tagged packet 0x3: force to priority-tag.	RW	0x0
11:10	EGR_ITAG_STS	To indicate whether keep inner tag status on egress port. 0x0: follow the current tag status. 0x1: force to untag packet 0x2: force to tagged packet 0x3: force to priority-tag.	RW	0x0
9	EGR_OTAG_KEEP	To indicate whether keep outer tag status on egress port. 0b0: Don't keep. 0b1: Keep.	RW	0x1
8	EGR_ITAG_KEEP	To indicate whether keep inner tag status on egress port. 0b0: Don't keep. 0b1: Keep.	RW	0x1
7	IGR_OTAG_KEEP	To indicate whether keep outer tag status on ingress port. 0b0: Don't keep. 0b1: Keep.	RW	0x1
6	IGR_ITAG_KEEP	To indicate whether keep inner tag status on ingress port. 0b0: Don't keep. 0b1: Keep.	RW	0x1
5:4	OTPID_IDX	Index of port n outer TPID Point to VLAN_TAG_TPID_CTRL.OTPID	RW	0x0
3	OTPID_KEEP	Keep original O-TPID 0b0: Use the per-port egress configured O-TPID. 0b1: Keep using the original O-TPID if the ingress packet is outer-tagged.	RW	0x1
2:1	ITPID_IDX	Index of port n inner TPID Point to VLAN_TAG_TPID_CTRL.ITPID	RW	0x0
0	ITPID_KEEP	Keep original I-TPID 0b0: Use the per-port egress configured I-TPID. 0b1: Keep using the original I-TPID if the ingress packet is inner-tagged.	RW	0x1

## VLAN

TABLE TYPE : 0x3

TABLE INDEX : 0 - 4095

### VLAN Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127:71	MBR	Member
70:64	MSTI	MSTP Instance ID

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	L2_HKEY_UCAST	Select L2 lookup hash key for L2 unicast and broadcast traffic 0b0: FID = VID (for IVL mode) 0b1: FID = VLAN_CTRL.UC_SVL_FID or VLAN.SVL_FID (for SVL mode)
62	L2_HKEY_MCAST	Select L2 lookup hash key for L2 multicast traffic 0b0: FID = VID (for IVL mode) 0b1: FID = VLAN_CTRL.MC_SVL_FID or VLAN.SVL_FID (for SVL mode)
61:52	L3_INTF_ID	L3 Interface ID
51:48	VLAN_PROFILE	VLAN Profile Index
47:32	GROUP_MASK	Group Bitmask
31	L2_TNL_LST_VALID	Valid bit to indicate whether L2_TNL_LST_IDX is valid.
30:18	L2_TNL_LST_IDX	L2 Tunnel List Index / SVL FID
17:0	RESERVED	

## VLAN\_UNTAG

TABLE TYPE : 0x0

TABLE INDEX : 0 - 4095

VLAN Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:7	UNTAG	Untagged Member
6:0	RESERVED	

SECTION 4.2

## VLAN PROFILE

VLAN Profile module

### VLAN\_PROFILE\_SET

BASE ADDRESS : 0xBB009800

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 0x1C

DEFAULT VALUE : 0xFFFFFFFF, 0xFFFFFFFF, 0xFFFFFFFF, 0xFFFFFFFF, 0xFFFFFFFF, 0xFFFFFFFF, 0x0

This is a One-Dimension Common Register Array.

VLAN profile configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
223:208	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
207:206	L2_NEW_SA_LRN	L2 table learning behavior when receives a packet with new SMAC. 0b00: ASIC Auto Learn 0b01: learn as a suspend entry 0b10: not Learn 0b11: reserved	RW	0x0
205:203	L2_NEW_SA_ACT	Packet forwarding behavior when receives a packet with new SMAC. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
202	IPMC_BRIDGE_MODE	Bridging mode for IPv4 multicast packets: 0b0: MAC-based (forward as L2 multicast) 0b1: IP-based	RW	0x0
201	IP6MC_BRIDGE_MODE	Bridging mode for IPv6 multicast packets: 0b0: MAC-based (forward as L2 multicast) 0b1: IP-based	RW	0x0
200:198	L2MC_BRIDGE_LU_MIS_A CT	L2 Multicast Packet look up miss action 0x0: forward (Flooding to L2_UNKN_MC_FLD_PMSK) 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
197:195	IPMC_BRIDGE_LU_MIS_A CT	IPv4 Multicast Packet lookup miss action 0x0: forward (Flooding to IP4_UNKN_MC_FLD_PMSK) 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
194:192	IP6MC_BRIDGE_LU_MIS_ ACT	IPv6 Multicast Packet lookup miss action 0x0: forward (Flooding to IP6_UNKN_MC_FLD_PMSK) 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
191:185	RESERVED			
184:160	L2MC_UNKN_FLD_PMSK_5 2_32	Unknown L2 multicast flooding portmask [52:32]	RW	0xFFFFFFFF
159:128	L2MC_UNKN_FLD_PMSK_3 1_0	Unknown L2 multicast flooding portmask [31:0]	RW	0xFFFFFFFF
127:121	RESERVED			
120:96	IPMC_UNKN_FLD_PMSK_5 2_32	Unknown IP4 multicast flooding portmask[52:32]	RW	0xFFFFFFFF
95:64	IPMC_UNKN_FLD_PMSK_3 1_0	Unknown IP4 multicast flooding portmask[31:0]	RW	0xFFFFFFFF

Bits	Field	Description	Type	Default
63:57	RESERVED			
56:32	IP6MC_UNKN_FLD_PMSK_52_32	Unknown IP6 multicast flooding portmask[52:32]	RW	0xFFFFFFFF
31:0	IP6MC_UNKN_FLD_PMSK_31_0	Unknown IP6 multicast flooding portmask[31:0]	RW	0xFFFFFFFF

## SECTION 4.3

**(IEEE802.1V) PROTOCOL-BASED VLAN**

(IEEE802.1v) Protocol-based VLAN module

**VLAN\_PPB\_VLAN\_SET**

BASE ADDRESS : 0xBB00C000

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Specify IEEE 802.1v(Protocol-and-Port-based VLAN) configuration. Global eight protocol values are supported and each port can have different PPB VLAN for the same protocol value through VLAN\_PORT\_PPB\_VLAN.

Bits	Field	Description	Type	Default
31:18	RESERVED			
17:16	FRAME_TYPE	Packet frame type format. 0x0: Unknown 0x1: Ethernet 0x2: LLC_Other 0x3: LLC_SNAP(RFC1042)	RW	0x0
15:0	ETHER_TYPE	Ether type value.	RW	0x0

**VLAN\_PORT\_PPB\_VLAN\_SET**

BASE ADDRESS : 0xBB00C020

ARRAY INDEX1 : 0 - 56

ARRAY OFFSET1 : 0x20

ARRAY INDEX2 : 0 - 7

ARRAY OFFSET2 : 0x4

DEFAULT VALUE : 0x0

This is a Two-Dimension Common Register Array.

Per-port per-protocol specifies the Protocol-and-Port-based VLAN. A packet is given to the specified VID if its protocol value hit the PPB configuration.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:18	RESERVED			
17	VLAN_TYPE	To indicate the VLAN type: 0b0: Inner 0b1: Outer	RW	0x0
16	PRI_AS	Indicate to assign Priority. 0b0: Don't assign 0b1: Assign a new priority	RW	0x0
15:13	PRI	Protocol-and-port-based priority.	RW	0x0
12	VID_AS	Indicate to assign VLAN ID. 0b0: Don't assign 0b1: Assign a new VLAN ID	RW	0x0
11:0	VID	Protocol-and-port-based VLAN ID.	RW	0x0

— SECTION 4.4 —

### **MAC-BASED VLAN**

MAC-based VLAN module

— SECTION 4.5 —

### **IP-SUBNET-BASED VLAN**

IP-subnet-based VLAN module

— SECTION 4.6 —

### **VLAN RANGE CHECK**

VLAN Range Check module

### **VLAN\_IGR\_VID\_RNG\_CHK\_SET\_0**

BASE ADDRESS : 0xBB009C00

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Ingress VLAN ID range configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: original inner VLAN ID. 0b1: original outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

### **VLAN\_IGR\_VID\_RNG\_CHK\_SET\_1**

BASE ADDRESS : 0xBB009C80

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Ingress VLAN ID range configuration

Bits	Field	Description	Type	Default
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: original inner VLAN ID. 0b1: original outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

### **VLAN\_IGR\_VID\_RNG\_CHK\_SET\_2**

BASE ADDRESS : 0xBB009D00

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Ingress VLAN ID range configuration

Bits	Field	Description	Type	Default
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: original inner VLAN ID. 0b1: original outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

### **VLAN\_IGR\_VID\_RNG\_CHK\_SET\_3**

BASE ADDRESS : 0xBB009D80

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Ingress VLAN ID range configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: original inner VLAN ID. 0b1: original outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

### **VLAN\_EGR\_VID\_RNG\_CHK\_SET\_0**

BASE ADDRESS : 0xBB004944

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Egress VLAN ID range configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: internal inner VLAN ID. 0b1: internal outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

### **VLAN\_EGR\_VID\_RNG\_CHK\_SET\_1**

BASE ADDRESS : 0xBB0049C4

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Egress VLAN ID range configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: internal inner VLAN ID. 0b1: internal outer VLAN ID.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

## VLAN\_EGR\_VID\_RNG\_CHK\_SET\_2

BASE ADDRESS : 0xBB004A44

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Egress VLAN ID range configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: internal inner VLAN ID. 0b1: internal outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

## VLAN\_EGR\_VID\_RNG\_CHK\_SET\_3

BASE ADDRESS : 0xBB004AC4

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Egress VLAN ID range configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	TYPE	VID type selection. 0b0: internal inner VLAN ID. 0b1: internal outer VLAN ID.	RW	0x0
23:12	UPPER	VID upper bound.	RW	0x0
11:0	LOWER	VID lower bound.	RW	0x0

## SECTION 4.7

**VLAN TRANSLATION**

VLAN Translation module

**VLAN\_IVC\_BLK\_CTRL**

**BASE ADDRESS :** 0xBB009E00  
**ARRAY INDEX :** 0 - 15  
**ARRAY OFFSET :** 2 bits  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

VLAN ingress conversion block configuration (256 entries per-block, total 16 blocks)

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	MODE	Block table mode selection. 0b00: Ingress VLAN conversion. 0b01: MAC-based VLAN. 0b10: IP-subnet-based VLAN. 0b11: Reserved	RW	0x0

**VLAN\_L2TBL\_CNVT\_CTRL**

**REGISTER ADDRESS :** 0xBB0096CC  
**DEFAULT VALUE :** 0x0

VLAN translation (N-to-1) configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	GLB_VID_CNVT_EN	Enable N:1 VLAN aggregation function globally. 0b0: disabled (Port Extension related fields: E-CID ext, base) 0b1: enabled (VLAN aggregation related fields: Status, Priority, VID)	RW	0x0

**VLAN\_PORT\_L2TBL\_CNVT\_CTRL**

**BASE ADDRESS :** 0xBB0096D0  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Port Register Array.

VLAN translation (N-to-1) configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:4	RESERVED			
3	PRI_CNVT_EN	Enable egress port to take back the priority to replace the priority of VLAN tag from MAC address table.	RW	0x0
2	PRI_TAG_ACT	Learning VID selection for priority tagged packet. 0b0: 0 (VID = 0) 0b1: port-based VID	RW	0x0
		Note: when it is set to 1, port-based VID will be selected based on VID_SELECT.		
1	VID_SELECT	L2 table log VID selection. 0b0: Inner VID 0b1: Outer VID	RW	0x0
0	VID_CNVT_EN	Enable egress port to take back the VID to replace the VLAN from MAC address table. 0b0: disable 0b1: enable	RW	0x0

### **VLAN\_TRUNK\_L2TBL\_CNVT\_CTRL\_3**

BASE ADDRESS : 0xBB0097B4

ARRAY INDEX : 0 - 127

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-trunk VLAN translation (N-to-1) configuration 3

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	PRI_CNVT_EN	Enable egress port to take back the priority to replace the priority of VLAN tag from MAC address table.	RW	0x0

### **VLAN\_TRUNK\_L2TBL\_CNVT\_CTRL\_2**

BASE ADDRESS : 0xBB0097C4

ARRAY INDEX : 0 - 127

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-trunk VLAN translation (N-to-1) configuration 2

Bits	Field	Description	Type	Default
0	PRI_TAG_ACT	Learning VID selection for priority tagged packet. 0b0: 0 (VID = 0) 0b1: port-based VID  Note: when it is set to 1, port-based VID will be selected based on VID_SELECT.	RW	0x0

### VLAN\_TRUNK\_L2TBL\_CNVT\_CTRL\_1

BASE ADDRESS : 0xBB0097D4  
 ARRAY INDEX : 0 - 127  
 ARRAY OFFSET : 1 bit  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-trunk VLAN translation (N-to-1) configuration 1

Bits	Field	Description	Type	Default
0	VID_SELECT	L2 table log VID selection. 0b0: Inner VID 0b1: Outer VID	RW	0x0

### VLAN\_TRUNK\_L2TBL\_CNVT\_CTRL\_0

BASE ADDRESS : 0xBB0097E4  
 ARRAY INDEX : 0 - 127  
 ARRAY OFFSET : 1 bit  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-trunk VLAN translation (N-to-1) configuration 0

Bits	Field	Description	Type	Default
0	VID_CNVT_EN	Enable egress port to take back the VID to replace the VLAN from MAC address table. 0b0: disable 0b1: enable	RW	0x0

### VLAN\_PORT\_IVC\_CTRL

BASE ADDRESS : 0xBB009E04  
 PORT INDEX : 0 - 56  
 PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

#### VLAN Ingress Conversion Configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:7	RESERVED			
6:5	VID_RANGE_SET_IDX	To specify ingress vlan range check set	RW	0x0
4	IVC_EN	Enable IVC (Ingress VLAN Conversion) 0b0: Disable 0b1: Enable	RW	0x0
3	IP_VLAN_EN	Enable MAC-based VLAN Look up 0b0: Disable 0b1: Enable	RW	0x0
2	MAC_VLAN_EN	Enable IP-Subnet-based VLAN Look up 0b0: Disable 0b1: Enable	RW	0x0
1	OTAG_LU_MIS_DROP	To indicate the default action for the original outer tagged packet. 0b0: Forward 0b1: Drop	RW	0x0
0	ITAG_LU_MIS_DROP	To indicate the default action for the original inner tagged packet. 0b0: Forward 0b1: Drop	RW	0x0

#### VLAN\_PORT\_EVC\_CTRL

BASE ADDRESS : 0xBB004B44

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

#### VLAN Egress Conversion Configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:5	RESERVED			
4:3	VID_RANGE_SET_IDX	To specify egress vlan range check set	RW	0x0
2	EVC_EN	Enable EVC (Egress VLAN Conversion)	RW	0x0
1	OTAG_LU_MIS_DROP	To indicate the default action for the internal outer tagged packet. 0b0: Forward 0b1: Drop	RW	0x0
0	ITAG_LU_MIS_DROP	To indicate the default action for the internal inner tagged packet. 0b0: Forward 0b1: Drop	RW	0x0

## **VLAN\_IVC\_ENTRY\_INDICATION**

BASE ADDRESS : 0xBB009EE8

ARRAY INDEX : 0 - 2047

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

IVC rule hit indication register

Bits	Field	Description	Type	Default
0	HIT	Hit status (write 1 to clear)	RW1C	0x0

## **VLAN\_EVC\_ENTRY\_INDICATION**

BASE ADDRESS : 0xBB004C28

ARRAY INDEX : 0 - 1023

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

EVC rule hit indication register

Bits	Field	Description	Type	Default
0	HIT	Hit status (write 1 to clear)	RW1C	0x0

## **VLAN\_IVC\_ENTRY\_INDICATION\_AGGR**

BASE ADDRESS : 0xBB009FE8

ARRAY INDEX : 0 - 1

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Aggregated IVC rule hit-indication register

Bits	Field	Description	Type	Default
31:0	HIT_MSK	Each bit means the aggregated status for 32 rules in a row.	RO	0x0

## **VLAN\_EVC\_ENTRY\_INDICATION\_AGGR**

REGISTER ADDRESS : 0xBB004CA8

DEFAULT VALUE : 0x0

Aggregated EVC rule hit-indication register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	HIT_MSK	Each bit means the aggregated status for 32 rules in a row.	RO	0x0

## VLAN\_EVC

TABLE TYPE : 0x1

TABLE INDEX : 0 - 1023

Egress VLAN Conversion

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	PORT_TYPE	Port type
189:183	PORT_ID	Port ID
182	INT_OTAG_IF	Internal outer tag status
181:179	INT_OPRI	Internal outer priority (PCP)
178:167	INT_OVID	Internal outer VLAN ID
166	INT_ITAG_IF	Internal inner tag status
165:163	INT_IPRI	Internal inner priority (PCP)
162:151	INT_IVID	Internal inner VLAN ID
150:119	INT_VID_RNG_CHK	Internal VLAN range check
118	BMSK_PORT_TYPE	Care bitmask of PORT_TYPE
117:111	BMSK_PORT_ID	Care bitmask of PORT_ID
110	BMSK_INT_OTAG_IF	Care bitmask of INT_OTAG_IF
109:107	BMSK_INT_OPRI	Care bitmask of INT_OPRI
106:95	BMSK_INT_OVID	Care bitmask of INT_OVID
94	BMSK_INT_ITAG_IF	Care bitmask of INT_ITAG_IF
93:91	BMSK_INT_IPRI	Care bitmask of INT_IPRI
90:79	BMSK_INT_IVID	Care bitmask of INT_IVID
78:47	BMSK_INT_VID_RNG_CHK	Care bitmask of INT_VID_RNG_CHK
46	OVID_ASSIGN	O-TPID assign
45:44	OVID_IDX	O-TPID index
43	OTAG_STS_ASSIGN	Outer tag status assign
42	OTAG_STS	Outer tag status
41	OPRI_ASSIGN	Outer tag priority assign
40:38	OPRI	Outer tag priority
37:36	OVID_ASSIGN	Outer VLAN ID assign

<b>Bits</b>	<b>Field</b>	<b>Description</b>
35:24	OVID	Outer VLAN ID
23	ITPID_ASSIGN	I-TPID assign
22:21	ITPID_IDX	I-TPID index
20	ITAG_STS_ASSIGN	Inner tag status assign
19	ITAG_STS	Inner tag status
18:17	IPRI_ASSIGN	Inner tag priority assign
16:14	IPRI	Inner tag priority
13:12	IVID_ASSIGN	Inner VLAN ID assign
11:0	IVID	Inner VLAN ID

## VLAN\_IP\_BASED

TABLE TYPE : 0x4

TABLE INDEX : 0 - 2047

IP-subnet-based VLAN

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	PORT_TYPE	Port type
189:183	PORT_ID	Port ID
182	OVID_VALID	Outer tag VLAN ID valid
181	IVID_VALID	Inner tag VLAN ID valid
180:149	SIP	Source IP address
148:119	RESERVED	
118	BMSK_PORT_TYPE	Care bitmask of PORT_TYPE
117:111	BMSK_PORT_ID	Care bitmask of PORT_ID
110	BMSK_OVID_VALID	Care bitmask of OVID_VALID
109	BMSK_IVID_VALID	Care bitmask of IVID_VALID
108:77	BMSK_SIP	Care bitmask of SIP
76:47	RESERVED	
46:44	FWD_ACT	Forwarding action
43	IGNORE_IGR_FLTR	Ignore ingress VLAN filtering
42	BSSID_ASSIGN	BSSID assign
41:30	BSSID_IDX	BSSID index
29	VLAN_TYPE	Vlan type selection
28	TPID_ASSIGN	TPID assign
27:26	TPID_IDX	TPID index
25	TAG_STS_ASSIGN	Tag status assign
24	TAG_STS	Tag status
23	PRI_ASSIGN	Priority assign

<b>Bits</b>	<b>Field</b>	<b>Description</b>
22:20	PRI	Priority
19	VID_ASSIGN	VID assign
18:7	VID	VID
6:0	RESERVED	

## VLAN\_IVC

TABLE TYPE : 0x4

TABLE INDEX : 0 - 2047

Ingress VLAN Conversion

<b>Bits</b>	<b>Field</b>	<b>Description</b>
223	VALID	Valid bit
222	PORT_TYPE	Port type
221:215	PORT_ID	Port ID
214	ORI_OTAG_IF	Original outer tag status
213:211	ORI_OPRI	Original outer priority (PCP)
210:199	ORI_OVID	Original outer VLAN ID
198	ORI_ITAG_IF	Original inner tag status
197:195	ORI_IPRI	Original inner priority (PCP)
194:183	ORI_IVID	Original inner VLAN ID
182:151	ORI_VID_RNG_CHK	Original VLAN range check
150	BMSK_PORT_TYPE	Care bitmask of PORT_TYPE
149:143	BMSK_PORT_ID	Care bitmask of PORT_ID
142	BMSK_ORI_OTAG_IF	Care bitmask of ORI_OTAG_IF
141:139	BMSK_ORI_OPRI	Care bitmask of ORI_OPRI
138:127	BMSK_ORI_OVID	Care bitmask of ORI_OVID
126	BMSK_ORI_ITAG_IF	Care bitmask of ORI_ITAG_IF
125:123	BMSK_ORI_IPRI	Care bitmask of ORI_IPRI
122:111	BMSK_ORI_IVID	Care bitmask of ORI_IVID
110:79	BMSK_ORI_VID_RNG_CHK	Care bitmask of ORI_VID_RNG_CHK
78	OTPID_ASSIGN	O-TPID assign
77:76	OTPID_IDX	O-TPID index
75	OTAG_STS_ASSIGN	Outer tag status assign
74	OTAG_STS	Outer tag status
73:72	OPRI_ASSIGN	Outer tag priority assign
71:69	OPRI	Outer tag priority
68:67	OVID_ASSIGN	Outer VLAN ID assign
66:55	OVID	Outer VLAN ID
54	ITPID_ASSIGN	I-TPID assign

<b>Bits</b>	<b>Field</b>	<b>Description</b>
53:52	ITPID_IDX	I-TPID index
51	ITAG_STS_ASSIGN	Inner tag status assign
50	ITAG_STS	Inner tag status
49	IPRI_ASSIGN	Inner tag priority assign
48:46	IPRI	Inner tag priority
45:44	IVID_ASSIGN	Inner VLAN ID assign
43:32	IVID	Inner VLAN ID
31	INTERFACE_ID_ASSIGN	Interface ID assign
30:21	INTERFACE_ID	Interface ID
20:0	RESERVED	

## VLAN\_MAC\_BASED

TABLE TYPE : 0x4

TABLE INDEX : 0 - 2047

MAC-based VLAN

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	PORT_TYPE	Port type
189:183	PORT_ID	Port ID
182	OVID_VALID	Outer tag VLAN ID valid
181	IVID_VALID	Inner tag VLAN ID valid
180:133	SMAC	Source MAC address
132:119	RESERVED	
118	BMSK_PORT_TYPE	Care bitmask of PORT_TYPE
117:111	BMSK_PORT_ID	Care bitmask of PORT_ID
110	BMSK_OVID_VALID	Care bitmask of OVID_VALID
109	BMSK_IVID_VALID	Care bitmask of IVID_VALID
108:61	BMSK_SMAC	Care bitmask of SMAC
60:47	RESERVED	
46:44	FWD_ACT	Forwarding action
43	IGNORE_IGR_FLTR	Ignore ingress VLAN filtering
42	BSSID_ASSIGN	BSSID assign
41:30	BSSID_IDX	BSSID index
29	VLAN_TYPE	Vlan type selection
28	TPID_ASSIGN	TPID assign
27:26	TPID_IDX	TPID index
25	TAG_STS_ASSIGN	Tag status assign
24	TAG_STS	Tag status

<b>Bits</b>	<b>Field</b>	<b>Description</b>
23	PRI_ASSIGN	Priority assign
22:20	PRI	Priority
19	VID_ASSIGN	VID assign
18:7	VID	VID
6:0	RESERVED	

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## CHAPTER 5

# Layer 2 Bridging

The chapter describes features related to layer 2

SECTION 5.1

<b>RMA</b>
------------

This section describes the configurations for reserved multicast addresses

### **RMA\_CTRL\_0**

REGISTER ADDRESS : 0xBB008800

DEFAULT VALUE : 0x14

Reserved Multicast Address control register 0.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RMA_OF_ACT	Reserved Multicast Address 01-80-C2-00-00-0F Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
29:28	RMA_OE_ACT	Reserved Multicast Address 01-80-C2-00-00-0E Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
27:26	RMA_OD_ACT	Reserved Multicast Address 01-80-C2-00-00-0D Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
25:24	RMA_OC_ACT	Reserved Multicast Address 01-80-C2-00-00-0C Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23:22	RMA_0B_ACT	Reserved Multicast Address 01-80-C2-00-00-0B Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
21:20	RMA_0A_ACT	Reserved Multicast Address 01-80-C2-00-00-0A Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
19:18	RMA_09_ACT	Reserved Multicast Address 01-80-C2-00-00-09 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
17:16	RMA_08_ACT	Reserved Multicast Address 01-80-C2-00-00-08 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
15:14	RMA_07_ACT	Reserved Multicast Address 01-80-C2-00-00-07 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
13:12	RMA_06_ACT	Reserved Multicast Address 01-80-C2-00-00-06 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
11:10	RMA_05_ACT	Reserved Multicast Address 01-80-C2-00-00-05 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
9:8	RMA_04_ACT	Reserved Multicast Address 01-80-C2-00-00-04 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
7:6	RMA_03_ACT	Reserved Multicast Address 01-80-C2-00-00-03 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:4	RMA_02_ACT	Reserved Multicast Address 01-80-C2-00-00-02 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU Note: When it is set to "Trap", this type packet should be able to bypass STP and VLAN drop ("VLAN ingress/egress filter", "VLAN error", "VLAN accept frame type" and "CFI = 1").	RW	0x1
3:2	RMA_01_ACT	Reserved Multicast Address 01-80-C2-00-00-01 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x1
1:0	RESERVED			

## RMA\_CTRL\_1

REGISTER ADDRESS : 0xBB008804

DEFAULT VALUE : 0x0

Reserved Multicast Address control register 1.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RMA_1F_ACT	Reserved Multicast Address 01-80-C2-00-00-1F Configuration whose ethernet neither 0x88CC nor 0x88F7. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
29:28	RMA_1E_ACT	Reserved Multicast Address 01-80-C2-00-00-1E Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
27:26	RMA_1D_ACT	Reserved Multicast Address 01-80-C2-00-00-1D Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
25:24	RMA_1C_ACT	Reserved Multicast Address 01-80-C2-00-00-1C Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23:22	RMA_1B_ACT	Reserved Multicast Address 01-80-C2-00-00-1B Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
21:20	RMA_1A_ACT	Reserved Multicast Address 01-80-C2-00-00-1A Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
19:18	RMA_19_ACT	Reserved Multicast Address 01-80-C2-00-00-19 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
17:16	RMA_18_ACT	Reserved Multicast Address 01-80-C2-00-00-18 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
15:14	RMA_17_ACT	Reserved Multicast Address 01-80-C2-00-00-17 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
13:12	RMA_16_ACT	Reserved Multicast Address 01-80-C2-00-00-16 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
11:10	RMA_15_ACT	Reserved Multicast Address 01-80-C2-00-00-15 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
9:8	RMA_14_ACT	Reserved Multicast Address 01-80-C2-00-00-14 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
7:6	RMA_13_ACT	Reserved Multicast Address 01-80-C2-00-00-13 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:4	RMA_12_ACT	Reserved Multicast Address 01-80-C2-00-00-12 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
3:2	RMA_11_ACT	Reserved Multicast Address 01-80-C2-00-00-11 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
1:0	RMA_10_ACT	Reserved Multicast Address 01-80-C2-00-00-10 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

## RMA\_CTRL\_2

REGISTER ADDRESS : 0xBB008808

DEFAULT VALUE : 0x0

Reserved Multicast Address control register 2.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RMA_2F_ACT	Reserved Multicast Address 01-80-C2-00-00-2F Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
29:28	RMA_2E_ACT	Reserved Multicast Address 01-80-C2-00-00-2E Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
27:26	RMA_2D_ACT	Reserved Multicast Address 01-80-C2-00-00-2D Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
25:24	RMA_2C_ACT	Reserved Multicast Address 01-80-C2-00-00-2C Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23:22	RMA_2B_ACT	Reserved Multicast Address 01-80-C2-00-00-2B Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
21:20	RMA_2A_ACT	Reserved Multicast Address 01-80-C2-00-00-2A Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
19:18	RMA_29_ACT	Reserved Multicast Address 01-80-C2-00-00-29 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
17:16	RMA_28_ACT	Reserved Multicast Address 01-80-C2-00-00-28 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
15:14	RMA_27_ACT	Reserved Multicast Address 01-80-C2-00-00-27 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
13:12	RMA_26_ACT	Reserved Multicast Address 01-80-C2-00-00-26 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
11:10	RMA_25_ACT	Reserved Multicast Address 01-80-C2-00-00-25 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
9:8	RMA_24_ACT	Reserved Multicast Address 01-80-C2-00-00-24 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
7:6	RMA_23_ACT	Reserved Multicast Address 01-80-C2-00-00-23 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:4	RMA_22_ACT	Reserved Multicast Address 01-80-C2-00-00-22 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
3:2	RMA_21_ACT	Reserved Multicast Address 01-80-C2-00-00-21 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU	RW	0x0
1:0	RMA_20_ACT	Reserved Multicast Address 01-80-C2-00-00-20 Configuration. 0b00: Forward 0b01: Drop 0b10: Trap to local CPU 0b11: Trap to master CPU  Note: When the rma within 01-80-C2-00-00-20~2F is set to "Trap", this kind of packet should be able to bypass STP and VLAN drop ("VLAN ingress/egress filter", "VLAN error", "VLAN accept frame type" and "CFI = 1").	RW	0x0

### **RMA\_MIRROR\_CTRL**

REGISTER ADDRESS : 0xBB00880C

DEFAULT VALUE : 0x0

RMA Packet Mirror Control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	MIRROR_ACT	Decides whether RMA packets could be mirrored. 0b0: whether mirroring packet depends on mirror setting 0b1: Cancel Rx/Tx mirror configuration	RW	0x0

### **RMA\_SMAC\_LRN\_CTRL**

BASE ADDRESS : 0xBB008810

PORT INDEX : 0 - 47

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Reserved Multicast Address SMAC-learning control register. RMA\_SMAC\_LEARNING\_CTRL[i] is for 01-80-C2-00-00-i.

Bits	Field	Description	Type	Default
0	LRN	Learning behavior for Reserved Multicast Address 01-80-C2-00-00-00 ~ 01-80-C2-00-00-2F. 0b0: Not learn the SAMC 0b1: Learn the SMAC	RW	0x1

## RMA\_MGN\_LRN\_CTRL

REGISTER ADDRESS : 0xBB008818

DEFAULT VALUE : 0x7

Reserved Multicast Address Management Packet learning control register.

Bits	Field	Description	Type	Default
31:3	RESERVED			
2	PTP_LRN	Learning behavior for PTP. 0b0: Not learn the SAMC 0b1: Learn the SMAC	RW	0x1
1	LLDP_LRN	Learning behavior for LLDP. 0b0: Not learn the SAMC 0b1: Learn the SMAC	RW	0x1
0	EAPOL_LRN	Learning behavior for EAPOL. 0b0: Not learn the SAMC 0b1: Learn the SMAC	RW	0x1

## RMA\_PORT\_BPDU\_CTRL

BASE ADDRESS : 0xBE00881C

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Reserved Multicast Address BPDU (01-80-C2-00-00-00) control register.

Bits	Field	Description	Type	Default
2:0	ACT	BPDU (01-80-C2-00-00-00) Configuration. 0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU 0x4: Flood(limited by RMA_BPDU_FLD_PMSK) 0x5~0x7: reserved Note: When it is set to "Trap", BPDU packet should be able to bypass VLAN.	RW	0x0

## RMA\_PORT\_PTP\_CTRL

BASE ADDRESS : 0xBB008834

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

PTP control register.

Bits	Field	Description	Type	Default
31:4	RESERVED			
3:2	UDP_ACT	<p>The PTP UDP packet (EthernetII &amp;&amp; !PPPoE &amp;&amp; UDP DPort=319  320 &amp;&amp; no Fragment) takes configured action.  (UDP includes UDP &amp; UDP Lite)</p> <p>0b00: Forward  0b01: Drop  0b10: Trap to local CPU  0b11: Trap to master CPU</p> <p>Note: When it is set to "Trap", PTP packet should be able to bypass STP.</p>	RW	0x0
1:0	ETH2_ACT	<p>The PTP Ethernet II packet (EthernetII &amp;&amp; !PPPoE &amp;&amp; EtherType == 0x88f7) takes configured action.</p> <p>0b00: Forward  0b01: Drop  0b10: Trap to local CPU  0b11: Trap to master CPU</p> <p>Note: When it is set to "Trap", PTP packet should be able to bypass STP.</p>	RW	0x0

## RMA\_PORT\_LLDP\_CTRL

BASE ADDRESS : 0xBB008918

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

LLDP control register.

Bits	Field	Description	Type	Default
2:0	ACT	<p>The packet whose ethertype is 0X88CC is treated as LLDP and take configured action.</p> <p>0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU 0x4: Flood(limited by RMA_LLDP_FLD_PMSK) 0x5~0x7: Reserved</p> <p>Note: When it is set to "Trap", LLDP packet should be able to bypass STP and VLAN drop ("VLAN ingress/egress filter", "VLAN error", "VLAN accept frame type" and "CFI = 1").</p>	RW	0x0

### **RMA\_PORT\_EAPOL\_CTRL**

BASE ADDRESS : 0xBB008930

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

EAPOL control register.

Bits	Field	Description	Type	Default
2:0	ACT	<p>The packet whose ethertype is 0X888E is treated as EAPOL and take configured action.</p> <p>0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU 0x4: Flood(limited by RMA_EAPOL_FLD_PMSK) 0x5~0x7: reserved</p> <p>Note: When it is set to "Trap", EAPOL packet should be able to bypass STP and VLAN drop ("VLAN ingress/egress filter", "VLAN error", "VLAN accept frame type" and "CFI = 1").</p>	RW	0x0

### **RMA\_FLD\_PMSK**

REGISTER ADDRESS : 0xBB008948

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFFF

hma\_FLD\_PMSK

Bits	Field	Description	Type	Default
63:57	RESERVED			

Bits	Field	Description	Type	Default
56:32	PMSK_1	RMA flood to this portmask (port 32 to 56) when the action is forward and L2 Table lookup miss(include BPDU,EAPOL);when action with flooding (except BPDU,EAPOL) also reference this portmask.	RW	0x1FFFFFFF
31:0	PMSK_0	RMA flood to this portmask (port 0 to 31) when the action is forward and L2 Table lookup miss(include BPDU,EAPOL);when action with flooding (except BPDU,EAPOL) also reference this portmask.	RW	0xFFFFFFFF

### **RMA\_BPDU\_FLD\_PMSK**

REGISTER ADDRESS : 0xBB008950

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFFF

BPDU flooding portmask when port BPDU action is flooding

Bits	Field	Description	Type	Default
63:57	RESERVED			
56:32	PMSK_1	BPDU flood to this portmask (port 32 to 56) when BPDU action is flood to all ports.	RW	0x1FFFFFFF
31:0	PMSK_0	BPDU flood to this portmask (port 0 to 31) when BPDU action is flood to all ports.	RW	0xFFFFFFFF

### **RMA\_EAPOL\_FLD\_PMSK**

REGISTER ADDRESS : 0xBB008958

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFFF

EAPOL flooding portmask when port EAPOL action is flooding

Bits	Field	Description	Type	Default
63:57	RESERVED			
56:32	PMSK_1	EAPOL flood to this portmask (port 32 to 56) when EAPOL action is flood to all ports.	RW	0x1FFFFFFF
31:0	PMSK_0	EAPOL flood to this portmask (port 0 to 31) when EAPOL action is flood to all ports.	RW	0xFFFFFFFF

### **RMA\_LLDP\_FLD\_PMSK**

REGISTER ADDRESS : 0xBB008960

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFFF

LLDP flooding portmask when port LLDP action is flooding

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:57	RESERVED			
56:32	PMSK_1	LLDP flood to this portmask (port 32 to 56) when LLDP action is flood to all ports.	RW	0x1FFFFFFF
31:0	PMSK_0	LLDP flood to this portmask (port 0 to 31) when LLDP action is flood to all ports.	RW	0xFFFFFFFF

### **RMA\_USR\_DEF\_FLD\_PMSK**

REGISTER ADDRESS : 0xBB008968

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFFF

User-defined RMA flooding portmask when User-defined RMA action is flooding

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:57	RESERVED			
56:32	PMSK_1	User-defined RMA flood to this portmask (port 32 to 56) when User-defined RMA action is flood to all ports.	RW	0x1FFFFFFF
31:0	PMSK_0	User-defined RMA flood to this portmask (port 0 to 31) when User-defined RMA action is flood to all ports.	RW	0xFFFFFFFF

### **RMA\_USR\_DEF\_CTRL**

BASE ADDRESS : 0xBB008970

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x14

DEFAULT VALUE : 0x80, 0x0, 0x0, 0x0, 0x0

This is a One-Dimension Common Register Array.

User-defined Reserved Multicast Address.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
159:144	RESERVED			
143:128	ADDR_MAX_HI	The address [47:32] of maximum boundary of user-defined RMA.	RW	0x0
127:96	ADDR_MAX_LO	The address [31:0] of maximum boundary of user-defined RMA.	RW	0x0
95:80	RESERVED			
79:64	ADDR_MIN_HI	The address [47:32] of minimum boundary of user-defined RMA.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:32	ADDR_MIN_LO	The address [31:0] of minimum boundary of user-defined RMA.	RW	0x0
31:25	RESERVED			
24:9	ETHER_TYPE	Ether type value	RW	0x0
8	BYPASS_VLAN	User-defined RMA bypass VLAN drop ("VLAN ingress/egress filter", "VLAN error", "VLAN accept frame type" and "CFI = 1"). This works only if EN is set as enabled. 0b0: Follow VLAN decision 0b1: Bypass (works only if the ACT field is trapping to CPU or forward)	RW	0x0
7	LRN	0b0: Not learn the SMAC 0b1: Learn the SMAC	RW	0x1
6	BYPASS_STP	User-defined RMA bypass "Blocking" and "Learning" port state of spanning tree. This works only if EN is set as enabled. 0b0: Drop by STP 0b1: Bypass (works only if the ACT field is trapping to CPU)	RW	0x0
5:3	ACT	User-defined RMA action. 0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU 0x4: Flood (limited by RMA_USR_DEF_FLD_PMSK) 0x5~0x7: reserved	RW	0x0
2:1	TYPE	Compare field. 0b00: Compare ADDR only 0b01: Compare EtherType only 0b10: Both ADDR and EtherType 0b11: Reserved	RW	0x0
0	EN	Enable this User-defined RMA rule. 0b0: Disable 0b1: Enable	RW	0x0

**SECTION 5.2**
**LINK AGGREGATION**

Link Aggregation module configurations including trunk member, hash keys and traffic separation configuration. Stacking port trunking is also involved.

**TRK\_ID\_CTRL**

BASE ADDRESS : 0xBB00B800

ARRAY INDEX : 0 - 51

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Specify the trunk ID of local link aggregation group

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:8	RESERVED			
7	TRK_VALID	TRUNK_ID Entry valid or not.	RW	0x0
6:0	TRK_ID	ID of link aggregation group.	RW	0x0

## TRK\_MBR\_CTRL

BASE ADDRESS : 0xBB00B8D0

ARRAY INDEX : 0 - 51

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Specify ports to be trunk member ports of local link aggregation group

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:56	RESERVED			
55:32	TRK_PPM_55_32	Member port mask (port 32 to 55) of local link aggregation group. 0b0: individual port 0b1: aggregation port	RW	0x0
31:0	TRK_PPM_31_0	Member port mask (port 0 to 31) of local link aggregation group. 0b0: individual port 0b1: aggregation port	RW	0x0

## TRK\_HASH\_CTRL

BASE ADDRESS : 0xBB00BA70

ARRAY INDEX : 0 - 1

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x3FFF

This is a One-Dimension Common Register Array.

Specify the hash algorithm parameter mask. There are two sets of L2 hash algorithm and 2 sets of L3 hash algorithm settings in the system.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			

Bits	Field	Description	Type	Default
13:4	L3_HASH_MSK	10 bits mask for link aggregation group hash parameter selection for IPv4 and IPv6 packets Bit 0: SPP (source physical port) Bit 1: SMAC Bit 2: DMAC Bit 3: VLAN ID Bit 4: SIP Bit 5: DIP Bit 6: L4SPORT Bit 7: L4DPORT Bit 8: Protocol ID Bit 9: IPv6 Flow Label Note that it could be an arbitrary combination	RW	0x3FF
3:0	L2_HASH_MSK	4 bits mask for link aggregation group hash parameter selection for L2 packets Bit 0: SPP (source physical port) Bit 1: SMAC Bit 2: DMAC Bit 3: VLAN ID Note that it could be an arbitrary combination	RW	0xF

## TRK\_CTRL

REGISTER ADDRESS : 0xBB00BA78

DEFAULT VALUE : 0x44

Specify the trunk global control functions including stacking port hash control, stand alone mode control, port separation control in stand alone mode and non-terminate tunnel hash layer selection

Bits	Field	Description	Type	Default
31:7	RESERVED			
6	LINK_DOWN_AVOID	This field decides if link down port would be avoided to be selected TX port. It works on stand alone mode and local first packets in stacking mode. Bit value: 0b0: do not care port link status 0b1: avoid TX to link down port	RW	0x1
5	CONGST_AVOID	This field decides if congest port would be avoided to be selected TX port. It works on stand alone mode and local first packets in stacking mode. Bit value: 0b0: do not care port congest status 0b1: avoid TX to congested port	RW	0x0
4	LOCAL_FIRST	Decide if trunk load balancing runs on local first mode Bit value: 0b0: normal load balancing 0b1: local first load balancing	RW	0x0
3	STK_HASH_CAL	Decide if stacking port trunk calculates the hash value or follows the hash value in stacking header. Bit value: 0b0: follow the hash value in stacking header 0b1: calculate hash value	RW	0x0

Bits	Field	Description	Type	Default
2	TRK_STAND_ALONE_MODE	<p>Configure the enable status of stand-alone mode for normal port link aggregation. In stand-alone mode, hardware would skip the link down trunk member ports.</p> <p>Bit value:</p> <ul style="list-style-type: none"> <li>0b0: disable stand-alone mode</li> <li>0b1: enable stand-alone mode</li> </ul>	RW	0x1
1	SEP_PORT_SEL	<p>If SEP_KWN_MC_EN and SEP_FLOOD_EN are both enabled, this register decides the separated port.</p> <p>Bit value:</p> <ul style="list-style-type: none"> <li>0b0: FLOOD known MC packets are transmitted to member port with largest ID.</li> <li>0b1: FLOOD packets are transmitted to member port with largest ID; known MC packets are transmitted to member port with second largest ID.</li> </ul>	RW	0x0
0	NON_TMN_TUNNEL_HASH_SEL	<p>IP header layer select of trunk hash for non-terminate tunnel packets.</p> <p>Bit value:</p> <ul style="list-style-type: none"> <li>0b0: Select the first layer (outer layer) to do trunk hash.</li> <li>0b1: Select the second layer (inner layer) to do trunk hash.</li> </ul>	RW	0x0

### TRK\_SHFT\_CTRL

REGISTER ADDRESS : 0xBB00BA7C

DEFAULT VALUE : 0x0

Specify the hash algorithm parameter shift bits.

Bits	Field	Description	Type	Default
31:30	RESERVED			
29:27	SHIFT_BITS_FLOW_LABEL	Shift bits of IPv6 flow label hash key.	RW	0x0
26:24	SHIFT_BITS_PROTO	Shift bits of protocol hash key.	RW	0x0
23:21	SHIFT_BITS_L4DPORT	Shift bits of L4 DPORT hash key.	RW	0x0
20:18	SHIFT_BITS_L4SPORT	Shift bits of L4 SPORT hash key.	RW	0x0
17:15	SHIFT_BITS_DIP	Shift bits of DIP hash key.	RW	0x0
14:12	SHIFT_BITS_SIP	Shift bits of SIP hash key.	RW	0x0
11:9	SHIFT_BITS_VLAN	Shift bits of VLAN ID hash key.	RW	0x0
8:6	SHIFT_BITS_DMAC	Shift bits of DMAC hash key.	RW	0x0
5:3	SHIFT_BITS_SMAC	Shift bits of SMAC hash key.	RW	0x0
2:0	SHIFT_BITS_SPP	Shift bits of Source Physical Port hash key.	RW	0x0

### TRK\_LOCAL\_TBL\_REFRESH

REGISTER ADDRESS : 0xBB00BA80

DEFAULT VALUE : 0x0

Trigger to refresh local trunk table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	TRK_LOCAL_TBL_REFRESH	Trigger the local partial trunk table refresh. Bit value: 0b0: local partial trunk table refresh is done 0b1: trigger to refresh local partial trunk table	RWAC	0x0

## TRK\_LOCAL\_TBL

BASE ADDRESS : 0xBB00BA84

ARRAY INDEX : 0 - 51

ARRAY OFFSET : 0x14

DEFAULT VALUE : 0x3FFFFFFE, 0x3FFFFFFF, 0xFFFFBE0, 0x3FFFFFFF, 0x3F

This is a One-Dimension Common Register Array.

Specify the trunk entry information of local switch.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
159:141	RESERVED			
140:137	NUM_TX_CANDI	Number of valid TX candidate ports of this trunk group.	RO	0x0
136	L2_HASH_MSK_IDX	Index of hash parameters mask of a trunk for L2 packets. (index to L2_HASH_MASK)	RO	0x0
135	IP4_HASH_MSK_IDX	Index of hash parameters mask of a trunk for IPv4 packets. (index to L3_HASH_MASK)	RO	0x0
134	IP6_HASH_MSK_IDX	Index of hash parameters mask of a trunk for IPv6 packets. (index to L3_HASH_MASK)	RO	0x0
133:128	TRK_PORT7	Local trunk port 7, 0x3f: non-local port ID.	RO	0x3F
127:126	RESERVED			
125:120	TRK_PORT6	Local trunk port 6, 0x3f: non-local port ID.	RO	0x3F
119:114	TRK_PORT5	Local trunk port 5, 0x3f: non-local port ID.	RO	0x3F
113:108	TRK_PORT4	Local trunk port 4, 0x3f: non-local port ID.	RO	0x3F
107:102	TRK_PORT3	Local trunk port 3, 0x3f: non-local port ID.	RO	0x3F
101:96	TRK_PORT2	Local trunk port 2, 0x3f: non-local port ID.	RO	0x3F
95:92	RESERVED			
91:86	TRK_PORT1	Local trunk port 1, 0x3f: non-local port ID.	RO	0x3F
85:80	TRK_PORT0	Local trunk port 0, 0x3f: non-local port ID.	RO	0x3F
79:74	SEP_FLOOD_PORT	Port ID that the flood packets are sent to. 0x3f: non-local port ID. 0x3e: SEP_FLOOD_EN is disabled.	RO	0x3E

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
73:68	SEP_KWN_MC_PORT	Port ID that the known L2MC and IPMC packets are sent to. 0x3f: non-local port ID. 0x3e: SEP_KWN_MC_EN is disabled.	RO	0x3E
67:64	NUM_AVL_TX_CANDI	Number of valid TX candidate ports for local first or stand alone mode of this trunk group.	RO	0x0
63:62	RESERVED			
61:56	AVL_TRK_PORT7	Available trunk port 7 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
55:50	AVL_TRK_PORT6	Available trunk port 6 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
49:44	AVL_TRK_PORT5	Available trunk port 5 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
43:38	AVL_TRK_PORT4	Available trunk port 4 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
37:32	AVL_TRK_PORT3	Available trunk port 3 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
31:30	RESERVED			
29:24	AVL_TRK_PORT2	Available trunk port 2 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
23:18	AVL_TRK_PORT1	Available trunk port 1 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
17:12	AVL_TRK_PORT0	Available trunk port 0 for local first and stand alone mode. 0x3f: the port is not available.	RO	0x3F
11:6	AVL_SEP_FLOOD_PORT	Available Port ID that the flood packets are sent to in stand-alone mode. 0x3f: no available port.	RO	0x3E
5:0	AVL_SEP_KWN_MC_PORT	Available Port ID that the known L2MC and IPMC packets are sent to in stand-alone mode. 0x3f: no available port.	RO	0x3E

## TRK\_STK\_CTRL

BASE ADDRESS : 0xBB00BE94

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Specify stacking ports or EST ports to be trunk member ports and hash algorithm index

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18	L2_HASH_MSK_IDX	Index of hash parameters mask of a trunk for L2 packets. (index to L2_HASH_MASK)	RW	0x0
17	IP4_HASH_MSK_IDX	Index of hash parameters mask of a trunk for IPv4 packets. (index to L3_HASH_MASK)	RW	0x0
16	IP6_HASH_MSK_IDX	Index of hash parameters mask of a trunk for IPv6 packets. (index to L3_HASH_MASK)	RW	0x0

Bits	Field	Description	Type	Default
15:0	STK_TRK_PPM	Member port mask of link aggregation group for stacking ports Bit value: 0b0: individual port 0b1: aggregation port	RW	0x0

## **LOCAL\_PORT\_TRK\_MAP**

BASE ADDRESS : 0xBB004CAC  
PORT INDEX : 0 - 55  
PORT OFFSET : 0x4  
DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Map local port to trunk ID for port ACL usage

Bits	Field	Description	Type	Default
31:8	RESERVED			
7	IS_TRK_MBR	Decide if the port is a trunk member port Bit value: 0b0: the port is not a trunk member port 0b1: the port is a trunk member port	RW	0x0
6:0	TRK_ID	The joined trunk ID of the port	RW	0x0

## **LAG**

TABLE TYPE : 0x0  
TABLE INDEX : 0 - 127

Link Aggregation Table

Bits	Field	Description
95:93	RESERVED	
92:89	NUM_TX_CANDI	Number of valid TX candidate ports of this trunk group.
88	L2_HASH_MSK_IDX	Index of hash parameters mask of a trunk for L2 packets. (index to L2_HASH_MASK)
87	IP4_HASH_MSK_IDX	Index of hash parameters mask of a trunk for IPv4 packets. (index to L3_HASH_MASK)
86	IP6_HASH_MSK_IDX	Index of hash parameters mask of a trunk for IPv6 packets. (index to L3_HASH_MASK)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
85	SEP_FLOOD_EN	Enable to separate the flooding packets (including unknown L2MC, IPMC, and DLF(destination lookup fail) packets) to a specific port for a trunk group. Bit value: 0b0: Disable the traffic separation. 0b1: Enable the traffic separation.
84	SEP_KWN_MC_EN	Enable to separate the known L2MC and IPMC packets to a specific port for a trunk group. Bit value: 0b0: Disable the traffic separation. 0b1: Enable the traffic separation.
83:80	TRK_DEV7	Device ID 7
79:74	TRK_PORT7	Trunk port 7
73:70	TRK_DEV6	Device ID 6
69:64	TRK_PORT6	Trunk port 6
63:62	RESERVED	
61:58	TRK_DEVS	Device ID 5
57:52	TRK_PORT5	Trunk port 5
51:48	TRK_DEV4	Device ID 4
47:42	TRK_PORT4	Trunk port 4
41:38	TRK_DEV3	Device ID 3
37:32	TRK_PORT3	Trunk port 3
31:30	RESERVED	
29:26	TRK_DEV2	Device ID 2
25:20	TRK_PORT2	Trunk port 2
19:16	TRK_DEV1	Device ID 1
15:10	TRK_PORT1	Trunk port 1
9:6	TRK_DEV0	Device ID 0
5:0	TRK_PORT0	Trunk port 0

## SRC\_TRK\_MAP

TABLE TYPE : 0xD

TABLE INDEX : 0 - 1023

Source port to source trunk ID mapping table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
31	TRK_ID_VALID	
30:24	TRK_ID	Trunk ID valid or not
23:0	RESERVED	

## SECTION 5.3

**SPANNING TREE**

Spanning Tree module

**ST\_CTRL**

REGISTER ADDRESS : 0xBB008000

DEFAULT VALUE : 0x0

Spanning Tree global control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	MSTI_MODE	<p>Indicates that instance ID is either from FID_MSTI of VLAN table or force using 0 (CIST).            0b0: normal mode (from VLAN table)            0b1: force mode (force using 0)</p> <p>Note: The configuration only affects MSTI but not FID.</p>	RW	0x0

**MSTI**

TABLE TYPE : 0x5

TABLE INDEX : 0 - 127

**MSTI**

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127:126	STATE_PORT55	STP Port State of Port 55
125:124	STATE_PORT54	STP Port State of Port 54
123:122	STATE_PORT53	STP Port State of Port 53
121:120	STATE_PORT52	STP Port State of Port 52
119:118	STATE_PORT51	STP Port State of Port 51
117:116	STATE_PORT50	STP Port State of Port 50
115:114	STATE_PORT49	STP Port State of Port 49
113:112	STATE_PORT48	STP Port State of Port 48
111:110	STATE_PORT47	STP Port State of Port 47
109:108	STATE_PORT46	STP Port State of Port 46
107:106	STATE_PORT45	STP Port State of Port 45
105:104	STATE_PORT44	STP Port State of Port 44
103:102	STATE_PORT43	STP Port State of Port 43
101:100	STATE_PORT42	STP Port State of Port 42
99:98	STATE_PORT41	STP Port State of Port 41
97:96	STATE_PORT40	STP Port State of Port 40

<b>Bits</b>	<b>Field</b>	<b>Description</b>
95:94	STATE_PORT39	STP Port State of Port 39
93:92	STATE_PORT38	STP Port State of Port 38
91:90	STATE_PORT37	STP Port State of Port 37
89:88	STATE_PORT36	STP Port State of Port 36
87:86	STATE_PORT35	STP Port State of Port 35
85:84	STATE_PORT34	STP Port State of Port 34
83:82	STATE_PORT33	STP Port State of Port 33
81:80	STATE_PORT32	STP Port State of Port 32
79:78	STATE_PORT31	STP Port State of Port 31
77:76	STATE_PORT30	STP Port State of Port 30
75:74	STATE_PORT29	STP Port State of Port 29
73:72	STATE_PORT28	STP Port State of Port 28
71:70	STATE_PORT27	STP Port State of Port 27
69:68	STATE_PORT26	STP Port State of Port 26
67:66	STATE_PORT25	STP Port State of Port 25
65:64	STATE_PORT24	STP Port State of Port 24
63:62	STATE_PORT23	STP Port State of Port 23
61:60	STATE_PORT22	STP Port State of Port 22
59:58	STATE_PORT21	STP Port State of Port 21
57:56	STATE_PORT20	STP Port State of Port 20
55:54	STATE_PORT19	STP Port State of Port 19
53:52	STATE_PORT18	STP Port State of Port 18
51:50	STATE_PORT17	STP Port State of Port 17
49:48	STATE_PORT16	STP Port State of Port 16
47:46	STATE_PORT15	STP Port State of Port 15
45:44	STATE_PORT14	STP Port State of Port 14
43:42	STATE_PORT13	STP Port State of Port 13
41:40	STATE_PORT12	STP Port State of Port 12
39:38	STATE_PORT11	STP Port State of Port 11
37:36	STATE_PORT10	STP Port State of Port 10
35:34	STATE_PORT9	STP Port State of Port 9
33:32	STATE_PORT8	STP Port State of Port 8
31:30	STATE_PORT7	STP Port State of Port 7
29:28	STATE_PORT6	STP Port State of Port 6
27:26	STATE_PORT5	STP Port State of Port 5
25:24	STATE_PORT4	STP Port State of Port 4
23:22	STATE_PORT3	STP Port State of Port 3
21:20	STATE_PORT2	STP Port State of Port 2
19:18	STATE_PORT1	STP Port State of Port 1
17:16	STATE_PORT0	STP Port State of Port 0
		0b00: Disabled State 0b01: Blocking/Listening State 0b10: Learning State 0b11: Forwarding State
15:0	RESERVED	

Bits	Field	Description
SECTION 5.4		
<b>PORT ISOLATION</b>		

Port Isolation module

### **PORT\_ISO\_RESTRICT\_ROUTE\_CTRL**

REGISTER ADDRESS : 0xBB008100

DEFAULT VALUE : 0x0

Bits	Field	Description	Type	Default
31:1	RESERVED			
0	RESTRICT_ROUTE	Indicates whether the routed packet is restricted by Port-based port isolation. 0b0: not restricted 0b1: restricted	RW	0x0

### **PORT\_ISO\_VB\_ISO\_PMSK\_CTRL**

BASE ADDRESS : 0xBB008A00

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0xC

DEFAULT VALUE : 0x0, 0x0, 0x0

This is a One-Dimension Common Register Array.

Port isolation VLAN-based isolated port mask control register, which specifies the VLAN-based isolated port mask and VID.

Bits	Field	Description	Type	Default
95:89	RESERVED			
88:64	VB_ISO_MBR_1	VLAN-based isolated port mask (port 56 to 32). Ports which have corresponding port mask clear(i.e. 0) in this mask could not communicate with each other, while ports which have corresponding port mask set could communicate with any other ports.	RW	0x0
63:32	VB_ISO_MBR_0	VLAN-based isolated port mask (port 31 to 0). Ports which have corresponding port mask clear(i.e. 0) in this mask could not communicate with each other, while ports which have corresponding port mask set could communicate with any other ports.	RW	0x0
31:25	RESERVED			
24:13	VID_UPPER	Upper bound VLAN ID	RW	0x0
12:1	VID_LOWER	Lower bound VLAN ID.	RW	0x0

Bits	Field	Description	Type	Default
0	VALID	Indicates whether this entry is valid 0b0: entry invalid 0b1: entry valid	RW	0x0

## **PORT\_ISO\_VB\_EGR\_CTRL**

BASE ADDRESS : 0xBB008B80

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Port isolation VLAN-based isolated port mask bypass control register.

Bits	Field	Description	Type	Default
0	VB_ISO_EGR_BYPASS	Indicates whether bypasses VB_ISO_MBR filter in egress direction. 0b0: disable bypass 0b1: enable bypass	RW	0x0

## **PORT\_ISO\_CTRL**

TABLE TYPE : 0x1

TABLE INDEX : 0 - 1023

Port isolation configuration that each port can specify a port list to communicate with.

Bits	Field	Description
63:7	PMSK	Port-based isolated port mask. Packets incoming from the source port to which this configuration applied could only be forwarded to those ports which have corresponding port mask set.
6:0	RESERVED	

### SECTION 5.5

## **MAC FORWARDING CONTROL**

MAC Forwarding Control module

### **L2\_CTRL**

REGISTER ADDRESS : 0xBB00C800

DEFAULT VALUE : 0x4006

L2 global control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18:16	ECID_RPF_FAIL_ACT	Action for ECID RPF check failed. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
15	PE_LM_FLTR	Enable bit to filter packet if PE forwarding table lookup miss 0b0: disable (flood to potential ports) 0b1: enable (drop PE lookup miss packet)	RW	0x0
14:13	RESERVED			
12	LUTCAM_EN	The state of CAM using for lookup table. 0b0: disable 0b1: enable	RW	0x0
11	STK_AUTO_LRNN	Stacking port auto learning mode. 0b0: disable 0b1: enable	RW	0x0
10:8	HASH_FULL_ACT	SA learning hash full action. Note: If CAM is disabled, ASIC will directly take action configured here after 4-way hash failed. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
7	LINK_DOWN_P_INVLD	Invalidates the L2 entries whose associated ports are link down 0b0: disable 0b1: enable	RW	0x0
6:5	MC_BC_SA_ACT	Decides the action of the packet whose SA is multicast SA or broadcast SA 0b00: forward 0b01: drop 0b10: trap to local CPU 0b11: trap to master CPU	RW	0x0
4:3	ZERO_SA_ACT	Decides the action of the packet whose SA is all 0 0b00: forward 0b01: drop 0b10: trap to local CPU 0b11: trap to master CPU	RW	0x0
2	ZERO_SA_LRNN	The learning behavior for an all-zero mac-address (00-00-00-00-00-00). 0b0: not learn 0b1: learn	RW	0x1
1	L2_HASH_ALGO_BLK1	L2 hash algorithm for block 1 of lookup table 0b0: algorithm 0 0b1: algorithm 1	RW	0x1

Bits	Field	Description	Type	Default
0	L2_HASH_ALGO_BLK0	L2 hash algorithm for block 0 of lookup table 0b0: algorithm 0 0b1: algorithm 1	RW	0x0

## L2\_AGE\_CTRL

REGISTER ADDRESS : 0xBB00C804

DEFAULT VALUE : 0x1C00177

L2 age control register.

Bits	Field	Description	Type	Default
31:26	RESERVED			
25	KEEP_AGE_OUT_ENTRY_V_ALID	Decides whether keep an age out entry valid. 0b0: disable 0b1: enable	RW	0x0
24:22	SUS_AGE_MAX	The Age Value when learning a suspend entry : 0b0:not do suspend learning 0b1-7: age value when learning a suspend entry	RW	0x7
21	RESERVED			
20:0	AGE_UNIT	The granularity of aging bits in L2 table. Aging bits reduce one every AGEUNIT* 0.1 seconds Disable aging when AGE_UNIT == 0.	RW	0x000177

## L2\_PORT\_AGE\_CTRL

BASE ADDRESS : 0xBB00C808

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Configuration of port age state.

Bits	Field	Description	Type	Default
0	AGE_EN	The port age state. 0b0: disable 0b1: enable	RW	0x1

## L2\_TRK\_AGE\_CTRL

BASE ADDRESS : 0xBB00C810

ARRAY INDEX : 0 - 127

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Configuration of trunk age state.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	AGE_EN	The trunk age state. 0b0: disable 0b1: enable	RW	0x1

## L2\_PORT\_SALRN

BASE ADDRESS : 0xBB00C820

PORT INDEX : 0 - 56

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Per ingress port source mac learning behavior.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	SALRN	L2 table SA learning behavior 00b: ASIC Auto Learn 01b: learn as a suspend entry 10b: not Learn 11b: reserved	RW	0x0

## L2\_PORT\_NEW\_SA\_FWD

BASE ADDRESS : 0xBB00C830

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

The forwarding behavior for packet whose source mac is new to system.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	NEW_SA_FWD	Packet forwarding behavior when receives a packet with new SMAC. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

## L2\_PORT\_DYN\_MV\_ACT

BASE ADDRESS : 0xBB00C848

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configuration of the action of L2 dynamic entry when a host moves to another port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	ACT	Dynamic port move action. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

## L2\_PORT\_DYN\_MV\_LRN

BASE ADDRESS : 0xBB00C860

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Configuration of the learning behavior of L2 dynamic entry when a host moves to another port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	LRN	The learning behavior when dynamic port move. 0b0: not learn 0b1: learn	RW	0x1

## L2\_GLB\_STT\_PORT\_MV\_ACT

REGISTER ADDRESS : 0xBB00C8CC

DEFAULT VALUE : 0x0

Configuration of the action of L2 static entry when a host moves to another port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			
2:0	ACT	Static port move action. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

## L2\_GLB\_STT\_PORT\_MV\_LRN

REGISTER ADDRESS : 0xBB00C8D0

DEFAULT VALUE : 0x0

Configuration of the learning behavior of L2 static entry when a host moves to another port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	LRN	The learning behavior when static port move. 0b0: not learn 0b1: learn	RW	0x0

## L2\_PORT\_MV\_FORBID

BASE ADDRESS : 0xBB00C8D4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

To configure port move forbidden states

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EN	The port move forbidden state. 0b0: disable 0b1: enable	RW	0x0

## L2\_TRK\_MV\_FORBID

BASE ADDRESS : 0xBB00C8DC

ARRAY INDEX : 0 - 25

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

To configure trunk port move forbidden states

Bits	Field	Description	Type	Default
0	EN	The trunk port move forbidden state. 0b0: disable 0b1: enable	RW	0x0

## L2\_PORT\_MV\_FORBID\_CTRL

REGISTER ADDRESS : 0xBB00C8E0

DEFAULT VALUE : 0x0

Port move forbidden action.

Bits	Field	Description	Type	Default
31:2	RESERVED			
1:0	FORBID_ACT	Port move forbidden action. 0x0: forward 0x1: drop 0x2: trap to CPU 0x3: copy to CPU	RW	0x0

## L2\_PORT\_SABLK\_CTRL

BASE ADDRESS : 0xBB00C8E4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

To configure port SA Block states

Bits	Field	Description	Type	Default
0	EN	SA blocking state of the port. 0b0: disable 0b1: enable	RW	0x0

## L2\_PORT\_DABLK\_CTRL

BASE ADDRESS : 0xBB00C8EC

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

To configure port DA Block states

Bits	Field	Description	Type	Default
0	EN	DA blocking state of the port. 0b0: disable 0b1: enable	RW	0x0

## L2\_UNKN\_UC\_FLD\_PMSK

REGISTER ADDRESS : 0xBB00C8F4

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFF

Unknown Unicast Flood Portmaks

Bits	Field	Description	Type	Default
63:57	RESERVED			
56:32	PMSK_1	Bit <56:32> of flooding port mask for unknown unicast packet.	RW	0x1FFFFFF
31:0	PMSK_0	Bit <31:0> of flooding port mask for unknown unicast packet.	RW	0xFFFFFFFF

## L2\_BC\_FLD\_PMSK

REGISTER ADDRESS : 0xBB00C8FC

DEFAULT VALUE : 0xFFFFFFFF, 0x1FFFFFF

Broadcast Flood Portmaks

Bits	Field	Description	Type	Default
63:57	RESERVED			
56:32	PMSK_1	Bit <56:32> of flooding port mask for broadcast packet.	RW	0x1FFFFFF
31:0	PMSK_0	Bit <31:0> of flooding port mask for broadcast packet.	RW	0xFFFFFFFF

## L2\_PORT\_UC\_LM\_ACT

BASE ADDRESS : 0xBB00C904

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

L2 unicast packet lookup miss action

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	ACT	<p>Per port L2 unicast packet lookup miss action.</p> <p>Note: A L2 unicast packet is deemed to KNOWN if its DMAC and forwarding VID are equal to switchs MAC and the PVID of CPU port respectively.</p> <p>0x0: forward (Flooding to L2_UNKN_UC_FLD_PMSK)  0x1: drop  0x2: trap to local CPU  0x3: copy to local CPU  0x4: trap to master CPU  0x5: copy to master CPU  0x6~0x7: reserved</p>	RW	0x0

## L2\_LRNCNSTRCT\_CTRL

REGISTER ADDRESS : 0xBB00C964

DEFAULT VALUE : 0x7FFF8

Whole system learning constraint control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18:3	CONSTRCT_NUM	<p>Learning constraint number of system.</p> <p>0x0: never learning  0x1~0xFFFFE: learning constraint number  0xFFFFF: unlimited</p>	RW	0xFFFF
2:0	ACT	<p>Learning constraint action of system.</p> <p>0x0: forward  0x1: drop  0x2: trap to local CPU  0x3: copy to local CPU  0x4: trap to master CPU  0x5: copy to master CPU  0x6~0x7: reserved</p>	RW	0x0

## L2\_LRNCNSTRCT\_CNT

REGISTER ADDRESS : 0xBB00C968

DEFAULT VALUE : 0x0

Whole system learning constraint counter register.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	LRN_CNT	Learnt counter of system.	RW	0x0

## L2\_LRNL\_PORT\_CONSTRT\_CTRL

BASE ADDRESS : 0xBB00C96C

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x7FFF8

This is a One-Dimension Port Register Array.

Port learning constraint control register.

Bits	Field	Description	Type	Default
31:19	RESERVED			
18:3	CONSTRT_NUM	Learning constraint number of port. 0x0: never learning 0x1~0xFFFF: learning constraint number 0xFFFF: unlimited	RW	0xFFFF
2:0	ACT	Learning constraint action of port. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

## L2\_LRNL\_PORT\_CONSTRT\_CNT

BASE ADDRESS : 0xBB00CA50

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Port learning constraint counter register.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	LRN_CNT	Learnt counter of port.	RW	0x0

Bits	Field	Description	Type	Default
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### L2\_LRNLTRK\_CONSTRT\_CTRL

BASE ADDRESS : 0xBB00CB34  
 ARRAY INDEX : 0 - 25  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x7FFF8

This is a One-Dimension Common Register Array.

Trunk learning constraint control register.

Bits	Field	Description	Type	Default
31:19	RESERVED			
18:3	CONSTRT_NUM	Learning constraint number of trunk port. 0x0: never learning 0x1~0xFFFFE: learning constraint number 0xFFFFF: unlimited	RW	0xFFFF
2:0	ACT	Learning constraint action of trunk port. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

### L2\_LRNLTRK\_CONSTRT\_CNT

BASE ADDRESS : 0xBB00CB9C  
 ARRAY INDEX : 0 - 25  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Trunk learning constraint counter register.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	LRN_CNT	Learnt counter of trunk port.	RW	0x0

### L2\_LRNLVLAN\_CONSTRT\_ENTRY

BASE ADDRESS : 0xBB00CC04

ARRAY INDEX : 0 - 31  
ARRAY OFFSET : 0x8  
DEFAULT VALUE : 0xFFFF, 0x0

This is a One-Dimension Common Register Array.

VLAN learning constraint entry.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:44	RESERVED			
43:32	FVID	The FVID to be compared.	RW	0x0
31:23	RESERVED			
22:16	PORT_ID	The port ID to be compared. <6:6>: trunk or physical port. 0b0: physical port 0b1: trunk port <5:0>: port Id or trunk id which is depends on <6:6>	RW	0x0
15:0	CONSTRT_NUM	Learning constraint number of VLAN learning constraint entry. 0x0: never learning 0x1~0xFFFFE: learning constraint number 0xFFFF: unlimited	RW	0xFFFF

## L2\_LRNL\_VLAN\_CONSTRT\_CNT

BASE ADDRESS : 0xBB00CD04  
ARRAY INDEX : 0 - 31  
ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

The learnt mac counter of VLAN constraint.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	LRN_CNT	Learnt count of VLAN learning constraint entry.	RW	0x0

## L2\_VLAN\_CONSTRT\_CTRL

REGISTER ADDRESS : 0xBB00CD84  
DEFAULT VALUE : 0x0

VLAN learning constraint control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			

Bits	Field	Description	Type	Default
2:0	ACT	VLAN learning constraint action. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

## L2\_TBL\_FLUSH\_CTRL

REGISTER ADDRESS : 0xBB00CD9C

DEFAULT VALUE : 0x0, 0x0

L2 table flush control register. Specify the fvid and/or port to flush entries or replace the SLP field of L2 table

Bits	Field	Description	Type	Default
63:61	RESERVED			
60	STS	It is used to trigger ASIC to start the flush/replace action. It will be auto cleared after the flush/replace process is finished. 0b0: finish 0b1: busy	RW	0x0
59	ACT	Flush or replace of entry. 0b0: flush 0b1: replace	RW	0x0
58	FVID_CMP	Whether compare FID/VID. 0b0: disable 0b1: enable	RW	0x0
57	AGG_VID_CMP	When GLB_VID_CNVT_EN=0, this field is defined as "Whether compare ECID"; When GLB_VID_CNVT_EN=1, this field is defined as "Whether compare AGG_VID". 0b0: disable 0b1: enable	RW	0x0
56	PORT_CMP	Whether compare PORT_ID. 0b0: disable 0b1: enable	RW	0x0
55:54	ENTRY_TYPE	The detail action type to take. 0x0: all L2 Unicast Entry 0x1: L2 dynamic, suspend and SABLK & DABLK Entry 0x2: NextHop Etnry 0x3: clear TAGSTS, AGG_VID and AGG_PRI	RW	0x0
53:43	PORT_ID	The port ID to be compared. <10:10>: trunk or physical port: 0b0: physical port 0b1: trunk In physical port, <5:0> is port ID and <9:6> is device ID. In trunk port, <6:0> is trunk ID and <9:7> is reserved.	RW	0x0

Bits	Field	Description	Type	Default
42:32	REPLACING_PORT_ID	Replaceing port ID. <10:10>: trunk or physical port. 0b0: physical port 0b1: trunk  In physical port, <5:0> is port ID and <9:6> is device ID. In trunk port, <6:0> is trunk ID and <9:7> is reserved.  Note: This field is meanful only when ACT = "replace".	RW	0x0
31:20	FVID	The FID/VID to be compared and replaced/flushed.	RW	0x0
19:0	AGG_VID	When GLB_VID_CNVT_EN=0, this field is defined as "ECID to be compared"; When GLB_VID_CNVT_EN=1, this field is defined as "AGG_VID to be compared".	RW	0x0

## L2\_SRC\_P\_FLTR

BASE ADDRESS : 0xBB00CDA4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

Source port filter configuration.

Bits	Field	Description	Type	Default
0	SRC_FLTR_EN	Decides whether filter the ingress port from outgoing portmask. 0b0: disable 0b1: enable	RW	0x1

## L2\_SA\_ACT\_REF

BASE ADDRESS : 0xBB00CDAC

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

L2 new sa action source configuration.

Bits	Field	Description	Type	Default
0	SRC	New SA action source select. 0b0: reference port configure 0b1: reference vlan profile configure	RW	0x0

Bits	Field	Description	Type	Default
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## L2\_HASH\_FULL\_CNT

REGISTER ADDRESS : 0xBB00CDB4

DEFAULT VALUE : 0x0

L2 hash full counter

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	CNT	L2 Learning Hash Full counter	RC	0x0

## L2\_CAM\_MC

TABLE TYPE : 0x1

TABLE INDEX : 0 - 127

L2 Table (Multicast)

Bits	Field	Description
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b0: L2 entry 0b01: OpenFlow
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124	RESERVED	
123:112	FID	FID/VID
111:64	MAC	MAC address
63	LOCAL_FWD	Forward packet to the portmask indexed by MCAST_PMSK_IDX.
62	NEXT_HOP	Next Hop (need to routing)
61:50	MC_PMSK_IDX	Multicast Portmask Index
49	REMOTE_FWD	Forward packet to the VXLAN tunnel(s) indexed by L2_TNL_LST_IDX.
48:36	L2_TNL_LST_IDX	L2 Tunnel List Index
35	BPE_TYPE	BPE type indicator
34:0	RESERVED	

## L2\_CAM\_UC

TABLE TYPE : 0x1

TABLE INDEX : 0 - 127

L2 Table (Unicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b0: L2 entry 0b1: OpenFlow
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124	RESERVED	
123:112	FID	FID/VID
111:64	MAC	MAC address
63	L2_TNL	L2 tunnel indicator
62	NEXT_HOP	Next Hop (need to routing)
61	IS_TRK	IPv6 Multicast 0b0: Non IPv6 multicast entry 0b1: IPv6 multicast entry
60:51	SPA	Source Port
50:48	AGE	Aging Time
47	SA_BLK	Source MAC address blocking
46	DA_BLK	Destination MAC address blocking
45	STATIC	Static entry
44	SUSPEND	Suspend (wait to be removed)
43	TAGSTS	VLAN ID (used by VLAN aggregation)
42:40	AGG_PRI	VLAN ID (used by VLAN aggregation)
39:28	AGG_VID	VLAN ID (used by VLAN aggregation)
27:0	RESERVED	

## L2\_CB\_MC

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Table (Control Bridge Multicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
95	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
94	FMT	Indicates entry format 0b00: L2 entry 0b01: OpenFlow
93	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
92	RESERVED	
91:80	FID	FID/VID
79:32	MAC	MAC address
31	LOCAL_FWD	Forward packet to the portmask indexed by MCAST_PMSK_IDX.
30	RESERVED	
29:18	MCAST_PMSK_IDX	Index to multicast portmask table. The field is used only if LOCAL_FWD=1.
17	ECID_LST_EN	ECID list enable bit 0b0: invalid ECID_PMSK_LST_IDX 0b1: valid ECID_PMSK_LST_IDX
16:4	ECID_PMSK_LST_IDX	Entry index of ECID and Portmask (ECID_PMSK_LST) table It's available when ECID_LST_EN is set to 1
3:0	RESERVED	

## L2\_CB\_UC

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Table (Control Bridge Unicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b00: L2 entry 0b01: OpenFlow
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124	RESERVED	
123:112	FID	FID/VID
111:64	MAC	MAC address
63	L2_TNL	L2 tunnel indicator
62	NEXT_HOP	Next Hop (need to routing)
61	IS_TRK	IPv6 Multicast 0b0: Non IPv6 multicast entry 0b1: IPv6 multicast entry

<b>Bits</b>	<b>Field</b>	<b>Description</b>
60:51	SPA	Source Port
50:48	AGE	Aging Time
47	SA_BLK	Source MAC address blocking
46	DA_BLK	Destination MAC address blocking
45	STATIC	Static entry
44	SUSPEND	Suspend (wait to be removed)
43:24	ECID	When GLB_VID_CNVT_EN = 0, this field is defined as E-CID. Bit[19:12]: Extension of the E-CID Bit[11:0]: Base of the E-CID When GLB_VID_CNVT_EN = 1, this field is interpreted as N:1 VLAN information. Then the associated E-CID of the entry should be treated as zero (0).
23:0	RESERVED	

## L2\_MC

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Table (Multicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b0: L2 entry 0b1: OpenFlow
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124	HASH_MSB	
123:112	FID	FID/VID
111:64	MAC	MAC address
63	LOCAL_FWD	Forward packet to the portmask indexed by MCAST_PMSK_IDX.
62	NEXT_HOP	Next Hop (need to routing)
61:50	MC_PMSK_IDX	Multicast Portmask Index
49	REMOTE_FWD	Forward packet to the VXLAN tunnel(s) indexed by L2_TNL_LST_IDX.
48:36	L2_TNL_LST_IDX	L2 Tunnel List Index
35	BPE_TYPE	BPE type indicator
34:0	RESERVED	

## L2\_UC

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Table (Unicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b0: L2 entry 0b01: OpenFlow
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124	HASH_MSB	Stores the MSB of hash value. It is used to restore complete FID.
123:112	FID	FID/VID
111:64	MAC	MAC address
63	L2_TNL	L2 tunnel indicator
62	NEXT_HOP	Next Hop (need to routing)
61	IS_TRK	IPv6 Multicast 0b0: Non IPv6 multicast entry 0b1: IPv6 multicast entry
60:51	SPA	Source Port
50:48	AGE	Aging Time
47	SA_BLK	Source MAC address blocking
46	DA_BLK	Destination MAC address blocking
45	STATIC	Static entry
44	SUSPEND	Suspend (wait to be removed)
43	TAGSTS	VLAN ID (used by VLAN aggregation)
42:40	AGG_PRI	VLAN ID (used by VLAN aggregation)
39:28	AGG_VID	VLAN ID (used by VLAN aggregation)
27:0	RESERVED	

## MC\_PMSK

TABLE TYPE : 0x2

TABLE INDEX : 0 - 4095

Multicast Portmask Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:7	PMSK	Portmask

Bits	Field	Description
6:0	RESERVED	

SECTION 5.6

## L2 & IP MULTICAST

L2 &amp; IP Multicast module

SECTION 5.7

## L2 ENTRY NOTIFICATION

L2 Entry Notification module

### L2\_NTFY\_CTRL

REGISTER ADDRESS : 0xBB00CDC8

DEFAULT VALUE : 0xE21FFE

Notification control register. If enabled, whenever L2 table is modified (new learn, age out, port move, or suspend learn) ASIC will make a notification to specific buffer.

Bits	Field	Description	Type	Default
31:24	RESERVED			
23	DYN_NTFY_AGE_OUT_EN	Enable dynamic entry age out notification. Note: works only if NTFY_EN and DYN_NTFY_EN are both enabled. 0b0: disable 0b1: enable	RW	0x1
22	DYN_NTFY_PORT_MV_EN	Enable dynamic entry port move notification. Note: works only if NTFY_EN and DYN_NTFY_EN are both enabled. 0b0: disable 0b1: enable	RW	0x1
21	DYN_NTFY_NEW_LRN_EN	Enable dynamic entry new learn notification. Note: works only if NTFY_EN and DYN_NTFY_EN are both enabled. 0b0: disable 0b1: enable	RW	0x1
20:19	NTFY_DST_TPYE	Type of Destion CPU 0b00: send to local CPU 0b01: send to master CPU 0b10: send to all CPU 0b11: interrupt mode (through NIC)	RW	0x0
18	HASHFULL_NTFY_EN	Hash full event notification enable or disable. 0: disable 1: enable	RW	0x0
17	LD_FLUSH_NTFY_EN	Linkdown flush entry event notification enable or disable. 0: disable 1: enable	RW	0x1
16	TAGSTS_NTFY_EN	Tag status or AGG_PRI or AGG_VID modification event notification enable or disable. 0: disable 1: enable	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15	SUS_NTFY_EN	Enables suspend entry new learn event notification. 0b0: disable 0b1: enable	RW	0x0
14	DASABLK_NTFY_EN	Enables SA/DA BLK entry notification. 0b0: disable 0b1: enable	RW	0x0
13	STTC_NTFY_EN	Enables static entry notification. 0b0: disable 0b1: enable	RW	0x0
12	DYN_NTFY_EN	Enables dynamic entry notification. 0b0: disable 0b1: enable	RW	0x1
11:1	BP_THR	The threshold to start backpressure (not time-out entry but still learning) 0x7ff: disable backpressure	RW	0x7FF
0	NTFY_EN	Enable entry notification. Defaultly, dynamic entry only. (include NEXTHOP, exclude SUSPEND entry) 0b0: disable 0b1: enable	RW	0x0

## L2\_NTFY\_PKT\_CTRL

REGISTER ADDRESS : 0xBB005704

DEFAULT VALUE : 0x1040

Packet mode notification control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:15	RESERVED			
14	ITAGIF	Decides whether contains inner tag	RW	0x0
13:7	MAX_EVENT	The max events contained in a notification packet.	RW	0x20
6:0	MIN_LEN	The min length of a notification packet.	RW	0x40

## L2\_NTFY\_PKT\_TIMEOUT

REGISTER ADDRESS : 0xBB005708

DEFAULT VALUE : 0xA

Packet mode notification timeout register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:0	TIMEOUT	Timeout to send notification packet. (unit: us)	RW	0x0000A

## L2\_NTFY\_PKT\_MAC

REGISTER ADDRESS : 0xBB00570C

DEFAULT VALUE : 0x0, 0x0, 0x0, 0x0

MAC address content register for packet mode notification.

Bits	Field	Description	Type	Default
127:112	RESERVED			
111:96	DMAC_HI	The address [47:32] of DMAC in a notification packet.	RW	0x0
95:64	DMAC_LO	The address [31:0] of DMAC in a notification packet	RW	0x0
63:48	RESERVED			
47:32	SMAC_HI	The address [47:32] of SMAC in a notification packet.	RW	0x0
31:0	SMAC_LO	The address [31:0] of SMAC in a notification packet	RW	0x0

## L2\_NTFY\_PKT\_ITAG

REGISTER ADDRESS : 0xBB00571C

DEFAULT VALUE : 0x0

Inner tag content register for external CPU.

Bits	Field	Description	Type	Default
31:0	RESERVED			

## L2\_NTFY\_RST

REGISTER ADDRESS : 0xBB00140C

DEFAULT VALUE : 0x0

Reset L2 notification state machine and local buffer

Bits	Field	Description	Type	Default
31:1	RESERVED			
0	RST	Reset L2 message state machine and local buffer. 0: done 1: reset and busy	RWAC	0x0

## L2\_NTFY\_PKT\_MAGIC\_NUM

REGISTER ADDRESS : 0xBB000580

DEFAULT VALUE : 0x0

L2 notification magic num in packet

Bits	Field	Description	Type	Default
31:8	RESERVED			
7:0	NUM	L2 notification magic number in packet mode	RW	0x0

## L2\_NTFY\_RING\_BASE\_ADDR

REGISTER ADDRESS : 0xBB0009DC

DEFAULT VALUE : 0x0

Get the first pointer address of notification descriptor ring.  
(word alignment, irrelevant, LSB 2 bits)

Bits	Field	Description	Type	Default
31:0	BASE_ADDR	Ring's base address	RW	0x0

## L2\_NTFY\_RING\_CUR\_ADDR

REGISTER ADDRESS : 0xBB0009E0

DEFAULT VALUE : 0x0

Get the current pointer address of notification descriptor ring.  
(word alignment, irrelevant, LSB 2 bits)

Bits	Field	Description	Type	Default
31:0	CUR_ADDR	Ring's current entry address	RO	0x0

## L2\_NTFY\_IF\_INTR\_MSK

REGISTER ADDRESS : 0xBB0009E4

DEFAULT VALUE : 0x0

Enable various interrupt signal of CPU interface.

Bits	Field	Description	Type	Default
31:3	RESERVED			

Bits	Field	Description	Type	Default
2	NTFY_DONE	Enable interrupt for successful DMA notification. 0b0: disable 0b1: enable	RW	0x0
1	NTFY_BUF_RUN_OUT	Enable interrupt for running out of notification descriptor. 0b0: disable 0b1: enable	RW	0x0
0	LOCAL_NTFY_BUF_RUN_OUT	Enable interrupt for running out of local notification buffer. 0b0: disable 0b1: enable	RW	0x0

## L2\_NTFY\_IF\_INTR\_STS

REGISTER ADDRESS : 0xBB0009E8

DEFAULT VALUE : 0x0

Get the various pending status of CPU interface.  
(Write '1' to clear)

Bits	Field	Description	Type	Default
31:3	RESERVED			
2	NTFY_DONE	Pending status of the NTFY_DONE interrupt.	RW1C	0x0
1	NTFY_BUF_RUN_OUT	Pending status of the NTFY_BUF_RUN_OUT interrupt.	RW1C	0x0
0	LOCAL_NTFY_BUF_RUN_OUT	Pending status of the LOCAL_NTFY_BUF_RUN_OUT interrupt.	RW1C	0x0

## L2\_NTFY\_NIC\_FIFO\_STS

REGISTER ADDRESS : 0xBB0009EC

DEFAULT VALUE : 0x10000000

Local buffer used count in Interrupt mode

Bits	Field	Description	Type	Default
31:25	RESERVED			
24	FIFO_EMPTY	The FIFO (local buffer) used in Interrupt mode is empty. 0b0: not empty 0b1: empty	RO	0x1
23:12	PEAK	The peak used count of local buffer in Interrupt mode	RC	0x0
11:0	CNT	The current used count of local buffer in Interrupt mode	RO	0x0

## L2\_NTFY\_PKT\_FIFO\_STS

REGISTER ADDRESS : 0xBB000584

DEFAULT VALUE : 0x1000000

Local buffer used count in Packet mode

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	FIFO_EMPTY	The FIFO (local buffer) used in Packet mode is empty. 0b0: not empty 0b1: empty	RO	0x1
23:12	PEAK	The peak used count of local buffer in Packet mode	RC	0x0
11:0	CNT	The current used count of local buffer in packet mode	RO	0x0

## L2\_NTFY\_NIC\_TIMEOUT

REGISTER ADDRESS : 0xBB0009F0

DEFAULT VALUE : 0x1000A

L2 message timeout use (unit is 1ms)

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:17	RESERVED			
16	CFG_L2MSG_TMROUT_EN	L2 message timeout enable	RW	0x1
15:0	TIMEOUT	L2 message timeout selection	RW	0x000A

## L2\_NTFY\_PKT\_QUE\_ID

REGISTER ADDRESS : 0xBB000588

DEFAULT VALUE : 0x0

The queue ID for notification packet

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:5	RESERVED			
4:0	QID	The queue ID for notification packet	RW	0x0

SECTION 5.8

**L2 MISC.**

L2 Misc. module

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## CHAPTER 6

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# Traffic Suppression

The chapter describes features related to traffic suppression

SECTION 6.1

## STORM CONTROL

Unknown Unicast/Multicast/Broadcast Storm Control and Protocol Storm Control modules

### **STORM\_CTRL**

REGISTER ADDRESS : 0xBB00B000

DEFAULT VALUE : 0x0

Storm control global configuration.

Bits	Field	Description	Type	Default
31:11	RESERVED			
10	INC_BYPASS_PKT	Storm control includes or excludes bypass packets. It applies to all storm control leaky buckets of all ports. 0b0: Exclude bypass packets. 0b1: Include bypass packets.	RW	0x0
9	INC_IFG	Storm control includes or excludes 12-byte IFG and 8-byte Preamble. It applies to all storm control leaky buckets of all ports. 0b0: Exclude 20Bytes. 0b1: Include 20Bytes.	RW	0x0
8	ARP_VLAN_CONSTRT	The state ARP protocol storm control constrained by VLAN module. If it's enabled, ARP protocol storm only suppresses the packet hitting the VLAN entry which GROUP_MASK<0> is 0b1. 0b0: disable 0b1: enable	RW	0x0
7	DHCP_VLAN_CONSTRT	The state DHCP protocol storm control constrained by VLAN module. If it's enabled, DHCP protocol storm only suppresses the packet hitting the VLAN entry which GROUP_MASK<1> is 0b1. 0b0: disable 0b1: enable	RW	0x0

Bits	Field	Description	Type	Default
6	ADMIT_RIP	Admit RIP(Routing Information Packet) and OSPF(Open Shortest Path First) packets bypassing storm control. 0b0: disable 0b1: enable	RW	0x0
5	ADMIT_DHCP	Admit DHCP and DHCPv6 packets bypassing storm control. 0b0: disable 0b1: enable	RW	0x0
4	ADMIT_ARPREQ	Admit ARP Request and Neighbor Solicitation for ICMPv6 (Type:135) packets bypassing storm control. 0b0: disable 0b1: enable	RW	0x0
3	ADMIT_RMA	Admit RMA (exclude BPDU) packet bypassing storm control. 0b0: disable 0b1: enable	RW	0x0
2	ADMIT_BPDU	Admit BPDU packet bypassing storm control. 0b0: disable 0b1: enable	RW	0x0
1	ADMIT_RTKPKT	Admit Realtek control packet (ethertype=0x8899) bypassing storm control. 0b0: disable 0b1: enable	RW	0x0
0	ADMIT_IGMP	Admit IPv4 IGMP, IPv6 MLDv1/v2 and PIM packets bypassing storm control. 0b0: disable 0b1: enable	RW	0x0

## STORM\_LB\_CTRL

REGISTER ADDRESS : 0xBB00B004

DEFAULT VALUE : 0x3500AB

Unicast, multicast and broadcast storm control leaky bucket refill configurations.

Bits	Field	Description	Type	Default
31:16	TICK	TICK is used for time slot size unit on all ports. In 650 MHz and 325 MHz clocks, the default value is 53 (0x35). In 175 MHz clock, the default value is 123 (0x7B). Note: If the clock rate is changed, please update the parameter.	RW	0x0035
15:0	TKN	TKN is used for adding budget in each time slot on all ports. In 650 MHz clock, the default value is 171 (0xAB). In 325 MHz clock, the default value is 342 (0x156). In 175 MHz clock, the default value is 1474 (0x5C2). Unit: Byte. Note: If the clock rate is changed, please update the parameter.	RW	0x00AB

Bits	Field	Description	Type	Default
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### STORM\_LB\_PPS\_CTRL

REGISTER ADDRESS : 0xBB00B008

DEFAULT VALUE : 0x271

PPS mode storm control leaky bucket refill configurations.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:4	TICK	<p>TICK is used for time slot size unit on all ports.  In 650 MHz clock, the default value is 39 (0x27).  In 325 MHz clock, the default value is 77 (0x4D).  In 175 MHz clock, the default value is 42 (0x2A).  Note: If the clock rate is changed, please update the parameter.</p>	RW	0x027
3:0	TKN	<p>TKN (token) is used for adding budget in each time slot on all ports.  In 650 MHz, 325 MHz and 175 MHz clocks, the default value is 1.  Unit: Packet.  Note: If the clock rate is changed, please update the parameter.</p>	RW	0x1

### STORM\_LB\_PROTO\_CTRL

REGISTER ADDRESS : 0xBB00A000

DEFAULT VALUE : 0x135F1B

Protocol storm control leaky bucket refill configurations.

Bits	Field	Description	Type	Default
31:0	TICK	<p>Time slot size unit for protocol storm control leaky bucket (PPS mode). The token (TKN) is fixed to 1.  Unit: clock  Note:  In 650 MHz clock and pps mode, the default value is 1,269,531 (0x135F1B).  In 325 MHz clock and pps mode, the default value is 634,766 (0x9AF8E).  In 175 MHz clock and pps mode, the default value is 341,797 (0x53725).  asic should change the value for different system clock.</p>	RW	0x00135F1B

### STORM\_PORT\_CTRL

**BASE ADDRESS :** 0xBB00B00C  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify ingress port storm control type

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	MODE	Storm control is based on packet count or byte count of port. 0b0: pps (packet per second) 0b1: bps (byte per second)	RW	0x0

### **STORM\_PORT\_UC\_CTRL**

**BASE ADDRESS :** 0xBB00B014  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 0x8  
**DEFAULT VALUE :** 0x8000, 0xFFFFF

This is a One-Dimension Port Register Array.

Ingress port unicast storm control configurations.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:58	RESERVED			
57	TYPE	Unicast type selection 0b0: unknown unicast 0b1: both known and unknown unicast	RW	0x0
56	EN	Unicast storm control status of port. 0b0: disable 0b1: enable	RW	0x0
55:32	RATE	Unicast storm control rate of port. In pps mode, the unit is 1pps and the valid bit is 24-bit in 650MHz. In 325MHz or 175MHz, the valid bit is 22-bit. In bps mode, the unit is 16kbps and the valid bit is 20-bit. Storm control rate in bps mode = RATE * 16 kbps (In value 0, the traffic is blocking)	RW	0xFFFFFFF
31:16	RESERVED			
15:0	BURST	Unicast burst size of storm control leaky bucket for specific port. In pps mode, the unit is 1 packet. In bps mode, the unit is 1 byte. Note: The value should be larger than Token value in pps mode.	RW	0x8000

## **STORM\_PORT\_UC\_LB\_RST**

BASE ADDRESS : 0xBB00B1DC

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port unicast storm control leaky bucket.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset unicast storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

## **STORM\_PORT\_UC\_EXCEED\_FLAG**

BASE ADDRESS : 0xBB00B1E4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port unicast storm control exceed status.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	FLAG	Unicast storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

## **STORM\_PORT\_MC\_CTRL**

BASE ADDRESS : 0xBB00B1EC

PORT INDEX : 0 - 56

PORT OFFSET : 0x8

DEFAULT VALUE : 0x8000, 0xFFFFF

This is a One-Dimension Port Register Array.

Ingress port multicast storm control configurations.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:58	RESERVED			
57	TYPE	Multicast type selection 0b0: unknown Multicast 0b1: both known and unknown Multicast	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
56	EN	Multicast storm control status of port. 0b0: disabled 0b1: enabled	RW	0x0
55:32	RATE	Multicast storm control rate of port. In pps mode, the unit is 1pps and the valid bit is 24-bit in 650MHz. In 325MHz or 175MHz, the valid bit is 22-bit. In bps mode, the unit is 16kbps and the valid bit is 20-bit. Storm control rate in bps mode = RATE * 16 kbps (In value 0, the traffic is blocking)	RW	0x0FFFFF
31:16	RESERVED			
15:0	BURST	Multicast burst size of storm control leaky bucket for specific port. In pps mode, the unit is 1 packet. In bps mode, the unit is 1 byte. Note: The value should be larger than Token value in pps mode.	RW	0x8000

## STORM\_PORT\_MC\_LB\_RST

BASE ADDRESS : 0xBB00B3B4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port multicast storm control leaky bucket.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset multicast storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

## STORM\_PORT\_MC\_EXCEED\_FLAG

BASE ADDRESS : 0xBB00B3BC

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port multicast storm control exceed status.

Bits	Field	Description	Type	Default
0	FLAG	Multicast storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

## STORM\_PORT\_BC\_CTRL

BASE ADDRESS : 0xBB00B3C4

PORT INDEX : 0 - 56

PORT OFFSET : 0x8

DEFAULT VALUE : 0x8000, 0xFFFFF

This is a One-Dimension Port Register Array.

Ingress port broadcast storm control configurations.

Bits	Field	Description	Type	Default
63:57	RESERVED			
56	EN	Broadcast storm control status of port. 0b0: disabled 0b1: enabled	RW	0x0
55:32	RATE	Broadcast storm control rate of port. In pps mode, the unit is 1pps and the valid bit is 24-bit in 650MHz. In 325MHz or 175MHz, the valid bit is 22-bit. In bps mode, the unit is 16kbps and the valid bit is 20-bit. Storm control rate in bps mode = RATE * 16 kbps (In value 0, the traffic is blocking)	RW	0x0FFFFF
31:16	RESERVED			
15:0	BURST	Broadcast burst size of storm control leaky bucket for specific port In pps mode, the unit is 1 packet. In bps mode, the unit is 1 byte. Note: The value should be larger than Token value in pps mode.	RW	0x8000

## STORM\_PORT\_BC\_LB\_RST

BASE ADDRESS : 0xBB00B58C

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port broadcast storm control leaky bucket.

Bits	Field	Description	Type	Default
0	RST	Reset broadcast storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

## STORM\_PORT\_BC\_EXCEED\_FLAG

BASE ADDRESS : 0xBB00B594

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port broadcast storm control exceed status.

Bits	Field	Description	Type	Default
0	FLAG	Broadcast storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

## STORM\_PORT\_PROTO\_DHCP\_CTRL

BASE ADDRESS : 0xBB00A004

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x1FF20

This is a One-Dimension Port Register Array.

Ingress port DHCP storm control configurations.

Bits	Field	Description	Type	Default
31:21	RESERVED			
20	EN	Status of DHCP storm control of port. 0b0: disable 0b1: enable	RW	0x0
19:17	RESERVED			
16:8	RATE	DHCP storm control rate of port. Unit: 1pps	RW	0x1FF
7:0	BURST	Burst size of DHCP storm control of port. The unit is 1 packet. Note: The value should be larger than Token value in pps mode.	RW	0x20

## **STORM\_PORT\_PROTO\_DHCP\_LB\_RST**

BASE ADDRESS : 0xBB00A0E8

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port DHCP storm control leaky bucket.

Bits	Field	Description	Type	Default
0	RST	Reset DHCP storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

## **STORM\_PORT\_PROTO\_DHCP\_EXCEED\_FLAG**

BASE ADDRESS : 0xBB00A0F0

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port DHCP storm control exceed status.

Bits	Field	Description	Type	Default
0	FLAG	DHCP storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

## **STORM\_PORT\_PROTO\_BPDU\_CTRL**

BASE ADDRESS : 0xBB00A0F8

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x1FF20

This is a One-Dimension Port Register Array.

Ingress port BPDU storm control configurations.

Bits	Field	Description	Type	Default
31:21	RESERVED			
20	EN	Status of BPDU storm control of port. 0b0: disable 0b1: enable	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
19:17	RESERVED			
16:8	RATE	BPDUs storm control rate of port. Unit: 1pps	RW	0x1FF
7:0	BURST	Burst size of BPDUs storm control of port. The unit is 1 packet. Note: The value should be larger than Token value in pps mode.	RW	0x20

### **STORM\_PORT\_PROTO\_BPDU\_LB\_RST**

BASE ADDRESS : 0xBB00A1DC

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port BPDUs storm control leaky bucket.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset BPDUs storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

### **STORM\_PORT\_PROTO\_BPDU\_EXCEED\_FLAG**

BASE ADDRESS : 0xBB00A1E4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port BPDUs storm control exceed status.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	FLAG	BPDUs storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

### **STORM\_PORT\_PROTO\_IGMP\_CTRL**

BASE ADDRESS : 0xBB00A1EC

PORT INDEX : 0 - 56

**PORT OFFSET :** 0x4  
**DEFAULT VALUE :** 0x1FF20

This is a One-Dimension Port Register Array.

Ingress port IGMP storm control configurations.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20	EN	Status of IGMP storm control of port. 0b0: disable 0b1: enable	RW	0x0
19:17	RESERVED			
16:8	RATE	IGMP storm control rate of port. Unit: 1pps	RW	0x1FF
7:0	BURST	Burst size of IGMP storm control of port. The unit is 1 packet. Note: The value should be larger than Token value in pps mode.	RW	0x20

### **STORM\_PORT\_PROTO\_IGMP\_LB\_RST**

**BASE ADDRESS :** 0xBB00A2D0  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port IGMP storm control leaky bucket.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset IGMP storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

### **STORM\_PORT\_PROTO\_IGMP\_EXCEED\_FLAG**

**BASE ADDRESS :** 0xBB00A2D8  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port IGMP storm control exceed status.

Bits	Field	Description	Type	Default
0	FLAG	IGMP storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

## STORM\_PORT\_PROTO\_ARP\_CTRL

BASE ADDRESS : 0xBB00A2E0

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x1FF20

This is a One-Dimension Port Register Array.

Ingress port ARP storm control configurations.

Bits	Field	Description	Type	Default
31:21	RESERVED			
20	EN	Status of ARP storm control of port. 0b0: disable 0b1: enable	RW	0x0
19:17	RESERVED			
16:8	RATE	ARP storm control rate of port. Unit: 1pps	RW	0x1FF
7:0	BURST	Burst size of ARP storm control for specific port. The unit is 1 packet. Note: The value should be larger than Token value in pps mode.	RW	0x20

## STORM\_PORT\_PROTO\_ARP\_LB\_RST

BASE ADDRESS : 0xBB00A3C4

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port ARP storm control leaky bucket.

Bits	Field	Description	Type	Default
0	RST	Reset ARP storm control leaky bucket token for specific port. Write 1 to reset.	RWAC	0x0

## **STORM\_PORT\_PROTO\_ARP\_EXCEED\_FLAG**

BASE ADDRESS : 0xBB00A3CC

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate port ARP storm control exceed status.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	FLAG	ARP storm control meter exceed status of port n. Write 1 to clear. 0b0: not exceed 0b1: exceed	RW1C	0x0

### SECTION 6.2

## **INGRESS BANDWIDTH CONTROL**

Ingress Bandwidth Control module

### **IGBW\_CTRL**

REGISTER ADDRESS : 0xBB00E000

DEFAULT VALUE : 0x0

Ingress bandwidth control global control register 0.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:9	RESERVED			
8	INC_BYPASS_PKT	Ingress bandwidth control includes or excludes bypass packets. It applies to all ports. 0b0: exclude 0b1: include	RW	0x0
7	INC_IFG	Ingress bandwidth control includes or excludes the Preamble & IFG (20 Bytes). 0b0: exclude 0b1: include	RW	0x0
6	ADMIT_RIP	Admit RIP(Routing Information Packet) and OSPF(Open Shortest Path First) packets bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0
5	ADMIT_DHCP	Admit DHCP and DHCPv6 packets bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0
4	ADMIT_ARPREQ	Admit ARP Request and Neighbor Solicitation for ICMPv6 (Type:135) packets bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0

Bits	Field	Description	Type	Default
3	ADMIT_RMA	Admit RMA (exclude BPDU) packet bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0
2	ADMIT_BPDU	Admit BPDU packet bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0
1	ADMIT_RTKPKT	Admit Realtek control packet (ethertype=0x8899) bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0
0	ADMIT_IGMP	Admit IPv4 IGMP, IPv6 MLDv1/v2 and PIM packets bypassing ingress bandwidth control. 0b0: disable 0b1: enable	RW	0x0

### IGBW\_LB\_CTRL

REGISTER ADDRESS : 0xBB00E004

DEFAULT VALUE : 0x3500AB

Specify ingress port leaky bucket <T, B>

Bits	Field	Description	Type	Default
31:16	TICK	TICK is used for time slot size unit on all ports. In 650 MHz and 325 MHz clocks, the default value is 53 (0x35). In 175 MHz clock, the default value is 123 (0x7B). Note: If the clock rate is changed, please update the parameter.	RW	0x0035
15:0	TKN	TKN (token) is used for adding budget in each time slot on all ports. In 650 MHz clock, the default value is 171 (0xAB). In 325 MHz clock, the default value is 342 (0x156). In 175 MHz clock, the default value is 1474 (0x5C2). Unit: Byte. Note: If the clock rate is changed, please update the parameter.	RW	0x00AB

### IGBW\_PORT\_CTRL

BASE ADDRESS : 0xBB00E008

PORT INDEX : 0 - 56

PORT OFFSET : 0x8

DEFAULT VALUE : 0x1194, 0xFFFFF

This is a One-Dimension Port Register Array.

Ingress port bandwidth control configuration.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:53	RESERVED			
52	EN	The state of ingress bandwidth control of port. 0b0: disable 0b1: enable Note: if leaky bucket is disabled, leaky bucket token counter of the port will be cleared by ASIC.	RW	0x0
51:32	RATE	Ingress bandwidth of port. Unit: 16kbps Controlled rate = RATE * 16kbps Value 0 is blocking.	RW	0xFFFFF
31:16	RESERVED			
15:0	BURST	Burst size (high threshold) of ingress bandwidth WFQ LB of port. Unit: Byte	RW	0x1194

### **IGBW\_PORT\_LB\_RST**

BASE ADDRESS : 0xBB00E1D0  
PORT INDEX : 0 - 56  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset ingress port leaky bucket

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset ingress bandwidth LB token of all WFQ queues for specified port. Write 1 to clear.	RWAC	0x0

### **IGBW\_PORT\_EXCEED\_FLAG**

BASE ADDRESS : 0xBB00E1D8  
PORT INDEX : 0 - 56  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate ingress bandwidth exceed

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	FLAG	Indicate ingress bandwidth exceed of port.	RW1C	0x0

Bits	Field	Description	Type	Default
------	-------	-------------	------	---------

## IGBW\_PORT\_FC\_CTRL

BASE ADDRESS : 0xBB00E1E0  
 PORT INDEX : 0 - 56  
 PORT OFFSET : 1 bit  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Indicate ingress bandwidth flow control ability

Bits	Field	Description	Type	Default
0	EN	The state of ingress bandwidth control flowCtrl ability of port. 0b0: disable 0b1: enable	RW	0x0

## IGBW\_PAGE\_CNT\_CTRL

REGISTER ADDRESS : 0xBB00EBE0  
 DEFAULT VALUE : 0x0

Ingress bandwidth debug counter configuration.

Bits	Field	Description	Type	Default
31:10	RESERVED			
9	RST	Reset all maximum used page count counters of ingress system queue (bypass queue and drop queue). 0b0: normal 0b1: reset	RWAC	0x0
8	PORT_RST	Reset all maximum used page count counters of ingress queue for specified port defined by PORT_IDX. 0b0: normal 0b1: reset	RWAC	0x0
7:6	RESERVED			
5:0	PORT_IDX	Select port index of ingress queue used page count.	RW	0x0

SECTION 6.3

## EGRESS BANDWIDTH CONTROL

Egress Port and Egress Queue Bandwidth Control module

## **EGBW\_ENCAP\_CTRL**

REGISTER ADDRESS : 0xBB004D8C

DEFAULT VALUE : 0x0

Egress bandwidth encapsulate control global control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	CPU_TAG_FEED_BACK	Enable encapsulate feed back when add CPU Tag in BPS mode. 0b0: Disable 0b1: Enable (Including CPU Tag)	RW	0x0

## **EGBW\_CTRL**

REGISTER ADDRESS : 0xEB002158

DEFAULT VALUE : 0x0

Egress bandwidth control global control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:2	RESERVED			
1	INC_IFG	Egress bandwidth control includes or excludes the Preamble & IFG (20 Bytes). 0b0: exclude 0b1: include	RW	0x0
0	RATE_MODE_CPU	CPU port rate limit mode. 0b0: pps 0b1: bps	RW	0x0

## **EGBW\_CPU\_PPS\_LB\_CTRL**

REGISTER ADDRESS : 0xBB00215C

DEFAULT VALUE : 0x26C01

Egress bandwidth leaky bucket configuration of CPU port PPS mode.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:8	TICK	Time slot size unit for PPS mode leaky bucket of CPU port. In 650 MHz clock, the default value is 620 (0x26c). In 325 MHz clock, the default value is 310 (0x136). In 175 MHz clock, the default value is 167 (0xA7). Note: asic should change the value for different system clock.	RW	0x00026C

Bits	Field	Description	Type	Default
7:0	TKN	TKN is used for adding budget in each time slot for PPS mode leaky bucket of CPU port. In 650 MHz, 325 MHz and 175 MHz clock, the default value is 1. Unit: Packet.	RW	0x01

## EGBW\_LB\_CTRL

REGISTER ADDRESS : 0xBB002160

DEFAULT VALUE : 0x3500AB

Egress bandwidth leaky bucket configuration.

Bits	Field	Description	Type	Default
31:16	TICK	TICK is used for time slot size unit on all port. In 650 MHz and 325 MHz clocks, the default value is 53 (0x35). In 175 MHz clock, the default value is 62 (0x3E). Note: When the clock rate is changed, please update the parameter.	RW	0x0035
15:0	TKN	TKN is used for adding budget in each time slot on all port. In 650 MHz clock, the default value is 171 (0xAB). In 325 MHz clock, the default value is 342 (0x156). In 175 MHz clock, the default value is 743 (0x2E7). Unit: Byte. Note: When the clock rate is changed, please update the parameter.	RW	0x00AB

## EGBW\_PORT\_CTRL

BASE ADDRESS : 0xBB002164

PORT INDEX : 0 - 56

PORT OFFSET : 0x8

DEFAULT VALUE : 0x1194, 0xFFFFF

This is a One-Dimension Port Register Array.

Egress port bandwidth control configuration.

Bits	Field	Description	Type	Default
63:53	RESERVED			
52	EN	The state of egress bandwidth control of port. 0b0: disable 0b1: enable Note: if bandwidth is disabled, leaky bucket token counter of the port will be cleared by ASIC.	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
51:32	RATE	Egress bandwidth of port. Unit: 16kbps Egress bandwidth rate = RATE * 16kbps Value 0 is blocking. Note: CPU port in PPS mode, the Unit is packet.	RW	0xFFFFF
31:16	RESERVED			
15:0	BURST	Burst size of egress bandwidth leaky bucket of port. Unit: Byte Note: CPU port in PPS mode, the Unit is packet.	RW	0x1194

### **EGBW\_PORT\_LB\_RST**

BASE ADDRESS : 0xBB00232C  
 PORT INDEX : 0 - 56  
 PORT OFFSET : 1 bit  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset all WFQ leaky buckets of specified egress port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset egress bandwidth bucket tokens of WFQ leaky buckets and port leaky bucket for specified port.	RWAC	0x0

### **EGBW\_PORT\_Q\_MAX\_LB\_RST\_SET0**

BASE ADDRESS : 0xBB002334  
 ARRAY INDEX1 : 0 - 51  
 ARRAY OFFSET1 : 0x4  
 ARRAY INDEX2 : 0 - 7  
 ARRAY OFFSET2 : 1 bit  
 DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

Reset egress queue maximum bandwidth leaky bucket of normal port 0 ~ port 51.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset egress queue maximum bandwidth leaky bucket token of specified queue.	RWAC	0x0

## EGBW\_PORT\_Q\_MAX\_LB\_RST\_SET1

**BASE ADDRESS :** 0xBB002404  
**ARRAY INDEX1 :** 52 - 55  
**ARRAY OFFSET1 :** 0x4  
**ARRAY INDEX2 :** 0 - 11  
**ARRAY OFFSET2 :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

Reset egress queue maximum bandwidth leaky bucket of normal port 52 ~ port 55.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset egress queue maximum bandwidth leaky bucket token of specified queue.	RWAC	0x0

## EGBW\_CPU\_Q\_MAX\_LB\_CTRL

**BASE ADDRESS :** 0xBB002414  
**ARRAY INDEX :** 0 - 31  
**ARRAY OFFSET :** 0x8  
**DEFAULT VALUE :** 0x1194, 0xFFFFF

This is a One-Dimension Common Register Array.

Specify CPU port egress queue max bandwidth control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:53	RESERVED			
52	EN	The state of egress queue maximum bandwidth control of queue. 0b0: disable 0b1: enable Note: if leaky bucket is disabled, leaky bucket token counter of the port will be cleared by ASIC.	RW	0x0
51:32	RATE	Egress queue maximum bandwidth. Unit: 16kbps Egress bandwidth rate = RATE * 16kbps Value 0 is blocking.	RW	0xFFFFF
31:16	RESERVED			
15:0	BURST	Burst size of egress queue maximum bandwidth LB. Unit: Byte	RW	0x1194

## EGBW\_CPU\_Q\_MAX\_LB\_RST

**BASE ADDRESS :** 0xBB002514

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Reset egress queue maximum bandwidth leaky bucket of CPU port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset egress queue maximum bandwidth leaky bucket token for specified queue of CPU port.	RWAC	0x0

### **EGBW\_PORT\_Q\_ASSURED\_LB\_RST\_SET0**

BASE ADDRESS : 0xBB002518

ARRAY INDEX1 : 0 - 51

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 7

ARRAY OFFSET2 : 1 bit

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

Reset egress queue assured bandwidth leaky bucket of port 0 ~ port 51.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset egress queue assured leaky bucket token of specified queue.	RWAC	0x0

### **EGBW\_PORT\_Q\_ASSURED\_LB\_RST\_SET1**

BASE ADDRESS : 0xBB0025E8

ARRAY INDEX1 : 52 - 55

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 11

ARRAY OFFSET2 : 1 bit

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

Reset egress queue assured bandwidth leaky bucket of port 52 ~ port 55.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RST	Reset egress queue assured leaky bucket token of specified queue.	RWAC	0x0

## EGR\_Q\_BW

TABLE TYPE : 0x0

TABLE INDEX : 0 - 55

Egress Queue Bandwidth

<b>Bits</b>	<b>Field</b>	<b>Description</b>
927	ASSURED_MODE_Q11	Assured leaky bucket mode for queue 11 of specified port. 0b0: Shared bandwidth. 0b1: Fixed bandwidth.
926	ASSURED_MODE_Q10	Assured leaky bucket mode for queue 10 of specified port.
925	ASSURED_MODE_Q9	Assured leaky bucket mode for queue 9 of specified port.
924	ASSURED_MODE_Q8	Assured leaky bucket mode for queue 8 of specified port.
923	ASSURED_MODE_Q7	Assured leaky bucket mode for queue 7 of specified port.
922	ASSURED_MODE_Q6	Assured leaky bucket mode for queue 6 of specified port.
921	ASSURED_MODE_Q5	Assured leaky bucket mode for queue 5 of specified port.
920	ASSURED_MODE_Q4	Assured leaky bucket mode for queue 4 of specified port.
919	ASSURED_MODE_Q3	Assured leaky bucket mode for queue 3 of specified port.
918	ASSURED_MODE_Q2	Assured leaky bucket mode for queue 2 of specified port.
917	ASSURED_MODE_Q1	Assured leaky bucket mode for queue 1 of specified port.
916	ASSURED_MODE_Q0	Assured leaky bucket mode for queue 0 of specified port.
915:900	ASSURED_LB_BURST_Q11	Burst size of assured bandwidth leaky bucket for queue 11 of specified port. Unit: Byte
899:884	ASSURED_LB_BURST_Q10	Burst size of assured bandwidth leaky bucket for queue 10 of specified port.
883:868	ASSURED_LB_BURST_Q9	Burst size of assured bandwidth leaky bucket for queue 9 of specified port.
867:852	ASSURED_LB_BURST_Q8	Burst size of assured bandwidth leaky bucket for queue 8 of specified port.
851:836	ASSURED_LB_BURST_Q7	Burst size of assured bandwidth leaky bucket for queue 7 of specified port.
835:820	ASSURED_LB_BURST_Q6	Burst size of assured bandwidth leaky bucket for queue 6 of specified port.
819:804	ASSURED_LB_BURST_Q5	Burst size of assured bandwidth leaky bucket for queue 5 of specified port.
803:788	ASSURED_LB_BURST_Q4	Burst size of assured bandwidth leaky bucket for queue 4 of specified port.
787:772	ASSURED_LB_BURST_Q3	Burst size of assured bandwidth leaky bucket for queue 3 of specified port.
771:756	ASSURED_LB_BURST_Q2	Burst size of assured bandwidth leaky bucket for queue 2 of specified port.
755:740	ASSURED_LB_BURST_Q1	Burst size of assured bandwidth leaky bucket for queue 1 of specified port.
739:724	ASSURED_LB_BURST_Q0	Burst size of assured bandwidth leaky bucket for queue 0 of specified port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
723:704	ASSURED_BW_Q11	Rate of assured bandwidth for queue 11 of specified port. Rate is configured value * 16kbps, and value 0 is blocking. Unit: 16kbps
703:684	ASSURED_BW_Q10	Rate of assured bandwidth for queue 10 of specified port.
683:664	ASSURED_BW_Q9	Rate of assured bandwidth for queue 9 of specified port.
663:644	ASSURED_BW_Q8	Rate of assured bandwidth for queue 8 of specified port.
643:624	ASSURED_BW_Q7	Rate of assured bandwidth for queue 7 of specified port.
623:604	ASSURED_BW_Q6	Rate of assured bandwidth for queue 6 of specified port.
603:584	ASSURED_BW_Q5	Rate of assured bandwidth for queue 5 of specified port.
583:564	ASSURED_BW_Q4	Rate of assured bandwidth for queue 4 of specified port.
563:544	ASSURED_BW_Q3	Rate of assured bandwidth for queue 3 of specified port.
543:524	ASSURED_BW_Q2	Rate of assured bandwidth for queue 2 of specified port.
523:504	ASSURED_BW_Q1	Rate of assured bandwidth for queue 1 of specified port.
503:484	ASSURED_BW_Q0	Rate of assured bandwidth for queue 0 of specified port.
483	ASSURED_BW_EN_Q11	Assured bandwidth state for queue 11 of specified port. 0b0: disable 0b1: enable Note: if bandwidth is disabled, leaky bucket token counter of the queue will be cleared by ASIC.
482	ASSURED_BW_EN_Q10	Assured bandwidth state for queue 10 of specified port.
481	ASSURED_BW_EN_Q9	Assured bandwidth state for queue 9 of specified port.
480	ASSURED_BW_EN_Q8	Assured bandwidth state for queue 8 of specified port.
479	ASSURED_BW_EN_Q7	Assured bandwidth state for queue 7 of specified port.
478	ASSURED_BW_EN_Q6	Assured bandwidth state for queue 6 of specified port.
477	ASSURED_BW_EN_Q5	Assured bandwidth state for queue 5 of specified port.
476	ASSURED_BW_EN_Q4	Assured bandwidth state for queue 4 of specified port.
475	ASSURED_BW_EN_Q3	Assured bandwidth state for queue 3 of specified port.
474	ASSURED_BW_EN_Q2	Assured bandwidth state for queue 2 of specified port.
473	ASSURED_BW_EN_Q1	Assured bandwidth state for queue 1 of specified port.
472	ASSURED_BW_EN_Q0	Assured bandwidth state for queue 0 of specified port.
471:456	MAX_LB_BURST_Q11	Burst size of maximum bandwidth leaky bucket for queue 11 of specified port. Unit: Byte
455:440	MAX_LB_BURST_Q10	Burst size of maximum bandwidth leaky bucket for queue 10 of specified port.
439:424	MAX_LB_BURST_Q9	Burst size of maximum bandwidth leaky bucket for queue 9 of specified port.
423:408	MAX_LB_BURST_Q8	Burst size of maximum bandwidth leaky bucket for queue 8 of specified port.
407:392	MAX_LB_BURST_Q7	Burst size of maximum bandwidth leaky bucket for queue 7 of specified port.
391:376	MAX_LB_BURST_Q6	Burst size of maximum bandwidth leaky bucket for queue 6 of specified port.
375:360	MAX_LB_BURST_Q5	Burst size of maximum bandwidth leaky bucket for queue 5 of specified port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
359:344	MAX_LB_BURST_Q4	Burst size of maximum bandwidth leaky bucket for queue 4 of specified port.
343:328	MAX_LB_BURST_Q3	Burst size of maximum bandwidth leaky bucket for queue 3 of specified port.
327:312	MAX_LB_BURST_Q2	Burst size of maximum bandwidth leaky bucket for queue 2 of specified port.
311:296	MAX_LB_BURST_Q1	Burst size of maximum bandwidth leaky bucket for queue 1 of specified port.
295:280	MAX_LB_BURST_Q0	Burst size of maximum bandwidth leaky bucket for queue 0 of specified port.
279:260	MAX_BW_Q11	Rate of maximum bandwidth for queue 11 of specified port. Rate is configured value * 16kbps, and value 0 is blocking. Unit: 16kbps
259:240	MAX_BW_Q10	Rate of maximum bandwidth for queue 10 of specified port.
239:220	MAX_BW_Q9	Rate of maximum bandwidth for queue 9 of specified port.
219:200	MAX_BW_Q8	Rate of maximum bandwidth for queue 8 of specified port.
199:180	MAX_BW_Q7	Rate of maximum bandwidth for queue 7 of specified port.
179:160	MAX_BW_Q6	Rate of maximum bandwidth for queue 6 of specified port.
159:140	MAX_BW_Q5	Rate of maximum bandwidth for queue 5 of specified port.
139:120	MAX_BW_Q4	Rate of maximum bandwidth for queue 4 of specified port.
119:100	MAX_BW_Q3	Rate of maximum bandwidth for queue 3 of specified port.
99:80	MAX_BW_Q2	Rate of maximum bandwidth for queue 2 of specified port.
79:60	MAX_BW_Q1	Rate of maximum bandwidth for queue 1 of specified port.
59:40	MAX_BW_Q0	Rate of maximum bandwidth for queue 0 of specified port.
39	MAX_BW_EN_Q11	Maximum bandwidth state for queue 11 of specified port. 0b0: disable 0b1: enable Note: if bandwidth is disabled, leaky bucket token counter of the queue will be cleared by ASIC.
38	MAX_BW_EN_Q10	Maximum bandwidth state for queue 10 of specified port.
37	MAX_BW_EN_Q9	Maximum bandwidth state for queue 9 of specified port.
36	MAX_BW_EN_Q8	Maximum bandwidth state for queue 8 of specified port.
35	MAX_BW_EN_Q7	Maximum bandwidth state for queue 7 of specified port.
34	MAX_BW_EN_Q6	Maximum bandwidth state for queue 6 of specified port.
33	MAX_BW_EN_Q5	Maximum bandwidth state for queue 5 of specified port.
32	MAX_BW_EN_Q4	Maximum bandwidth state for queue 4 of specified port.
31	MAX_BW_EN_Q3	Maximum bandwidth state for queue 3 of specified port.
30	MAX_BW_EN_Q2	Maximum bandwidth state for queue 2 of specified port.
29	MAX_BW_EN_Q1	Maximum bandwidth state for queue 1 of specified port.
28	MAX_BW_EN_Q0	Maximum bandwidth state for queue 0 of specified port.
27:0	RESERVED	

## SECTION 6.4

## METER MARKER

Meter Marker (Dual Leaky Bucket/srTCM/trTCM) module

### **METER\_GLB\_CTRL**

REGISTER ADDRESS : 0xBB00411C

DEFAULT VALUE : 0x0

Meter table global control register.

Bits	Field	Description	Type	Default
31:3	RESERVED			
2	TRTCM_TYPE	Select trTCM type 0b0: trTCM 0b1: modified trTCM	RW	0x0
1	SRTCM_SMART_TKN	If $T0(t) < TKN \&& T1(t) >= TKN$ , $T1(t) = T1(t) + B$ Else $T0(t) = T0(t) + B$  1'b0: Enable 1'b1: Disable	RW	0x0
0	INCL_PREIFG	Packet length including preamble and IFG or not 0b0: exclude 0b1: include	RW	0x0

### **METER\_LB\_EXCEED\_STS**

BASE ADDRESS : 0xBB004120

ARRAY INDEX1 : 0 - 15

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 31

ARRAY OFFSET2 : 1 bit

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

meter leaky buckets exceeding status. A bit would be set to 1 for a meter if it satisfies "drop packet" or "color packet red". It is write 1 to clear.

Bits	Field	Description	Type	Default
0	LB_EXCEED	1 bit flag per meter entry to show if the meter entry ever drops a packet or color a packet to red. Write 1 to clear. 0b0: no packet is dropped or colored red by this meter 0b1: some packets are ever dropped or colored red by this meter	RW1C	0x0

## METER\_LB\_GLB\_EXCEED\_STS

REGISTER ADDRESS : 0xBB004160

DEFAULT VALUE : 0x0

Meter leaky buckets aggregated exceeding status. A 32 bits mask by aggregated 512 bits LB\_EXCEED every 16 bits.

Bits	Field	Description	Type	Default
31:0	LB_EXCEED	Meter leaky buckets aggregated exceeding status. A bit stands to 16 bits aggregated result of LB_EXCEED, for example, LB_GLB_EXCEED[0] aggregates LB_EXCEED[0][0] ~ LB_EXCEED[0][15]. If any of the cared 16 bits of LB_EXCEED is 1, the respective LB_GLB_EXCEED bit is 1. It is read only.	RO	0x0

## METER

TABLE TYPE : 0x1

TABLE INDEX : 0 - 511

Meter entry configuration

Bits	Field	Description
95:94	TYPE	Entry Type 0x0: Disable 0x1: DLB 0x2: srTCM 0x3: trTCM
93	MODE	Entry mode 0b0: byte-based 0b1: packet-based
92	COLOR_AWARE	0: color blind 1: color aware
91	RST	Reset specific entry leaky bucket
90:71	LB0_RATE	rate of meter entry leaky bucket 0 byte mode unit is 16Kbps packet mode unit is 1 pps
70:54	LB0_BS	burst size of meter entry leaky bucket 0 byte mode unit is 128 bytes packet mode unit is 1 packet
53:34	LB1_RATE	rate of meter entry leaky bucket 1 byte mode unit is 16Kbps packet mode unit is 1 pps
33:17	LB1_BS	burst size of meter entry leaky bucket 1 byte mode unit is 128 bytes packet mode unit is 1 packet
16:0	RSERVED	

## CHAPTER 7

# Buffer Management

The chapter describes features related to buffer management

### SECTION 7.1

## FLOWCONTROL & BACKPRESSURE

Flow Control, Egress Drop and Backpressure module

### FC\_CTRL

REGISTER ADDRESS : 0xBB005048

DEFAULT VALUE : 0x1

Flow control global configuration.

Bits	Field	Description	Type	Default
31:1	RESERVED			
0	GUAR_PAGE_EN	Guarantee page state for all ports. 0b0: disabled 0b1: enabled. Reserved page. Other ports can't use it.	RW	0x1

### FC\_PORT\_ACT\_CTRL

BASE ADDRESS : 0xBB00504C

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x1000008

This is a One-Dimension Port Register Array.

Specify action to take if pause frame is sent but still receive packets.

Bits	Field	Description	Type	Default
31:25	RESERVED			
24	ACT	Flow control action of port. If ACT=0b0, after Pause ON frame is sent, incoming packets always can be received. 0b0: Receive 0b1: Drop	RW	0x1

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
23:13	RESERVED			
12:0	ALLOW_PAGE_CNT	Flow control allowed page count of port. In FC_ACT = Drop, after Pause ON frame is sent, the number of pages that port can receive is AL- LOW_PAGE_CNT.	RW	0x0008

### **FC\_GLB\_SYS\_UTIL\_THR**

REGISTER ADDRESS : 0xBB005130  
DEFAULT VALUE : 0x348

System packet buffer utilization threshold.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:13	RESERVED			
12:0	THR	System packet buffer utilization threshold.	RW	0x0348

### **FC\_GLB\_DROP\_THR**

REGISTER ADDRESS : 0xBB005134  
DEFAULT VALUE : 0x16C1

System force drop threshold. The packet is dropped immediately regardless flow control state if the threshold is reached.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:13	RESERVED			
12:0	DROP_ALL	System force drop threshold.	RW	0x16C1

### **FC\_GLB\_HI\_THR**

REGISTER ADDRESS : 0xBB005138  
DEFAULT VALUE : 0xDF40DC2

Specify global high on/off threshold for flow control on.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The high on threshold of public used page count.	RW	0x0DF4
15:13	RESERVED			
12:0	OFF	The high off threshold of public used page count.	RW	0x0DC2

## **FC\_GLB\_LO\_THR**

REGISTER ADDRESS : 0xBB00513C

DEFAULT VALUE : 0x9CE099C

Specify global low on/off threshold for flow control on.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The low on threshold of public used page count.	RW	0x09CE
15:13	RESERVED			
12:0	OFF	The low off threshold of public used page count.	RW	0x099C

## **FC\_GLB\_FCOFF\_HI\_THR**

REGISTER ADDRESS : 0xEB005140

DEFAULT VALUE : 0xDF40DC2

Specify global high on/off threshold for flow control off.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The high on threshold of public used page count in flow control off state.	RW	0x0DF4
15:13	RESERVED			
12:0	OFF	The high off threshold of public used page count in flow control off state.	RW	0x0DC2

## **FC\_GLB\_FCOFF\_LO\_THR**

REGISTER ADDRESS : 0xBB005144

DEFAULT VALUE : 0x9CE099C

Specify global low on/off threshold for flow control off.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The low on threshold of public used page count in flow control off state.	RW	0x09CE
15:13	RESERVED			
12:0	OFF	The low off threshold of public used page count in flow control off state.	RW	0x099C

## **FC\_JUMBO\_HI\_THR**

REGISTER ADDRESS : 0xBB005148

DEFAULT VALUE : 0x7A0076E

Specify jumbo mode high on/off threshold for flow control on.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The high on threshold of public used page count for jumbo mode.	RW	0x07A0
15:13	RESERVED			
12:0	OFF	The high off threshold of public used page count for jumbo mode.	RW	0x076E

## **FC\_JUMBO\_LO\_THR**

REGISTER ADDRESS : 0xBB00514C

DEFAULT VALUE : 0x37A0348

Specify jumbo mode low on/off threshold for flow control on.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The low on threshold of public used page count for jumbo mode.	RW	0x037A
15:13	RESERVED			
12:0	OFF	The low off threshold of public used page count for jumbo mode.	RW	0x0348

## **FC\_JUMBO\_FCOFF\_HI\_THR**

REGISTER ADDRESS : 0xBB005150

DEFAULT VALUE : 0x7A0076E

Specify jumbo mode high on/off threshold for flow control off.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The high on threshold of public used page count in flow control off state for jumbo mode.	RW	0x07A0
15:13	RESERVED			
12:0	OFF	The high off threshold of public used page count in flow control off state for jumbo mode.	RW	0x076E

Bits	Field	Description	Type	Default
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### FC\_JUMBO\_FCOFF\_LO\_THR

REGISTER ADDRESS : 0xBB005154

DEFAULT VALUE : 0x37A0348

Specify jumbo mode low on/off threshold for flow control off.

Bits	Field	Description	Type	Default
31:29	RESERVED			
28:16	ON	The low on threshold of public used page countin flow control off state for jumbo mode.	RW	0x037A
15:13	RESERVED			
12:0	OFF	The low off threshold of public used page countin flow control off state for jumbo mode.	RW	0x0348

### FC\_JUMBO\_THR\_ADJUST

REGISTER ADDRESS : 0xBB005158

DEFAULT VALUE : 0x87D00006

Jumbo mode configuration.

Bits	Field	Description	Type	Default
31	EN	System uses jumbo mode threshold in receiving packet which packet length is over than and equal to FC_JUMBO_THR_ADJUST.PKT_LEN. 0b0: disabled 0b1: enabled	RW	0x1
30	STS	Indicate whether system entered jumbo mode 0b0 : normal mode 0b1 : jumbo mode	RO	0x0
29:16	PKT_LEN	The minimum packet length boundary to enter jumbo mode. Unit: Byte	RW	0x07D0
15:13	RESERVED			
12:0	SYS_USFD_PAGE THR	System used page threshold to exit jumbo mode	RW	0x0006

### FC\_PORT\_HI\_THR

BASE ADDRESS : 0xBB00515C

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x12C00FA

This is a One-Dimension Common Register Array.

Specify per port high on/off threshold of set0-3 for flow control on.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The high on threshold of port used page count.	RW	0x012C
15:13	RESERVED			
12:0	OFF	The high off threshold of port used page count.	RW	0x00FA

## FC\_PORT\_LO\_THR

BASE ADDRESS : 0xBB00516C  
ARRAY INDEX : 0 - 3  
ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x14000F

This is a One-Dimension Common Register Array.

Specify port low on/off threshold of set 0-3 for flow control on.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The low on threshold of port used page count.	RW	0x0014
15:13	RESERVED			
12:0	OFF	The low off threshold of port used page count.	RW	0x000F

## FC\_PORT\_FCOFF\_HI\_THR

BASE ADDRESS : 0xBB00517C  
ARRAY INDEX : 0 - 3  
ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x12C00FA

This is a One-Dimension Common Register Array.

Specify port high on/off threshold of set 0-3 for flow control off.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	The high on threshold of port used page count in flow control OFF state.	RW	0x012C
15:13	RESERVED			

Bits	Field	Description	Type	Default
12:0	OFF	The high off threshold of port used page count in flow control OFF state.	RW	0x00FA

## FC\_PORT\_FCOFF\_LO\_THR

BASE ADDRESS : 0xBB00518C  
 ARRAY INDEX : 0 - 3  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Specify port low on/off threshold of set 0-3 for flow control off.

Bits	Field	Description	Type	Default
31:29	RESERVED			
28:16	ON	The low on threshold of port used page count in flow control OFF state.	RW	
15:13	RESERVED			
12:0	OFF	The low off threshold of port used page count in flow control OFF state.	RW	

## FC\_PORT\_GUAR\_THR

BASE ADDRESS : 0xBB00519C  
 ARRAY INDEX : 0 - 3  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0xF

This is a One-Dimension Common Register Array.

Specify port guarantee threshold of set 0-3.

Bits	Field	Description	Type	Default
31:13	RESERVED			
12:0	THR	The guarantee threshold of port used page count.	RW	0x000F

## FC\_PORT\_THR\_SET\_SEL

BASE ADDRESS : 0xBB0051AC  
 PORT INDEX : 0 - 56  
 PORT OFFSET : 2 bits  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Per port select the port threshold from set 0-3 to use. In jumbo mode, system takes set 3 for all ports.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	IDX	Per port select the threshold set. 0x0: set 0 0x1: set 1 0x2: set 2 0x3: set 3	RW	0x0

### **FC\_PORT\_EGR\_DROP\_CTRL**

BASE ADDRESS : 0xBB00A800

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x2

This is a One-Dimension Port Register Array.

Per port egress drop control.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			
2	EGR_DROP_ALGO	Select egress drop algorithm for specified port (CPU port only supports Egress Drop). 0b0: Egress Drop 0b1: SWRED	RW	0x0
1	REF_RXCNGST	Reference ingress port congestion state when doing egress queue drop. 0b0: disable 0b1: enable	RW	0x1
0	HOL_PRVNT_EN	Flooding traffic HOL prevention function ability of ingress port. 0b0: disable 0b1: enable The setting takes effect if ingress port flow control is enabled.	RW	0x0

### **FC\_HOL\_PRVNT\_CTRL**

REGISTER ADDRESS : 0xBB00A8E4

DEFAULT VALUE : 0xF

Global HOL prevention configuration.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:4	RESERVED			

Bits	Field	Description	Type	Default
3	BC_EN	Enable egress drop for broadcast packet. 0b0: disable (Flow Control) 0b1: enable (Egress Drop) The setting takes effect ONLY if ingress port flow control is enabled and ingress port HOL_PRVNT_EN is set.	RW	0x1
2	L2_MC_EN	Enable egress drop for L2 multicast packet. 0b0: disable (Flow Control) 0b1: enable (Egress Drop) The setting takes effect ONLY if ingress port flow control is enabled and ingress port HOL_PRVNT_EN is set.	RW	0x1
1	IP_MC_EN	Enable egress drop for IPv4/IPv6 multicast packet. 0b0: disable (Flow Control) 0b1: enable (Egress Drop) The setting takes effect ONLY if ingress port flow control is enabled and ingress port HOL_PRVNT_EN is set.	RW	0x1
0	UNKN_UC_EN	Enable egress drop for unknown unicast packet. 0b0: disable (Flow Control) 0b1: enable (Egress Drop) The setting takes effect ONLY if ingress port flow control is enabled and ingress port HOL_PRVNT_EN is set.	RW	0x1

## FC\_PORT\_Q\_EGR\_FORCE\_DROP\_CTRL\_SET0

BASE ADDRESS : 0xBB00A8F8

ARRAY INDEX1 : 0 - 51

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 7

ARRAY OFFSET2 : 1 bit

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

Queue egress force drop control for normal port 0 ~ port 51.

Bits	Field	Description	Type	Default
0	EN	Force egress queue drop no matter what ingress port flow control status for specified port. 0b0: disable 0b1: enable	RW	0x0

## FC\_PORT\_Q\_EGR\_FORCE\_DROP\_CTRL\_SET1

BASE ADDRESS : 0xBB00A9B8

ARRAY INDEX1 : 52 - 55

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 11

ARRAY OFFSET2 : 1 bit

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (1 bit per field)

Queue egress force drop control for normal port 52 ~ port 55.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EN	Force egress queue drop no matter what ingress port flow control status for specified port. 0b0: disable 0b1: enable	RW	0x0

### **FC\_CPU\_Q\_EGR\_FORCE\_DROP\_CTRL**

BASE ADDRESS : 0xBB00A9C8

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Queue egress force drop control for CPU port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EN	Force egress queue drop no matter what ingress port flow control status for CPU port. 0b0: disable 0b1: enable	RW	0x0

### **FC\_Q\_EGR\_DROP\_THR**

BASE ADDRESS : 0xBB002618

ARRAY INDEX1 : 0 - 11

ARRAY OFFSET1 : 0xC

ARRAY INDEX2 : 0 - 2

ARRAY OFFSET2 : 0x4

DEFAULT VALUE : 0x0

This is a Two-Dimension Common Register Array.

Specify egress queue drop threshold.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	ON	Egress queue drop ON threshold for specified queue.	RW	

Bits	Field	Description	Type	Default
15:13	RESERVED			
12:0	OFF	Egress queue drop OFF threshold for specified queue.	RW	

## FC\_CPU\_Q\_EGR\_DROP\_THR

BASE ADDRESS : 0xBB0026A8

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x4E0044

This is a One-Dimension Common Register Array.

Specify egress queue drop threshold for CPU port.

Bits	Field	Description	Type	Default
31:29	RESERVED			
28:16	ON	The threshold of queue egress drop on for specified queue ID of CPU port.	RW	0x004E
15:13	RESERVED			
12:0	OFF	The threshold of queue egress drop off for specified queue ID of CPU port.	RW	0x0044

## FC\_PORT\_EGR\_DROP\_THR\_SET\_SEL

BASE ADDRESS : 0xBB002728

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Per egress port select egress queue drop threshold from set 0-2. In jumbo mode, system takes set 2 for all ports.

Bits	Field	Description	Type	Default
1:0	IDX	Select egress queue drop threshold set of egress port. 0x0: set 0 (FC_Q_EGR_DROP_THR_SET0) 0x1: set 1 (FC_Q_EGR_DROP_THR_SET1) 0x2: set 2 (FC_Q_EGR_DROP_THR_SET2) 0x3: reserved	RW	0x0

## FC\_REPCT\_FCOFF\_THR

REGISTER ADDRESS : 0xBB008180

DEFAULT VALUE : 0x0

Replication queue drop thresholds. Packet is going to be replicated should be enqueued to replication queue first. If replication queue is full and packet is from an flow control disabled port, then packet is dropped and will not be replicated.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	DROP_ON	Replication queue high threshold. System starts to drop replication packet if high threshold is reached. Unit: Page.	RW	0x0
15:13	RESERVED			
12:0	DROP_OFF	Replication queue low threshold. System stops to drop replication packet if low threshold is reached. Unit: Page.	RW	0x0

### **FC\_GLB\_PAGE\_CNT**

REGISTER ADDRESS : 0xBB0051BC

DEFAULT VALUE : 0x0

System dynamic used page count and maximum used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX_CNT	This register latches the maximum value of system used page count.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of system used page count.	RO	0x0

### **FC\_PB\_PAGE\_CNT**

REGISTER ADDRESS : 0xBB0051C0

DEFAULT VALUE : 0x0

Dynamic public used page count and maximum public used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX_CNT	This register latches the maximum value of public used page count.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of public used page count.	RO	0x0

Bits	Field	Description	Type	Default
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### FC\_PORT\_PAGE\_CNT

BASE ADDRESS : 0xBB0051C4  
 PORT INDEX : 0 - 56  
 PORT OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Per ingress port dynamic used page count and maximum used page count.

Bits	Field	Description	Type	Default
31:29	RESERVED			
28:16	MAX_CNT	This register indicates the maximum value of used page count of specific ingress port.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of used page count of specific ingress port.	RO	0x0

### FC\_CPU\_EGR\_PAGE\_CNT

REGISTER ADDRESS : 0xBB00273C  
 DEFAULT VALUE : 0x0

Specify CPU port maximum and dynamic used page count.

Bits	Field	Description	Type	Default
31:30	RESERVED			
29:16	MAX_CNT	This register indicates the maximum value of used page count of CPU port.	RW1C	0x0
15:14	RESERVED			
13:0	CNT	This register indicates the current value of used page count of CPU port.	RO	0x0

### FC\_CPU\_Q\_EGR\_PAGE\_CNT

BASE ADDRESS : 0xBB002740  
 ARRAY INDEX : 0 - 31  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Specify CPU port per output queue maximum and dynamic used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:16	MAX_CNT	This register indicates the maximum value of used page count of specific CPU port egress queue.	RW1C	0x0
15:14	RESERVED			
13:0	CNT	This register indicates the current value of used page count of specific CPU port egress queue.	RO	0x0

### **FC\_ALE\_NON\_REPCT\_Q\_PAGE\_CNT**

REGISTER ADDRESS : 0xBB008184

DEFAULT VALUE : 0x0

Non-Replication Queue dynamic used page count and maximum used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX_CNT	This register indicates the maximum value of used page count of non-replication queue.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of used page count of non-replication queue.	RO	0x0

### **FC\_ALE\_REPCT\_Q\_PAGE\_CNT**

REGISTER ADDRESS : 0xBB008188

DEFAULT VALUE : 0x0

Replication Queue dynamic used page count and maximum used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX_CNT	This register indicates the maximum value of used page count of replication queue.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of used page count of replication queue.	RO	0x0

## **FC REPCT\_FCON\_PAGE\_CNT**

REGISTER ADDRESS : 0xBB00818C

DEFAULT VALUE : 0x0

Replication Queue Flow Control ON packets dynamic used page count and maximum used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX_CNT	This register indicates the maximum value of used page count of Flow Control ON packets in replication queue.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of used page count of Flow Control ON packets in replication queue.	RO	0x0

## **FC REPCT\_FCOFF\_PAGE\_CNT**

REGISTER ADDRESS : 0xBB008190

DEFAULT VALUE : 0x0

Replication Queue Flow Control OFF packets dynamic used page count and maximum used page count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX_CNT	This register indicates the maximum value of used page count of Flow Control OFF packets in replication queue.	RWAC	0x0
15:13	RESERVED			
12:0	CNT	This register indicates the current value of used page count of Flow Control OFF packets in replication queue.	RO	0x0

## **FC REPCT\_FCOFF\_DROP\_CNT**

REGISTER ADDRESS : 0xBB008194

DEFAULT VALUE : 0x0

Replication Queue Flow Control OFF drop packet count.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:16	REPCT_HSA_FREE_CNT	This register can indicate dynamic free page count of extra HSA in replicated module.	RO	0x0

Bits	Field	Description	Type	Default
15:0	CNT	This register indicates drop packet count of Flow Control OFF packets in replication queue.	RW1C	0x0

## FC\_HSA\_PAGE\_CNT

REGISTER ADDRESS : 0xBB0027C0

DEFAULT VALUE : 0x0

HSA dynamic used page count and maximum used page count.

Bits	Field	Description	Type	Default
31:28	RESERVED			
27:16	MAX_CNT	This register can indicate maximum page count of extra HSA.	RWAC	0x0
15:12	RESERVED			
11:0	CNT	This register can indicate dynamic page count of extra HSA.	RO	0x0

## OUT\_Q

TABLE TYPE : 0x1

TABLE INDEX : 0 - 55

Output Queue

Bits	Field	Description
415:414	RESERVED	
413:400	PORT_USED_PAGE_CNT	This register indicates dynamic used page count of an egress port.
399:398	RESERVED	
397:384	PORT_MAX_USED_PAGE_CNT	This register latches the maximum used page count of an egress port. Write 1 to clear. Maximum count of egress port and egress queue would be clear.
383:382	RESERVED	
381:368	USED_PAGE_CNT_Q11	Dynamic used page counts for output queue 11.
367:366	RESERVED	
365:352	MAX_USED_PAGE_CNT_Q11	maximum used page counts for output queue 11.
351:350	RESERVED	
349:336	USED_PAGE_CNT_Q10	Dynamic used page counts for output queue 10.
335:334	RESERVED	
333:320	MAX_USED_PAGE_CNT_Q10	maximum used page counts for output queue 10.
319:318	RESERVED	

<b>Bits</b>	<b>Field</b>	<b>Description</b>
317:304	USED_PAGE_CNT_Q9	Dynamic used page counts for output queue 9.
303:302	RESERVED	
301:288	MAX_USED_PAGE_CNT_Q9	maximum used page counts for output queue 9
287:286	RESERVED	
285:272	USED_PAGE_CNT_Q8	Dynamic used page counts for output queue 8.
271:270	RESERVED	
269:256	MAX_USED_PAGE_CNT_Q8	maximum used page counts for output queue 8.
255:254	RESERVED	
253:240	USED_PAGE_CNT_Q7	Dynamic used page counts for output queue 7.
239:238	RESERVED	
237:224	MAX_USED_PAGE_CNT_Q7	maximum used page counts for output queue 7.
223:222	RESERVED	
221:208	USED_PAGE_CNT_Q6	Dynamic used page counts for output queue 6.
207:206	RESERVED	
205:192	MAX_USED_PAGE_CNT_Q6	maximum used page counts for output queue 6.
191:190	RESERVED	
189:176	USED_PAGE_CNT_Q5	Dynamic used page counts for output queue 5.
175:174	RESERVED	
173:160	MAX_USED_PAGE_CNT_Q5	maximum used page counts for output queue 5.
159:158	RESERVED	
157:144	USED_PAGE_CNT_Q4	Dynamic used page counts for output queue 4.
143:142	RESERVED	
141:128	MAX_USED_PAGE_CNT_Q4	maximum used page counts for output queue 4.
127:126	RESERVED	
125:112	USED_PAGE_CNT_Q3	Dynamic used page counts for output queue 3.
111:110	RESERVED	
109:96	MAX_USED_PAGE_CNT_Q3	maximum used page counts for output queue 3.
95:94	RESERVED	
93:80	USED_PAGE_CNT_Q2	Dynamic used page counts for output queue 2.
79:78	RESERVED	
77:64	MAX_USED_PAGE_CNT_Q2	maximum used page counts for output queue 2.
63:62	RESERVED	
61:48	USED_PAGE_CNT_Q1	Dynamic used page counts for output queue 1.
47:46	RESERVED	
45:32	MAX_USED_PAGE_CNT_Q1	maximum used page counts for output queue 1.
31:30	RESERVED	
29:16	USED_PAGE_CNT_Q0	Dynamic used page counts for output queue 0.
15:14	RESERVED	
13:0	MAX_USED_PAGE_CNT_Q0	maximum used page counts for output queue 0.

## SECTION 7.2

**CONGESTION AVOIDANCE**

Simplified Weighted Random Early Detection(SWRED) and Special Congest

**SWRED\_Q\_DROP\_RATE**

**BASE ADDRESS :** 0xBB0027C4  
**ARRAY INDEX1 :** 0 - 11  
**ARRAY OFFSET1 :** 0x4  
**ARRAY INDEX2 :** 0 - 2  
**ARRAY OFFSET2 :** 8 bits  
**DEFAULT VALUE :** 0x0

This is a Two-Dimension Register Field Array. (8 bits per field)

SWRED queue-based drop probability configuration.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
7:0	RATE	Drop rate of egress queue m(0~11) for drop precedence n(0~2). Drop probability = RATE/1023. Suggest: drop probability < 0.1	RW	0x0

**SWRED\_Q\_THR**

**BASE ADDRESS :** 0xBB0027F4  
**ARRAY INDEX1 :** 0 - 11  
**ARRAY OFFSET1 :** 0xC  
**ARRAY INDEX2 :** 0 - 2  
**ARRAY OFFSET2 :** 0x4  
**DEFAULT VALUE :** 0x0

This is a Two-Dimension Common Register Array.

SWRED queue-based threshold control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:29	RESERVED			
28:16	MAX	Maximum drop threshold of egress queue m(0~11) for drop precedence n(0~2). Unit: page.	RW	0x0
15:13	RESERVED			
12:0	MIN	Minimum drop threshold of egress queue m(0~11) for drop precedence n(0~2). Unit: page.	RW	0x0

## **SWRED\_DROP\_CNTR\_PIDX**

BASE ADDRESS : 0xBB008600

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 6 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (6 bits per field)

Select specific port for SWRED drop counters group.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	PORT_IDX_GRP	Select port index of group y(0-3) for SWRED drop counter.	RW	0x0

## **SWRED\_DROP\_CNTR\_CIDX**

BASE ADDRESS : 0xBB008604

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Select specific color for SWRED drop counters group.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	COLOR_IDX_GRP	Select color index of group y(0-3) for SWRED drop counter. 0x0: Green 0x1: Yellow 0x2: Red 0x3: Reserved	RW	0x0

## **SWRED\_DROP\_CNTR**

BASE ADDRESS : 0xBB008608

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 16 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (16 bits per field)

SWRED drop counters.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15:0	DROP_CNTR_GRP	The SWRED drop counter of group y(0-3).	RO	0x0

## **SWRED\_DROP\_CNTR\_RST**

REGISTER ADDRESS : 0xBB008610

DEFAULT VALUE : 0x0

SWRED drop counter reset control.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	RST	Reset all of the SWRED drop counters. 0b0: normal 0b1: reset Note: write 0b1 to reset and self-clear to 0b0.	RWAC	0x0

## **SC\_PORT\_TIMER**

BASE ADDRESS : 0xBB006014

PORT INDEX : 0 - 55

PORT OFFSET : 0x80

DEFAULT VALUE : 0x20002

This is a One-Dimension Port Register Array.

Specify the congest sustain timer threshold for special congestion.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:24	RESERVED			
23:20	CNGST_TMR_H	Half duplex congest timer, unit in seconds.	RO	0x0
19:16	CNGST_SUST_TMR_LMT_H	Half duplex congest sustain timer limit, unit in seconds. If CNGST_TMR_H >= CNGST_SUST_TMR_LMT_H, this port will enter special congest state. If CNGST_SUST_TMR_LMT_H!=0, special congest function is enabled.	RW	0x2
15:8	RESERVED			
7:4	CNGST_TMR	Congest timer, unit in seconds.	RO	0x0
3:0	CNGST_SUST_TMR_LMT	Full duplex congest sustain timer limited, unit in seconds. If CNGST_TMR>=CNGST_SUST_TMR_LMT, this port will enter Special Congest State. When CNGST_SUST_TMR_LMT!=0, special congest function is enabled.	RW	0x2

## **SC\_DRAIN\_OUT\_THR**

REGISTER ADDRESS : 0xBB002884

DEFAULT VALUE : 0x140014

Specify the drain out threshold for special congest.

Bits	Field	Description	Type	Default
31:29	RESERVED			
28:16	DRAIN_OUT_THR_H	Half duplex drain out threshold. When cntPnOQPage > DRAIN_OUT_THR_H, switch will enter Drain Out State and start to drain out. "cntPnOQPage":total output queue used page count of output port.	RW	0x0014
15:13	RESERVED			
12:0	DRAIN_OUT_THR	Full duplex drain out threshold. When cntPnOQPage>DRAIN_OUT_THR, this port will enter Drain Out State and start to drain out. cntPnOQPage is total output queue used page count of output port.	RW	0x0014

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## CHAPTER 8

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# Quality of Service

The chapter describes features related to QoS

SECTION 8.1

### INGRESS PRIORITY DECISION

Ingress Priority and Drop Precedence Decision module

#### PRI\_SEL\_CTRL

REGISTER ADDRESS : 0xBB009000

DEFAULT VALUE : 0x0

Priority Decision global control register

Bits	Field	Description	Type	Default
31:12	RESERVED			
11	PORT_PRI_REMAP_EN	Remap port-based internal priority by PRI_SEL_REMAP_IPRI_CFI0 or PRI_SEL_REMAP_OPRI_DEI0. 0b0: disable 0b1: enable (Remap)	RW	0x0
10	PROTO_VLAN_PRI_REMAP_EN	Enable or disable Protocol-and-port-based VLAN priority to remap by Inner-tag-based Priority Remapping Table or Outer-tag-based Priority Remapping Table. 0b0: disable 0b1: enable	RW	0x0
9	MAC_IP_VLAN_PRI_REMAP_EN	Enable or disable MAC-based/IP-subnet-based VLAN priority to remap by Inner-tag-based Priority Remapping Table or Outer-tag-based Priority Remapping Table. 0b0: disable 0b1: enable	RW	0x0
8	DSCP_INVLD_EN	Enable/disable invalidate specific DSCP function. 0b0: disabled 0b1: enabled	RW	0x0
7:6	RESERVED			
5:0	DSCP_INVLD_VAL	Invalidate specific DSCP value. If DSCP value matches the configuration, system treats DSCP-based priority as Null.	RW	0x0

## PRI\_SEL\_PORT\_CTRL

BASE ADDRESS : 0xBB009004

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Priority Decision port-based control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	PORT_PRI_REMAP_TBL_SEL	Select remapping table for port-based internal priority remapping. 0b0: use inner-priority with CFI 0 remapping table(PRI_SEL_REMAP_IPRI_CFI0) 0b1: use outer-priority with DEI 0 remapping table(PRI_SEL_REMAP_OPRI_DEI0)	RW	0x0

## PRI\_SEL\_REMAP\_PORT

BASE ADDRESS : 0xBB00900C

PORT INDEX : 0 - 56

PORT OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_PORT	Port-based internal priority.	RW	0x0

## PRI\_SEL\_REMAP\_IPRI\_CFI0

BASE ADDRESS : 0xBB009024

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a inner-tag priority with CFI 0.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_CFI0_IPRI	Remap inner-priority with CFI=0 to internal priority. The default remapped value of inner-tag priority 0~7: {0x1, 0x0, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **PRI\_SEL\_REMAP\_IPRI\_CFI1**

BASE ADDRESS : 0xBB009028  
 ARRAY INDEX : 0 - 7  
 ARRAY OFFSET : 3 bits  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a inner-tag priority with CFI 1.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_CFI1_IPRI	Remap inner-priority with CFI=1 to internal priority. The default remapped value of inner-tag priority 0~7: {0x1, 0x0, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **PRI\_SEL\_REMAP\_OPRI\_DEI0**

BASE ADDRESS : 0xBB00902C  
 ARRAY INDEX : 0 - 7  
 ARRAY OFFSET : 3 bits  
 DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a outer-tag priority with DEI 0.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_DEI0_OPRI	Remap outer-priority with DEI = 0 to internal priority. The default value of outer priority 0~7: {0x1, 0x0, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **PRI\_SEL\_REMAP\_OPRI\_DEI1**

BASE ADDRESS : 0xBB009030  
 ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a outer-tag priority with DEI 1.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_DEI1_OPRI	Remap outer-priority with DEI = 1 to internal priority. The default value of outer priority 0~7: {0x1, 0x0, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **PRI\_SEL\_REMAP\_DSCP**

BASE ADDRESS : 0xBB009034

ARRAY INDEX : 0 - 63

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a DSCP value.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_DSCP	Remap DSCP to internal priority.	RW	0x0

### **PRI\_SEL\_REMAP\_11E**

BASE ADDRESS : 0xBB009050

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a 802.11e TID value.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	INTPRI_11E	Remap 802.11e TID to internal priority.	RW	0x0

### **PRI\_SEL\_REMAP MPLS**

BASE ADDRESS : 0xBB009058

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for a MPLS TC value.

Bits	Field	Description	Type	Default
2:0	INTPRI_MPLS_TC	Remap MPLS TC to internal priority.	RW	0x0

### PRI\_SEL\_REMAP\_1BR

BASE ADDRESS : 0xBB00905C

ARRAY INDEX1 : 0 - 1

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 7

ARRAY OFFSET2 : 3 bits

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (3 bits per field)

Specify a 3-bit internal priority for E-PCP and E-DEI of 802.1BR E-TAG.

Bits	Field	Description	Type	Default
2:0	INTPRI_1BR	Remap E-PCP and E-DEI of 802.1BR E-TAG to internal priority.	RW	0x0

### PRI\_SEL\_PORT\_TBL\_IDX\_CTRL

BASE ADDRESS : 0xBB009064

PORT INDEX : 0 - 56

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Specify the index 0~3 of priority selection tables for a port.

Bits	Field	Description	Type	Default
1:0	IDX	The index of priority selection table for a port.	RW	0x0

### PRI\_SEL\_TBL\_CTRL

BASE ADDRESS : 0xBB009074

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x11112111, 0x1111

This is a One-Dimension Common Register Array.

Configure weight values for all priority source

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:48	RESERVED			
47:44	WT_1BR	Priority selection weight of 802.1BR-based Priority.	RW	0x1
43:40	WT_MPLS	Priority selection weight of MPLS-based Priority.	RW	0x1
39:36	WT_11E	Priority selection weight of 802.11E-based Priority.	RW	0x1
35:32	WT_TUNNEL	Priority selection weight of Tunnel-based Priority.	RW	0x1
31:28	WT_ROUT	Priority selection weight of Routing-based priority assignment	RW	0x1
27:24	WT_PROTO_VLAN	Priority selection weight of Protocol-and-port-based VLAN Priority.	RW	0x1
23:20	WT_MAC_VLAN	Priority selection weight of MAC-based VLAN Priority.	RW	0x1
19:16	WT_OTAG	Priority selection weight of Outer-tag-based Priority.	RW	0x1
15:12	WT_ITAG	Priority selection weight of Inner-tag-based Priority.	RW	0x2
11:8	WT_DSCP	Priority selection weight of DSCP-based Priority.	RW	0x1
7:4	WT_VACL	Priority selection weight of VLAN-ACL-based Priority.	RW	0x1
3:0	WT_PORT	Priority selection weight of Port-based Priority. 0x0: ignore the priority source 0x1 ~ 0xC: weight value, 0xC is highest If weights of all priority sources are 0(ignore), the internal priority is 0.	RW	0x1

## **DP\_SEL\_REMAP\_ITAG\_CFI0**

BASE ADDRESS : 0xBB009094

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Remap ITAG priority with CFI 0 to drop precedence

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	DP_CFI0_IPRI	DP value of specified inner-tag priority with CFI 0. 0x0~0x2: indicates colors: green, yellow and red 0x3: reserved	RW	0x0

### **DP\_SEL\_REMAP\_ITAG\_CFI1**

BASE ADDRESS : 0xBB009098

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Remap ITAG priority with CFI 1 to drop precedence

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	DP_CFI1_IPRI	DP value of specified inner-tag priority with CFI 1. 0x0~0x2: indicates colors: green, yellow and red 0x3: reserved	RW	0x0

### **DP\_SEL\_REMAP\_OTAG\_DEI0**

BASE ADDRESS : 0xBB00909C

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Remap OTAG priority with DEI 0 to drop precedence

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	DP_DEI0_OPRI	DP value of specified outer-tag priority with DEI 0. 0x0~0x2: indicates colors: green, yellow and red 0x3: reserved	RW	0x0

### **DP\_SEL\_REMAP\_OTAG\_DEI1**

BASE ADDRESS : 0xBB0090A0

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Remap OTAG priority with DEI 1 to drop precedence

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	DP_DEI1_OPRI	DP value of specified outer-tag priority with DEI 1. 0x0~0x2: indicates colors: green, yellow and red 0x3: reserved	RW	0x0

### **DP\_SEL\_REMAP\_DSCP**

BASE ADDRESS : 0xBB0090A4

ARRAY INDEX : 0 - 63

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Remap DSCP to drop precedence

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	DP_DSCP	DP value of specified DSCP. 0x0~0x2: indicates colors: green, yellow and red 0x3: reserved	RW	0x0

### **DP\_SEL\_REMAP MPLS**

BASE ADDRESS : 0xBB0090B4

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Remap MPLS traffic class(TC) to drop precedence

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	DP_TC	DP value of specified TC. 0x0~0x2: indicates colors: green, yellow and red 0x3: reserved	RW	0x0

### **DP\_SEL\_PORT\_TBL\_CTRL**

BASE ADDRESS : 0xBB0090B8

PORT INDEX : 0 - 56

**PORT OFFSET :** 0x4  
**DEFAULT VALUE :** 0x1111

This is a One-Dimension Port Register Array.

Configure weight values of DSCP-based, Inner-tag-based, Outer-tag-based and MPLS-based DP assignments for a port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13:12	WT_MPLS	DP selection weight of MPLS-based DP assignment of specified port n. 0x0: ignore the DP source 0x1 ~ 0x3: weight value, 0x3 is highest	RW	0x1
11:10	RESERVED			
9:8	WT_OTAG	DP selection weight of outer-tag-based DP assignment of specified port n. 0x0: ignore the DP source 0x1 ~ 0x3: weight value, 0x3 is highest	RW	0x1
7:6	RESERVED			
5:4	WT_ITAG	DP selection weight of inner-tag-based DP assignment of specified port n. 0x0: ignore the DP source 0x1 ~ 0x3: weight value, 0x3 is highest	RW	0x1
3:2	RESERVED			
1:0	WT_DSCP	DP selection weight of DSCP-based DP assignment of specified port n. 0x0: ignore the DP source 0x1 ~ 0x3: weight value, 0x3 is highest	RW	0x1

## SECTION 8.2

### SCHEDULING & QUEUE MANAGEMENT

Scheduling & Queue Management module

#### QM\_INTPRI2QID\_CTRL

**BASE ADDRESS :** 0xBB00A9D0  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 3 bits  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Mapping Internal Priority 0-7 to Normal Port Queue ID 0-7.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	QID	Assign normal port queue ID(0~7) of specified internal-priority.	RW	0x0

## **QM\_CPUQID2QID\_CTRL**

BASE ADDRESS : 0xBB00A9D4

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Mapping CPU Queue ID 0-31 to Normal Port Queue ID 0-7.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	QID	Assign normal port queue ID(0~7) of specified CPU queue ID(0~31).	RW	0x0

## **QM\_CPUQID2XGSQID\_CTRL**

BASE ADDRESS : 0xBB00A9E4

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 4 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (4 bits per field)

Mapping CPU Queue ID 0-31 to XG Stacking Port Queue ID 0-11.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:0	QID	Assign XG Stacking port queue ID(0~11) of specified CPU queue ID(0~31). 0x0~0xB: queue ID 0~11. 0xC~0xF: invalid value.	RW	0x0

## **QM\_RSN2CPUQID\_CTRL\_0**

REGISTER ADDRESS : 0xBB00A9F4

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	CPU2CPU_TALK	CPU queue ID (CPU to CPU talk)	RW	0x0
24:20	TTL_EXCPT	CPU queue ID (OF IP/MPLS TTL exception)	RW	0x0
19:15	OF_TBL_LU_MISS	CPU queue ID (OF table lookup miss)	RW	0x0
14:0	RESERVED			

### **QM\_RSN2CPUQID\_CTRL\_1**

REGISTER ADDRESS : 0xBB00A9F8

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	OAMPDU	CPU queue ID (OAMPDU)	RW	0x0
24:20	CFM	CPU queue ID (CFM)	RW	0x0
19:15	CFM_ETHDM	CPU queue ID (CFM-ETHDM)	RW	0x0
14:10	PASR_EXCPT	CPU queue ID (Parser exception)	RW	0x0
9:5	MALFORM_PKT	CPU queue ID (Malformed packet)	RW	0x0
4:0	IP_MAC_BINDING	CPU queue ID (IP MAC binding)	RW	0x0

### **QM\_RSN2CPUQID\_CTRL\_2**

REGISTER ADDRESS : 0xBB00A9FC

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	L3_IPUC_RPF	CPU queue ID (L3 IPUC RPF)	RW	0x0
24:20	INN_OUTER_CFI_EQL_1	CPU queue ID (inner/outer CFI=1)	RW	0x0
19:15	IVC	CPU queue ID (MAC-based/IP-subnet-based IVC)	RW	0x0
14:10	IGR_VLAN_FLTR	CPU queue ID (Ingress VLAN filter)	RW	0x0
9:5	L2_UC_MC_BRIDGE_LU_MISS	CPU queue ID (L2 UC/MC bridge lookup miss)	RW	0x0
4:0	IP4_IP6_BRIDGE_LU_MISS	CPU queue ID (IP4/IP6 bridge lookup miss)	RW	0x0

### **QM\_RSN2CPUQID\_CTRL\_3**

REGISTER ADDRESS : 0xBB00AA00

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
29:25	PTP	CPU queue ID (PTP)	RW	0x0
24:20	RMA_USR_DEF	CPU queue ID (User-defined RMA 0 to 3)	RW	0x0
19:15	EST_TX_LEN_EXCEED	EST TX length exceed	RW	0x0
14:10	EST_MISMATCH	Rx EST header mismatch	RW	0x0
9:5	ECID_EQ_PCID	ECID equal to Rx Port's PCID	RW	0x0
4:0	RMA_BPDU	CPU queue ID (BPDU)	RW	0x0

#### **QM\_RSN2CPUQID\_CTRL\_4**

REGISTER ADDRESS : 0xBB00AA04

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	RMA_LACP	CPU queue ID (LACP)	RW	0x0
24:20	RMA_LLDP	CPU queue ID (LLDP)	RW	0x0
19:15	RMA_EAPOL	CPU queue ID (EAPOL)	RW	0x0
14:10	RMA_XX	CPU queue ID (01-80-C2-00-00-XX)	RW	0x0
9:5	TUNL_MAC_IF	CPU queue ID (Tunnel Interface MAC)	RW	0x0
4:0	TUNL_IP_CHK	CPU queue ID (Tunnel IP addr check)	RW	0x0

#### **QM\_RSN2CPUQID\_CTRL\_5**

REGISTER ADDRESS : 0xBB00AA08

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	ROUTER_MAC_IF	CPU queue ID (Router Interface MAC)	RW	0x0
24:20	L3_IPUC_NON_IP_PKT	CPU queue ID (L3 IPUC non-IP packet)	RW	0x0
19:15	IP4_IP6_HDR_ERR	CPU queue ID (IP4/IP6 Header Error)	RW	0x0
14:10	ROUTING_IP_CHK	CPU queue ID (Routing IP addr check)	RW	0x0
9:5	L3_ROUTING_DIP_DMAC_MISMATCH	CPU queue ID (L3 routing DIP/DMAC mismatch)	RW	0x0
4:0	IP6_UC_MC_HOP_BY_HOP	CPU queue ID (IP6 UC/MC hop by hop option)	RW	0x0

### **QM\_RSN2CPUQID\_CTRL\_6**

REGISTER ADDRESS : 0xBB00AA0C

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	IP6_UC_MC_ROUTING_HD_R	CPU queue ID (IP6 UC/MC routing header)	RW	0x0
24:20	IP4_IP_OPT	CPU queue ID (IP4 IP option)	RW	0x0
19:15	IP4_IP6_MC_ROUTING_L_U_MISS	CPU queue ID (IP4/IP6 MC routing lookup miss)	RW	0x0
14:10	L3_IPUC_NULL_ROUTE	CPU queue ID (L3 IPUC null route)	RW	0x0
9:5	L3_IPUC_PBR_NULL_ROUTE	CPU queue ID (L3 IPUC PBR null route)	RW	0x0
4:0	L3_UC_HOST_ROUTE	CPU queue ID (L3 UC host route)	RW	0x0

### **QM\_RSN2CPUQID\_CTRL\_7**

REGISTER ADDRESS : 0xBB00AA10

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	L3_UC_NET_ROUTE	CPU queue ID (L3 UC net route)	RW	0x0
24:20	L3_MC_BRIDGE_ENTRY	CPU queue ID (L3 MC bridge entry)	RW	0x0
19:15	L3_MC_ROUTE_ENTRY	CPU queue ID (L3 MC route entry)	RW	0x0
14:10	L3_IPMC_RPF	CPU queue ID (L3 IPMC RPF)	RW	0x0
9:5	ROUTING_EXCPT_NH_ERR_ACT	CPU queue ID (Routing exception - NOT a Next-Hop Entry)	RW	0x0
4:0	ROUTING_EXCPT_NH_AGE_OUT_ACT	CPU queue ID (Routing exception - Next-Hop Entry Age-out)	RW	0x0

### **QM\_RSN2CPUQID\_CTRL\_8**

REGISTER ADDRESS : 0xBB00AA14

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	ROUTING_EXCPT_ROUTE_TO_TUNNEL_ACT	CPU queue ID (Routing exception - NH is a tunnel interface while Port Routing)	RW	0x0
24:20	IP4_IP6_ICMP_REDRT	CPU queue ID (IP4/IP6 ICMP redirect)	RW	0x0
19:15	IPUC_MTU	CPU queue ID (IPUC MTU)	RW	0x0
14:10	IPMC_MTU	CPU queue ID (IPMC MTU)	RW	0x0
9:5	IPUC_TTL	CPU queue ID (IPUC TTL)	RW	0x0
4:0	IPMC_TTL	CPU queue ID (IPMC TTL)	RW	0x0

### QM\_RSN2CPUQID\_CTRL\_9

REGISTER ADDRESS : 0xBB00AA18

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	IGMP_MLD	CPU queue ID (IGMP/MLD)	RW	0x0
24:20	DHCP_DHCP6	CPU queue ID (DHCP/DHCP6)	RW	0x0
19:15	ARP_REQ REP	CPU queue ID (ARP Request/Reply)	RW	0x0
14:10	NEIGHBOR_DISCOVER	CPU queue ID (Neighbor Discovery)	RW	0x0
9:5	IP4_IP6_RSV_ADDR	CPU queue ID (IP4/IP6 reserved address)	RW	0x0
4:0	MPLS_EXCPT	CPU queue ID (MPLS exception)	RW	0x0

### QM\_RSN2CPUQID\_CTRL\_10

REGISTER ADDRESS : 0xBB00AA1C

DEFAULT VALUE : 0x0

Mapping CPU trap reason 0-63 to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:10	RESERVED			
9:5	L2_NTFY	CPU queue ID (L2 Notification)	RW	0x0
4:0	NORMAL_FWD	CPU queue ID (Normal forward)	RW	0x0

### QM\_FLAG2CPUQID\_CTRL\_0

REGISTER ADDRESS : 0xBB00AA20

DEFAULT VALUE : 0x0

Mapping CPU trap flags to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	MAC_CST	CPU queue ID (MAC Constrain)	RW	0x0
24:20	NEW_SA	CPU queue ID (New SA)	RW	0x0
19:15	PMV_FORBID	CPU queue ID (Port Move Forbid)	RW	0x0
14:10	L2_STTC_PMV	CPU queue ID (Static Port Move)	RW	0x0
9:5	L2_DYN_PMV	CPU queue ID (Dynamic Port Move)	RW	0x0
4:0	HASH_FULL	CPU queue ID (Hash Full)	RW	0x0

### QM\_FLAG2CPUQID\_CTRL\_1

REGISTER ADDRESS : 0xBB00AA24

DEFAULT VALUE : 0x0

Mapping CPU trap flags to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:25	INVALID_SA	CPU queue ID (Multicast/Broadcast/Zero SA)	RW	0x0
24:20	ATK_TYPE	CPU queue ID (Attack Prevention)	RW	0x0
19:15	ACL_HIT	CPU queue ID (ACL hit)	RW	0x0
14:10	OF_HIT	CPU queue ID (OpenFlow hit)	RW	0x0
9:5	TT_HIT	CPU queue ID (Tunnel Terminate hit)	RW	0x0
4:0	MIR_HIT	CPU queue ID (Mirror)	RW	0x0

### QM\_FLAG2CPUQID\_CTRL\_2

REGISTER ADDRESS : 0xBB00AA28

DEFAULT VALUE : 0x0

Mapping CPU trap flags to CPU port Queue ID 0-31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:15	RESERVED			
14:10	L2_ERR_PKT	CPU queue ID (L2 CRC error)	RW	0x0
9:5	L3_ERR_PKT	CPU queue ID (L3 check sum error)	RW	0x0

Bits	Field	Description	Type	Default
4:0	RESERVED			

### SCHED\_PORT\_Q\_CTRL\_SET0

BASE ADDRESS : 0xBB002888  
 ARRAY INDEX1 : 0 - 51  
 ARRAY OFFSET1 : 0x20  
 ARRAY INDEX2 : 0 - 7  
 ARRAY OFFSET2 : 0x4  
 DEFAULT VALUE : 0x1

This is a Two-Dimension Common Register Array.

Specify per-output queue scheduler configuration for port 0~51.

Bits	Field	Description	Type	Default
31:9	RESERVED			
8	STRICT_EN	Strict priority ability of specified queue of a port. 0b0: disable 0b1: enable	RW	0x0
7	RESERVED			
6:0	WEIGHT	Weight value of specified queue of a port for WRR/WFQ. Value 0 is blocking. Value 1~127 implies weight 1~127.	RW	0x01

### SCHED\_PORT\_Q\_CTRL\_SET1

BASE ADDRESS : 0xBB002F08  
 ARRAY INDEX1 : 52 - 55  
 ARRAY OFFSET1 : 0x30  
 ARRAY INDEX2 : 0 - 11  
 ARRAY OFFSET2 : 0x4  
 DEFAULT VALUE : 0x1

This is a Two-Dimension Common Register Array.

Specify per-output queue scheduler configuration for port 52~55.

Bits	Field	Description	Type	Default
31:9	RESERVED			
8	STRICT_EN	Strict priority ability of specified queue of a port. 0b0: disable 0b1: enable	RW	0x0
7	RESERVED			

Bits	Field	Description	Type	Default
6:0	WEIGHT	Weight value of specified queue of a port for WRR/WFQ. Value 0 is blocking. Value 1~127 implies weight 1~127.	RW	0x01

## SCHED\_CPU\_Q\_CTRL

BASE ADDRESS : 0xBB002FC8

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x1

This is a One-Dimension Common Register Array.

Specify per-output queue scheduler configuration for CPU port.

Bits	Field	Description	Type	Default
31:9	RESERVED			
8	STRICT_EN	Strict priority ability of specified queue of CPU port. 0b0: disable 0b1: enable	RW	0x0
7	RESERVED			
6:0	WEIGHT	Weight value of specified queue of CPU port for WRR/WFQ. Value 0 is blocking. Value 1~127 implies weight 1~127.	RW	0x01

## SCHED\_PORT\_ALGO\_CTRL

BASE ADDRESS : 0xBB003048

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify port output queue schedule type.

Bits	Field	Description	Type	Default
0	SCED_TYPE	Scheduling type selection WFQ or WRR for port n. 0b0: WFQ (Weighted Fair-Queuing) (default) 0b1: WRR (Weighted Round-Robin)	RW	0x0

## SCHED\_CTRL

REGISTER ADDRESS : 0xBB003050

DEFAULT VALUE : 0x8

Scheduler global configuration register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:4	RESERVED			
3:0	MAX_Q_PKT_TH	Max queuing packets threshold (packet count). If the number of queuing packets of a port is less than Q_PKT_THR_MAX, system considers the port is empty.	RW	0x8

SECTION 8.3

## REMARKING

Inner-priority, Outer-priority, DEI/CFI and DSCP Remarking modules

## RMK\_CTRL

REGISTER ADDRESS : 0xBB004D90

DEFAULT VALUE : 0x3

Specify different remarking sources for different remarking modules and default remarking configuration.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:10	RESERVED			
9:8	IPRI_RMK_SRC	Select inner-priority remarking source. If original packet doesn't have inner-tag, system follows IPRI_DFLT_SRC to decide TX inner-priority. 0x0: internal priority 0x1: original inner-priority 0x2: original outer-priority(*) 0x3: original DSCP value(*)	RW	0x0
7:6	OPRI_RMK_SRC	Select outer-priority remarking source. 0x0: internal priority 0x1: original inner-priority 0x2: original outer-priority(*) 0x3: original DSCP value(*)	RW	0x0
5	DEI_RMK_SRC	Select DEI/CFI remarking source. 0b0: internal priority 0b1: DP	RW	0x0

Bits	Field	Description	Type	Default								
4:2	DSCP_RMK_SRC	<p>Select DSCP remarking source.</p> <p>0x0: internal priority  0x1: original inner-priority(*)  0x2: original outer-priority(*)  0x3: original DSCP value  0x4: drop precedence(color)  0x5: drop precedence and internal priority  0x6~0x7: reserved</p> <p>If DSCP_RMK_SRC=0x4, according to RFC2475, only packet with DSCP value AF11~AF43 will be remarked.  AF Group: Low drop(green), Med drop(yellow), High drop(red)</p> <table> <tr><td>Class 1:</td><td>AF11(001010), AF12(001100), AF13(001110)</td></tr> <tr><td>Class 2:</td><td>AF21(010010), AF22(010100), AF23(010110)</td></tr> <tr><td>Class 3:</td><td>AF31(011010), AF32(011100), AF33(011110)</td></tr> <tr><td>Class 4:</td><td>AF41(100010), AF42(100100), AF43(100110)</td></tr> </table>	Class 1:	AF11(001010), AF12(001100), AF13(001110)	Class 2:	AF21(010010), AF22(010100), AF23(010110)	Class 3:	AF31(011010), AF32(011100), AF33(011110)	Class 4:	AF41(100010), AF42(100100), AF43(100110)	RW	0x0
Class 1:	AF11(001010), AF12(001100), AF13(001110)											
Class 2:	AF21(010010), AF22(010100), AF23(010110)											
Class 3:	AF31(011010), AF32(011100), AF33(011110)											
Class 4:	AF41(100010), AF42(100100), AF43(100110)											
1	IPRI_DFLT_CFG	<p>Select RX port or TX port's configuration (IPRI_DFLT_SRC) to decide inner-priority default value.</p> <p>0b0: use RX port configuration to decide inner-priority  0b1: use TX port configuration to decide inner-priority</p>	RW	0x1								
0	OPRI_DFLT_CFG	<p>Select RX port or TX port's configuration(OPRI_DFLT_SRC) to decide outer-priority default value</p> <p>0b0: use RX port configuration to decide outer-priority  0b1: use TX port configuration to decide outer-priority</p>	RW	0x1								

## RMK\_PORT\_CTRL

**BASE ADDRESS :** 0xBB004D94  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Port Register Array.

Specify remarking abilities and default remarking source for different remarking modules.

Bits	Field	Description	Type	Default
31:5	RESERVED			
4	DEI_RMK_TAG_SEL	<p>Select inner-tag or outer-tag to remark DEI bit.</p> <p>0b0: inner-tag  0b1: outer-tag</p>	RW	0x0

Bits	Field	Description	Type	Default
3	DEI_RMK_EN	Enable/Disable DEI remarking for a port. 0b0: disable 0b1: enable	RW	0x0
2	DSCP_RMK_EN	Enable/Disable DSCP remarking for a port. 0b0: disable 0b1: enable	RW	0x0
1	OPRI_RMK_EN	Enable/Disable outer-tag priority remarking for a port. 0b0: disable 0b1: enable	RW	0x0
0	IPRI_RMK_EN	Enable/Disable inner-priority remarking for a port. 0b0: disable 0b1: enable	RW	0x0

### **RMK\_INTPRI2IPRI\_CTRL**

BASE ADDRESS : 0xBB004E78

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure internal priority to inner-priority remarking table.

Bits	Field	Description	Type	Default
2:0	IPRI	New inner-priority for specified internal priority. The default inner-priority remarking value of each internal priority 0~15: {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7, 0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **RMK\_IPRI2IPRI\_CTRL**

BASE ADDRESS : 0xBB004E7C

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure original inner-priority to inner-priority remarking table.

Bits	Field	Description	Type	Default
2:0	IPRI	New inner-priority for specified original inner-priority. The default inner-priority remarking value of each original inner-tag priority 0~7: {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

Bits	Field	Description	Type	Default
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### **RMK\_OPRI2IPRI\_CTRL**

BASE ADDRESS : 0xBB004E80

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure original outer-priority to inner-priority remarking table.

Bits	Field	Description	Type	Default
2:0	IPRI	New inner-priority for specified original outer-priority.  The default inner-priority remarking value of each original outer-tag priority 0~7: {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **RMK\_DSCP2IPRI\_CTRL**

BASE ADDRESS : 0xBB004E84

ARRAY INDEX : 0 - 63

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure DSCP to inner-priority remarking table.

Bits	Field	Description	Type	Default
2:0	IPRI	New inner-priority for specified DSCP.	RW	0x0

### **RMK\_INTPRI2OPRI\_CTRL**

BASE ADDRESS : 0xBB004EA0

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure internal priority to outer-priority remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	OPRI	New outer-priority for specified internal priority. The default outer-priority remarking value of each internal priority 0~15: {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7, 0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **RMK\_IPRI2OPRI\_CTRL**

BASE ADDRESS : 0xBB004EA4  
ARRAY INDEX : 0 - 7  
ARRAY OFFSET : 3 bits  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure original inner-priority to outer-priority remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	OPRI	New outer-priority for specified original inner-priority. The default outer-priority remarking value of each original inner-tag priority 0~7: {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **RMK\_OPRI2OPRI\_CTRL**

BASE ADDRESS : 0xBB004EA8  
ARRAY INDEX : 0 - 7  
ARRAY OFFSET : 3 bits  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure original outer-priority to outer-priority remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	OPRI	New outer-priority for specified original outer-priority. The default outer-priority remarking value of each original outer-tag priority 0~7: {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7}	RW	

### **RMK\_DSCP2OPRI\_CTRL**

BASE ADDRESS : 0xBB004EAC

ARRAY INDEX : 0 - 63

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

Configure DSCP to outer-priority remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	OPRI	New outer-priority value for the specified DSCP.	RW	0x0

### **RMK\_INTPRI2DEI\_CTRL**

BASE ADDRESS : 0xBB004EC8

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Configure internal priority to DEI/CFI remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	DEI	New DEI/CFI for specified internal priority.	RW	0x0

### **RMK\_DP2DEI\_CTRL**

BASE ADDRESS : 0xBB004ECC

ARRAY INDEX : 0 - 2

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Configure DP to DEI/CFI remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	DEI	New DEI/CFI for specified drop precedence.	RW	0x0

### **RMK\_INTPRI2DSCP\_CTRL**

BASE ADDRESS : 0xBB004ED0

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 6 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (6 bits per field)

Configure internal priority to DSCP remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	DSCP	New DSCP value for the specified internal-priority.	RW	0x0

### **RMK\_IPRI2DSCP\_CTRL**

BASE ADDRESS : 0xBB004ED8

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 6 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (6 bits per field)

Configure original inner-priority to DSCP remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	DSCP	New DSCP value for the specified inner-priority.	RW	0x0

### **RMK\_OPRI2DSCP\_CTRL**

BASE ADDRESS : 0xBB004EE0

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 6 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (6 bits per field)

Configure original outer-priority to DSCP remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	DSCP	New DSCP value for the specified outer-priority.	RW	0x0

### **RMK\_DSCP2DSCP\_CTRL**

BASE ADDRESS : 0xBB004EE8

ARRAY INDEX : 0 - 63

ARRAY OFFSET : 6 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (6 bits per field)

Configure DSCP to DSCP remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	DSCP	New DSCP value for the specified DSCP.	RW	0x0

## **RMK\_DPINTPRI2DSCP\_CTRL**

**BASE ADDRESS :** 0xBB004F1C  
**ARRAY INDEX1 :** 0 - 2  
**ARRAY OFFSET1 :** 0x8  
**ARRAY INDEX2 :** 0 - 7  
**ARRAY OFFSET2 :** 6 bits  
**DEFAULT VALUE :** 0x0

This is a Two-Dimension Register Field Array. (6 bits per field)

Configure DP and internal priority to DSCP remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	DSCP	New DSCP value for the specified drop precedence and internal-priority.	RW	0x0

## **REMARK**

**TABLE TYPE :** 0x4  
**TABLE INDEX :** 0 - 1023

Remark Configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>
31:11	RESERVED	
10:8	IPRI_DFLT_PRI	Default inner-priority for specific port.
7	RESERVED	
6:4	OPRI_DFLT_PRI	Default outer-priority for specific port.
3	RESERVED	
2	IPRI_DFLT_SRC	Default inner-priority source for specific port n. 0b0: copy internal priority 0b1: use REMARK.IPRI_DFLT_PRI (default value)
1:0	OPRI_DFLT_SRC	Default outer-priority source for specific port. 0x0: copy internal priority 0x1: use REMARK.OPRI_DFLT_PRI. The priority is constrained by RMK_CTRL.OPRI_DFLT_CFG (default value). 0x2: copy RX inner-priority. If original packet without inner-tag, system follows REMARK. OPRI_DFLT_SRC = 0x1 behavior. 0x3: Reserved

Bits	Field	Description
SECTION 8.4		
<b>802.1QAV</b>		

(IEEE802.1Qav) AV Bridging module that provides per port AVB class A/B enable control, class A/B queue ID, priority configuration and non class A/B packet redirect/ remark configuration.

### **AVB\_PORT\_CLASS\_A\_EN**

BASE ADDRESS : 0xBB008C14  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per port specify the port role for Class A in AVB domain.  
(Note: Must also configure the AVB\_PORT\_CLASS\_A\_EN\_MAC to take effect)

Bits	Field	Description	Type	Default
0	EN	Specify the port role for Class A. 0b0: boundary port 0b1: core port	RW	0x0

### **AVB\_PORT\_CLASS\_B\_EN**

BASE ADDRESS : 0xBB008C1C  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per port specify the port role for Class B in AVB domain.  
(Note: Must also configure the AVB\_PORT\_CLASS\_B\_EN\_MAC to take effect)

Bits	Field	Description	Type	Default
0	EN	Specify the port role for Class B. 0b0: boundary port 0b1: core port	RW	0x0

### **AVB\_PORT\_CLASS\_A\_EN\_MAC**

BASE ADDRESS : 0xBB004F34  
PORT INDEX : 0 - 55

PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

(This is a duplicate register, refer to AVB\_PORT\_CLASS\_A\_EN)

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EN	(This is a duplicate register, refer to AVB_PORT_CLASS_A_EN)	RW	0x0

### **AVB\_PORT\_CLASS\_B\_EN\_MAC**

BASE ADDRESS : 0xBB004F3C  
PORT INDEX : 0 - 55  
PORT OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

(This is a duplicate register, refer to AVB\_PORT\_CLASS\_B\_EN)

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	EN	(This is a duplicate register, refer to AVB_PORT_CLASS_B_EN)	RW	0x0

### **AVB\_CTRL**

REGISTER ADDRESS : 0xBB008004  
DEFAULT VALUE : 0x9DD3

The register controls the corresponding priority and queue for Class A/B AV stream.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:18	RESERVED			
17:15	CLASS_NON_B_REDIRECT_QID	Specify the Queue ID for redirecting Non-Class B packet.  (In order to avoid Non-Class B packet occupying the queue reserved for the Class B, it forces Non-Class B packet to use this queue.)	RW	0x1
14:12	CLASS_NON_A_REDIRECT_QID	Specify the Queue ID for redirecting Non-Class A packet.  (In order to avoid Non-Class A packet occupying the queue reserved for the Class A, it forces the Non-Class A packet to use this queue.)	RW	0x1
11:9	CLASS_B_QID	Specify the Queue ID for enqueuing Class B packet.  (Class B packets will be forced to use this queue)	RW	0x6

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
8:6	CLASS_A_QID	Specify the Queue ID for enqueuing Class A packet. (Class A packets will be forced to use this queue)	RW	0x7
5:3	CLASS_B_PRI	Specify the PCP value of Class B. (If a packet with the PCP value comes from an AVB core/boundary port, it will be identified as Class B/Non-Class B packet.)	RW	0x2
2:0	CLASS_A_PRI	Specify the PCP value of Class A. (If a packet with the PCP value comes from an AVB core/boundary port, it will be identified as Class A/Non-Class A packet.)	RW	0x3

## **AVB\_CTRL\_MAC**

REGISTER ADDRESS : 0xBB004F44

DEFAULT VALUE : 0x13

The register controls the corresponding priority used by remarking on egress.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:6	RESERVED			
5:3	CLASS_NON_B_RMK_PRI	Specify the priority to be remarked for Non-Class B packet. (To avoid next-hop device assuming a Non-Class B packet to be a Class B packet, a Non-Class B packet will be remarked to this priority if it is going out on a core port.)	RW	0x2
2:0	CLASS_NON_A_RMK_PRI	Specify the priority to be remarked for Non-Class A packet. (To avoid the next-hop device to assuming a Non-Class A packet to be a Class A packet, a Non-Class A packet will be remarked to this priority if it is going out on a core port.)	RW	0x3

## CHAPTER 9

# Layer 3

The chapter describes features related to layer 3

SECTION 9.1

## LAYER 3 ROUTING

Layer 3 unicast & multicast routing module

### L3\_IP\_ROUTE\_CTRL

REGISTER ADDRESS : 0xBB00F000

DEFAULT VALUE : 0xF

IP routing related options

Bits	Field	Description	Type	Default
31:12	RESERVED			
11:10	NON_IP_ACT	Action for NON-IP packets which matched ingress router MAC with action configured as L3 process. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
9	URPF_BASE_SEL	uRPF base selection bit to determine which uRPF configuration (URPF_CHK_EN, URPF_DFLT_ROUTE_EN, URPF_CHK_MODE) is prefered. 0b0: L3-interface-based 0b1: Port-based	RW	0x0
8:6	NH_AGE_OUT_ACT	Action for the exception caused by next-hop entry age-out. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x4: copy to local CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
5:4	NH_ERR_ACT	Action for the exception caused by next-hop entry error. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:2	IP6_HDR_ERR_ACT	Action performed on IPv6 packets with header error. 0x0: drop 0x1: trap to local CPU (and able to bridge) 0x2: trap to master CPU (and able to bridge) 0x3: able to bridge but not route	RW	0x3
1:0	IP_HDR_ERR_ACT	Action performed on IPv4 packets with header error. 0x0: drop 0x1: trap to local CPU (and able to bridge) 0x2: trap to master CPU (and able to bridge) 0x3: able to bridge but not route	RW	0x3

### L3\_HOST\_TBL\_CTRL

REGISTER ADDRESS : 0xBB00F004

DEFAULT VALUE : 0x0

L3 host table control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:6	RESERVED			
5	MC_HASH_ALG_SEL_1	Hash algorithm selection for multicast lookup table 1 (the last 6k entries of whole L3 host table) 0b0: using hash algorithm 0 0b1: using hash algorithm 1	RW	0x0
4	MC_HASH_ALG_SEL_0	Hash algorithm selection for multicast lookup table 0 (the first 6k entries of whole L3 host table) 0b0: using hash algorithm 0 0b1: using hash algorithm 1	RW	0x0
3	UC_HASH_ALG_SEL_1	Hash algorithm selection for unicast lookup table 1 (the last 6k entries of whole L3 host table) 0b0: using hash algorithm 0 0b1: using hash algorithm 1	RW	0x0
2	UC_HASH_ALG_SEL_0	Hash algorithm selection for unicast lookup table 0 (the first 6k entries of whole L3 host table) 0b0: using hash algorithm 0 0b1: using hash algorithm 1	RW	0x0
1	LU_FORCE_MODE_HASH_KEY_SEL	Hash key selection (only available when L3_LU_MODE_SEL = 0b0) 0b0: (VRF, SIP, GIP) 0b1: (VRF, SIP, GIP, VID)	RW	0x0
0	LU_MODE_SEL	L3 table lookup mode selection. 0b0: force to lookup with IPMC_TYPE=1 (as L2+L3) 0b1: based on the routing capacity for each packet: Bridging only => (VRF, SIP, GIP, VID), IPMC_TYPE = 0. Bridging and Routing => (VRF, SIP, GIP), IPMC_TYPE = 1.	RW	0x0

### L3\_IPUC\_ROUTE\_CTRL

REGISTER ADDRESS : 0xBB00F008

DEFAULT VALUE : 0x1000

IPv4 unicast routing configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18	PKT_TO_CPU_TARGET	The CPU target of trapping/copying packet while doing IPv4 unicast routing. (referred by L3 IPv4 unicast entry) 0b0: local CPU. 0b1: master CPU.	RW	0x0
17:16	TTL_FAIL_ACT	Action performed on IPv4 unicast packets with TTL check failed. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
15:14	MTU_FAIL_ACT	Action performed on IPv4 unicast packets with MTU check failed. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
13:11	HDR_OPT_ACT	Action performed on IPv4 unicast packets with IP header options. 0x0: drop 0x1: trap to local CPU 0x2: route 0x3: copy to local CPU and route 0x4: trap to master CPU 0x5: copy to master CPU and route 0x6~0x7: reserved	RW	0x2
10:9	DMAC_MC_ACT	Action performed on IPv4 unicast packets with multicast DMAC. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
8:7	DMAC_BC_ACT	Action performed on IPv4 unicast packets with broadcast DMAC. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: bridge (could not be routed)	RW	0x0
6:5	ZERO_SIP_ACT	Action performed on IPv4 unicast packets with zero SIP. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
4:3	BAD_DIP_ACT	Action performed on IPv4 unicast packets with illegal DIP. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:1	BAD_SIP_ACT	Action performed on IPv4 unicast packets with illegal SIP. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
0	GLB_EN	Globally enable IPv4 unicast routing or not. 0x0: disable 0x1: enable	RW	0x0

### L3\_IP6UC\_ROUTE\_CTRL

REGISTER ADDRESS : 0xBB00F00C

DEFAULT VALUE : 0xA200

IPv6 unicast routing configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:23	RESERVED			
22	PKT_TO_CPU_TARGET	The CPU target of trapping/copying packet while doing IPv6 unicast routing. (referred by L3 IPv6 unicast entry) 0b0: local CPU. 0b1: master CPU.	RW	0x0
21:20	HL_FAIL_ACT	Action performed on IPv6 unicast packets with hop limit check failed. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
19:18	MTU_FAIL_ACT	Action performed on IPv6 unicast packets with MTU check failed. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
17:15	HDR_ROUTE_ACT	Action performed on IPv6 unicast packets with routing extension headers. 0x0: drop 0x1: trap to local CPU 0x2: route (i.e., don't care) 0x3: copy to local CPU and route 0x4: trap to master CPU 0x5: copy to master CPU and route 0x6~0x7: reserved	RW	0x1
14:12	HBH_ERR_ACT	Action performed on IPv6 unicast packets with hop-by-hop extension header error. 0x0: drop 0x1: trap to local CPU 0x2: route (i.e., don't care) 0x3: copy to local CPU and route 0x4: trap to master CPU 0x5: copy to master CPU and route 0x6~0x7: reserved	RW	0x2

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
11:9	HBH_ACT	Action performed on IPv6 unicast packets with hop-by-hop extension header. 0x0: drop 0x1: trap to local CPU 0x2: route (i.e., don't care) 0x3: copy to local CPU and route 0x4: trap to master CPU 0x5: copy to master CPU and route 0x6~0x7: reserved	RW	0x1
8:7	DMAC_MISMATCH_ACT	Action performed on IPv6 unicast packets with multicast DMAC. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
6:5	ZERO_SIP_ACT	Action performed on IPv6 unicast packets with zero SIP. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
4:3	BAD_DIP_ACT	Action performed on IPv6 unicast packets with illegal DIP. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
2:1	BAD_SIP_ACT	Action performed on IPv6 unicast packets with illegal SIP. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
0	GLB_EN	Globally enable IPv6 unicast routing or not. 0x0: disable 0x1: enable	RW	0x0

### L3\_IPMC\_ROUTE\_CTRL

REGISTER ADDRESS : 0xBB00F010

DEFAULT VALUE : 0x73E

IPv4 multicast routing configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15	PKT_TO_CPU_TARGET	The CPU target of trapping/copying packet while doing IPv4 multicast routing. (referred by L3 IPv4 multicast entry) 0b0: local CPU. 0b1: master CPU.	RW	0x0

Bits	Field	Description	Type	Default
14:13	TTL_FAIL_ACT	Action for IPv4 multicast packets which fail the TTL boundary check. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
12:11	MTU_FAIL_ACT	Action performed on IPv4 multicast packets with MTU check failed. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
10	SRC_INTF_FLTR_EN	Enable source interface filtering for IPv4 multicast routed packets or not. 0x0: disable 0x1: enable	RW	0x1
9:7	HDR_OPT_ACT	Action performed on IPv4 multicast packets with IP header options. 0x0: drop 0x1: trap to local CPU and bridge 0x2: route+bridge (i.e., don't care) 0x3: copy to local CPU and route+bridge 0x4: trap to master CPU and bridge 0x5: copy to master CPU and route+bridge 0x6: bridge only 0x7: reserved	RW	0x6
6:5	DMAC_MISMATCH_ACT	Action performed on IPv4 multicast packets with DMA mismatch. 0x0: drop 0x1: bridge 0x2: trap to local CPU and bridge 0x3: trap to master CPU and bridge	RW	0x1
4:3	ZERO_SIP_ACT	Action performed on IPv4 multicast packets with zero SIP. 0x0: drop 0x1: trap to local CPU and bridge 0x2: trap to master CPU and bridge 0x3: bridge but not route	RW	0x3
2:1	BAD_SIP_ACT	Action performed on IPv4 multicast packets with illegal S.P. 0x0: drop 0x1: trap to local CPU and bridge 0x2: trap to master CPU and bridge 0x3: bridge but not route	RW	0x3
0	GLB_EN	Globally enable IPv4 multicast routing or not. 0x0: disable 0x1: enable	RW	0x0

### L3\_IP6MC\_ROUTE\_CTRL

REGISTER ADDRESS : 0xBB00F014

DEFAULT VALUE : 0x1DB3E

IPv6 multicast routing configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:22	RESERVED			
21	PKT_TO_CPU_TARGET	The CPU target of trapping/copying packet while doing IPv6 multicast routing. (referred by L3 IPv6 multicast entry) 0b0: local CPU. 0b1: master CPU.	RW	0x0
20:19	HL_FAIL_ACT	Action for IPv6 multicast packets which fail the hop limit boundary check. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
18:17	MTU_FAIL_ACT	Action performed on IPv6 multicast packets with MTU check failed. 0x0: drop 0x1: trap to local CPU 0x2: trap to master CPU 0x3: reserved	RW	0x0
16	SRC_INTF_FLTR_EN	Enable source interface filtering for IPv6 multicast routed packets or not. 0x0: disable 0x1: enable	RW	0x1
15:13	HDR_ROUTE_ACT	Action performed on IPv6 multicast packets with routing extension headers. 0x0: drop 0x1: trap to local CPU and bridge 0x2: route+bridge (i.e., don't care) 0x3: copy to local CPU and route+bridge 0x4: trap to master CPU and bridge 0x5: copy to master CPU and route+bridge 0x6: bridge only 0x7: reserved	RW	0x6
12:10	HBH_ERR_ACT	Action performed on IPv6 multicast packets with hop-by-hop extension header error 0x0: drop 0x1: trap to local CPU and bridge 0x2: route+bridge (i.e., don't care) 0x3: copy to local CPU and route+bridge 0x4: trap to master CPU and bridge 0x5: copy to master CPU and route+bridge 0x6: bridge only 0x7: reserved	RW	0x6
9:7	HBH_ACT	Action performed on IPv6 multicast packets with hop-by-hop extension header. 0x0: drop 0x1: trap to local CPU and bridge 0x2: route+bridge (i.e., don't care) 0x3: copy to local CPU and route+bridge 0x4: trap to master CPU and bridge 0x5: copy to master CPU and route+bridge 0x6: bridge only 0x7: reserved	RW	0x6
6:5	DMAC_MISMATCH_ACT	Action performed on IPv6 multicast packets with DMAC mismatch. 0x0: drop 0x1: bridge 0x2: trap to local CPU and bridge 0x3: trap to master CPU and bridge	RW	0x1

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
4:3	ZERO_SIP_ACT	Action performed on IPv6 multicast packets with zero SIP. 0x0: drop 0x1: trap to local CPU and bridge 0x2: trap to master CPU and bridge 0x3: bridge but not route	RW	0x3
2:1	BAD_SIP_ACT	Action performed on IPv6 multicast packets with illegal SIP. 0x0: drop 0x1: trap to local CPU and bridge 0x2: trap to master CPU and bridge 0x3: bridge but not route	RW	0x3
0	GLB_EN	Globally enable IPv6 multicast routing or not. 0x0: disable 0x1: enable	RW	0x0

### L3\_PORT\_IP\_ROUTE\_CTRL

BASE ADDRESS : 0xBB00F018

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Port-based IPv4 routing configurations

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:6	RESERVED			
5:3	URPF_FAIL_ACT	Action performed on IPv4 unicast packets with uRPF check failed. 0x0: drop 0x1: trap to local CPU 0x2: forward (permit) 0x3: copy to local CPU (trap and forward) 0x4: trap to master CPU 0x5: copy to master CPU (trap and forward) 0x6~0x7: reserved	RW	0x0
2	URPF_CHK_MODE	uRPF check mode for IPv4 unicast routing packets. 0b0: loose mode 0b1: strict mode	RW	0x0
1	URPF_DFLT_ROUTE_EN	Enable default route when IPv4 unicast packets performing uRPF check. 0b0: disable 0b1: enable	RW	0x0
0	URPF_CHK_EN	Enable uRPF check for IPv4 unicast routing packets or not. 0b0: disable 0b1: enable	RW	0x0

### **L3\_PORT\_IP6\_ROUTE\_CTRL**

BASE ADDRESS : 0xBB00F0FC

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Port-based IPv6 routing configurations

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:6	RESERVED			
5:3	URPF_FAIL_ACT	Action performed on IPv6 unicast packets with uRPF check failed. 0x0: drop 0x1: trap to local CPU 0x2: forward (permit) 0x3: copy to local CPU (trap and forward) 0x4: trap to master CPU 0x5: copy to master CPU (trap and forward) 0x6~0x7: reserved	RW	0x0
2	URPF_CHK_MODE	uRPF check mode for IPv6 unicast routing packets. 0b0: loose mode 0b1: strict mode	RW	0x0
1	URPF_DFLT_ROUTE_EN	Enable default route when IPv6 unicast packets performing uRPF check. 0b0: disable 0b1: enable	RW	0x0
0	URPF_CHK_EN	Enable uRPF check for IPv6 unicast routing packets or not. 0b0: disable 0b1: enable	RW	0x0

### **L3\_INTF\_IP\_MTU**

BASE ADDRESS : 0xBB00F1E0

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x5DC

This is a One-Dimension Common Register Array.

L3 interface IPv4 MTU configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13:0	MTU	Length of the MTU in the unit of byte. (For IPv4) Indexed by L3 egress interface.	RW	0x05DC

### **L3\_INTF\_IP6\_MTU**

BASE ADDRESS : 0xBB00F220

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x5DC

This is a One-Dimension Common Register Array.

L3 interface IPv6 MTU configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13:0	MTU	Length of the MTU in the unit of byte. (For IPv6) Indexed by L3 egress interface.	RW	0x05DC

### **L3\_ENTRY\_MV\_CTRL**

REGISTER ADDRESS : 0xBB00F260

DEFAULT VALUE : 0x0

L3 entry maintenance control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:16	TO	Index of the first entry after operation. Relevant only for move operation.	RW	0x0
15:2	FROM	Index of the first entry before operation.	RW	0x0
1	CMD	Command performed on the L3 entries. 0b0: clear 0b1: move	RW	0x0
0	EXEC	Execution status of operation logic. 0b0: idle (completed) 0b1: busy (executing)	RWAC	0x0

### **L3\_ENTRY\_MV\_PARAM**

REGISTER ADDRESS : 0xBB00F264

DEFAULT VALUE : 0x0

L3 entry maintenance parameter register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:14	RESERVED			
13:0	LEN	The number of entries to be operated.	RW	0x0

Bits	Field	Description	Type	Default
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### L3\_ECMP\_HASH\_CTRL

REGISTER ADDRESS : 0xBB00F268

DEFAULT VALUE : 0x0

L3 ECMP hash control register

Bits	Field	Description	Type	Default
31:21	RESERVED			
20:19	L4_DPORT_BIT_OFFSET	Bit offset of DPORT[15:0] (used to avoid polarization issue)	RW	0x0
18:17	L4_SPORT_BIT_OFFSET	Bit offset of SPORT[15:0] (used to avoid polarization issue)	RW	0x0
16:15	DIP_BIT_OFFSET	Bit offset of DIP[31:0] (used to avoid polarization issue)	RW	0x0
14:13	SIP_BIT_OFFSET	Bit offset of SIP[31:0] (used to avoid polarization issue)	RW	0x0
12	L4_DPORT_INC	Include L4 destination port (HSB.L4_DPORT, 16-bit) or not. 0b0: not include (XOR with all-zeros) 0b1: include For IP fragment packets or non TCP/UDP packet, always ignore this setting.	RW	0x0
11	L4_SPORT_INC	Include L4 source port (HSB.L4_SPORT, 16-bit) or not. 0b0: not include (XOR with all-zeros) 0b1: include For IP fragment packets or non TCP/UDP packet, always ignore this setting.	RW	0x0
10	L4_PROTO_INC	Include L4 protocol (HSB.IP_PROTO, 8-bit) or not. 0b0: not include (XOR with all-zeros) 0b1: include	RW	0x0
9	IP6_LABEL_INC	Include IPv6 Flow_label (HSB.IP6_FLOW_LBL, 20-bit) for IPv6 only. 0b0: not include (XOR with all-zeros) 0b1: include Note: For IPv4, use 0 to calculate the hash index.	RW	0x0
8	DSCP_INC	Include DSCH (HSB.IP_TOS[7:2], 6-bit) or not. 0b0: not include (XOR with all-zeros) 0b1: include	RW	0x0
7	DIP_INC	Include DIP (HSB.IP_DIP, IP_DIP1, IP_DIP2, IP_DIP3) or not. 0b0: not include (XOR with all-zeros) 0b1: include Note: Use 0 instead of DIP1~DIP3 for IPv4 packets.	RW	0x0

Bits	Field	Description	Type	Default
6	SIP_INC	Include SIP (HSB.IP_SIP, IP_SIP1, IP_SIP2, IP_SIP3) or not. 0b0: not include (XOR with all-zeros) 0b1: include Note: Use 0 instead of SIP1~SIP3 for IPv4 packets.	RW	0x0
5	TRUNK_ID_INC	Include source trunk_id (HSM.TRUNK_ID) or not. 0b0: not include (XOR with all-zeros) 0b1: include	RW	0x0
4	RX_PORT_INC	Include physical source port (HSB.SPP) or not. 0b0: not include (XOR with all-zeros) 0b1: include	RW	0x0
3:0	UNIVERSAL_ID	universal ID (to avoid polarization issue), always include this field.	RW	0x0

### L3\_MONT\_CNTR\_CTRL

BASE ADDRESS : 0xBB00F26C

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

#### L3 Monitor Counter Control Register

Bits	Field	Description	Type	Default
31:18	RESERVED			
17	RST	Write 1 to clear the internal counter then auto clear this RST bit by ASIC.	RWAC	0x0
16	LATCH	Write 1 to trigger H/W latches the value into the COUNT field. 0b0: normal idle state 0b1: trigger to latch the counter value	RWAC	0x0
15	MODE	Counter mode: 0b0: packet-count 0b1: byte-count	RW	0x0
14:0	INDEX	Index of the monitored L3 entry	RW	0x0

### L3\_MONT\_CNTR\_DATA

BASE ADDRESS : 0xBB00F27C

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

### L3 Monitor Counter Data Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:42	RESERVED			
41:32	CNT_HI	Count (MSB)	RO	0x0
31:0	CNT_LO	Count (LSB)	RO	0x0

### **L3\_HW\_LU\_KEY\_CTRL**

REGISTER ADDRESS : 0xBB00F29C

DEFAULT VALUE : 0x0

L3 hardware-aided lookup key control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	ROUND	Round bit value of match key for multicast entry in TCAM lookup 0b0: round 0 for searching (S, G) entry 0b1: round 1 for searching (*, G) entry	RW	0x0
23:22	ENTRY_TYPE	Entry type 0x0: IPv4 unicast entry (search key: {VRF, IP[31:0]}) 0x1: IPv4 multicast entry (search key: {VRF, SIP[31:0], DIP[31:0], VID }) 0x2: IPv6 unicast entry (search key: {VRF, IP[127:0]}) 0x3: IPv6 multicast entry (search key: {VRF, SIP[127:0], DIP[127:0], VID })	RW	0x0
21	IPMC_TYPE	IPMC type 0b0: L2 only 0b1: L2+L3	RW	0x0
20:13	VRF	VRF ID	RW	0x0
12	MC_KEY_SEL	L3 IP multicast lookup key selection: 0b0: VID (SIP/*, GIP, VID) 0b1: Interface ID (SIP/*, GIP, INTF)	RW	0x0
11:0	VID_INTF_ID	VLAN ID / Interface ID	RW	0x0

### **L3\_HW\_LU\_KEY\_SIP\_CTRL**

REGISTER ADDRESS : 0xBB00F2A0

DEFAULT VALUE : 0x0, 0x0, 0x0, 0x0

L3 hardware-aided lookup SIP address control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
127:96	IP3	IPv6 address [127:96] (SIP for multicast)	RW	0x0
95:64	IP2	IPv6 address [95:64] (SIP for multicast)	RW	0x0
63:32	IP1	IPv6 address [63:32] (SIP for multicast)	RW	0x0
31:0	IP	IPv4/IPv6 Source IP address [31:0] (for multicast)	RW	0x0

### L3\_HW\_LU\_KEY\_DIP\_CTRL

REGISTER ADDRESS : 0xBB00F2B0

DEFAULT VALUE : 0x0, 0x0, 0x0, 0x0

L3 hardware-aided lookup DIP address control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
127:96	IP3	IPv6 address [127:96] (IP for unicast, DIP for multicast)	RW	0x0
95:64	IP2	IPv6 address [95:64] (IP for unicast, DIP for multicast)	RW	0x0
63:32	IP1	IPv6 address [63:32] (IP for unicast, DIP for multicast)	RW	0x0
31:0	IP	IPv4/IPv6 address [31:0] (for unicast) Destination IP address [31:0] (for multicast)	RW	0x0

### L3\_HW\_LU\_CTRL

REGISTER ADDRESS : 0xBB00F2C0

DEFAULT VALUE : 0x0

L3 hardware-aided lookup control register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15	EXEC_TCAM	Executing status (read/write) 0b0: normal/ready 0b1: executing/busy Note: Write 1 to trigger hardware to search the entry	RWAC	0x0
14	RESULT_TCAM	Search result (read only) 0b0: not found 0b1: found	RO	0x0
13:0	ENTRY_IDX_TCAM	Index of the found entry (read only)	RO	0x0

### L3\_ECMP

TABLE TYPE : 0x5

TABLE INDEX : 0 - 255

ECMP Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191:189	HASH_0_NH_ID	Selector of nexthop index for hash value 0 (i-th nexthop index = value + 1)
188:186	HASH_1_NH_ID	Selector of nexthop index for hash value 1 (i-th nexthop index = value + 1)
185:183	HASH_2_NH_ID	Selector of nexthop index for hash value 2 (i-th nexthop index = value + 1)
182:180	HASH_3_NH_ID	Selector of nexthop index for hash value 3 (i-th nexthop index = value + 1)
179:177	HASH_4_NH_ID	Selector of nexthop index for hash value 4 (i-th nexthop index = value + 1)
176:174	HASH_5_NH_ID	Selector of nexthop index for hash value 5 (i-th nexthop index = value + 1)
173:171	HASH_6_NH_ID	Selector of nexthop index for hash value 6 (i-th nexthop index = value + 1)
170:168	HASH_7_NH_ID	Selector of nexthop index for hash value 7 (i-th nexthop index = value + 1)
167:165	HASH_8_NH_ID	Selector of nexthop index for hash value 8 (i-th nexthop index = value + 1)
164:162	HASH_9_NH_ID	Selector of nexthop index for hash value 9 (i-th nexthop index = value + 1)
161:159	HASH_10_NH_ID	Selector of nexthop index for hash value 10 (i-th nexthop index = value + 1)
158:156	HASH_11_NH_ID	Selector of nexthop index for hash value 11 (i-th nexthop index = value + 1)
155:153	HASH_12_NH_ID	Selector of nexthop index for hash value 12 (i-th nexthop index = value + 1)
152:150	HASH_13_NH_ID	Selector of nexthop index for hash value 13 (i-th nexthop index = value + 1)
149:147	HASH_14_NH_ID	Selector of nexthop index for hash value 14 (i-th nexthop index = value + 1)
146:144	HASH_15_NH_ID	Selector of nexthop index for hash value 15 (i-th nexthop index = value + 1)
143:131	NH_IDX_1	Index to the 1st nex-hop table entry.
130:118	NH_IDX_2	Index to the 2nd nex-hop table entry.
117:105	NH_IDX_3	Index to the 3rd nex-hop table entry.
104:92	NH_IDX_4	Index to the 4th nex-hop table entry.
91:79	NH_IDX_5	Index to the 5th nex-hop table entry.
78:66	NH_IDX_6	Index to the 6th nex-hop table entry.
65:53	NH_IDX_7	Index to the 7th nex-hop table entry.
52:40	NH_IDX_8	Index to the 8th nex-hop table entry.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
39	METER_EN	Enable bit of meter
38:30	METER_IDX	Meter index
29:0	RESERVED	

### L3\_EGR\_INTF

TABLE TYPE : 0x8

TABLE INDEX : 0 - 1023

Egress Interface Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127:116	DST_VID	Destination forwarding VID
115:68	SMAC_ADDR	Source MAC address
67:64	IP_MTU_IDX	Index to IPv4 MTU entry
63:60	IP6_MTU_IDX	Index to IPv6 MTU entry
59:52	IPMC_TTL_SCOPE	TTL scope for IPv4 multicast packets
51:44	IP6MC_HL_SCOPE	Hop-limit scope for IPv6 multicast packets
43:41	IP_ICMP_REDIRECT_ACT	Forwarding action for IPv4 ICMP packet whose outgoing L3 interface is identical to the incoming one. 0x0: Trap to local CPU 0x1: Drop 0x2: Forward 0x3: Copy to local CPU 0x4: Trap to master CPU 0x5: Copy to master CPU 0x6~0x7: Reserved
40:38	IP6_ICMP_REDIRECT_AC T	Forwarding action for IPv6 ICMPv6 packet whose outgoing L3 interface is identical to the incoming one. 0x0: Trap to local CPU 0x1: Drop 0x2: Forward 0x3: Copy to local CPU 0x4: Trap to master CPU 0x5: Copy to master CPU 0x6~0x7: Reserved
37:35	IP_PBR_ICMP_REDIRECT _ACT	Forwarding action for PBR IPv4 ICMP packet whose outgoing L3 interface is identical to the incoming one. 0x0: Trap to local CPU 0x1: Drop 0x2: Forward 0x3: Copy to local CPU 0x4: Trap to master CPU 0x5: Copy to master CPU 0x6~0x7: Reserved

<b>Bits</b>	<b>Field</b>	<b>Description</b>
34:32	IP6_PBR_ICMP_REDIREC T_ACT	Forwarding action for PBR IPv6 ICMPv6 packet whose outgoing L3 interface is identical to the incoming one. 0x0: Trap to local CPU 0x1: Drop 0x2: Forward 0x3: Copy to local CPU 0x4: Trap to master CPU 0x5: Copy to master CPU 0x6~0x7: Reserved
31	TUNNEL_IF	If set, this is a tunnel interface and TUNNEL_IDX is available.
30:22	TUNNEL_IDX	Tunnel Start entry index
21:0	RESERVED	

### L3\_EGR\_INTF\_CNTR

TABLE TYPE : 0xD

TABLE INDEX : 0 - 1023

Outgoing Interface Counter

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191:150	IF_OUT_OCTETS	Byte count
149:114	IF_OUT_UCAST_PKTS	Packet count of Unicast
113:78	IF_OUT_MULTICAST_PKTS	Packet count of Multicast
77:42	IF_OUT_BROADCAST_PKTS	Packet count of Broadcast
41:6	IF_OUT_DISCARDS	Discarded packet count
5:0	RESERVED	

### L3\_EGR\_INTF\_LIST

TABLE TYPE : 0x9

TABLE INDEX : 0 - 16383

Outgoing Interface List

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:50	OIL_NEXT	OIL pointer to next OIL entry
49	TTL_DEC	TTL decrement or not. (should disable for routing to CPU or MVR) 0b0: Disabled 0b1: Enabled
48	TTL_CHK	Enable TTL/HopLimit check or not. (should disable for routing to CPU or MVR) 0b0: Disabled 0b1: Enabled

Bits	Field	Description
47	SA_REPLACE	Enable the replacement of SMAC with the SA of L3_EGR_INTF or not. (should disable for routing to CPU or MVR) 0b0: Do not replace SA. 0b1: Replace the SA with L3_EGR.INTF.SMAC
46:37	L3_EGR_INTF_IDX	Outgoing interface index [9:0] (0~1023)
36	L2_TNL_VALID	Destination mode: 0b0: Non-tunnel or IP tunnel (refer to L3_EGR_INTF_IDX) 0b1: L2 tunnel (refer to MC_PMSK_L2_TUNNEL_IDX)
35:24	MC_PMSK_L2_TNL_IDX	Multicast portmask entry index [11:0] (0~4095) if L3_EGR_INTF_IDX.TUNNEL_IF = 0 / L2 tunnel index [11:0] (0~2047 + 64) if L3_EGR_INTF_IDX.TUNNEL_IF = 1 and L2_TNL_VALID = 1.
23	DST_PORT_TYPE	Destination Port Type (indicate the function of Multicast portmask entry index) 0b0: portmask index (MC_PMSK_L2_TUNNEL_IDX[11:0]: MC_PMSK index) 0b1: offset portmask (MC_PMSK_L2_TUNNEL_IDX[10:8]: offset, MC_PMSK_L2_TUNNEL_IDX[7:0]: portmask) When offset is 0x0, portmask represents as port 0 to 7. When offset is 0x1, portmask represents as port 8 to 15. When offset is 0x2, portmask represents as port 16 to 23. When offset is 0x3, portmask represents as port 24 to 31. When offset is 0x4, portmask represents as port 32 to 39. When offset is 0x5, portmask represents as port 40 to 47. When offset is 0x6, portmask represents as port 48 to 55. When offset is 0x7, portmask represents as port 56 to 63.
22:1	ECID	Outgoing ECID = { GRP [1:0], EXT [7:0], BASE [11:0] }
0	RESERVED	

### L3\_EGR\_TUNNEL\_INTF\_CNTR

TABLE TYPE : 0xF

TABLE INDEX : 0 - 383

Outgoing Tunnel Interface Counter

Bits	Field	Description
127:86	IF_OUT_OCTETS	Byte count
85:50	IF_OUT_PKTS	Packet count
49:14	IF_OUT_DROPS	Dropped packet count
13:0	RESERVED	

### L3\_HOST\_ROUTE\_IP6MC\_0

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

## Host Routing Table (IPv6 Multicast) for 6N+0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:115	VRF_ID	KEY: Virtual Route Forwarding database ID (0~63)
114:83	SIP6	KEY: Source IPv6[127:96] address
82	MC_KEY_SEL	KEY: lookup key selection (0: VID, 1: interface ID), This value is obtained from IGR_L3_INTF table.
81:75	RESERVED	
74	VID_CMP	OPT: Compare VID or net. 0b0: ignore VID 0b1: compare, entry matches only when entry's VID is also identical to the packet's incoming VID.
73	L2_EN	Enable L2 bridging information of this entry 0b0: disabled 0b1: enabled
72:71	L2_ACT	Action performed on packets: 0x0: bridge (refer to MC_PMSK_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (bridge and trap to local CPU) 0x3: drop Relevant only when L2_EN is set Note: The target CPU is indicated by L3_IP6MC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
70:59	L2_MC_PMSK_IDX	Multicast portmask entry index for bridging. Relevant only when L2_EN is set
58	L2_MC_TNL_LST_VALID	To indicate whether L2_MC_TNL_LST_IDX is valid.
57:45	L2_MC_TNL_LST_IDX	Index of L2 tunnel list table entry
44	L3_EN	Enable L3 routing information of this entry 0b0: disabled 0b1: enabled
43:42	L3_ACT	Action performed on packets: 0x0: route (refer to ECMP_EN/NH_IDX/ECMP_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (route and trap) 0x3: drop Relevant only when L3_EN is set Note: The target CPU is indicated by L3_IP6MC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
41	RPF_CHK_EN	Performs RPF check or not. 0b0: disable RPF check 0b1: enable RPF check Relevant only for L3_EN is set

Bits	Field	Description
40:39	RPF_FAIL_ACT	Action performed if packets failed RPF check. Relevant only if RPF_CHK_EN is set 0x0: trap to local/master CPU 0x1: drop 0x2: copy to local/master CPU (route and trap) 0x3: ASSERT_CHK: find out whether incoming interface is in the list of outgoing interface.
38:25	OIL_IDX	OIL entry index.
24	OIL_IDX_VALID	Valid bit of OIL entry index.
23:16	TTL_MIN	Referred TTL minimum, used to compare with the routed packet's TTL value. If the routed packet's TTL is less than TTL_MIN, the packet should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
15	HIT	Hit indication bit
14:0	RESERVED	

## L3\_HOST\_ROUTE\_IP6MC\_1

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Multicast) for 6N+1

Bits	Field	Description
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:75	SIP6	KEY: Source IP[6[95:48] address
74	QOS_EN	Option: Assign a new internal priority
73:71	QOS_PRI	Internal priority (only available when QOS_EN = 1)
70:55	STACK_FWD_PMSK	Forwarding portmask for stacking system. (bit [15:0] are mapped to port 15 ~ 0 that can be stacking ports)
		Note: In a stacking system, this portmask is used to indicate which ports want to receive one copy of the traffic.
54:43	L2_CHK_VID	Check VID for L2 info validation
42:31	L3_RPF_ID	RPF VID or INTF_ID for L3 info validation

<b>Bits</b>	<b>Field</b>	<b>Description</b>
30:17	MTU_MAX	Referred MTU maximum, used to compare with the routed packet's L3 payload length. If the L3 length is greater than MTU_MAX, it should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
16:0	RESERVED	

## L3\_HOST\_ROUTE\_IP6MC\_2

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Multicast) for 6N+2

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62	FMT	KEY: Format type (0 for L3, 1 for Openflow)
61:60	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
59	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
58:11	SIP6	KEY: Source IPv6 [47:0] address
10:11	RESERVED	
10:0	RESERVED	

## L3\_HOST\_ROUTE\_IP6MC\_3

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Multicast) for 6N+3

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62	FMT	KEY: Format type (0 for L3, 1 for Openflow)
61:60	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast

<b>Bits</b>	<b>Field</b>	<b>Description</b>
59	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
58:27	GIP6	KEY: Group IPv6 [127:96] address
26:15	VID	KEY: Forwarding VID
14:11	RESERVED	
10:0	RESERVED	

### L3\_HOST\_ROUTE\_IP6MC\_4

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Multicast) for 6N+4

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62	FMT	KEY: Format type (0 for L3, 1 for Openflow)
61:60	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
59	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
58:11	GIP6	KEY: Group IPv6 [47:0] address
10:11	RESERVED	
10:0	RESERVED	

### L3\_HOST\_ROUTE\_IP6MC\_5

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Multicast) for 6N+5

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62	FMT	KEY: Format type (0 for L3, 1 for Openflow)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
61:60	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
59	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
58:11	GIP6	KEY: Group IPv6 [47:0] address
10:11	RESERVED	
10:0	RESERVED	

### L3\_HOST\_ROUTE\_IP6UC\_0

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Unicast) for 3N+0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123:116	VRF_ID	KEY: Virtual Route Forwarding database ID
115:84	IP6	KEY: IPv6[127:96] address
83:75	RESERVED	
74	DST_NULL_INTF	Indicates whether associated destination interface is NULL or not. 0b0: Non-null interface. 0b1: Null interface.
73:71	ACT	Action performed on DIP: 0x0: route (refer to ECMP_EN/NH_ECMP_IDX) 0x1: trap to local/master CPU (packets destined for CPU) 0x2: copy to local/master CPU (trap and route) 0x3: drop (For NULL interface) 0x4~0x7: Reserved Note: The target CPU is indicated by L3_IP6UC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
70	ECMP_EN	Option: ECMP enabled or not. 0b0: disabled 0b1: enabled
69:57	NH_ECMP_IDX	Next-hop entry index (13bits) or ECMP entry index (8bits). If ECMP_EN is 0, this field indicates an index to the next-hop entry. Otherwise, it indexes into the ECMP entry.
56	TTL_DEC	Option: TTL decrement by 1
55	TTL_CHK	Option: TTL check

<b>Bits</b>	<b>Field</b>	<b>Description</b>
54	QOS_EN	Option: Assign a new internal priority
53:51	QOS_PRI	Internal priority (only available when QOS_EN = 1)
50:16	RESERVED	
15	HIT	Hit indication bit
14:0	RESERVED	

### L3\_HOST\_ROUTE\_IP6UC\_1

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Unicast) for 3N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62	FMT	KEY: Format type (0 for L3, 1 for Openflow)
61:60	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
59:12	IP6	KEY: IPv6 [79:32] address
11	RESERVED	
10:0	RESERVED	

### L3\_HOST\_ROUTE\_IP6UC\_2

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv6 Unicast) for 3N+2

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62	FMT	KEY: Format type (0 for L3, 1 for Openflow)
61:60	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
59:12	IP6	KEY: IPv6 [127:80] address
11	RESERVED	
10:0	RESERVED	

## L3\_HOST\_ROUTE\_IPMC\_0

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv4 Multicast) for 2N+0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:115	VRF_ID	KEY: Virtual Route Forwarding database ID (0~63)
114:83	SIP	KEY: Source IPv4 address
82	MC_KEY_SEL	KEY: lookup key selection (0: VID, 1: interface ID), This value is obtained from IGR_L3_INTF table.
81:75	RESERVED	
74	VID_CMP	OPT: Compare VID or net. 0b0: ignore VID 0b1: compare, entry matches only when entry's VID is also identical to the packet's incoming VID.
73	L2_EN	Enable L2 bridging information of this entry 0b0: disabled 0b1: enabled
72:71	L2_ACT	Action performed on packets: 0x0: bridge (refer to MC_PMSK_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (bridge and trap to local CPU) 0x3: drop Relevant only when L2_EN is set Note: The target CPU is indicated by L3_IPMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
70:59	L2_MC_PMSK_IDX	Multicast portmask entry index for bridging. Relevant only when L2_EN is set
58	L2_MC_TNL_LST_VALID	To indicate whether L2_MC_TNL_LST_IDX is valid.
57:45	L2_MC_TNL_LST_IDX	Index of L2 tunnel list table entry
44	L3_EN	Enable L3 routing information of this entry 0b0: disabled 0b1: enabled
43:42	L3_ACT	Action performed on packets: 0x0: route (refer to ECMP_EN/NH_IDX/ECMP_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (route and trap) 0x3: drop Relevant only when L3_EN is set Note: The target CPU is indicated by L3_IPMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
41	RPF_CHK_EN	Performs RPF check or not. 0b0: disable RPF check 0b1: enable RPF check Relevant only for L3_EN is set
40:39	RPF_FAIL_ACT	Action performed if packets failed RPF check. Relevant only if RPF_CHK_EN is set 0x0: trap to local/master CPU 0x1: drop 0x2: copy to local/master CPU (route and trap) 0x3: ASSERT_CHK: find out whether incoming interface is in the list of outgoing interface.
38:25	OIL_IDX	OIL entry index.
24	OIL_IDX_VALID	Valid bit of OIL entry index.
23:16	TTL_MIN	Referred TTL minimum, used to compare with the routed packet's TTL value. If the routed packet's TTL is less than TTL_MIN, the packet should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
15	HIT	Hit indication bit
14:0	RESERVED	

## L3\_HOST\_ROUTE\_IPMC\_1

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv4 Multicast) for 2N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:91	GIP	KEY: Group IPv4 address
90:79	VID	OPT: Forwarding VID. If VID_CMP is set, this field must be checked against the packet's forwarding VID; If VID_CMP is clear, this field is used as the RPF_VID.
78:75	RESERVED	
74	QOS_EN	Option: Assign a new internal priority
73:71	QOS_PRI	Internal priority (only available when QOS_EN = 1)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
70:55	STACK_FWD_PMSK	Forwarding portmask for stacking system. (bit [15:0] are mapped to port 15 ~ 0 that can be stacking ports)  Note: In a stacking system, this portmask is used to indicate which ports want to receive one copy of the traffic.
54:43	L2_CHK_VID	Check VID for L2 info validation
42:31	L3_RPF_ID	RPF VID or INTF_ID for L3 info validation
30:17	MTU_MAX	Referred MTU maximum, used to compare with the routed packet's L3 payload length. If the L3 length is greater than MTU_MAX, it should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
16:0	RESERVED	

### **L3\_HOST\_ROUTE\_IPUC**

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

Host Routing Table (IPv4 Unicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123:116	VRF_ID	KEY: Virtual Route Forwarding database ID
115:84	IP	KEY: IPv4 address
83:75	RESERVED	
74	DST_NULL_INTF	Indicates whether associated destination interface is NULL or not. 0b0: Non-null interface. 0b1: Null interface.
73:71	ACT	Action performed on DIP: 0x0: route (refer to ECMP_EN/NH_ECMP_IDX) 0x1: trap to local/master CPU (packets destined for CPU) 0x2: copy to local/master CPU (trap and route) 0x3: drop (For NULL interface) 0x4~0x7: Reserved Note: The target CPU is indicated by L3_IPUC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
70	ECMP_EN	Option: ECMP enabled or not. 0b0: disabled 0b1: enabled
69:57	NH_ECMP_IDX	Next-hop entry index (13bits) or ECMP entry index (8bits). If ECMP_EN is 0, this field indicates an index to the next-hop entry. Otherwise, it indexes into the ECMP entry.
56	TTL_DEC	Option: TTL decrement by 1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
55	TTL_CHK	Option: TTL check
54	QOS_EN	Option: Assign a new internal priority
53:51	QOS_PRI	Internal priority (only available when QOS_EN = 1)
50:16	RESERVED	
15	HIT	Hit indication bit
14:0	RESERVED	

### L3\_IGR\_INTF

TABLE TYPE : 0x7

TABLE INDEX : 0 - 1023

Ingress Interface Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:56	VRF_ID	Virtual route forwarding ID
55	IPUC_ROUTE_EN	IPv4 unicast routing enable bit
54	IP6UC_ROUTE_EN	IPv6 unicast routing enable bit
53	IPMC_ROUTE_EN	IPv4 multicast routing enable bit (refer to the multicast section for detail)
52	IP6MC_ROUTE_EN	IPv6 multicast routing enable bit (refer to the multicast section for detail)
51:50	IPMC_ROUTE_LU_MIS_AC_T	Forwarding action when IPv4 multicast routing looks up miss. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved
49:48	IP6MC_ROUTE_LU_MIS_A_C T	Forwarding action when IPv6 multicast routing looks up miss. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved
47:45	IPMC_ACT_224_0_0_X	Action for IPv4 multicast packet whose DIP=224.0.0.0~224.0.0.255 (Local Network Control Block, 224.0.0/24) 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved
44:42	IPMC_ACT_224_0_1_X	Action for IPv4 multicast packet whose DIP=224.0.1.0~224.0.1.255 (Internetwork Control Block, 224.0.1/24) 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved

<b>Bits</b>	<b>Field</b>	<b>Description</b>
41:39	IPMC_ACT_239_X_X_X	Action for IPv4 multicast packet whose DIP=239.0.0.0~239.255.255.255 (Scoped Multicast Ranges/Organization-Local Scope, 239/8) 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved
38:36	IP6MC_ACT_0000_00XX	Action for IPv6 multicast packet whose last 32 bits is 0000:00XX 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved
35:33	IP6_MLD_ACT_0_X_X	Action for IPv6 MLD packet whose DIP = FF0X:0:0:0:0:X:X 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved
32:30	IP6_MLD_ACT_DB8_X_X	Action for IPv6 MLD packet whose DIP = FF0X::DB8:0:0 (Documentation Addresses, FF0X::DB8:0:0/96) 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved
29:27	IP6_ND_ACT	Action for IPv6 neighbor discovery packet 0x0: Forward (follow bridging action if hit an entry, or flood-in-VLAN) 0x1: Trap to local CPU 0x2: Copy to local CPU 0x3: Trap to master CPU 0x4: Copy to master CPU 0x5: Drop 0x6~0x7: Reserved
26:24	IP_URPF_FAIL_ACT	Action performed on IPv4 unicast packets with uRPF check failed. 0x0: drop 0x1: trap to local CPU 0x2: forward (permit) 0x3: copy to local CPU (trap and forward) 0x4: trap to master CPU 0x5: copy to master CPU (trap and forward) 0x6~0x7: reserved
23	IP_URPF_CHK_EN	Enable uRPF check for IPv4 unicast routing packets or not. 0b0: disable 0b1: enable
22	IP_URPF_DFLT_ROUTE_E_N	Enable default route when IPv4 unicast packets performing uRPF check. 0b0: disable 0b1: enable

<b>Bits</b>	<b>Field</b>	<b>Description</b>
21	IP_URPF_CHK_MODE	uRPF check mode for IPv4 unicast routing packets. 0b0: loose mode 0b1: strict mode
20:18	IP6_URPF_FAIL_ACT	Action performed on IPv4 unicast packets with uRPF check failed. 0x0: drop 0x1: trap to local CPU 0x2: forward (permit) 0x3: copy to local CPU (trap and forward) 0x4: trap to master CPU 0x5: copy to master CPU (trap and forward) 0x6~0x7: reserved
17	IP6_URPF_CHK_EN	Enable uRPF check for IPv6 unicast routing packets or not. 0b0: disable 0b1: enable
16	IP6_URPF_DFLT_ROUTE_EN	Enable default route when IPv6 unicast packets performing uRPF check. 0b0: disable 0b1: enable
15	IP6_URPF_CHK_MODE	uRPF check mode for IPv6 unicast routing packets. 0b0: loose mode 0b1: strict mode
14	MC_KEY_SEL	For IP multicast packets, select the L3 lookup key. 0b0: VID i.e., (SIP, GIP, VID) and (*, GIP, VID). 0b1: interface ID i.e., (SIP, GIP, INTF) and (*, GIP, INTF).
13:0	RESERVED	

### L3\_IGR\_INTF\_CNTR

TABLE TYPE : 0xC

TABLE INDEX : 0 - 1023

Incoming Interface Counter

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191:150	IF_IN_OCTETS	Byte count
149:114	IF_IN_UCAST_PKTS	Packet count of Unicast
113:78	IF_IN_MULTICAST_PKTS	Packet count of Multicast
77:42	IF_IN_BROADCAST_PKTS	Packet count of Broadcast
41:6	IF_IN_DISCARDS	Discarded packet count
5:0	RESERVED	

### L3\_IGR\_TUNNEL\_INTF\_CNTR

TABLE TYPE : 0xE

TABLE INDEX : 0 - 383

Incoming Tunnel Interface Counter

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127:86	IF_IN_OCTETS	Byte count
85:50	IF_IN_PKTS	Packet count
49:14	IF_IN_DROPS	Dropped packet count
13:0	RESERVED	

### L3\_NEXTHOP

TABLE TYPE : 0x6

TABLE INDEX : 0 - 8191

Next-Hop Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
31:16	DMAC_IDX	Index to L2 entry to obtain MAC address and outgoing port information to next-hop.
15:6	L3_EGR_INTF_IDX	Index to L3 outgoing interface entry
5:0	RESERVED	

### L3\_PREFIX\_ROUTE\_IP6MC\_0

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Multicast) for 6N+0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	KEY: Format type (0 for L3, 1 for Openflow)
189:188	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
187	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
186:179	VRF_ID	KEY: Virtual Route Forwarding database ID (0~63)
178:147	SIP6	KEY: Source IPv6[127:96] address
146	MC_KEY_SEL	KEY: lookup key selection (0: VID, 1: interface ID), This value is obtained from IGR_L3_INTF table.
145:139	RESERVED	
138	BMSK_FMT	Care Bitmask: FMT

<b>Bits</b>	<b>Field</b>	<b>Description</b>
137:136	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
135	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
134:127	BMSK_VRF_ID	Care Bitmask: VRF_ID
126:95	BMSK_SIP6	Care Bitmask: SIP6
94	BMSK_MC_KEY_SEL	Care Bitmask: MC_KEY_SEL
93:87	BMSK_RESERVED	
86	VID_CMP	OPT: Compare VID or net. 0b0: ignore VID 0b1: compare, entry matches only when entry's VID is also identical to the packet's incoming VID.
85	L2_EN	Enable L2 bridging information of this entry 0b0: disabled 0b1: enabled
84:83	L2_ACT	Action performed on packets: 0x0: bridge (refer to MC_PMSK_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (bridge and trap to local CPU) 0x3: drop Relevant only when L2_EN is set Note: The target CPU is indicated by L3_IP6MC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
82:71	L2_MC_PMSK_IDX	Multicast portmask entry index for bridging. Relevant only when L2_EN is set
70	L2_MC_TNL_LST_VALID	To indicate whether L2_MC_TNL_LST_IDX is valid.
69:57	L2_MC_TNL_LST_IDX	Index of L2 tunnel list table entry
56	L3_EN	Enable L3 routing information of this entry 0b0: disabled 0b1: enabled
55:54	L3_ACT	Action performed on packets: 0x0: route (refer to ECMP_EN/NH_IDX/ECMP_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (route and trap) 0x3: drop Relevant only when L3_EN is set Note: The target CPU is indicated by L3_IP6MC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
53	RPF_CHK_EN	Performs RPF check or not. 0b0: disable RPF check 0b1: enable RPF check Relevant only for L3_EN is set
52:51	RPF_FAIL_ACT	Action performed if packets failed RPF check. Relevant only if RPF_CHK_EN is set 0x0: trap to local/master CPU 0x1: drop 0x2: copy to local/master CPU (route and trap) 0x3: ASSERT_CHK: find out whether incoming interface is in the list of outgoing interface.
50:37	OIL_IDX	OIL entry index.
36	OIL_IDX_VALID	Valid bit of OIL entry index.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
35:28	TTL_MIN	Referred TTL minimum, used to compare with the routed packet's TTL value. If the routed packet's TTL is less than TTL_MIN, the packet should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
27	HIT	Hit indication bit
26:0	RESERVED	

### L3\_PREFIX\_ROUTE\_IP6MC\_1

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Multicast) for 6N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	KEY: Format type (0 for L3, 1 for Openflow)
189:188	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
187	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
186:139	SIP6	KEY: Source IPv6 [95:48] address
138	BMSK_FMT	Care Bitmask: FMT
137:136	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
135	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
134:87	BMSK_IP6	Care Bitmask: GIP6
86	QOS_EN	Option: Assign a new internal priority
85:83	QOS_PRI	Internal priority (only available when QOS_EN = 1)
82:67	STACK_FWD_PMSK	Forwarding portmask for stacking system. (Bit0~3 are mapped to port 48~51 that can be stacking ports)  Note: In a stacking system, this portmask is used to indicate which ports want to receive one copy of the traffic.
66:55	L2_CHK_VID	Check VID for L2 info validation
54:43	L3_RPF_ID	RPF VID or INTF_ID for L3 info validation
42:29	MTU_MAX	Referred MTU maximum, used to compare with the routed packet's L3 payload length. If the L3 length is greater than MTU_MAX, it should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
28:0	RESERVED	

### **L3\_PREFIX\_ROUTE\_IP6MC\_2**

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Multicast) for 6N+2

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:75	SIP6	KEY: Source IPv6 [47:0] address
74	BMSK_FMT	Care Bitmask: FMT
73:72	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
71	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
70:23	BMSK_IP6	Care Bitmask: GIP6
22:0	RESERVED	

### **L3\_PREFIX\_ROUTE\_IP6MC\_3**

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Multicast) for 6N+3

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:91	GIP6	KEY: Group IPv6[127:96] address
90:79	VID	KEY: Forwarding VID
78	ROUND	KEY: Round of the searching. (0 for first round, 1 for second round)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
77:75	RESERVED	
74	BMSK_FMT	Care Bitmask: FMT
73:72	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
71	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
70:39	BMSK_GIP6	Care Bitmask: GIP6
38:27	BMSK_VID	Care Bitmask: VID
26	BMSK_ROUND	Care Bitmask: ROUND
25:23	BMSK_RESERVED	
22:0	RESERVED	

### L3\_PREFIX\_ROUTE\_IP6MC\_4

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Multicast) for 6N+4

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:75	GIP6	KEY: Group IPv6 [95:48] address
74	BMSK_FMT	Care Bitmask: FMT
73:72	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
71	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
70:23	BMSK_IP6	Care Bitmask: GIP6
22:0	RESERVED	

### L3\_PREFIX\_ROUTE\_IP6MC\_5

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Multicast) for 6N+5

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
122:75	GIP6	KEY: Group IPv6 [47:0] address
74	BMSK_FMT	Care Bitmask: FMT
73:72	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
71	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
70:23	BMSK_IP6	Care Bitmask: GIP6
22:0	RESERVED	

### L3\_PREFIX\_ROUTE\_IP6UC\_0

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Unicast) for 3N+0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	KEY: Format type (0 for L3, 1 for Openflow)
189:188	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
187:180	VRF_ID	KEY: Virtual Route Forwarding database ID
179:148	IP6	KEY: IPv6[127:96] address
147:139	RESERVED	
138	BMSK_FMT	Care Bitmask: FMT
137:136	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
135:128	BMSK_VRF_ID	Care Bitmask: VRF_ID
127:96	BMSK_IP6	Care Bitmask: IP6
95:87	BMSK_RESERVED	
86	HOST_ROUTE	To indicate if this is a host route.
85	DFLT_ROUTE	To indicate if this is a default route.
84	DST_NULL_INTF	Indicates whether associated destination interface is NULL or not. 0b0: Non-null interface. 0b1: Null interface.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
83:81	ACT	Action performed on DIP: 0x0: route (refer to ECMP_EN/NH_ECMP_IDX) 0x1: trap to local/master CPU (packets destined for CPU) 0x2: copy to local/master CPU (trap and route) 0x3: drop (For NULL interface) 0x4~0x7: Reserved Note: The target CPU is indicated by L3_IP6UC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
80	ECMP_EN	Option: ECMP enabled or not. 0b0: disabled 0b1: enabled
79:67	NH_ECMP_IDX	Next-hop entry index (13bits) or ECMP entry index (8bits). If ECMP_EN is 0, this field indicates an index to the next-hop entry. Otherwise, it indexes into the ECMP entry.
66	TTL_DEC	Option: TTL decrement by 1
65	TTL_CHK	Option: TTL check
64	QOS_EN	Option: Assign a new internal priority
63:61	QOS_PRI	Internal priority (only available when QOS_EN = 1)
60:28	RESERVED	
27	HIT	Hit indication bit
26:0	RESERVED	

### L3\_PREFIX\_ROUTE\_IP6UC\_1

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Unicast) for 3N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123:76	IP6	KEY: IPv6 [95:48] address
75	RESERVED	
74	BMSK_FMT	Care Bitmask: FMT
73:72	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
71:24	BMSK_IP6	Care Bitmask: IP6
23	BMSK_RESERVED	
22:0	RESERVED	

### **L3\_PREFIX\_ROUTE\_IP6UC\_2**

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv6 Unicast) for 3N+2

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	KEY: Format type (0 for L3, 1 for Openflow)
125:124	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
123:76	IP6	KEY: IPv6 [47:0] address
75	RESERVED	
74	BMSK_FMT	Care Bitmask: FMT
73:72	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
71:24	BMSK_IP6	Care Bitmask: IP6
23	BMSK_RESERVED	
22:0	RESERVED	

### **L3\_PREFIX\_ROUTE\_IPMC\_0**

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv4 Multicast) for 2N+0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	KEY: Format type (0 for L3, 1 for Openflow)
189:188	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
187	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
186:179	VRF_ID	KEY: Virtual Route Forwarding database ID (0~63)
178:147	SIP	KEY: Source IPv4 address
146	MC_KEY_SEL	KEY: lookup key selection (0: VID, 1: interface ID), This value is obtained from IGR_L3_INTF table.
145:139	RESERVED	
138	BMSK_FMT	Care Bitmask: FMT

<b>Bits</b>	<b>Field</b>	<b>Description</b>
137:136	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
135	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
134:127	BMSK_VRF_ID	Care Bitmask: VRF_ID
126:95	BMSK_SIP	Care Bitmask: SIP
94	BMSK_MC_KEY_SEL	Care Bitmask: MC_KEY_SEL
93:87	BMSK_RESERVED	
86	VID_CMP	OPT: Compare VID or net. 0b0: ignore VID 0b1: compare, entry matches only when entry's VID is also identical to the packet's incoming VID.
85	L2_EN	Enable L2 bridging information of this entry 0b0: disabled 0b1: enabled
84:83	L2_ACT	Action performed on packets: 0x0: bridge (refer to MC_PMSK_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (bridge and trap to local CPU) 0x3: drop Relevant only when L2_EN is set Note: The target CPU is indicated by L3_IPMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
82:71	L2_MC_PMSK_IDX	Multicast portmask entry index for bridging. Relevant only when L2_EN is set
70	L2_MC_TNL_LST_VALID	To indicate whether L2_MC_TNL_LST_IDX is valid.
69:57	L2_MC_TNL_LST_IDX	Index of L2 tunnel list table entry
56	L3_EN	Enable L3 routing information of this entry 0b0: disabled 0b1: enabled
55:54	L3_ACT	Action performed on packets: 0x0: route (refer to ECMP_EN/NH_IDX/ECMP_IDX) 0x1: trap to local/master CPU 0x2: copy to local/master CPU (route and trap) 0x3: drop Relevant only when L3_EN is set Note: The target CPU is indicated by L3_IPMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
53	RPF_CHK_EN	Performs RPF check or not. 0b0: disable RPF check 0b1: enable RPF check Relevant only for L3_EN is set
52:51	RPF_FAIL_ACT	Action performed if packets failed RPF check. Relevant only if RPF_CHK_EN is set 0x0: trap to local/master CPU 0x1: drop 0x2: copy to local/master CPU (route and trap) 0x3: ASSERT_CHK: find out whether incoming interface is in the list of outgoing interface.
50:37	OIL_IDX	OIL entry index.
36	OIL_IDX_VALID	Valid bit of OIL entry index.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
35:28	TTL_MIN	Referred TTL minimum, used to compare with the routed packet's TTL value. If the routed packet's TTL is less than TTL_MIN, the packet should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
27	HIT	Hit indication bit
26:0	RESERVED	

## L3\_PREFIX\_ROUTE\_IPMC\_1

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv4 Multicast) for 2N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	KEY: Format type (0 for L3, 1 for Openflow)
189:188	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
187	IPMC_TYPE	KEY: Relevant only if ENTRY_TYPE is 0x1 or 0x3 0b0: pure L2 entry 0b1: L2+L3 entry
186:155	GIP	KEY: Group/Destination IPv4 address
154:143	VID	KEY: Forwarding VID
142	ROUND	KEY: Round of the searching. (0 for first round, 1 for second round)
141:139	RESERVED	
138	BMSK_FMT	Care Bitmask: FMT
137:136	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
135	BMSK_IPMC_TYPE	Care Bitmask: IPMC_TYPE
134:103	BMSK_GIP	Care Bitmask: GIP
102:91	BMSK_VID	Care Bitmask: VID
90	BMSK_ROUND	Care Bitmask: ROUND
89:87	BMSK_RESERVED	
86	QOS_EN	Option: Assign a new internal priority
85:83	QOS_PRI	Internal priority (only available when QOS_EN = 1)
82:67	STACK_FWD_PMSK	Forwarding portmask for stacking system. (Bit0~3 are mapped to port 48~51 that can be stacking ports)
		Note: In a stacking system, this portmask is used to indicate which ports want to receive one copy of the traffic.
66:55	L2_CHK_VID	Check VID for L2 info validation

<b>Bits</b>	<b>Field</b>	<b>Description</b>
54:43	L3_RPF_ID	RPF VID or INTF_ID for L3 info validation
42:29	MTU_MAX	Referred MTU maximum, used to compare with the routed packet's L3 payload length. If the L3 length is greater than MTU_MAX, it should be copied to local/master CPU according to L3_IPxMC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
28:0	RESERVED	

## **L3\_PREFIX\_ROUTE\_IPUC**

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

Prefix Routing Table (IPv4 Unicast)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	KEY: Format type (0 for L3, 1 for Openflow)
189:188	ENTRY_TYPE	KEY: Type of the routing entry 0x0: IPv4 Unicast 0x1: IPv4 Multicast 0x2: IPv6 Unicast 0x3: IPv6 Multicast
187:180	VRF_ID	KEY: Virtual Route Forwarding database ID
179:148	IP	KEY: IPv4 address
147:139	RESERVED	
138	BMSK_FMT	Care Bitmask: FMT
137:136	BMSK_ENTRY_TYPE	Care Bitmask: ENTRY_TYPE
135:128	BMSK_VRF_ID	Care Bitmask: VRF_ID
127:96	BMSK_IP	Care Bitmask: IP
95:87	BMSK_RESERVED	
86	HOST_ROUTE	To indicate if this is a host route.
85	DFLT_ROUTE	To indicate if this is a default route.
84	DST_NULL_INTF	Indicates whether associated destination interface is NULL or not. 0b0: Non-null interface. 0b1: Null interface.
83:81	ACT	Action performed on DIP: 0x0: route (refer to ECMP_EN/NH_ECMP_IDX) 0x1: trap to local/master CPU (packets destined for CPU) 0x2: copy to local/master CPU (trap and route) 0x3: drop (For NULL interface) 0x4~0x7: Reserved Note: The target CPU is indicated by L3_IPUC_ROUTE_CTRL.PKT_TO_CPU_TARGET.
80	ECMP_EN	Option: ECMP enabled or not. 0b0: disabled 0b1: enabled

<b>Bits</b>	<b>Field</b>	<b>Description</b>
79:67	NH_ECMP_IDX	Next-hop entry index (13bits) or ECMP entry index (8bits). If ECMP_EN is 0, this field indicates an index to the next-hop entry. Otherwise, it indexes into the ECMP entry.
66	TTL_DEC	Option: TTL decrement by 1
65	TTL_CHK	Option: TTL check
64	QOS_EN	Option: Assign a new internal priority
63:61	QOS_PRI	Internal priority (only available when QOS_EN = 1)
60:28	RESERVED	
27	HIT	Hit indication bit
26:0	RESERVED	

## L3\_ROUTER\_MAC

TABLE TYPE : 0x2

TABLE INDEX : 0 - 1023

Router MAC Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
159	VALID	Valid bit
158	PORT_TYPE	Type of the infress port (0 for individual port, 1 for trunk port)
157:151	PORT_ID	For individual port, to indicate the port ID (0 ~ 51) For trunk port, to indicate the trunk group (0 ~ 127)
150:141	INTF_ID	Ingress interface ID (0 ~ 1023)
140:93	MAC	MAC address of the L3 interface
92	LU_PHASE	Lookup phase (0 for tunneling only, 1 for routing only, don't care for both)
91:87	RESERVED	
86	BMSK_PORT_TYPE	Care Bitmask: PORT_TYPE
85:79	BMSK_PORT_ID	Care Bitmask: PORT_ID
78:69	BMSK_INTF_ID	Care Bitmask: INTF_ID
68:21	BMSK_MAC	Care Bitmask: MAC
20	BMSK_LU_PHASE	Care Bitmask: LU_PHASE
19	RESERVED	
18:15	BMSK_RESERVED	
14:12	ACT	Action performed on the packets 0x0: L3 process 0x1: L2 bridging 0x2: Trap to local CPU 0x3: Copy to local CPU and L3 process 0x4: Trap to master CPU 0x5: Copy to master CPU and L3 process 0x6~0x7: Reserved
11:0	RESERVED	

SECTION 9.2

**LAYER 3 MISC**

Layer 3 misc. features

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## CHAPTER 10

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# Tunneling

The chapter describes features related to tunneling

SECTION 10.1	<b>IP TUNNEL</b>
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IP Tunnel module

### **TUNNEL\_IP\_ADDR\_CHK\_CTRL**

REGISTER ADDRESS : 0xBB00F2C4

DEFAULT VALUE : 0x0

Tunnel IP Address Checking Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:17	RESERVED			
16:15	TT_6RD_DIP_FAIL_TRAP	If set, packet with 6RD destination address check failure will be trapped to CPU. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0
14	TT_6RD_DIP_CHK	If set, 6RD destination address check is enabled.	RW	0x0
13:12	TT_6TO4_DIP_FAIL_TRAP	If set, packet with 6to4 destination address check failure will be trapped to CPU. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0
11	TT_6TO4_DIP_CHK	If set, 6to4 destination address check is enabled.	RW	0x0
10:9	TT_6TO4_SIP_FAIL_TRAP	If set, packet with 6to4 source address check failure will be trapped to CPU. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0
8	TT_6TO4_SIP_CHK	If set, 6to4 source address check is enabled.	RW	0x0
7:6	TT_ISATAP_SIP_FAIL_TRAP	If set, packet with ISATAP source address check failure will be trapped to CPU. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0

Bits	Field	Description	Type	Default
5	TT_ISATAP_SIP_MAP_CH_K	If set, ISATAP IPv6/IPv4 matched source address check is enabled.	RW	0x0
4	TT_ISATAP_SIP_TYPE_C_HK	If set, ISATAP private/public source address check is enabled.	RW	0x0
3:2	TT_IP6_SIP_FAIL_TRAP	If set, packet with IPv6 source address check failure will be trapped to CPU. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0
1	TT_IP6_SIP_IP4MAP_CH_K	If set, IPv4 mapped address is forbidden for IPv6 source address.	RW	0x0
0	TT_IP6_SIP_IP4CMPT_C_HK	If set, IPv4 compatible address is forbidden for IPv6 source address.	RW	0x0

## TUNNEL\_ROUTE\_CTRL

REGISTER ADDRESS : 0xBB00F2C8

DEFAULT VALUE : 0x0

Tunnel Routing Control Register

Bits	Field	Description	Type	Default
31:6	RESERVED			
5:4	ROUTE_TO_TUNNEL_ACT	Forwarding action for the tunnel-start packet causes routing exception (next-hop is a tunnel interface while post routing). 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0
3:2	TTL_FAIL_ACT	Forwarding action for the tunnel-start packet with TTL=0 or Hop-Limit=0. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0
1:0	MTU_FAIL_ACT	Forwarding action for the tunnel-start packet with exceeded MTU size. 0x0: Drop 0x1: Trap to local CPU 0x2: Trap to master CPU 0x3: Reserved	RW	0x0

## TUNNEL\_IP\_IDENTIFICATION

REGISTER ADDRESS : 0xBB004F48

DEFAULT VALUE : 0x0

## IP Identification Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	ID	Identification of IP header for packet encapsulation	RW	0x0

**TUNNEL\_6RD\_DOMAIN**

BASE ADDRESS : 0xBB00F2CC  
 ARRAY INDEX : 0 - 1  
 ARRAY OFFSET : 0xC  
 DEFAULT VALUE : 0x0, 0x0, 0x0

This is a One-Dimension Common Register Array.

## 6RD Domain Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
95:64	PREFIX_HI	6RD prefix (map to IPv6 address [127:96])	RW	0x0
63:33	PREFIX_LO	6RD prefix (map to IPv6 address [95:65])	RW	0x0
32	RESERVED			
31:11	RESERVED			
10:6	IPv4_MASK_LEN	Length of IPv4 netmask	RW	0x0
5:0	PREFIX_LEN	Length of 6RD prefix	RW	0x0

**TUNNEL\_QOS\_PROFILE**

BASE ADDRESS : 0xBB000100  
 ARRAY INDEX : 0 - 63  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

## Tunnel QoS Profile Configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:18	RESERVED			
17:15	OPRI	Tunnel outer tag priority	RW	0x0
14:13	OPRI_SRC	Select tunnel outer tag priority source. 0b00: use specified OPRI 0b01: passenger outer tag (CAPWAP only) 0b10: internal priority 0b11: port-based priority	RW	0x0
12:10	IPRI	Tunnel inner tag priority	RW	0x0

Bits	Field	Description	Type	Default
9:8	IPRI_SRC	Select tunnel inner tag priority source. 0b00: use specified IPRI 0b01: passenger inner tag (CAPWAP only) 0b10: internal priority 0b11: port-based priority	RW	0x0
7:2	DSCP	Tunnel DSCP value	RW	0x0
1:0	DSCP_SRC	Select tunnel DSCP source. 0b00: use specified DSCP 0b01: inner DSCP 0b10: internal priority remarking 0b11: reserved	RW	0x0

## TUNNEL\_VXLAN\_CTRL

REGISTER ADDRESS : 0xBB000528

DEFAULT VALUE : 0x12B5

VxLAN Tunnel Configuration

Bits	Field	Description	Type	Default
31:17	RESERVED			
16	UDP_SPORT_PARSE_EN	To enable parser to recognize VxLAN header when its port number appears as a source port. 0b0: disable 0b1: enable	RW	0x0
15:0	UDP_PORT	UDP port number for parsing VxLAN header. (default: 4789)	RW	0x12B5

## TUNNEL\_START

TABLE TYPE : 0x9

TABLE INDEX : 0 - 383

IP Tunnel Start Table

Bits	Field	Description
63	ENTRY_TYPE	Outer IP version: 0b0: Outer IPv4 0b1: Outer IPv6
62:60	TUNNEL_TYPE	Tunnel Type: 0b000: Configured tunnel 0b001: ISATAP (outer IPv4 only) 0b010: 6to4 (outer IPv4 only) 0b011: 6RD (outer IPv4 only) 0b100: GRE 0b101: VXLAN/UDP 0b110: CAPWAP/UDP or CAPWAP/UDP-Lite 0b111: VXLAN-GPE/UDP

<b>Bits</b>	<b>Field</b>	<b>Description</b>
59	TTL_ASSIGN	To indicate whether assign a new TTL value
58	TTL_DEC	If set, TTL is decreased.
57:0	RESERVED	

## TUNNEL\_START\_ENCAP

TABLE TYPE : 0x5

TABLE INDEX : 0 - 383

IP Tunnel Start Table (Encap)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
287	ENTRY_TYPE	Outer IP version: 0b0: Outer IPv4 0b1: Outer IPv6
286:284	TUNNEL_TYPE	Tunnel Type: 0b000: Configured tunnel 0b001: ISATAP (outer IPv4 only) 0b010: 6to4 (outer IPv4 only) 0b011: 6RD (outer IPv4 only) 0b100: GRE 0b101: VXLAN/UDP 0b110: CAPWAP/UDP or CAPWAP/UDP-Lite 0b111: VXLAN-GPE/UDP
283:252	SIP	Source IP address
251:220	DIP	Destination IP address
219	TTL_ASSIGN	To indicate whether assign a new TTL value
218:211	TTL	TTL (available when TTL_ASSIGN=1)
210	TTL_DEC	If set, TTL is decreased.
209	DONT_FRAG_ASSIGN	Indicate whether assign a specific Don't Fragment bit. 0b0: use the inner don't frag bit for outer tunnel packet 0b1: assign the flag by DONT_FRAG bit
208	DONT_FRAG	Don't fragment bit (available when DONT_FRAG_ASSIGN=1)
207:192	L4_MSB_DATA	GRE key[31:16]/L4 source port
191:176	L4_LSB_DATA	GRE key[15:0]/L4 destination port
175:170	QOS_PROFILE_IDX	Index to the QoS profile
169	VXLAN_ITAG_ACT	Indicate the passenger's inner tag status: 0b0: untag (force) 0b1: keep original tag (status and content) Note: only used by VxLAN tunnel.
168	VXLAN_OTAG_ACT	Indicate the passenger's outer tag status: 0b0: untag (force) 0b1: keep original tag (status and content) Note: only used by VxLAN tunnel.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
167:144	VXLAN_VNI	(Only used by VXLAN/VXLAN-GPE tunnel.)
		For Traditional Pipeline: bit [23:16] - reserved bit [15:8] - VXLAN flags bit [7:0] - VXLAN Next-Protocol
		For Openflow Pipeline: 24-bit value of VXLAN Network Identifier (VNI)
143	GRE_KEY_ADD	Indicate to insert a GRE key (available when TUNNEL_TYPE=GRE)
142:0	RESERVED	

## TUNNEL\_START\_ENCAP\_1

TABLE TYPE : 0x5

TABLE INDEX : 0 - 383

IP Tunnel Start Table (N+1)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	ENTRY_TYPE	Outer IP version: 0b0: Outer IPv4 0b1: Outer IPv6 (should be 0b1)
126:31	SIP	Source IPv6 address MSB[127:32]
30:11	FLOW_LABEL	Flow Label ID of IPv6 header
10:0	RESERVED	

## TUNNEL\_START\_ENCAP\_2

TABLE TYPE : 0x5

TABLE INDEX : 0 - 383

IP Tunnel Start Table (N+2)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	ENTRY_TYPE	Outer IP version: 0b0: Outer IPv4 0b1: Outer IPv6 (should be 0b1)
126:31	DIP	Destination IPv6 address MSB[127:32]
30:0	RESERVED	

## TUNNEL\_TERMINATION

TABLE TYPE : 0x8

TABLE INDEX : 0 - 383

## IP Tunnel Termination Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
255	VALID	Valid bit
254	ENTRY_TYPE	Entry type 0b0: Outer IPv4 0b1: Outer IPv6
253:244	INTF_ID	Ingress L3 Interface ID
243:212	SIP	Source IP address
211:180	DIP	Destination IP address
179:177	PROTO_TYPE	IPv4 Protocol/IPv6 Next Header It should be one of the following types: 0x0: 0x04 (4) - IPv4 0x1: 0x29 (41) - IPv6 0x2: 0x2F (47) - GRE 0x3: 0x11 (17) - UDP (VxLAN or CAPWAP) 0x4: 0x88 (136) - UDP-Lite (CAPWAP) 0x5~0x7: reserved
176:161	L4_MSB_DATA	GRE key[31:16] / L4 source port [15:0] (It's valid when PROTO_TYPE=GRE or UDP)
160:145	L4_LSB_DATA	GRE key[15:0] / L4 destination port [15:0] (It's valid when PROTO_TYPE=GRE or UDP)
144	GRE_KEY_EXIST	GRE key exist bit
143	BMSK_ENTRY_TYPE	Care bitmask of ENTRY_TYPE
142:133	BMSK_INTF_ID	Care bitmask of INTF_ID
132:101	BMSK_SIP	Care bitmask of SIP
100:69	BMSK_DIP	Care bitmask of DIP
68:66	BMSK_PROTO_TYPE	Care bitmask of PROTO_TYPE
65:50	BMSK_L4_MSB_DATA	Care bitmask of L4_MSB_DATA
49:34	BMSK_L4_LSB_DATA	Care bitmask of L4_LSB_DATA
33	BMSK_GRE_KEY_EXIST	Care bitmask of GRE_KEY_EXIST
32:30	TUNNEL_TYPE	Tunnel Type: 0b000: Configured tunnel 0b001: ISATAP (outer IPv4 only) 0b010: 6to4 (outer IPv4 only) 0b011: 6RD (outer IPv4 only) 0b100: GRE 0b101: VXLAN/UDP 0b110: CAPWAP/UDP+Lite 0b111: VXLAN-GPE/UDP
29	INNER_IPV4_EN	Indicate whether IPv4 payload is allowed in this GRE tunnel. (Shared with 6RD_INDEX bit)
28	INNER_IPV6_EN	Indicate whether IPv6 payload is allowed in this GRE tunnel.
27	USE_TUNNEL_TTL	Indicate whether use the outer TTL for inner routing process 0b0: use the inner TTL 0b1: use the outer TTL
26	USE_TUNNEL_DSCP	Indicate whether use the outer DSCP for inner routing process 0b0: use the inner DSCP 0b1: use the outer TTL

<b>Bits</b>	<b>Field</b>	<b>Description</b>
25	KEEP_TUNNEL_DSCP	Indicate whether keep the inner DSCP for Egress Remarking 0b0: don't keep (could be replaced by Egress Remarking) 0b1: keep (could NOT be replaced by Egress Remarking)
24:23	PRI_SEL_TBL_IDX	Index to the priority selection table
22:20	INT_PRI	Internal Priority
19:10	TUNNEL_INTF_ID	For IP tunnel: Tunnel Interface ID [9:0] For L2 tunnel: MAC learning option [0] MAC learning 0b0: disallow to learn the MAC address 0b1: allow to learn the MAC address
9:0	RESERVED	

## **TUNNEL\_TERMINATION\_1**

TABLE TYPE : 0x8

TABLE INDEX : 0 - 383

IP Tunnel Termination Table (N+1)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
223	VALID	Valid bit
222	ENTRY_TYPE	Entry type 0b0: Outer IPv4 0b1: Outer IPv6 (should be 0b1)
221:126	SIP6	Source IPv6 address MSB[127:32]
125:112	RESERVED	
111	BMSK_ENTRY_TYPE	Care bitmask of ENTRY_TYPE
110:15	BMSK_SIP6	Care bitmask of SIP6
14:1	RESERVED	
0	RESERVED	

## **TUNNEL\_TERMINATION\_2**

TABLE TYPE : 0x8

TABLE INDEX : 0 - 383

IP Tunnel Termination Table (N+2)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
223	VALID	Valid bit
222	ENTRY_TYPE	Entry type 0b0: Outer IPv4 0b1: Outer IPv6 (should be 0b1)
221:126	DIP6	Destination IPv6 address MSB[127:32]

<b>Bits</b>	<b>Field</b>	<b>Description</b>
125:112	RESERVED	
111	BMSK_ENTRY_TYPE	Care bitmask of ENTRY_TYPE
110:15	BMSK_DIP6	Care bitmask of DIP6
14:1	RESERVED	
0	RESERVED	

— SECTION 10.2 —

## MPLS/VPLS

MPLS/VPLS module

### **MPLS\_GLB\_CTRL**

REGISTER ADDRESS : 0xBB00F2E4

DEFAULT VALUE : 0x0

MPLS global control

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:6	RESERVED			
5	LABEL_0_15_ACT	Forward action for reserved label 0x0: trap to CPU 0x1: forward	RW	0x0
4	TTL_FAIL	incoming or outgoin MPLS packet with TTL = 0 0b0: drop 0b1: trap to CPU	RW	0x0
3	HASH_SEL	Select MPLS decapsulation table hash algorithm 0b0: hash algorithm 0 0b1: hash algorithm 1	RW	0x0
2	CPU_SEL	Select packet trap to local or master CPU 0b0: trap to local CPU 0b1: trap to master CPU	RW	0x0
1	LABEL_NUM_EXCEED	incoming packet has over 2 MPLS label 0b0: drop 0b1: trap to CPU	RW	0x0
0	LABEL_UNKW	unknown MPLS label 0b0: drop 0b1: trap to CPU	RW	0x0

### **MPLS\_PARSE\_CTRL**

REGISTER ADDRESS : 0xBB00052C

DEFAULT VALUE : 0x1

Configurations for MPLS parsing indication.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	CTRL_WORD_EN	Enable parsing the control word ability for MPLS L2 VPN. 0b0: disable 0b1: enable	RW	0x1

## MPLS\_ENCAP\_CTRL

REGISTER ADDRESS : 0xBB004F4C

DEFAULT VALUE : 0x1

Configurations for MPLS encapsulation indication.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	CTRL_WORD_ADD	Encapsulate the control word for MPLS L2 VPN. The system doesn't support sequence number, thus, the control word encapsulated would be 4 byte 0. 0b0: disable 0b1: enable	RW	0x1

## MPLS\_DPINTPRI2TC\_CTRL

BASE ADDRESS : 0xBB004F50

ARRAY INDEX1 : 0 - 2

ARRAY OFFSET1 : 0x4

ARRAY INDEX2 : 0 - 7

ARRAY OFFSET2 : 3 bits

DEFAULT VALUE : 0x0

This is a Two-Dimension Register Field Array. (3 bits per field)

Configure DP and internal priority to MPLS TC remarking table.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	TC	New MPLS TC value for the specified drop precedence and internal-priority.	RW	0x0

## MPLS\_DECAP

TABLE TYPE : 0xA

TABLE INDEX : 0 - 2047

MPLS decapsulation table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126:107	LABEL	MPLS label
106:105	DECAP_ACT	Decapsulation action 0x0: pop label and then forward by internal info 0x1: pop label and pointer NH 0x2: pointer to NH 0x3: Reserved
104	ECMP_EN	Enable ECMP only if DECAP_ACT = 0x1 or 0x2. 0x0: interface ID is next hop index 0x1: interface ID is ECMP index
103	TTL_INHERIT	TTL inherit. Only work if DECAP_ACT = 0x0.
102	DSCP_INHERIT	DSCP inherit. Only work if DECAP_ACT = 0x0.
101:100	PRI_SEL_TBL_IDX	Pointer to priority selection table entry.
99	INT_PRI_EN	Enable internal priority assign.
98:96	INT_PRI	Assign internal priority.
95:83	INTF_ID	If DECAP_ACT = 0x0, pointer interface entry. If DECAP_ACT = 0x1 or 0x2 and ECMP_EN = 0, pointer to next hop entry. If DECAP_ACT = 0x1 or 0x2 and ECMP_EN = 1, pointer to ECMP entry.
82:0	RESERVED	

### MPLS\_DECAP\_CAM

TABLE TYPE : 0xB

TABLE INDEX : 0 - 63

MPLS decapsulation bcam

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126:107	LABEL	MPLS label
106:105	DECAP_ACT	Decapsulation action 0x0: pop label and then forward by internal info 0x1: pop label and pointer NH 0x2: pointer to NH 0x3: Reserved
104	ECMP_EN	Enable ECMP only if DECAP_ACT = 0x1 or 0x2. 0x0: interface ID is next hop index 0x1: interface ID is ECMP index
103	TTL_INHERIT	TTL inherit. Only work if DECAP_ACT = 0x0.
102	DSCP_INHERIT	DSCP inherit. Only work if DECAP_ACT = 0x0.
101:100	PRI_SEL_TBL_IDX	Pointer to priority selection table entry.
99	INT_PRI_EN	Enable internal priority assign.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
98:96	INT_PRI	Assign internal priority.
95:83	INTF_ID	If DECAP_ACT = 0x0, pointer interface entry. If DECAP_ACT = 0x1 or 0x2 and ECMP_EN = 0, pointer to next hop entry. If DECAP_ACT = 0x1 or 0x2 and ECMP_EN = 1, pointer to ECMP entry.
82:0	RESERVED	

## MPLS\_ENCAP

TABLE TYPE : 0x2

TABLE INDEX : 0 - 2047

MPLS encapsulation table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	LABEL_ACT	MPLS label action 0x0: push 0x1: swap
62:43	LABEL	MPLS label
42:32	NEXT_IDX	pointer to next encap entry
31	NEXT_EN	Enable to pointer to next encap entry. For two lable operation
30	TTL_ASSIGN	Assign TTL 0x0: use packet's TTL 0x1: assign specified TTL
29:22	TTL	Assign specified TTL
21:20	TC_ASSIGN	0x0: For swap action, keep original TC. For push action, If inner label, from MSB 3 bits of DSCP. If outer label, from inner label TC.  0x1: assign TC 0x2: internal priority remark
19:17	TC	Assign specified TC
16:0	RESERVED	

## MPLS\_NH

TABLE TYPE : 0xC

TABLE INDEX : 0 - 8191

MPLS next hop table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
31	VALID	Valid bit
30:20	MPLS_ENCAP_IDX	MPLS encapsulation table index
19:0	RESERVED	

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## CHAPTER 11

# Statistic Counters

The chapter describes features related to statistic MIB counters, flow-based counters, private counters and debug counters.

SECTION 11.1

## MIB CONTROL

MIB Control Configuration

### **STAT\_RST**

REGISTER ADDRESS : 0xBB007EF4

DEFAULT VALUE : 0x0

MIB reset configurations.

Bits	Field	Description	Type	Default
31:2	RESERVED			
1	RST_MIB_VAL	Reset MIB counter(except private debug counter ) to '0' or all '1' 0b0: reset counter to 0 0b1: reset counter to all '1'	RW	0x0
0	RST_GLB_MIB	Reset global MIB counters. Write 1 to clear MIB counters. After resetting, the value is reset to 0	RWAC	0x0

### **STAT\_PORT\_RST**

REGISTER ADDRESS : 0xEB007EF8

DEFAULT VALUE : 0x0

Reset all of MIB counter for a specified port.

Bits	Field	Description	Type	Default
31:7	RESERVED			
6	RST_PORT_FLAG	Reset MIB counters for specified port ID defined in RST_PORT_MIB. Write 1 to clear MIB counters of the port. After resetting, the value is reset to 0	RWAC	0x0

Bits	Field	Description	Type	Default
5:0	RST_PORT_MIB	Reset MIB counters in STAT_PORT_MIB_CNTR, STAT_PORT_PRVTE_CNTR and STAT_PORT_PRVTE_E_Q_CNTR for specified port ID.	RW	0x0

## STAT\_CTRL

REGISTER ADDRESS : 0xBB005720

DEFAULT VALUE : 0xF

MIB control register.

Bits	Field	Description	Type	Default
31:5	RESERVED			
4	TX_MULTIPKT_CNT_PAUSE	Count Pause Frame in ifOutMulticastPkts. 1b0: disable 1b1: enable (counting Pause Frame)	RW	0x0
3	TX_STACK_CNT_HDR	Count stacking header length to TX packet length MIB counters in Stacking port. In EST port, count EST header length and stacking header length to TX packet length MIB counters (If EST header is invalid, it and stacking header should be considerd to be payload). 0b0: disable 0b1: enable (counting stacking header)	RW	0x1
2	RX_STACK_CNT_HDR	Count stacking header length to RX packet length MIB counters in Stacking port. In EST port, count EST header length and stacking header length to RX packet length MIB counters (If EST header is invalid, it and stacking header should be considerd to be payload). 0b0: disable 0b1: enable (counting stacking header)	RW	0x1
1	TX_CNT_TAG	Count tag length in TX packet length MIB counter. The configuration decides RSPAN tag length to count or not for RSPAN packet. For normal packet, it decides outer-tag, inner-tag and PE-tag lengths to count or not. (Exclude stacking port) 0b0: disable 0b1: enable	RW	0x1
0	RX_CNT_TAG	Count tag length in RX packet length MIB counter. The configuration decides RSPAN tag length to count or not for RSPAN packet. For normal packet, it decides outer-tag, inner-tag and PE-tag lengths to count or not. (Exclude stacking port) 0b0: disable 0b1: enable	RW	0x1

## **STAT\_CNT\_SET1\_CTRL**

REGISTER ADDRESS : 0xBB005724

DEFAULT VALUE : 0x30000040

Private flexible counter of set 1 control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:16	CNT_SET1_LEN_MAX	The maximum packet length boundary of private counter SET 1.	RW	0x3000
15:14	RESERVED			
13:0	CNT_SET1_LEN_MIN	The minimum packet length boundary of private counter SET 1.	RW	0x0040

## **STAT\_CNT\_SET0\_CTRL**

REGISTER ADDRESS : 0xBB005728

DEFAULT VALUE : 0x30000040

Private flexible counter of set 0 control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:30	RESERVED			
29:16	CNT_SET0_LEN_MAX	The maximum packet length boundary of private counter SET 0.	RW	0x3000
15:14	RESERVED			
13:0	CNT_SET0_LEN_MIN	The minimum packet length boundary of private counter SET 0.	RW	0x0040

### SECTION 11.2

## **MIB COUNTER**

Global and per-port MIB counter

## **STAT\_BRIDGE\_DOT1DTPLEARNEDENTRYDISCARDS**

REGISTER ADDRESS : 0xBB007F14

DEFAULT VALUE : 0x0

Bridge MIB dot1dTpLearnedEntryDiscards counter.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	dot1dTpLearnedEntryD iscards	<p>Learning miss.</p> <p>The total number of Forwarding Database entries, which have been or would have been learnt, but have been discarded due to lack of space to store them in the Forwarding Database.</p>	RO	0x0

## STAT\_PORT\_MIB\_CNTR

TABLE TYPE : 0x0

TABLE INDEX : 0 - 56

Per port standard MIB counters.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
1695: 1664	ifInOctets_H	<p>The total number of octets received on the interface, including framing characters.</p> <p>Bits[63:32]</p> <p>Note: count with tag length</p>
1663: 1632	ifInOctets_L	<p>The total number of octets received on the interface, including framing characters.</p> <p>Bits[31:0]</p> <p>Note: count with tag length</p>
1631: 1600	ifOutOctets_H	<p>The total number of octets transmitted out of the interface, including framing characters.</p> <p>Bits[63:32]</p> <p>Note: count with tag length</p>
1599: 1568	ifOutOctets_L	<p>The total number of octets transmitted out of the interface, including framing characters.</p> <p>Bits[31:0]</p> <p>Note: count with tag length</p>
1567: 1536	ifInUcastPkts_H	<p>The number of packets, delivered by this sub-layer to a higher (sub-)layer, which were not addressed to a multicast or broadcast address at this sub-layer.</p> <p>Bits[63:32]</p> <p>Note: only count valid frame.</p>
1535: 1504	ifInUcastPkts_L	<p>The number of packets, delivered by this sub-layer to a higher (sub-)layer, which were not addressed to a multicast or broadcast address at this sub-layer.</p> <p>Bits[31:0]</p> <p>Note: only count valid frame.</p>
1503: 1472	ifInMulticastPkts_H	<p>The number of packets, delivered by this sub-layer to a higher (sub-)layer, which were addressed to a multicast address at this sub-layer (Number of received valid multicast packets).</p> <p>Bits[63:32]</p> <p>Note: only count valid frame.</p>
1471: 1440	ifInMulticastPkts_L	<p>The number of packets, delivered by this sub-layer to a higher (sub-)layer, which were addressed to a multicast address at this sub-layer (Number of received valid multicast packets).</p> <p>Bits[31:0]</p> <p>Note: only count valid frame.</p>

<b>Bits</b>	<b>Field</b>	<b>Description</b>
1439: 1408	ifInBroadcastPkts_H	The number of packets, delivered by this sub-layer to a higher (sub-)layer, which were addressed to a broadcast address at this sub-layer (Number of received valid broadcast packets). Bits[63:32] Note: only count valid frame.
1407: 1376	ifInBroadcastPkts_L	The number of packets, delivered by this sub-layer to a higher (sub-)layer, which were addressed to a broadcast address at this sub-layer (Number of received valid broadcast packets). Bits[31:0] Note: only count valid frame.
1375: 1344	ifOutUcastPkts_H	The total number of packets that higher-level protocols requested be transmitted, and which were not addressed to a multicast or broadcast address at this sub-layer, including those that were discarded or not sent. Note: discarded or not sent(drain out) packets are count in.
1343: 1312	ifOutUcastPkts_L	The total number of packets that higher-level protocols requested be transmitted, and which were not addressed to a multicast or broadcast address at this sub-layer, including those that were discarded or not sent. Note: discarded or not sent(drain out) packets are count in.
1311: 1280	ifOutMulticastPkts_H	The total number of packets that higher-level protocols requested be transmitted, and which were addressed to a multicast address at this sub-layer, including those that were discarded or not sent. Note: discarded or not sent(drain out) packets are count in.
1279: 1248	ifOutMulticastPkts_L	The total number of packets that higher-level protocols requested be transmitted, and which were addressed to a multicast address at this sub-layer, including those that were discarded or not sent. Note: discarded or not sent(drain out) packets are count in.
1247: 1216	ifOutBroadcastPkts_H	The total number of packets that higher-level protocols requested be transmitted, and which were addressed to a broadcast address at this sub-layer, including those that were discarded or not sent. Note: discarded or not sent(drain out) packets are count in.
1215: 1184	ifOutBroadcastPkts_L	The total number of packets that higher-level protocols requested be transmitted, and which were addressed to a broadcast address at this sub-layer, including those that were discarded or not sent. Note: discarded or not sent(drain out) packets are count in.
1183: 1152	ifOutDiscards	The number of outbound packets which were chosen to be discarded even though no errors had been detected to prevent their being transmitted. One possible reason for discarding such a packet could be to free up buffer space. Note: drain out packet including drop packet by LateCollision or ExcessiveCollision
1151: 1120	dot1dTpPortInDiscards	Count of valid frames received which were discarded (i.e., filtered) by the Forwarding Process. Note: drop in ALE
1119: 1088	dot3StatsSingleCollisionFrames	A count of frames that are involved in a single collision, and are subsequently transmitted successfully.
1087: 1056	dot3StatsMultipleCollisions	A count of frames that are involved in more than one collision and are subsequently transmitted successfully.
1055: 1024	dot3StatsDeferredTransmissions	A count of frames for which the first transmission attempt on a particular interface is delayed because the medium is busy. Note: in half-duplex mode. The the counter does not include frames involved in collisions.
1023: 992	dot3StatsLateCollisions	The number of times that a collision is detected on a particular interface later than one slotTime into the transmission of a packet.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
991:960	dot3StatsExcessiveCollisions	A count of frames for which transmission on a particular interface fails due to excessive collisions. Note: the counter counting in IOL_MAX_RETRY_EN = enabled state.
959:928	dot3StatsSymbolErrors	For an interface operating at 100 Mb/s, the number of times there was an invalid data symbol when a valid carrier was present. Note: operating at 10M/100M/1G/10G bps.
927:896	dot3ControlInUnknownOpcodes	A count of MAC Control frames received on this interface that contain an opcode that is not supported by this device. Note: ether type = 8808 and opcode != 0x0001
895:864	dot3InPauseFrames	A count of MAC Control frames received on this interface with an opcode indicating the PAUSE operation.
863:832	dot3OutPauseFrames	A count of MAC Control frames transmitted on this interface with an opcode indicating the PAUSE operation.
831:800	etherStatsDropEvents	The total number of events in which packets were dropped by the probe due to lack of resources. Note that this number is not necessarily the number of packets dropped; it is just the number of times this condition has been detected. Note: drop before ALE due to lack of resources, ex. no system packet buffer, receiving packets after sending pause ON frame.
799:768	tx_etherStatsBroadcastPkts	The total number of good packets received that were directed to the broadcast address. Note that this does not include multicast packets.
767:736	tx_etherStatsMulticastPkts	The total number of good packets received that were directed to a multicast address. Note that this number does not include packets directed to the broadcast address.
735:704	tx_etherStatsCRCAignmentErrors	The total number of packets received that had a length (excluding framing bits, but including FCS octets) of between 64 and 1518 octets, inclusive, but had either a bad Frame Check Sequence (FCS) with an integral number of octets (FCS Error) or a bad FCS with a non-integral number of octets (Alignment Error). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: 64B~1518B(CRC bad)
703:672	rx_etherStatsCRCAignmentErrors	The total number of packets received that had a length (excluding framing bits, but including FCS octets) of between 64 and 1518 octets, inclusive, but had either a bad Frame Check Sequence (FCS) with an integral number of octets (FCS Error) or a bad FCS with a non-integral number of octets (Alignment Error). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: 64B~1518B(CRC bad)
671:640	tx_etherStatsUnderSizePkts	The total number of packets received that were less than 64 octets long (excluding framing bits, but including FCS octets) and were otherwise well formed. Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: CRC good only
639:608	rx_etherStatsUnderSizePkts	The total number of packets received that were less than 64 octets long (excluding framing bits, but including FCS octets) and were otherwise well formed. Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: CRC good only
607:576	tx_etherStatsOverSizePkts	The total number of packets received that were longer than 1518 octets (excluding framing bits, but including FCS octets) and were otherwise well formed. Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: CRC good only

<b>Bits</b>	<b>Field</b>	<b>Description</b>
575:544	rx_etherStatsOversizePkts	<p>The total number of packets received that were longer than 1518 octets (excluding framing bits, but including FCS octets) and were otherwise well formed.</p> <p>Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR</p> <p>Note: CRC good only</p>
543:512	tx_etherStatsFragments	<p>The total number of packets received that were less than 64 octets in length (excluding framing bits but including FCS octets) and had either a bad Frame Check Sequence (FCS) with an integral number of octets (FCS Error) or a bad FCS with a non-integral number of octets (Alignment Error)</p> <p>Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR</p> <p>Note: &lt;64B(CRC bad)</p>
511:480	rx_etherStatsFragments	<p>The total number of packets received that were less than 64 octets in length (excluding framing bits but including FCS octets) and had either a bad Frame Check Sequence (FCS) with an integral number of octets (FCS Error) or a bad FCS with a non-integral number of octets (Alignment Error)</p> <p>Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR</p> <p>Note: &lt;64B(CRC bad)</p>
479:448	tx_etherStatsJabbers	<p>The total number of packets received that were longer than 1518 octets (excluding framing bits, but including FCS octets), and had either a bad Frame Check Sequence (FCS) with an integral number of octets (FCS Error) or a bad FCS with a non-integral number of octets (Alignment Error).</p> <p>Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR</p> <p>Note: &gt;1518B(CRC bad)</p>
447:416	rx_etherStatsJabbers	<p>The total number of packets received that were longer than 1518 octets (excluding framing bits, but including FCS octets), and had either a bad Frame Check Sequence (FCS) with an integral number of octets (FCS Error) or a bad FCS with a non-integral number of octets (Alignment Error).</p> <p>Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR</p> <p>Note: &gt;1518B(CRC bad)</p>
415:384	tx_etherStatsCollisions	<p>The best estimate of the total number of collisions on this Ethernet segment</p>
383:352	tx_etherStatsPkts64Octets	<p>The total number of packets (including bad packets) received that were 64 octets in length (excluding framing bits but including FCS octets).</p> <p>Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR</p> <p>Note: good pkt + bad pkt</p>
351:320	rx_etherStatsPkts64Octets	<p>The total number of packets (including bad packets) received that were 64 octets in length (excluding framing bits but including FCS octets).</p> <p>Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR</p> <p>Note: good pkt + bad pkt</p>
319:288	tx_etherStatsPkts65to127Octets	<p>The total number of packets (including bad packets) received that were between 65 and 127 octets in length inclusive (excluding framing bits but including FCS octets).</p> <p>Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR</p> <p>Note: good pkt + bad pkt</p>
287:256	rx_etherStatsPkts65to127Octets	<p>The total number of packets (including bad packets) received that were between 65 and 127 octets in length inclusive (excluding framing bits but including FCS octets).</p> <p>Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR</p> <p>Note: good pkt + bad pkt</p>

<b>Bits</b>	<b>Field</b>	<b>Description</b>
255:224	tx_etherStatsPkts128 to255Octets	The total number of packets (including bad packets) received that were between 128 and 255 octets in length inclusive (excluding framing bits but including FCS octets).. Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: good pkt + bad pkt
223:192	rx_etherStatsPkts128 to255Octets	The total number of packets (including bad packets) received that were between 128 and 255 octets in length inclusive (excluding framing bits but including FCS octets).. Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: good pkt + bad pkt
191:160	tx_etherStatsPkts256 to511Octets	The total number of packets (including bad packets) received that were between 256 and 511 octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: good pkt + bad pkt
159:128	rx_etherStatsPkts256 to511Octets	The total number of packets (including bad packets) received that were between 256 and 511 octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: good pkt + bad pkt
127:96	tx_etherStatsPkts512 to1023Octets	The total number of packets (including bad packets) received that were between 512 and 1023 octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: good pkt + bad pkt
95:64	rx_etherStatsPkts512 to1023Octets	The total number of packets (including bad packets) received that were between 512 and 1023 octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: good pkt + bad pkt
63:32	tx_etherStatsPkts102 4to1518Octets	The total number of packets (including bad packets) received that were between 1024 and 1518 octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: good pkt + bad pkt
31:0	rx_etherStatsPkts102 4to1518Octets	The total number of packets (including bad packets) received that were between 1024 and 1518 octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: good pkt + bad pkt

**SECTION 11.3**
**FLOW-BASED COUNTER**

Flow-based counter for ACL and OpenFlow

**FLOW\_CNTR**

TABLE TYPE : 0x2

TABLE INDEX : 0 - 4095

Flow-based counter for ACL and OpenFlow.

Bits	Field	Description
95:94	CNTR_MODE	Counter mode. 0x0: packet counter and byte counter 0x1: packet counter and packet counter trigger threshold 0x2: byte counter and byte counter trigger threshold 0x3: disable
93:90	PKT_CNTR_H	Packet Counter [35:32]. When CNTR_MODE = 0x0/0x1, PKT_CNTR acts as packet counter. When CNTR_MODE = 0x2, PKT_CNTR acts as byte counter trigger threshold
89:58	PKT_CNTR_L	Packet Counter [31:0]. When CNTR_MODE = 0x0/0x1, PKT_CNTR acts as packet counter. When CNTR_MODE = 0x2, PKT_CNTR acts as byte counter trigger threshold
57:48	BYTE_CNTR_H	Byte Counter [41:32]. When CNTR_MODE = 0x0/0x2, BYTE_CNTR acts as packet counter. When CNTR_MODE = 0x1, BYTE_CNTR acts as packet counter trigger threshold.
47:16	BYTE_CNTR_L	Byte Counter [31:0]. When CNTR_MODE = 0x0/0x2, BYTE_CNTR acts as packet counter. When CNTR_MODE = 0x1, BYTE_CNTR acts as packet counter trigger threshold.
15:0	RESERVED	

**SECTION 11.4**
**PRIVATE COUNTER**

Per-port Private counter

**STAT\_PORT\_PRVTE\_CNT**

TABLE TYPE : 0x1

TABLE INDEX : 0 - 56

Per port private counters.

Bits	Field	Description
895:864	rx_etherStatsUndersizeRT	The total number of packets received that were less than 64 octets long (excluding framing bits, but including FCS octets) and were otherwise well formed. Note: Don't care RX_CNT_TAG, count real packet length. Note: CRC good only
863:832	tx_etherStatsPkts151OctetsRT	9toMax- The total number of packets (including bad packets) received that were between 1519 and Max octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: good pkt + bad pkt
831:800	rx_etherStatsPkts151OctetsRT	9toMax- The total number of packets (including bad packets) received that were between 1519 and Max octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: good pkt + bad pkt

<b>Bits</b>	<b>Field</b>		<b>Description</b>
799:768	tx_etherStatsPktsOverOctetsRT	rMax-	The total number of packets (including bad packets) received that were over Max octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR Note: good pkt + bad pkt
767:736	rx_etherStatsPktsOverOctetsRT	rMax-	The total number of packets (including bad packets) received that were over Max octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: good pkt + bad pkt
735:704	tx_etherStatsPktsFlebleOctetsSET1RT	xi-	The total number of CRC good packets received that were between CNT_SET1_LEN_MIN and CNT_SET1_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR
703:672	rx_etherStatsPktsFlebleOctetsSET1RT	xi-	The total number of CRC good packets received that were between CNT_SET1_LEN_MIN and CNT_SET1_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR
671:640	tx_etherStatsPktsFlebleOctetsCRCSET1RT	xi-	The total number of CRC bad packets received that were between CNT_SET1_LEN_MIN and CNT_SET1_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR
639:608	rx_etherStatsPktsFlebleOctetsCRCSET1RT	xi-	The total number of CRC bad packets received that were between CNT_SET1_LEN_MIN and CNT_SET1_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR
607:576	tx_etherStatsPktsFlebleOctetsSET0RT	xi-	The total number of CRC good packets received that were between CNT_SET0_LEN_MIN and CNT_SET0_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR
575:544	rx_etherStatsPktsFlebleOctetsSET0RT	xi-	The total number of CRC good packets received that were between CNT_SET0_LEN_MIN and CNT_SET0_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR
543:512	tx_etherStatsPktsFlebleOctetsCRCSET0RT	xi-	The total number of CRC bad packets received that were between CNT_SET0_LEN_MIN and CNT_SET0_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer TX_CNT_TAG / TX_STACK_CNT_HDR
511:480	rx_etherStatsPktsFlebleOctetsCRCSET0RT	xi-	The total number of CRC bad packets received that were between CNT_SET0_LEN_MIN and CNT_SET0_LEN_MAX octets in length inclusive (excluding framing bits but including FCS octets). Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR
479:448	lengthFieldErrorRT		The number of packets which length field < 1500 and the filed value doesn't match the real packet length excluding DA, SA, Type/Length Field and FCS. Note: refer RX_CNT_TAG / RX_STACK_CNT_HDR Note: the counter counting in IOL_LEN_ERR_EN = enabled state
447:416	falseCarrierTimesRT		The number of false carrier times(RX_ER is active and RXD is 0xE). It only counts 1 time for each IPG.
415:384	underSizeOctetsRT		The total octets of packets which packet length is under 64Bytes. Note: Don't care RX_CNT_TAG
383:352	framingErrorsRT		10G port only. The number of packets received with any control symbol except ERROR on XGMII interface.
351:320	RESERVED		

<b>Bits</b>	<b>Field</b>	<b>Description</b>
319:288	rxMacDiscardsRT	The number of packets dropped in MAC RX and switch core including bad packets and dropped packet due to lack of resource. (Totally Packets dropped before ALE)
287:256	rxMacIPGShortDropRT	The number of packets dropped before MAC RX due to IPG length is shorter than system limited IPG size configuration. (Totally Packets dropped before MAC RX)
255:224	egrQueue7OutPktRT	Transmit packet count of egress queue 7. Note: Include CRC good and bad packet.
223:192	egrQueue6OutPktRT	Transmit packet count of egress queue 6. Note: Include CRC good and bad packet.
191:160	egrQueue5OutPktRT	Transmit packet count of egress queue 5. Note: Include CRC good and bad packet.
159:128	egrQueue4OutPktRT	Transmit packet count of egress queue 4. Note: Include CRC good and bad packet.
127:96	egrQueue3OutPktRT	Transmit packet count of egress queue 3. Note: Include CRC good and bad packet.
95:64	egrQueue2OutPktRT	Transmit packet count of egress queue 2. Note: Include CRC good and bad packet.
63:32	egrQueue1OutPktRT	Transmit packet count of egress queue 1. Note: Include CRC good and bad packet.
31:0	egrQueue0OutPktRT	Transmit packet count of egress queue 0. Note: Include CRC good and bad packet.

### STAT\_PORT\_PRVTE\_E\_Q\_CNTR

TABLE TYPE : 0xE

TABLE INDEX : 0 - 56

Per port private egress queue counters.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127:112	egrQueue7DropPktRT	Drop packet count of egress queue 7. Note: Include CRC good and bad packet.
111:96	egrQueue6DropPktRT	Drop packet count of egress queue 6. Note: Include CRC good and bad packet.
95:80	egrQueue5DropPktRT	Drop packet count of egress queue 5. Note: Include CRC good and bad packet.
79:64	egrQueue4DropPktRT	Drop packet count of egress queue 4. Note: Include CRC good and bad packet.
63:48	egrQueue3DropPktRT	Drop packet count of egress queue 3. Note: Include CRC good and bad packet.
47:32	egrQueue2DropPktRT	Drop packet count of egress queue 2. Note: Include CRC good and bad packet.
31:16	egrQueue1DropPktRT	Drop packet count of egress queue 1. Note: Include CRC good and bad packet.
15:0	egrQueue0DropPktRT	Drop packet count of egress queue 0. Note: Include CRC good and bad packet.

Bits	Field	Description
SECTION 11.5		
<b>DEBUG COUNTER</b>		

Debug Counter module

### **STAT\_PRVTE\_DBG\_CNTR0**

REGISTER ADDRESS : 0xBB00D800

DEFAULT VALUE : 0x0

Private debug drop counter 0.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_0	The number of ALE receive packets	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR1**

REGISTER ADDRESS : 0xBB00D804

DEFAULT VALUE : 0x0

Private debug drop counter 1.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_1	RX Max Frame Size	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR2**

REGISTER ADDRESS : 0xBB00D808

DEFAULT VALUE : 0x0

Private debug drop counter 2.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_2	MAC Rx Drop	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR3**

REGISTER ADDRESS : 0xBB00D80C

DEFAULT VALUE : 0x0

Private debug drop counter 3.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_3	OpenFlow IP/MPLS TTL	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR4**

REGISTER ADDRESS : 0xBB00D810

DEFAULT VALUE : 0x0

Private debug drop counter 4.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_4	OpenFlow Table Miss	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR5**

REGISTER ADDRESS : 0xBB00D814

DEFAULT VALUE : 0x0

Private debug drop counter 5.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_5	Ingress Bandwidth (except Backdoor, IACL, EACL Bypass)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR6**

REGISTER ADDRESS : 0xBB00D818

DEFAULT VALUE : 0x0

Private debug drop counter 6.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_6	Special Congest	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR7**

REGISTER ADDRESS : 0xBB00D81C

DEFAULT VALUE : 0x0

Private debug drop counter 7.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_7	Egress Queue Drop / SWRED	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR8**

REGISTER ADDRESS : 0xBB00D820

DEFAULT VALUE : 0x0

Private debug drop counter 8.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_8	N/A	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR9**

REGISTER ADDRESS : 0xBB00D824

DEFAULT VALUE : 0x0

Private debug drop counter 9.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_9	Egress Link Status	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR10**

REGISTER ADDRESS : 0xBB00D828

DEFAULT VALUE : 0x0

Private debug drop counter 10.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_10	Stacking Unicast/NonUnicast TTL	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR11**

REGISTER ADDRESS : 0xBB00D82C

DEFAULT VALUE : 0x0

Private debug drop counter 11.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_11	Stacking NonUnicast Blocking PortMask	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR12**

REGISTER ADDRESS : 0xBB00D830

DEFAULT VALUE : 0x0

Private debug drop counter 12.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_12	L2 CRC	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR13**

REGISTER ADDRESS : 0xBB00D834

DEFAULT VALUE : 0x0

Private debug drop counter 13.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_13	Source Port Filter (Exclude Routed Packet)	RC	0x0

Bits	Field	Description	Type	Default
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### **STAT\_PRVTE\_DBG\_CNTR14**

REGISTER ADDRESS : 0xBB00D838

DEFAULT VALUE : 0x0

Private debug drop counter 14.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_14	Parser Packet Too Long	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR15**

REGISTER ADDRESS : 0xBB00D83C

DEFAULT VALUE : 0x0

Private debug drop counter 15.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_15	Parser Malform Packet	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR16**

REGISTER ADDRESS : 0xBB00D840

DEFAULT VALUE : 0x0

Private debug drop counter 16.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_16	MPLS Over 2 Label	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR17**

REGISTER ADDRESS : 0xBB00D844

DEFAULT VALUE : 0x0

Private debug drop counter 17.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_17	EACL Meter (learn)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR18**

REGISTER ADDRESS : 0xBB00D848

DEFAULT VALUE : 0x0

Private debug drop counter 18.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_18	IACL Meter	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR19**

REGISTER ADDRESS : 0xBB00D84C

DEFAULT VALUE : 0x0

Private debug drop counter 19.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_19	Protocol Storm	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR20**

REGISTER ADDRESS : 0xBB00D850

DEFAULT VALUE : 0x0

Private debug drop counter 20.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_20	Invalid CAPWAP Header or BPE Filter	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR21**

REGISTER ADDRESS : 0xBB00D854

DEFAULT VALUE : 0x0

Private debug drop counter 21.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_21	MAC/IP-Subnet Based VLAN	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR22**

REGISTER ADDRESS : 0xBB00D858

DEFAULT VALUE : 0x0

Private debug drop counter 22.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_22	OAM Parser	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR23**

REGISTER ADDRESS : 0xBB00D85C

DEFAULT VALUE : 0x0

Private debug drop counter 23.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_23	Unicast/Multicast RPF (learn)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR24**

REGISTER ADDRESS : 0xBB00D860

DEFAULT VALUE : 0x0

Private debug drop counter 24.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_24	IP-MAC Binding Match/Mismatch	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR25**

REGISTER ADDRESS : 0xBB00D864

DEFAULT VALUE : 0x0

Private debug drop counter 25.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_25	SA Block (learn but not update learning counter)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR26**

REGISTER ADDRESS : 0xBB00D868

DEFAULT VALUE : 0x0

Private debug drop counter 26.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_26	Tunnel IP Address Check (learn)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR27**

REGISTER ADDRESS : 0xBB00D86C

DEFAULT VALUE : 0x0

Private debug drop counter 27.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_27	EACL Drop (learn)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR28**

REGISTER ADDRESS : 0xBB00D870

DEFAULT VALUE : 0x0

Private debug drop counter 28.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_28	IACL Drop	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR29**

REGISTER ADDRESS : 0xBB00D874

DEFAULT VALUE : 0x0

Private debug drop counter 29.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_29	Attack Prevent	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR30**

REGISTER ADDRESS : 0xBB00D878

DEFAULT VALUE : 0x0

Private debug drop counter 30.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_30	System Port Limit Learn	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR31**

REGISTER ADDRESS : 0xBB00D87C

DEFAULT VALUE : 0x0

Private debug drop counter 31.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_31	OAMPDU (OAM disabled, option is drop)	RC	0x0

Bits	Field	Description	Type	Default
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### **STAT\_PRVTE\_DBG\_CNTR32**

REGISTER ADDRESS : 0xBB00D880

DEFAULT VALUE : 0x0

Private debug drop counter 32.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_32	CCM RX	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR33**

REGISTER ADDRESS : 0xBB00D884

DEFAULT VALUE : 0x0

Private debug drop counter 33.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_33	CFM Unknown Type	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR34**

REGISTER ADDRESS : 0xBB00D888

DEFAULT VALUE : 0x0

Private debug drop counter 34.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_34	LBM/LBR, LTM/LTR	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR35**

REGISTER ADDRESS : 0xBB00D88C

DEFAULT VALUE : 0x0

Private debug drop counter 35.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_35	Y.1731 (ETHDM)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR36**

REGISTER ADDRESS : 0xBB00D890

DEFAULT VALUE : 0x0

Private debug drop counter 36.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_36	VLAN Limit Learn	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR37**

REGISTER ADDRESS : 0xBB00D894

DEFAULT VALUE : 0x0

Private debug drop counter 37.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_37	VLAN Accept Frame Type	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR38**

REGISTER ADDRESS : 0xBB00D898

DEFAULT VALUE : 0x0

Private debug drop counter 38.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_38	CFI=1	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR39**

REGISTER ADDRESS : 0xBB00D89C

DEFAULT VALUE : 0x0

Private debug drop counter 39.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_39	Static/Dynamic Port moving	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR40**

REGISTER ADDRESS : 0xBB00D8A0

DEFAULT VALUE : 0x0

Private debug drop counter 40.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_40	Port move Forbid	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR41**

REGISTER ADDRESS : 0xBB00D8A4

DEFAULT VALUE : 0x0

Private debug drop counter 41.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_41	L3 CRC (learn)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR42**

REGISTER ADDRESS : 0xBB00D8A8

DEFAULT VALUE : 0x0

Private debug drop counter 42.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_42	BPDU/PTP/LLDP/EAPOL/RMA	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR43**

REGISTER ADDRESS : 0xBB00D8AC

DEFAULT VALUE : 0x0

Private debug drop counter 43.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_43	MSTP SRC Drop (Disabled, Blocking)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR44**

REGISTER ADDRESS : 0xBB00D8B0

DEFAULT VALUE : 0x0

Private debug drop counter 44.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_44	Invalid SA (Zero, Beast, Mcast)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR45**

REGISTER ADDRESS : 0xBB00D8B4

DEFAULT VALUE : 0x0

Private debug drop counter 45.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_45	New SA	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR46**

REGISTER ADDRESS : 0xBB00D8B8

DEFAULT VALUE : 0x0

Private debug drop counter 46.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_46	VLAN Ingress Filter	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR47**

REGISTER ADDRESS : 0xBB00D8BC

DEFAULT VALUE : 0x0

Private debug drop counter 47.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_47	Ingress VLAN Convert	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR48**

REGISTER ADDRESS : 0xBB00D8C0

DEFAULT VALUE : 0x0

Private debug drop counter 48.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_48	Gratuitous ARP	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR49**

REGISTER ADDRESS : 0xBB00D8C4

DEFAULT VALUE : 0x0

Private debug drop counter 49.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_49	MSTP SRC Drop (Learning) (learn)	RC	0x0

Bits	Field	Description	Type	Default
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### **STAT\_PRVTE\_DBG\_CNTR50**

REGISTER ADDRESS : 0xBB00D8C8

DEFAULT VALUE : 0x0

Private debug drop counter 50.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_50	L2 Hash Full	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR51**

REGISTER ADDRESS : 0xBB00D8CC

DEFAULT VALUE : 0x0

Private debug drop counter 51.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_51	L3 IPUC Non-IP	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR52**

REGISTER ADDRESS : 0xBB00D8D0

DEFAULT VALUE : 0x0

Private debug drop counter 52.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_52	MPLS Unknown Label	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR53**

REGISTER ADDRESS : 0xBB00D8D4

DEFAULT VALUE : 0x0

Private debug drop counter 53.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_53	TTL	RC	0x0

#### **STAT\_PRVTE\_DBG\_CNTR54**

REGISTER ADDRESS : 0xBB00D8D8  
DEFAULT VALUE : 0x0

Private debug drop counter 54.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_54	MTU	RC	0x0

#### **STAT\_PRVTE\_DBG\_CNTR55**

REGISTER ADDRESS : 0xBB00D8DC  
DEFAULT VALUE : 0x0

Private debug drop counter 55.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_55	ICMP Redirect	RC	0x0

#### **STAT\_PRVTE\_DBG\_CNTR56**

REGISTER ADDRESS : 0xBB00D8E0  
DEFAULT VALUE : 0x0

Private debug drop counter 56.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_56	Storm Control	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR57**

REGISTER ADDRESS : 0xBB00D8E4

DEFAULT VALUE : 0x0

Private debug drop counter 57.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_57	L3 DIP/DMAC Mismatch	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR58**

REGISTER ADDRESS : 0xBB00D8E8

DEFAULT VALUE : 0x0

Private debug drop counter 58.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_58	IPv4 IP Option / IPv6 Hop-by-Hop Extension Header	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR59**

REGISTER ADDRESS : 0xBB00D8EC

DEFAULT VALUE : 0x0

Private debug drop counter 59.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_59	Reserved IPv4/IPv6 Multicast	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR60**

REGISTER ADDRESS : 0xBB00D8F0

DEFAULT VALUE : 0x0

Private debug drop counter 60.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_60	IPv4/IPv6 Header Error	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR61**

REGISTER ADDRESS : 0xBB00D8F4

DEFAULT VALUE : 0x0

Private debug drop counter 61.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_61	Routing IP Addr Check	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR62**

REGISTER ADDRESS : 0xBB00D8F8

DEFAULT VALUE : 0x0

Private debug drop counter 62.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_62	Routing Exception	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR63**

REGISTER ADDRESS : 0xBB00D8FC

DEFAULT VALUE : 0x0

Private debug drop counter 63.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_63	DA Block	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR64**

REGISTER ADDRESS : 0xBB00D900

DEFAULT VALUE : 0x0

Private debug drop counter 64.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_64	OAM Mux	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR65**

REGISTER ADDRESS : 0xBB00D904

DEFAULT VALUE : 0x0

Private debug drop counter 65.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_65	Port Isolation	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR66**

REGISTER ADDRESS : 0xBB00D908

DEFAULT VALUE : 0x0

Private debug drop counter 66.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_66	VLAN Egress Filter	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR67**

REGISTER ADDRESS : 0xBB00D90C

DEFAULT VALUE : 0x0

Private debug drop counter 67.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_67	Mirror Isolate	RC	0x0

Bits	Field	Description	Type	Default
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### **STAT\_PRVTE\_DBG\_CNTR68**

REGISTER ADDRESS : 0xBB00D910

DEFAULT VALUE : 0x0

Private debug drop counter 68.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_68	MSTP Destination Drop (disabled,blocking, Learning)	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR69**

REGISTER ADDRESS : 0xBB00D914

DEFAULT VALUE : 0x0

Private debug drop counter 69.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_69	L2 Multicast Bridge	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR70**

REGISTER ADDRESS : 0xBB00D918

DEFAULT VALUE : 0x0

Private debug drop counter 70.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_70	IP Unicast/Multicast Routing Look-up Miss	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR71**

REGISTER ADDRESS : 0xBB00D91C

DEFAULT VALUE : 0x0

Private debug drop counter 71.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_71	Unknown L2 Unicast	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR72**

REGISTER ADDRESS : 0xBB00D920

DEFAULT VALUE : 0x0

Private debug drop counter 72.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_72	Unknown L2 Multicast	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR73**

REGISTER ADDRESS : 0xBB00D924

DEFAULT VALUE : 0x0

Private debug drop counter 73.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_73	Unknown IPv4 Multicast	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR74**

REGISTER ADDRESS : 0xBB00D928

DEFAULT VALUE : 0x0

Private debug drop counter 74.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_74	Unknown IPv6 Multicast	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR75**

REGISTER ADDRESS : 0xBB00D92C

DEFAULT VALUE : 0x0

Private debug drop counter 75.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_75	L3 Unicast/Multicast Route	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR76**

REGISTER ADDRESS : 0xBB00D930

DEFAULT VALUE : 0x0

Private debug drop counter 76.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_76	Unknown L2 Unicast Flooding Port Mask	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR77**

REGISTER ADDRESS : 0xBB00D934

DEFAULT VALUE : 0x0

Private debug drop counter 77.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	REASON_77	Broadcast Flooding Port Mask	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR78**

REGISTER ADDRESS : 0xBB00D938

DEFAULT VALUE : 0x0

Private debug drop counter 78.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_78	VLAN Profile Unknown L2 Multicast Flooding Port Mask	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR79**

REGISTER ADDRESS : 0xBB00D93C

DEFAULT VALUE : 0x0

Private debug drop counter 79.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_79	VLAN Profile Unknown IPv4 Multicast Flooding Port Mask	RC	0x0

### **STAT\_PRVTE\_DBG\_CNTR80**

REGISTER ADDRESS : 0xBB00D940

DEFAULT VALUE : 0x0

Private debug drop counter 80.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	REASON_80	VLAN Profile Unknown IPv6 Multicast Flooding Port Mask	RC	0x0

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## CHAPTER 12

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# Network Monitoring

The chapter describes features related to network monitoring

SECTION 12.1

## MIRRORING

Mirroring module that includes configuration of 4 mirroring entries and sampling rate.

### **MIR\_CTRL**

BASE ADDRESS : 0xBB00AF00

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Mirror control register including configuring mirroring set enable, mirror direction preference, SPM/DPM operation, mirroring port, mirroring port isolation.

Bits	Field	Description	Type	Default
31:19	RESERVED			
18:15	MTP_DEV	Device ID of mirror-to-port (MTP).	RW	0x0
14:9	MTP_PORT	Port ID of mirror-to-port (MTP).	RW	0x0
8	MTP_IS_TRK	Specify if the mirror-to-port (MTP) is a trunk port. 0b0: The MTP is not a trunk port. 0b1: The MTP is a trunk port and MTP_DEV and MTP_PORT are aggregated to identify trunk ID.	RW	0x0
7	TRAFFIC_SEL	Decide if port mirror and ACL mirror work for the packet that destination ports of normal forward contain MTP.  For non-unicast packet, if one of its destination ports of normal forward is MTP port, it is also affected by this register. 0b0: Packet that destination ports of normal forward contain MTP could be mirrored 0b1: Do not mirror for packet that destination ports of normal forward contain MTP	RW	0x0
6	MTP_TX_ISO	Isolate the mirror-to-port from forwarding normal traffic. 0b0: The MTP can forward mirrored and normal traffic. 0b1: The MTP only forward mirrored traffic, but can not forward normal traffic.	RW	0x0

Bits	Field	Description	Type	Default
5	MIR_MODE	If mirrored packet follows the TX modules of mirroring port (ex: VLAN tag status, remarking, EVC ...). 0b0: do not follow 0b1: follow mirroring port TX modules	RW	0x0
4	MIR_RX_TX_SEL	If SPM and DPM are both hit, this register decides if ingress mirror or egress mirror is done 0x0: only ingress mirror is done 0x1: only egress mirror is done	RW	0x0
3	MTP_SELF_FLTER	Packet transmitted from mirroring port is determined to be mirrored 0b0: it could be mirrored to mirroring port 0b1: the mirrored packet is filtered	RW	0x0
2	MIR_OP	Decides the traffic should hit SPM "OR" DPM or hit SPM "AND" DPM to be mirrored. 0b0: or 0b1: and	RW	0x0
1:0	MIR_TYPE	Mirroring set type selected. 0b00: disabled 0b01: port-based mirror 0b10: rspan mirror 0b11: flow-based mirror	RW	0x0

## **MIR\_SPM\_CTRL**

BASE ADDRESS : 0xBB00AF10

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Specify port 0 to 52 to mirror the source traffic from these ports

Bits	Field	Description	Type	Default
63:57	RESERVED			
56:32	SPM_56_32	Bitmap to select RX mirrored port (port 32 to 56). 0b0: disable 0b1: enable	RW	0x0
31:0	SPM_31_0	Bitmap to select RX mirrored port (port 0 to 31). 0b0: disable 0b1: enable	RW	0x0

## **MIR\_DPM\_CTRL**

BASE ADDRESS : 0xBB00AF30

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Specify port 0 to 52 to mirror the destination traffic to these ports

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:57	RESERVED			
56:32	DPM_56_32	Bitmap to select TX mirrored port (port 32 to 56). 0b0: disable 0b1: enable	RW	0x0
31:0	DPM_31_0	Bitmap to select TX mirrored port (port 0 to 31). 0b0: disable 0b1: enable	RW	0x0

### MIR\_SAMPLE\_RATE\_CTRL

BASE ADDRESS : 0xBB00AF50

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 16 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (16 bits per field)

Specify the sampling rate of mirrored traffic.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15:0	SAMPLE_RATE	The sampling rate of mirrored packets. SAMPLE_RATE = 0, just mirror, do not need to care about sampling rate; SAMPLE_RATE = n(n>0),sample the mirrored packets every n + 1 packets (skip n packets)	RW	0x0

### MIR\_QID\_CTRL

REGISTER ADDRESS : 0xBB00AF58

DEFAULT VALUE : 0x0

Specify the output queue of mirroring packets

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:4	RESERVED			
3:1	MIR_QID	Mirrored traffic enter output queue id at mirroring port	RW	0x0
0	MIR_QID_EN	Specify mirrored traffic output queue id 0b0: disable 0b1: enable	RW	0x0

## SECTION 12.2

## RSPAN

RSPAN module that provides RSPAN source and destination sessions related configurations.

### MIR\_RSPAN\_VLAN\_CTRL

**BASE ADDRESS :** 0xBB001410  
**ARRAY INDEX :** 0 - 3  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x81000000

This is a One-Dimension Common Register Array.

Specify the RX identified and TX added RSPAN VLAN tag for a mirroring set.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RSPAN_TAG_TPID	TPID of RSPAN tag. This is used to identify RX RSPAN traffic and to decide the TPID of TX added RSPAN VLAN tag.	RW	0x8100
15:13	RSPAN_TAG_PRI	Priority of RSPAN tag. This is used to decide PRI of the TX added RSPAN VLAN tag.	RW	0x0
12	RSPAN_TAG_CFI	CFI of RSPAN tag. This is used to decide CFI of the TX added RSPAN VLAN tag.	RW	0x0
11:0	RSPAN_TAG_VID	VID of RSPAN tag. This is used to identify RX RSPAN traffic and to decide VID of the TX added RSPAN VLAN tag.	RW	0x0

### MIR\_RSPAN\_TX\_CTRL

**BASE ADDRESS :** 0xBB001420  
**ARRAY INDEX :** 0 - 3  
**ARRAY OFFSET :** 1 bit  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify if the system adds RSPAN VLAN tag for mirrored traffic for a mirroring set.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	RSPAN_TX_TAG_ADD	Add RSPAN VLAN tag for mirrored packets that do not carry RSPAN VLAN tag for mirroring set n. This bit is ignored by packets which already have RSPAN VLAN tag. 0b0: disable 0b1: enable	RW	0x0

## MIR\_RSPAN\_RX\_TAG\_RM\_CTRL

BASE ADDRESS : 0xBB00AF5C

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify if the mirroring set removes the RSPAN VLAN tag of identified RX RSPAN traffic.

Bits	Field	Description	Type	Default
0	RSPAN_RX_TAG_RM	If RSPAN_RX_TAG_EN is 1, this bit decides if the RSPAN VLAN tag of RSPAN packet should be removed. This bit is ignored by packets that do not have RSPAN VLAN tag. 0b0: disable 0b1: enable	RW	0x0

SECTION 12.3	SFLOW
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sFlow module

## SFLOW\_CTRL

REGISTER ADDRESS : 0xBB008400

DEFAULT VALUE : 0x0

Provide the configuration to set the behaviour of sampling in case of the packet has been marked for sampling twice

Bits	Field	Description	Type	Default
31:2	RESERVED			
1	CPU_SEL	Select sample packet to local or master CPU 0b0: sample packet to local CPU 0b1: sample packet to master CPU	RW	0x0
0	SMPL_SEL	If any packet is marked for sampling twice for both (i.e. Ingress & Egress) then this bit will dictate which packet will be trapped, either Ingress packet or Egress packet. 0b0: Ingress Sampling 0b1: Egress Sampling	RW	0x0

## SFLOW\_PORT\_RATE\_CTRL

BASE ADDRESS : 0xBB008404

PORT INDEX : 0 - 55

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Specify the sampling rate for ingress and egress traffic

Bits	Field	Description	Type	Default
31:16	EGR_RATE	Packet sampling rate for egress traffic	RW	0x0
15:0	IGR_RATE	Packet sampling rate for ingress traffic	RW	0x0

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## CHAPTER 13

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# Packet Inspection Engine

The chapter describes features related to PIE

SECTION 13.1	PIE
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Packet Inspection Engine module

### **PIE\_BLK\_LOOKUP\_CTRL**

BASE ADDRESS : 0xBB004180

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x1

This is a One-Dimension Register Field Array. (1 bit per field)

PIE block lookup enable configuration.

Bits	Field	Description	Type	Default
0	LOOKUP_EN	Enable lookup for a specific PIE block, each bit represents a block. Lower block maps to lower bit. 0b0: disable 0b1: enable	RW	0x1

### **PIE\_BLK\_PHASE\_CTRL**

BASE ADDRESS : 0xBB004184

ARRAY INDEX : 0 - 31

ARRAY OFFSET : 3 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

PIE block phase configuration.

Bits	Field	Description	Type	Default
2:0	PHASE	Configure phase for a specific PIE block 0x0: VACL 0x1: IACL 0x2: EACL 0x3: Ingress flow table 0 0x4: Ingress flow table 3 0x5: Egress flow table 0	RW	0x0

### **PIE\_BLK\_GROUP\_CTRL**

BASE ADDRESS : 0xBB004194  
ARRAY INDEX : 0 - 31  
ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

PIE block grouping configuration.

Bits	Field	Description	Type	Default
31:9	RESERVED			
8:5	GRP_ID	Block group ID	RW	0x0
4:0	LOGIC_ID	Block logical ID	RW	0x0

### **PIE\_BLK\_TMPLTE\_CTRL**

BASE ADDRESS : 0xBE004214  
ARRAY INDEX : 0 - 31  
ARRAY OFFSET : 0x4  
DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Map PIE block to specified template.

Each block can map to two identical or different templates.

Bits	Field	Description	Type	Default
31:8	RESERVED			
7:4	BLK_TMPLTE2	It maps block to the second template.	RW	0x0
3:0	BLK_TMPLTE1	It maps block to the first template.	RW	0x0

### **PIE\_TMPLTE\_CTRL**

BASE ADDRESS : 0xBB004294

**ARRAY INDEX1 :** 5 - 9  
**ARRAY OFFSET1 :** 0xC  
**ARRAY INDEX2 :** 0 - 13  
**ARRAY OFFSET2 :** 6 bits  
**DEFAULT VALUE :** 0x0

This is a Two-Dimension Register Field Array. (6 bits per field)

Specify the field for the template.  
Each template contains 14 fields.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:0	TMPLTE_FIELD	Select field for template	RW	0x0

## PIE\_MV\_CTRL

**REGISTER ADDRESS :** 0xBB0042D0  
**DEFAULT VALUE :** 0x0

Specify the entry index to move. Both entry data and corresponding action bitmask & action data are moved. Note: movement mustn't happen between Ingress and Egress ACL blocks.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24:13	MV_TO	Move to this index.	RW	0x0
12:1	MV_FROM	Move from this index.	RW	0x0
0	MV	Move the PIE entries, and reset to 0 after moving is completed.	RWAC	0x0

## PIE\_MV\_LEN\_CTRL

**REGISTER ADDRESS :** 0xBB0042D4  
**DEFAULT VALUE :** 0x0

Specify the number of entries to move.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:13	RESERVED			
12:0	MV_LEN	The number of entries to move.	RW	0x0

## PIE\_CLR\_CTRL

**REGISTER ADDRESS :** 0xBB0042D8  
**DEFAULT VALUE :** 0x0

Specify the entry to clear. All the cleared entries will become invalid.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24:13	CLR_TO	Clear till to this index.	RW	0x0
12:1	CLR_FROM	Clear from this index.	RW	0x0
0	CLR	Clear the ACL entries, and reset to 0 after clearing is completed.	RWAC	0x0

## **PIE\_CTRL**

REGISTER ADDRESS : 0xBB0042DC

DEFAULT VALUE : 0x0

Specify VLAN template field in pre-defined templates.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:24	RESERVED			
23:22	E_TMPLTE0_IOTAG_SEL	Configure the template 0 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
21:20	E_TMPLTE1_IOTAG_SEL	Configure the template 1 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
19:18	E_TMPLTE2_IOTAG_SEL	Configure the template 2 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
17:16	E_TMPLTE4_IOTAG_SEL	Configure the template 4 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
15:14	V_TMPLTE0_IOTAG_SEL	Configure the template 0 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based and exclude IACL assignment) 0x3: Reserved	RW	0x0
13:12	V_TMPLTE1_IOTAG_SEL	Configure the template 1 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based and exclude IACL assignment) 0x3: Reserved	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
11:10	V_TMPLTE2_IOTAG_SEL	Configure the template 2 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based and exclude IACL assignment) 0x3: Reserved	RW	0x0
9:8	V_TMPLTE4_IOTAG_SEL	Configure the template 4 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based and exclude IACL assignment) 0x3: Reserved	RW	0x0
7:6	I_TMPLTE0_IOTAG_SEL	Configure the template 0 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
5:4	I_TMPLTE1_IOTAG_SEL	Configure the template 1 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
3:2	I_TMPLTE2_IOTAG_SEL	Configure the template 2 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0
1:0	I_TMPLTE4_IOTAG_SEL	Configure the template 4 VLAN 0x0: IVLAN (ITAG) 0x1: OVLAN (OTAG) 0x2: FVLAN (depends on forward based) 0x3: Reserved	RW	0x0

## PIE\_TAG\_CTRL

BASE ADDRESS : 0xBB0042E0

ARRAY INDEX : 0 - 9

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Specify tag template field in pre-defined templates.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	V_TMPLTE_IOTAG_FMT	Configure the template OTAG/ITAG format 0x0: originale 0x1: translated (exclude IACL assigment)	RW	0x0

## PIE\_RULE\_HIT\_INDICATION

**BASE ADDRESS :** 0xBB0042E4  
**ARRAY INDEX :** 0 - 127  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Common Register Array.

Per rule hit indication

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	RULE_INDICATION	<p>Rule hit indication. Write 1 to clear.            0b0: not hit            0b1: hit</p> <p>Each bit represents one rule hit indication.            Each register contains 32 rules hit indication.</p>	RW1C	0x0

## **PIE\_GLB\_HIT\_INDICATION**

**BASE ADDRESS :** 0xBB0044E4  
**ARRAY INDEX :** 0 - 3  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Common Register Array.

Block rule hit indication

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	GLB_INDICATION	<p>Global PIE rule hit indication. Read-Only.            0b0: not hit            0b1: hit</p> <p>Note: Each bit represents 32 PIE rule hit indication. Each register contains 32*32 PIE rule hit indication.</p>	RO	0x0

## **PIE\_MISC**

**REGISTER ADDRESS :** 0xBB0044F4  
**DEFAULT VALUE :** 0x0

PIE global misc configuration

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			
2	INTF_SEL	Select TSID/TTID field present filter field 0b0: filter interface index 0b1: filter tunnel index	RW	0x0

Bits	Field	Description	Type	Default
1	DP_SEL	Configure output DP value after meter action: 0b0: according to entry has deepest color (RED > YELLOW > GREEN) 0b1: according to the meter entry with lowest index	RW	0x0
0	ARP_MAC_CTRL	Select SMAC/DMAC fields present filter field for ARP packet. 0b0: filter L2 SMAC/DMAC 0b1: filter ARP sender/target hardware address	RW	0x0

## PIE\_FIELD\_SELTOR\_CTRL

BASE ADDRESS : 0xBB000530

ARRAY INDEX : 0 - 13

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

User definable 16-bit field. The field can be pull in to ACL template to filtering the user defined field.

Bits	Field	Description	Type	Default
31:11	RESERVED			
10	PAYLOAD_SEL	Select inner or outer payload for tunnel packet whether the packet hit tunnel terminated or not 0b0: outer 0b1: inner	RW	0x0
9:3	OFFSET	Field offset. Unit is two bytes.	RW	0x0
2:0	FMT	Field selector format. It defines the start address for 16-bits data. 0x0: Raw packet(begin with DA) 0x1: LLC packet(begin with length "0000-05FF") 0x2: L3 packet(Start after Ethertype field, VLAN tag is bypassed) 0x3: ARP/RARP packet (Start from ARP/RARP header) 0x4: IP packet (Start from IPv4/IPv6 header) 0x5: IP payload(Start from IP payload, also means start of layer 4 packet) 0x6: L4 payload (Start after TCP/UDP header, ICMP/IGMP is not supported)  Note1: 0x2~0x6 must consider LLC_SNAP encapsulation Note2: field selector can't be used to locate the VLAN tag info because VLAN tags are removed by RX MAC.	RW	0x0

## PIE\_ENCAP\_CTRL

REGISTER ADDRESS : 0xBB004F5C

DEFAULT VALUE : 0x0

Use for encap control

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	EGR_MIR_SFLOW_BYPASS_POST	Egress mirror and sFlow bypass post-ACL action exclude meter and log. 0b0: to do post-ACL 0b1: bypass post-ACL	RW	0x0

## **PIE\_MISC2**

REGISTER ADDRESS : 0xBB008008

DEFAULT VALUE : 0x0

PIE global misc configuration2

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	FILTER_8021BR_EN	Enable filter 802.1BR field 0b0: disable 0b1: enable	RW	0x0

SECTION 13.2

## **ACL**

The section describes registers related to ACL

### **ACL\_PORT\_LOCKUP\_CTRL**

BASE ADDRESS : 0xBB0044F8

PORT INDEX : 0 - 55

PORT OFFSET : 0x4

DEFAULT VALUE : 0x3

This is a One-Dimension Port Register Array.

Port-based ACL lookup control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:2	RESERVED			
1	EGR_LUT	Enable egress stage ACL lookup 0b0: Disable 0b1: Enable	RW	0x1
0	IGR_LUT	Enable ingress stage ACL lookup 1b0: Disable 1b1: Enable	RW	0x1

Bits	Field	Description	Type	Default
------	-------	-------------	------	---------

## ACL\_CTRL

REGISTER ADDRESS : 0xBB008C24

DEFAULT VALUE : 0x0

ACL global register

Bits	Field	Description	Type	Default
31:3	RESERVED			
2:0	LB_LIMIT	Limit maximum loopback times. Note: value 0x0 means unlimited.	RW	0x0

## EACL

TABLE TYPE : 0x0

TABLE INDEX : 0 - 4095

Egress ACL

Bits	Field	Description
703:688	FIELD_13	FIELD_13
687:672	FIELD_12	FIELD_12
671:656	FIELD_11	FIELD_11
655:640	FIELD_10	FIELD_10
639:624	FIELD_9	FIELD_9
623:608	FIELD_8	FIELD_8
607:592	FIELD_7	FIELD_7
591:576	FIELD_6	FIELD_6
575:560	FIELD_5	FIELD_5
559:544	FIELD_4	FIELD_4
543:528	FIELD_3	FIELD_3
527:512	FIELD_2	FIELD_2
511:496	FIELD_1	FIELD_1
495:480	FIELD_0	FIELD_0
479	MGNT_VLAN	Mangement VLAN
478	DEVMAC	DMAC hit device MAC
477	NOT_FIRST_FRAG	IPv4/IPv6 fragment offset != 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
476:474	FRAME_TYPE_L4	L4 protocol 0x0: UDP 0x1: TCP 0x2: ICMP/ICMPv6 0x3: IGMP(exclude MLD) 0x4: MLD 0x5: SCTP 0x6:L4 unknown 0x7: Reserved
473:472	FRAME_TYPE	Frame Type: 0x0: ARP 0x1: L2 only 0x2: IPv4 0x3: IPv6
471	OTAG_FMT	Outer Tag Format 0b0: tag 0b1: untag/priority tag
470	ITAG_FMT	Inner Tag Format 0b0: tag 0b1: untag/priority tag
469	OTAG_EXIST	Packet with outer tag
468	ITAG_EXIST	Packet with inner tag
467:466	FRAME_TYPE_L2	L2 Frame Type 0x0: Reserved 0x1: Ethernet 0x2: LLC_Other 0x3: LLC_SNAP
465	EGR_NML_PORT	Egress normal port (exclude CPU port & stacking port)
464	TID	Template ID
463:448	BMSK_FIELD_13	Bitmask field 13
447:432	BMSK_FIELD_12	Bitmask field 12
431:416	BMSK_FIELD_11	Bitmask field 11
415:400	BMSK_FIELD_10	Bitmask field 10
399:384	BMSK_FIELD_9	Bitmask field 9
383:368	BMSK_FIELD_8	Bitmask field 8
367:352	BMSK_FIELD_7	Bitmask field 7
351:336	BMSK_FIELD_6	Bitmask field 6
335:320	BMSK_FIELD_5	Bitmask field 5
319:304	BMSK_FIELD_4	Bitmask field 4
303:288	BMSK_FIELD_3	Bitmask field 3
287:272	BMSK_FIELD_2	Bitmask field 2
271:256	BMSK_FIELD_1	Bitmask field 1
255:240	BMSK_FIELD_0	Bitmask field 0
239	BMSK_MGMT_VLAN	Bitmask Management VLAN
238	BMSK_DEVMAC	Bitmask content too deep
237	BMSK_NOT_FIRST_FRAG	Bitmask IPv4/IPv6 fragment offset != 0
236:234	BMSK_FRAME_TYPE_L4	Bitmask L4 protocol
233:232	BMSK_FRAME_TYPE	Bitmask Frame Type
231	BMSK_OTAG_FMT	Bitmask Outer Tag Format

<b>Bits</b>	<b>Field</b>	<b>Description</b>
230	BMSK_ITAG_FMT	Bitmask Inner Tag Format
229	BMSK_OTAG_EXIST	Bitmask Packet with outer tag
228	BMSK_ITAG_EXIST	Bitmask Packet with inner tag
227:226	BMSK_FRAME_TYPE_L2	Bitmask L2 Frame Type
225	BMSK_EGR_NML_PORT	Bitmask egress normal port (exclude CPU port & stacking port)
224	BMSK_TID	Bitmask Template ID
223	VALID	Valid bit
222	NOT	NOT operation
221	RESERVED	
220	AND2	AND2 operation
219	ACT_MSK_METER	Meter action mask
218	ACT_MSK_LOG	Log action mask
217	ACT_MSK_RREMARK	Red remark action mask
216	ACT_MSK_YREMARK	Yellow remark action mask
215	ACT_MSK_REMARK	Remark action mask
214:212	RESERVED	
211	ACT_MSK_OPRI	outer 1p remark action mask
210	ACT_MSK_OVLAN	outer VLAN assignment action mask
209	ACT_MSK_IPRI	inner 1p remark action mask
208	ACT_MSK_IVLAN	inner VLAN assignment action mask
207:206	RESERVED	
205	ACT_MSK_RDROP	Red drop action mask
204	ACT_MSK_YDROP	Yellow drop action mask
203	ACT_MSK_DROP	Drop action mask mask For green packet if meter action is set. For all packet if meter action is unset.
202:200	RESERVED	
199	RESERVED	
198:190	METER_IDX	Meter index
189	DROP_ACT	Drop action For green packet if meter action is set. For all packet if meter action is unset.
188	YDROP_ACT	Yellow drop action
187	RDROP_ACT	Red drop action
186:163	RESERVED	
162:161	IVLAN_VID_ACT	Inner VLAN ID action
160:149	IVLAN_VID	Inner VLAN ID
148:147	IPRI_ACT	Inner priority action
146:144	IPRI_DATA	Inner priority
143:142	OVLAN_VID_ACT	Outer VLAN ID action
141:130	OVLAN_VID	Outer VLAN ID
129:128	OPRI_ACT	Outer priority action
127:125	OPRI_DATA	Outer priority

<b>Bits</b>	<b>Field</b>	<b>Description</b>
124:123	REMARK_ACT	Remark action For green packet if meter action is set. For all packet if meter action is unset.
122:115	REMARK_DATA	Remark value
114:113	YREMARK_ACT	Yellow remark action
112:105	YREMARK_DATA	Yellow remark value
104:103	RREMARK_ACT	Red remark action
102:95	RREMARK_DATA	Red remark value
94:63	RESERVED	
62	IVLAN_TPID_ACT	Inner VLAN TPID action
61:60	IVLAN_TPID_IDX	Inner VLAN TPID assign index
59	OVLAN_TPID_ACT	Outer VLAN TPID action
58:57	OVLAN_TPID_IDX	Outer VLAN TPID assign index
56:0	RESERVED	

## IACL

TABLE TYPE : 0x0

TABLE INDEX : 0 - 4095

### Ingress ACL

<b>Bits</b>	<b>Field</b>	<b>Description</b>
703:688	FIELD_13	FIELD_13
687:672	FIELD_12	FIELD_12
671:656	FIELD_11	FIELD_11
655:640	FIELD_10	FIELD_10
639:624	FIELD_9	FIELD_9
623:608	FIELD_8	FIELD_8
607:592	FIELD_7	FIELD_7
591:576	FIELD_6	FIELD_6
575:560	FIELD_5	FIELD_5
559:544	FIELD_4	FIELD_4
543:528	FIELD_3	FIELD_3
527:512	FIELD_2	FIELD_2
511:496	FIELD_1	FIELD_1
495:480	FIELD_0	FIELD_0
479	MGNT_VLAN	Mangement VLAN
478	DEVMAC	DMAC hit device MAC
477	NOT_FIRST_FRAG	IPv4/IPv6 fragment offset != 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
476:474	FRAME_TYPE_L4	L4 protocol 0x0: UDP 0x1: TCP 0x2: ICMP/ICMPv6 0x3: IGMP(exclude MLD) 0x4: MLD 0x5: SCTP 0x6:L4 unknown 0x7: Reserved
473:472	FRAME_TYPE	Frame Type: 0x0: ARP 0x1: L2 only 0x2: IPv4 0x3: IPv6
471	OTAG_FMT	Outer Tag Format 0b0: tag 0b1: untag/priority tag
470	ITAG_FMT	Inner Tag Format 0b0: tag 0b1: untag/priority tag
469	OTAG_EXIST	Packet with outer tag
468	ITAG_EXIST	Packet with inner tag
467:466	FRAME_TYPE_L2	L2 Frame Type 0x0: Reserved 0x1: Ethernet 0x2: LLC_Other 0x3: LLC_SNAP
465	IGR_NML_PORT	Ingress normal port (exclude CPU port & stacking port)
464	TID	Template ID
463:448	BMSK_FIELD_13	Bitmask field 13
447:432	BMSK_FIELD_12	Bitmask field 12
431:416	BMSK_FIELD_11	Bitmask field 11
415:400	BMSK_FIELD_10	Bitmask field 10
399:384	BMSK_FIELD_9	Bitmask field 9
383:368	BMSK_FIELD_8	Bitmask field 8
367:352	BMSK_FIELD_7	Bitmask field 7
351:336	BMSK_FIELD_6	Bitmask field 6
335:320	BMSK_FIELD_5	Bitmask field 5
319:304	BMSK_FIELD_4	Bitmask field 4
303:288	BMSK_FIELD_3	Bitmask field 3
287:272	BMSK_FIELD_2	Bitmask field 2
271:256	BMSK_FIELD_1	Bitmask field 1
255:240	BMSK_FIELD_0	Bitmask field 0
239	BMSK_MGMT_VLAN	Bitmask Management VLAN
238	BMSK_DEVMAC	Bitmask content too deep
237	BMSK_NOT_FIRST_FRAG	Bitmask IPv4/IPv6 fragment offset != 0
236:234	BMSK_FRAME_TYPE_L4	Bitmask L4 protocol
233:232	BMSK_FRAME_TYPE	Bitmask Frame Type
231	BMSK_OTAG_FMT	Bitmask Outer Tag Format

<b>Bits</b>	<b>Field</b>	<b>Description</b>
230	BMSK_ITAG_FMT	Bitmask Inner Tag Format
229	BMSK_OTAG_EXIST	Bitmask Packet with outer tag
228	BMSK_ITAG_EXIST	Bitmask Packet with inner tag
227:226	BMSK_FRAME_TYPE_L2	Bitmask L2 Frame Type
225	BMSK_IGR_NML_PORT	Bitmask ingress normal port (exclude CPU port & stacking port)
224	BMSK_TID	Bitmask Template ID
223	VALID	Valid bit
222	NOT	NOT operation
221	AND1	AND1 operation
220	AND2	AND2 operation
219	ACT_MSK_METER	Meter action mask
218	ACT_MSK_LOG	Log action mask
217	ACT_MSK_RREMARK	Red remark action mask
216	ACT_MSK_YREMARK	Yellow remark action mask
215	ACT_MSK_REMARK	Remark action mask
214	ACT_MSK_METADATA	Meta data action mask
213	ACT_MSK_PRIO	Priority assignment action mask
212	ACT_MSK_TAGSTS	Tag status action mask
211	ACT_MSK_OPRI	Outer 1p remark action mask
210	ACT_MSK_OVLAN	Outer VLAN assignment action mask
209	ACT_MSK_IPRI	Inner 1p remark action mask
208	ACT_MSK_IVLAN	Inner VLAN assignment action mask
207	ACT_MSK_MIR	Mirror action mask
206	ACT_MSK_FWD	Forward action mask
205	ACT_MSK_RDROP	Red drop action mask
204	ACT_MSK_YDROP	Yellow drop action mask
203	ACT_MSK_DROP	Drop action mask For green packet if meter action is set. For all packet if meter action is unset.
202	ACT_MSK_BYPASS	Bypass action mask
201	ACT_MSK_CPUQID	CPU QID action mask
200	RESERVED	
199	ACT_MSK_INVT_IP_RSV_FLAG	Invert IP reserve flag action mask
198:190	METER_IDX	Meter index
189	DROP_ACT	Drop action For green packet if meter action is set. For all packet if meter action is unset.
188	YDROP_ACT	Yellow drop action
187	RDROP_ACT	Red drop action
186:184	FWD_ACT	Forward action
183	FWD_CPU_PKT_FMT	Packet to CPU format
182	FWD_SA_LRN	Packet L2 learn action

<b>Bits</b>	<b>Field</b>	<b>Description</b>
181	FWD_SEL	Packet forward and drop selection
180:169	FWD_PORT_INFO	Packet forward port information
168:167	RESERVED	
166:165	MIR_ACT	Mirror action
164:163	MIR_IDX	Mirror index
162:161	IVLAN_VID_ACT	Inner VLAN ID action
160:149	IVLAN_VID	Inner VLAN ID
148:147	IPRI_ACT	Inner priority action
146:144	IPRI_DATA	Inner priority
143:142	OVLAN_VID_ACT	Outer VLAN ID action
141:130	OVLAN_VID	Outer VLAN ID
129:128	OPRI_ACT	Outer priority action
127:125	OPRI_DATA	Outer priority
124:123	REMARK_ACT	Remark action For green packet if meter action is set. For all packet if meter action is unset.
122:115	REMARK_DATA	Remark value
114:113	YREMARK_ACT	Yellow remark action
112:105	YREMARK_DATA	Yellow remark value
104:103	RREMARK_ACT	Red remark action
102:95	RREMARK_DATA	Red remark value
94:93	ITAGSTS	Inner tag status action
92:91	OTAGSTS	Outer tag status action
90:88	INTERNAL_PRIO	Internal priority
87:76	METADATA	Meta data action
75:64	METADATA_MSK	Meta data mask action
63	BYPASS_ACT	Bypass action
62	IVLAN_TPID_ACT	Inner VLAN TPID action
61:60	IVLAN_TPID_IDX	Inner VLAN TPID assign index
59	OVLAN_TPID_ACT	Outer VLAN TPID action
58:57	OVLAN_TPID_IDX	Outer VLAN TPID assign index
56:52	CPUQID	Assign CPU Queue ID for packet to CPU
51	BYPASS_ACTI	Bypass action
50:0	RESERVED	

## VACL

TABLE TYPE : 0x0

TABLE INDEX : 0 - 4095

VLAN ACL

<b>Bits</b>	<b>Field</b>	<b>Description</b>
703:688	FIELD_13	FIELD_13 data
687:672	FIELD_12	FIELD_12 data
671:656	FIELD_11	FIELD_11 data
655:640	FIELD_10	FIELD_10 data
639:624	FIELD_9	FIELD_9 data
623:608	FIELD_8	FIELD_8 data
607:592	FIELD_7	FIELD_7 data
591:576	FIELD_6	FIELD_6 data
575:560	FIELD_5	FIELD_5 data
559:544	FIELD_4	FIELD_4 data
543:528	FIELD_3	FIELD_3 data
527:512	FIELD_2	FIELD_2 data
511:496	FIELD_1	FIELD_1 data
495:480	FIELD_0	FIELD_0 data
479	MGNT_VLAN	Mangement VLAN
478	CONTENT_TOO_DEEP	packet content too deep
477	NOT_FIRST_FRAG	IPv4/IPv6 fragment offset != 0
476:474	FRAME_TYPE_L4	L4 protocol 0x0: UDP 0x1: TCP 0x2: ICMP/ICMPv6 0x3: IGMP(exclude MLD) 0x4: MLD 0x5: SCTP 0x6:L4 unknown 0x7: Reserved
473:472	FRAME_TYPE	Frame Type: 0x0: ARP 0x1: L2 only 0x2: IPv4 0x3: IPv6
471	OTAG_FMT	Outer Tag Format 0b0: tag 0b1: untag/priority tag
470	ITAG_FMT	Inner Tag Format 0b0: tag 0b1: untag/priority tag
469	OTAG_EXIST	Packet with outer tag
468	ITAG_EXIST	Packet with inner tag
467:466	FRAME_TYPE_L2	L2 Frame Type 0x0: Reserved 0x1: Ethernet 0x2: LLC_Other 0x3: LLC_SNAP
465	IGR_NML_PORT	Ingress normal port (exclude CPU port & stacking port)
464	TID	Template ID
463:448	BMSK_FIELD_13	Bitmask field 13
447:432	BMSK_FIELD_12	Bitmask field 12

<b>Bits</b>	<b>Field</b>	<b>Description</b>
431:416	BMSK_FIELD_11	Bitmask field 11
415:400	BMSK_FIELD_10	Bitmask field 10
399:384	BMSK_FIELD_9	Bitmask field 9
383:368	BMSK_FIELD_8	Bitmask field 8
367:352	BMSK_FIELD_7	Bitmask field 7
351:336	BMSK_FIELD_6	Bitmask field 6
335:320	BMSK_FIELD_5	Bitmask field 5
319:304	BMSK_FIELD_4	Bitmask field 4
303:288	BMSK_FIELD_3	Bitmask field 3
287:272	BMSK_FIELD_2	Bitmask field 2
271:256	BMSK_FIELD_1	Bitmask field 1
255:240	BMSK_FIELD_0	Bitmask field 0
239	BMSK_MGMT_VLAN	Bitmask Mangement VLAN
238	BMSK_CONTENT_TOO_DEEP	Bitmask content too deep
237	BMSK_NOT_FIRST_FRAG	Bitmask IPv4/IPv6 fragment offset != 0
236:234	BMSK_FRAME_TYPE_L4	Bitmask L4 protocol
233:232	BMSK_FRAME_TYPE	Bitmask Frame Type
231	BMSK_OTAG_FMT	Bitmask Outer Tag Format
230	BMSK_ITAG_FMT	Bitmask Inner Tag Format
229	BMSK_OTAG_EXIST	Bitmask Packet with outer tag
228	BMSK_ITAG_EXIST	Bitmask Packet with inner tag
227:226	BMSK_FRAME_TYPE_L2	Bitmask L2 Frame Type
225	BMSK_IGR_NML_PORT	Bitmask ingress normal port (exclude CPU port & stacking port)
224	BMSK_TID	Bitmask Template ID
223	VALID	Valid bit
222	NOT	NOT operation
221	AND1	AND1 operation
220	AND2	AND2 operation
219	ACT_MSK_METER	Meter action mask
218	ACT_MSK_LOG	Log action mask
217	ACT_MSK_RREMARK	Red remark action mask
216	ACT_MSK_YREMARK	Yellow remark action mask
215	ACT_MSK_REMARK	Remark action mask
214	ACT_MSK_METADATA	Meta data action mask
213	ACT_MSK_PRIO	Priority assignment action mask
212	ACT_MSK_TAGSTS	Tag status action mask
211	ACT_MSK_OPRI_RMK	Outer 1p remark action mask
210	ACT_MSK_OVLAN	Outer VLAN assignment action mask
209	ACT_MSK_IPRI_RMK	Inner 1p remark action mask
208	ACT_MSK_IVLAN	Inner VLAN assignment action mask
207	ACT_MSK_MIR	Mirror action mask

<b>Bits</b>	<b>Field</b>	<b>Description</b>
206	ACT_MSK_FWD	Forward action mask
205	ACT_MSK_RDROP	Red drop action mask
204	ACT_MSK_YDROP	Yellow drop action mask
203	ACT_MSK_DROP	Drop action mask For green packet if meter action is set. For all packet if meter action is unset.
202	ACT_MSK_BYPASS	Bypass action mask
201	ACT_MSK_QID	QID action mask
200	ACT_MSK_TUNNEL	Tunnel action mask
199	ACT_MSK_INVT_IP_RSV_FLAG	Invert IP reserve flag action mask
198:190	METER_IDX	Meter index
189	DROP_ACT	Drop action For green packet if meter action is set. For all packet if meter action is unset.
188	YDROP_ACT	Yellow drop action
187	RDROP_ACT	Red drop action
186:184	FWD_ACT	Forward action
183	FWD_CPU_PKT_FMT	Packet to CPU format
182	FWD_SA_LRN	Packet L2 learn action
181	FWD_SEL	Packet forward and drop selection
180:166	FWD_PORT_INFO	Packet forward port information
165	MIR_ACT	Mirror action
164:163	MIR_IDX	Mirror index
162:161	IVLAN_VID_ACT	Inner VLAN ID action
160:149	IVLAN_VID	Inner VLAN ID
148:147	IPRI_ACT	Inner priority action
146:144	IPRI_DATA	Inner priority
143:142	OVLAN_VID_ACT	Outer VLAN ID action
141:130	OVLAN_VID	Outer VLAN ID
129:128	OPRI_ACT	Outer priority action
127:125	OPRI_DATA	Outer priority
124:123	REMARK_ACT	Remark action For green packet if meter action is set. For all packet if meter action is unset.
122:115	REMARK_DATA	Remark value
114:113	YREMARK_ACT	Yellow remark action
112:105	YREMARK_DATA	Yellow remark value
104:103	RREMARK_ACT	Red remark action
102:95	RREMARK_DATA	Red remark value
94:93	ITAGSTS	Inner tag status action
92:91	OTAGSTS	Outer tag status action
90:88	INTERNAL_PRIO	Internal priority
87:76	METADATA	Meta data action

<b>Bits</b>	<b>Field</b>	<b>Description</b>
75:64	METADATA_MSK	Meta data mask action
63:61	BYPASS_ACT	Bypass action
60	QID_ACT	Queue ID action
59:55	QID	Queue ID value
54	TUNNEL_TYPE	Tunnel type action
53:42	TUNNEL_IDX	Pointer to tunnel index
41:0	RESERVED	

**SECTION 13.3**
**RANGE CHECK (PKT LEN/L4PORT)**

Range Check (port/vlan/ip/L4port) module

**RNG\_CHK\_CTRL**

**BASE ADDRESS :** 0xBB0045D8  
**ARRAY INDEX :** 0 - 15  
**ARRAY OFFSET :** 0x8  
**DEFAULT VALUE :** 0x0, 0x0

This is a One-Dimension Common Register Array.

TCP/UDP port and packet length range check configuration for filtering the TCP/UDP port range or packet length. The filtering result of each configuration can be examined through the ACL field "RNGCHK".

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:35	RESERVED			
34:32	TYPE	0x0: I-VID 0x1: O-VID 0x2: TCP/UDP source port 0x3: TCP/UDP destination port 0x4: TCP/UDP source or destination port 0x5: packet length (byte, include CRC) 0x6: L3 packet length (byte)	RW	0x0
31:16	UPPER	upper bond	RW	0x0
15:0	LOWER	lower bond	RW	0x0

**RNG\_CHK\_IP\_CTRL**

**BASE ADDRESS :** 0xBB004658  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Common Register Array.

IP address range check configuration for filtering the IP address range.

The filtering result of each configuration can be examined through the ACL field "IPRANGECHK".

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			
2:0	TYPE	Range check data type 0x0: IPv4 SIP 0x1: IPv4 DIP 0x2: IPv6 SIP 0x3: IPv6 DIP 0x4: IPv6 SIP[63:0] suffix 0x5: IPv6 DIP[63:0] suffix	RW	0x0

## **RNG\_CHK\_IP\_RNG**

BASE ADDRESS : 0xBB004678

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Upper bound and lower bound IP address of IP address range check configuration.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:32	IP_UPPER	IP address upper bound. Type=0x2/0x3: entry 0/4 represents IP6[31:0] entry 1/5 represents IP6[63:32] entry 2/6 represents IP6[95:64] entry 3/7 represents IP6[127:96] Type=0x4/0x5: entry 0/2/4/6 represents IP6[31:0] entry 1/3/5/7 represents IP6[63:32]	RW	0x0
31:0	IP_LOWER	IP address lower bound. Type=0x2/0x3: entry 0/4 represents IP6[31:0] entry 1/5 represents IP6[63:32] entry 2/6 represents IP6[95:64] entry 3/7 represents IP6[127:96] Type=0x4/0x5: entry 0/2/4/6 represents IP6[31:0] entry 1/3/5/7 represents IP6[63:32]	RW	0x0

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## CHAPTER 14

# Security

The chapter describes features related to security

SECTION 14.1

## 802.1X

IEEE 802.1X authentication

SECTION 14.2

## ATTACK PREVENTION

Prevents the device from various attacking

### ATK\_PRVNT\_PORT\_EN

BASE ADDRESS : 0xBB008C28

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Attack Prevention Port Enable register.

Enable Attack Prevention function on a port.

Bits	Field	Description	Type	Default
0	EN	Enable per-port attack prevention.	RW	0x0

### ATK\_PRVNT\_CTRL

REGISTER ADDRESS : 0xBB008C30

DEFAULT VALUE : 0x0

Attack Prevention Control register.

Enable various filtering of Attack Prevention function.

Bits	Field	Description	Type	Default
31:21	RESERVED			

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
20	CPU_SEL	Select which CPU attack packets are trapped to. 1b0: local CPU 1b1: master CPU	RW	0x0
19	TCP_FRAG_OFF_MIN	Filter TCP non-initial fragments carrying TCP header.	RW	0x0
18	SYN_RST	Filter TCP packets with SYN and RST set (SYN-RST Scan attack).	RW	0x0
17	SYN_FIN	Filter TCP packets with SYN and FIN set (SYN-FIN Scan attack).	RW	0x0
16	XMAS	Filter TCP packets with sequence number is zero and the FIN, URG, and PSH bits are set (XMAS Scan attack).	RW	0x0
15	NUL_SCAN	Filter TCP packets with sequence number is zero and the control-bits are zeroes.	RW	0x0
14	SYN_SPORT_LESS_1024	Filter TCP packets with SYN bit set and ACK bit not set and sport less than 1024.	RW	0x0
13	TCP_HDR_LEN_MIN	Check first TCP fragmented packet that don't have the full TCP header. (i.e., data offset field in TCP header should have the value larger than or equal to the configured value in ATK_PRVNT_TCP_CTRL.HDR_LEN_MIN)	RW	0x0
12	SMURF	Filter ICMP packets of ping request to a broadcast DIP (x.x.x.255) and the SIP is spoofed (victim).	RW	0x0
11	ICMPV6_PING_MAX	Filter ICMPv6 ping packets with payload size greater than the programmable value in ATK_PRVNT_ICMP_CTRL.PKT_LEN_MAX.	RW	0x0
10	ICMPV4_PING_MAX	Filter ICMPv4 ping packets with payload size greater than the programmable value in ATK_PRVNT_ICMP_CTRL.PKT_LEN_MAX.	RW	0x0
9	ICMP_FRAG_PKT	Filter fragmented ICMP packets (IPv4 & IPv6).	RW	0x0
8	IPV6_FRAG_LEN_MIN	Check for minimum size of IPV6 fragments. (If packet length is larger than ATK_PRVNT_IPV6_CTRL.FRAG_LEN_MIN)	RW	0x0
7	POD	Filter IPv4/IPv6 packets that length is larger than 64K bytes through fragments. (Ping of Death)	RW	0x0
6	TCP_BLAT	Filter IPv4/IPv6 TCP packets whose SPORT = DPORT.	RW	0x0
5	UDP_BLAT	Filter IPv4/IPv6 UDP packets whose SPORT = DPORT.	RW	0x0
4	LAND	Filter IPv4/IPv6 packets whose SIP = DIP.	RW	0x0
3	INVALID_HEADER	Filter header length in L3 header or CRC error.	RW	0x0
2	MCSA	Filter SMAC = multicast MAC	RW	0x0
1	ZERO_MAC	Filter DMAC = all zero or SMAC = all zero	RW	0x0
0	DA_EQUAL_SA	Filter packets with DMAC = SMAC.	RW	0x0

## ATK\_PRVNT\_ACT

REGISTER ADDRESS : 0xBB008C34

DEFAULT VALUE : 0x0

Attack Prevention Action register.

Determine the action (Drop/Trap) for each filtering of Attack Prevention function.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19	TCP_FRAG_OFF_MIN	Determine the action for the TCP_FRAG_OFF_MIN packets. 0b0: drop 0b1: trap	RW	0x0
18	SYN_RST	Determine the action for the SYN RST packets. 0b0: drop 0b1: trap	RW	0x0
17	SYN_FIN	Determine the action for the SYN FIN packets. 0b0: drop 0b1: trap	RW	0x0
16	XMAS	Determine the action for the XMAS packets. 0b0: drop 0b1: trap	RW	0x0
15	NULL_SCAN	Determine the action for the NULL SCAN packets. 0b0: drop 0b1: trap	RW	0x0
14	SYN_SPORT_LESS_1024	Determine the action for the SYN_SPORT_LESS_1024 packets. 0b0: drop 0b1: trap	RW	0x0
13	TCP_HDR_LEN_MIN	Determine the action for the TCP header too small packets. 0b0: drop 0b1: trap	RW	0x0
12	SMURF	Determine the action for the SMURF attack packets. 0b0: drop 0b1: trap	RW	0x0
11	ICMPV6_PING_MAX	Determine the action for the ICMPv6 ping attack packets. 0b0: drop 0b1: trap	RW	0x0
10	ICMPV4_PING_MAX	Determine the action for the ICMPv4 ping attack packets. 0b0: drop 0b1: trap	RW	0x0
9	ICMP_FRAG_PKT	Determine the action for the ICMP fragmented packets. 0b0: drop 0b1: trap	RW	0x0
8	IPV6_FRAG_LEN_MIN	Determine the action for the IPv6 minimum fragmented packets. 0b0: drop 0b1: trap	RW	0x0
7	POD	Determine the action for the POD attack packets. 0b0: drop 0b1: trap	RW	0x0

Bits	Field	Description	Type	Default
6	TCP_BLAT	Determine the action for the TCP blat attack packets. 0b0: drop 0b1: trap	RW	0x0
5	UDP_BLAT	Determine the action for the UDP blat attack packets. 0b0: drop 0b1: trap	RW	0x0
4	LAND	Determine the action for the LAND attack packets. 0b0: drop 0b1: trap	RW	0x0
3	INVALID_HEADER	Determine the action for the INVALID_HEADER packets. 0b0: drop 0b1: trap	RW	0x0
2	MCSA	Determine the action for the SA = multicast MAC attack packets. 0b0: drop 0b1: trap	RW	0x0
1	ZERO_MAC	Determine the action for the DA=all zero or SA = all zero attack packets. 0b0: drop 0b1: trap	RW	0x0
0	DA_EQUAL_SA	Determine the action for the DA=SA attack packets. 0b0: drop 0b1: trap	RW	0x0

### ATK\_PRVNT\_IPV6\_CTRL

REGISTER ADDRESS : 0xBB008C38

DEFAULT VALUE : 0x4D8

Attack Prevention IPv6 Control register.

The register controls the related to IPv6 filtering of Attack Prevention function.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:0	FRAG_LEN_MIN	Value to use when checking for minimum size of IPv6 fragments. (Checking is enabled by setting APCR.IPV6_MIN_FRAG_LEN)	RW	0x04D8

### ATK\_PRVNT\_ICMP\_CTRL

REGISTER ADDRESS : 0xBB008C3C

DEFAULT VALUE : 0x200

Attack Prevention ICMP Control register.

The register controls the related to ICMP filtering of Attack Prevention function.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	PKT_LEN_MAX	Maximum length of ICMP packet allowed before dropping.	RW	0x0200

### **ATK\_PRVNT\_TCP\_CTRL**

REGISTER ADDRESS : 0xBB008C40

DEFAULT VALUE : 0x14

Attack Prevention TCP Control register.

The register controls the related to TCP filtering of Attack Prevention function.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:5	RESERVED			
4:0	HDR_LEN_MIN	Minimum TCP header length allowed. (minimum 0 bytes, maximum 31 bytes)	RW	0x14

### **ATK\_PRVNT\_SMURF\_CTRL**

REGISTER ADDRESS : 0xBB008C44

DEFAULT VALUE : 0x0

Attack Prevention Smurf Control register.

The register controls the related to Smurf filtering of Attack Prevention function.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	NETMASK	Bitmask of Network Mask to check the Smurf.	RW	0x0

### **ATK\_PRVNT\_STS**

REGISTER ADDRESS : 0xBB008C48

DEFAULT VALUE : 0x0

Attack Prevention Status register.

To indicate the hit status for each filtering of Attack Prevention function.

(Read to clear)

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20	ARP_INVLD	When 'ARP invalid' is detected, the bit will be set to 1.	RC	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
19	TCP_FRAG_OFF_MIN	When ‘non-initial fragments carrying TCP header’ is detected, the bit will be set to 1.	RC	0x0
18	SYN_RST	When ‘SYN/RST Scan’ is detected, the bit will be set to 1.	RC	0x0
17	SYN_FIN	When ‘SYN/FIN Scan’ is detected, the bit will be set to 1.	RC	0x0
16	XMAS	When ‘XMAS Scan’ is detected, the bit will be set to 1.	RC	0x0
15	NULL_SCAN	When ‘Null Scan’ is detected, the bit will be set to 1.	RC	0x0
14	SYN_SPORT_LESS_1024	When ‘SYN with sport < 1024’ is detected, the bit will be set to 1.	RC	0x0
13	TCP_HDR_MIN	When ‘TCP header minimum length’ is detected, the bit will be set to 1.	RC	0x0
12	SMURF	When ‘Smurf attack’ is detected, the bit will be set to 1.	RC	0x0
11	ICMPV6_PING_MAX	When ‘ICMPv6 ping maximum’ is detected, the bit will be set to 1.	RC	0x0
10	ICMPV4_PING_MAX	When ‘ICMPv4 ping maximum’ is detected, the bit will be set to 1.	RC	0x0
9	ICMP_FRAG_PKT	When ‘ICMP fragmented packet’ is detected, the bit will be set to 1.	RC	0x0
8	IPV6_FRAG_LEN_MIN	When ‘IPv6 minimum fragment length’ is detected, the bit will be set to 1.	RC	0x0
7	POD	When ‘Ping of Death’ is detected, this bit will be set to 1.	RC	0x0
6	TCP_BLAT	When ‘TCP Blat attack’ is detected, the bit will be set to 1.	RC	0x0
5	UDP_BLAT	When ‘UDP Blat attack’ is detected, the bit will be set to 1.	RC	0x0
4	LAND	When ‘LAND attack’ is detected, this bit will be set to 1.	RC	0x0
3	INVALID_HEADER	When ‘INVALID_LEN’ is detected, the bit will be set to 1.	RC	0x0
2	MCSA	When ‘MCSA’ is detected, the bit will be set to 1.	RC	0x0
1	ZERO_MAC	When ‘ZERO_MAC’ is detected, the bit will be set to 1.	RC	0x0
0	DA_EQUAL_SA	When DAEQSA is detected, the bit will be set to 1.	RC	0x0

### **ATK\_PRVNT\_ARP\_INVLD\_PORT\_ACT**

BASE ADDRESS : 0xBB008C4C

PORT INDEX : 0 - 55

PORT OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Attack Prevention ARP Invalid Action Per-Port register.

Determine the action (Forward/Drop/Trap) of ARP invalid packets for each port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	ACT	Action for invalid ARP packets. 0b00: Forward 0b01: Drop 0b10: Trap 0b11: Reserved	RW	0x0

— SECTION 14.3 —

## CODE PROTECTION

Code Protection module

— SECTION 14.4 —

## IP-MAC BINDING

IP-MAC Binding module

### **SEC\_IP\_MAC\_BIND\_CTRL**

REGISTER ADDRESS : 0xBB008C5C

DEFAULT VALUE : 0x0

IP-MAC Binding Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:9	RESERVED			
8:6	LU_MIS_ACT	Forwarding action when table looks up miss. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
5:3	MISMATCH_ACT	Forwarding action when the bind isn't match 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0
2:0	MATCH_ACT	Forwarding action when the bind is match 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU 0x4: trap to master CPU 0x5: copy to master CPU 0x6~0x7: reserved	RW	0x0

Bits	Field	Description	Type	Default
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## **SEC\_PORT\_IP\_MAC\_BIND\_CTRL**

BASE ADDRESS : 0xBB008C60

PORT INDEX : 0 - 55

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Per-Port IP-MAC Binding Control Register

Bits	Field	Description	Type	Default
31:2	RESERVED			
1	ARP_CHK_EN	Enable status of IP-MAC bind checking for ARP packets 0b0: disable 0b1: enable	RW	0x0
0	CHK_EN	Enable status of MAC bind checking for non-ARP packets 0b0: disable 0b1: enable	RW	0x0

## **IP\_MAC\_BIND**

TABLE TYPE : 0x6

TABLE INDEX : 0 - 1023

IP-MAC Binding Table

Bits	Field	Description
127	VALID	Entry valid bit 0b0: Invalid 0b1: Valid
126:95	SIP	Source IPv4 address (Only the last 24 bits are available, record the last 24 bits of SIP, most 8 bits can be reversed with index) Note: For ARP packet, SIP means Sender IP Address field.
94:47	SMAC	Source MAC address Note: For ARP packet, SMAC means Sender Hardware Address field.
46	PORT_BIND	To indicate this packet should be checked the ingress port. 0b0: Don't need to check 0b1: Need to check
45	PORT_TYPE	Type of the port 0b0: Individual port 0b1: Trunk port
44:38	PORT_ID	For individual port (portType = 0), to indicate the port ID (0~51). For trunk port (portType = 1), to indicate the trunk ID (0~127).

<b>Bits</b>	<b>Field</b>	<b>Description</b>
37	VID_BIND	Bind Status of VLAN 0b0: Don't need to check VLAN 0b1: Need to check VLAN
36:25	VID	VLAN ID
24:0	RESERVED	

## IP\_MAC\_BIND\_CAM

TABLE TYPE : 0x7

TABLE INDEX : 0 - 63

IP-MAC Binding Table (CAM)

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Entry valid bit 0b0: Invalid 0b1: Valid
126:95	SIP	Source IPv4 address Note: For ARP packet, SIP means Sender IP Address field.
94:47	SMAC	Source MAC address Note: For ARP packet, SMAC means Sender Hardware Address field.
46	PORT_BIND	To indicate this packet should be checked the ingress port. 0b0: Don't need to check 0b1: Need to check
45	PORT_TYPE	Type of the port 0b0: Individual port 0b1: Trunk port
44:38	PORT_ID	For individual port (portType = 0), to indicate the port ID (0~51). For trunk port (portType = 1), to indicate the trunk ID (0~127).
37	VID_BIND	Bind Status of VLAN 0b0: Don't need to check VLAN 0b1: Need to check VLAN
36:25	VID	VLAN ID
24:0	RESERVED	

## IP\_MAC\_BIND\_DYN

TABLE TYPE : 0xA

TABLE INDEX : 0 - 2047

IP-MAC Binding Table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Entry valid bit 0b0: Invalid 0b1: Valid

Bits	Field	Description
126:95	SIP	Source IPv4 address (Only the last 24 bits are available, record the last 24 bits of SIP, most 8 bits can be reversed with index) Note: For ARP packet, SIP means Sender IP Address field.
94:47	SMAC	Source MAC address Note: For ARP packet, SMAC means Sender Hardware Address field.
46	PORT_BIND	To indicate this packet should be checked the ingress port. 0b0: Don't need to check 0b1: Need to check
45	PORT_TYPE	Type of the port 0b0: Individual port 0b1: Trunk port
44:38	PORT_ID	For individual port (portType = 0), to indicate the port ID (0~51). For trunk port (portType = 1), to indicate the trunk ID (0~127).
37	VID_BIND	Bind Status of VLAN 0b0: Don't need to check VLAN 0b1: Need to check VLAN
36:25	VID	VLAN ID
24:0	RESERVED	

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## CHAPTER 15

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# Diagnostic

The chapter describes features related to diagnostic

SECTION 15.1	OAM
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OAM module

### **OAM\_CTRL**

REGISTER ADDRESS : 0xBB008D40

DEFAULT VALUE : 0x0

Global configuration for OAM Loopback

Bits	Field	Description	Type	Default
31:5	RESERVED			
4	LRN	OAMPDU source MAC learning control 0b0: disable 0b1: enable	RW	0x0
3	CPU_SEL	Select packet trap to local or master CPU 0b0: trap to local CPU 0b1: trap to master CPU	RW	0x0
2	MAC_SWAP	If OAM Loopback is globally enabled (i.e. EN = 0x1) and parser is in 'Loopback' state (i.e. PAR_ACT = lb) then this bit takes effect. If this bit is enabled then swap the MAC Addresses (Source MAC & Destination MAC) else do not. 0b0: disable 0b1: enable	RW	0x0
1	DIS_ACT	If EN is set to disable then this bit takes effect. Such as if EN = disable and DIS_ACT = forward then OAM PDU traffic must be forwarded instead of drop. Value of this bit is forward or drop 0b0: drop 0b1: forward	RW	0x0
0	EN	Globally enable or disable OAM loopback feature 0b0: disable 0b1: enable	RW	0x0

### **OAM\_PORT\_ACT\_CTRL**

**BASE ADDRESS :** 0xBB008D44  
**PORT INDEX :** 0 - 55  
**PORT OFFSET :** 0x4  
**DEFAULT VALUE :** 0x3

This is a One-Dimension Port Register Array.

OAM Loopback configuration for Multiplexer & Parser to control non-OAMPDU Tx/Rx traffic

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			
2:1	PAR_ACT	Action to be taken by the Parser for non-OAMPDU Rx traffic 0x0: discard; drop all non-OAMPDU Rx traffic 0x1: forward; pass all non-OAMPDU Rx traffic to next module (e.g. L2 lookup) 0x2: loopback; pass all non-OAMPDU Rx traffic to Multiplexer for looping it back to the source port (except Pause Frame & CRC error frame). MAC learning must be OFF for looped back traffic on that port 0x3: trap; just trap the packets to CPU	RW	0x1
0	MUX_ACT	Action to be taken by the Multiplexer for non-OAMPDU Tx traffic 0b0: discard; drop all non-OAMPDU Tx traffic 0b1: forward; pass all non-OAMPDU Tx traffic to lower layer for transmission	RW	0x1

### **OAM\_GLB\_DYING\_GASP\_CTRL**

**REGISTER ADDRESS :** 0xBB001424  
**DEFAULT VALUE :** 0x260053

Dying Gasp power monitor control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:22	RESERVED			
21:20	DYING_GASP_VTH		RW	0x2
19:17	DYING_GASP_PKTCNT	Select how many dying gasp packets can be sent when power failure happen or trigger dying gasp.	RW	0x3
16	DYING_GASP_TRIG	Trigger dying gasp send, auto clear when completed. 0b0: normal 0b1: trigger dying gasp manually	RWAC	0x0
15:0	TBP_VAL	Time of Voltage below Vref period, represented by setting + 1 Digital Circuit using this register to deglitch. Timer interval depend on system clock..	RW	0x0053

## OAM\_PORT\_DYING\_GASP\_CTRL

BASE ADDRESS : 0xBB003058

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Per-port OAM dying gasp control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	PORT_OAM_DYING_GASP_EN	Enable the specific port to generate dying gasp message when detected power failure.  0b0: disable 0b1: enable	RW	0x0

## DYING\_GASP\_POLARITY\_CTRL

REGISTER ADDRESS : 0xBB001428

DEFAULT VALUE : 0x0

Dying gasp polarity control register.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	DYGASP_POLARITY	Trigger edge for digital circuit. If DYGASP_POLARITY=0b0, DY_GASP=0 is dying gasp state. DY_GASP=1 is normal state. If DYGASP_POLARITY=0b1, DY_GASP=1 is dying gasp state. DY_GASP=0 is normal state.	RW	0x0

SECTION 15.2	CFM
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CFM(802.1ag & Y.1731) module

## CFM\_RX\_CTRL

REGISTER ADDRESS : 0xBB00A400

DEFAULT VALUE : 0x0

Action to be taken on receiving CFM frames

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:3	RESERVED			
2	CPU_SEL	Select destination CPU for trapping action 0b0: trap to local CPU 0b1: trap to master CPU	RW	0x0
1:0	UNKN_RX	Action on receiving unknown type of CFM frames. 0x0: fwd 0x1: drop 0x2: trap to CPU	RW	0x0

### **CFM\_RX\_CCM\_CTRL**

REGISTER ADDRESS : 0xBB00A404

DEFAULT VALUE : 0x0

Action to be taken on receiving CFM CCM frame of different MD levels.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:14	MD_LV_7	Action on receiving CCM frames with MD Level 7. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD	RW	0x0
13:12	MD_LV_6	Action on receiving CCM frames with MD Level 6. 0x0: fwd 0x1: drop 0x2: trap to CPU 0b11: LFD	RW	0x0
11:10	MD_LV_5	Action on receiving CCM frames with MD Level 5. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD	RW	0x0
9:8	MD_LV_4	Action on receiving CCM frames with MD Level 4. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD	RW	0x0
7:6	MD_LV_3	Action on receiving CCM frames with MD Level 3. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:4	MD_LV_2	Action on receiving CCM frames with MD Level 2. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD	RW	0x0
3:2	MD_LV_1	Action on receiving CCM frames with MD Level 1. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD	RW	0x0
1:0	MD_LV_0	Action on receiving CCM frames with MD Level 0. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: LFD(link fault detection for ring protection)	RW	0x0

### **CFM\_RX\_LB\_CTRL**

REGISTER ADDRESS : 0xBB00A408

DEFAULT VALUE : 0x0

Action to be taken on receiving CFM Loopback frames of different MD levels.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:14	MD_LV_7	Action on receiving Loopback frames with MD Level 7. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
13:12	MD_LV_6	Action on receiving Loopback frames with MD Level 6. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
11:10	MD_LV_5	Action on receiving Loopback frames with MD Level 5. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
9:8	MD_LV_4	Action on receiving Loopback frames with MD Level 4. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
7:6	MD_LV_3	Action on receiving Loopback frames with MD Level 3. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
5:4	MD_LV_2	Action on receiving Loopback frames with MD Level 2. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
3:2	MD_LV_1	Action on receiving Loopback frames with MD Level 1. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
1:0	MD_LV_0	Action on receiving Loopback frames with MD Level 0. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0

## CFM\_RX\_LT\_CTRL

REGISTER ADDRESS : 0xBB00A40C

DEFAULT VALUE : 0x0

Action to be taken on receiving CFM Linktrace frames of different MD levels.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:14	MD_LV_7	Action on receiving Linktrace frames with MD Level 7. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
13:12	MD_LV_6	Action on receiving Linktrace frames with MD Level 6. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
11:10	MD_LV_5	Action on receiving Linktrace frames with MD Level 5. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
9:8	MD_LV_4	Action on receiving Linktrace frames with MD Level 4. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
7:6	MD_LV_3	Action on receiving Linktrace frames with MD Level 3. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
5:4	MD_LV_2	Action on receiving Linktrace frames with MD Level 2. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
3:2	MD_LV_1	Action on receiving Linktrace frames with MD Level 1. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0
1:0	MD_LV_0	Action on receiving Linktrace frames with MD Level 0. 0x0: fwd 0x1: drop 0x2: trap to CPU 0x3: unused	RW	0x0

### CCM\_RX\_LIFETIME\_CTRL

BASE ADDRESS : 0xBB00A410

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 10 bits

DEFAULT VALUE : 0xB

This is a One-Dimension Register Field Array. (10 bits per field)

Lifetime value which must be used to reset the ERP instance counters on receiving CCM frames for the corresponding instances.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
9:0	LIFETIME	Lifetime value which must be used to reset the ERP instance counters upon receiving a CCM frame for the corresponding instances. E.g. if a CCM packet satisfies the instance0 then value of lifetime0 must be taken to reset the instance0 counters.	RW	0x00B

### CCM\_RX\_INST\_CTRL

**BASE ADDRESS :** 0xBB00A41C  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x3F3F

This is a One-Dimension Common Register Array.

Instance based port configuration for CCM frame reception.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:28	RESERVED			
27:16	VID	VID associated with the corresponding instance. VID = 0 signifies untagged CCM frames are permitted and do not check VID in the frame if the received frame is tagged	RW	0x0
15	TRK_PRESENT1	MEMBER1 is trunk ID or port 0x0: port 0x1: trunk ID	RW	0x0
14:8	MEMBER1	Port number which would be participated in ERP and would receive CCM frames	RW	0x3F
7	TRK_PRESENT0	MEMBER1 is trunk ID or port 0x0: port 0x1: trunk ID	RW	0x0
6:0	MEMBER0	Port number which would be participated in ERP and would receive CCM frames	RW	0x3F

### **CCM\_RX\_INST\_CNT**

**BASE ADDRESS :** 0xBB00A43C  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Common Register Array.

Instance based counters which are corresponding to member configured to receive CCM frames.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:20	RESERVED			
19:10	CNTR1	Keep alive counter corresponding to member1, which has configured to receive CCM frames. If this counter reaches 0 then an interrupt must be fired. On receiving CCM packet this counter must be reset to the value configured by the corresponding LIFETIME of the same instance	RO	0x0
9:0	CNTR0	Keep alive counter corresponding to member0, which has configured to receive CCM frames. If this counter reaches 0 then an interrupt must be fired. On receiving CCM packet this counter must be reset to the value configured by the corresponding LIFETIME of the same instance	RO	0x0

## CCM\_TX\_TAG\_CTRL

REGISTER ADDRESS : 0xBB000594

DEFAULT VALUE : 0x0

Tag configuration register for CCM frames to be generated by packet-gen.

Bits	Field	Description	Type	Default
31:6	RESERVED			
5:3	PCP	Priority Code Point value of the tag to be used in the generated CCM frames.	RW	0x0
2	CFI	Canonical Format Identifier of the tag to be used in the generated CCM frames	RW	0x0
1:0	TPID	Index for pointer to outer TPID list for the generated CCM frames	RW	0x0

## CCM\_TX\_INST\_CTRL

BASE ADDRESS : 0xBB000598

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x80080000

This is a One-Dimension Common Register Array.

Instance-based configuration for CCM frames to be generated with a certain interval.

Bits	Field	Description	Type	Default
31	VID_ADD	This bit signifies whether outer VLAN tag would be inserted into the CCM frames generated by PORT0 & PORT1. If enable then VLAN tag would be inserted into the CCM frames else not. 0b0: disable 0b1: enable	RW	0x1
30:19	VID	Outer VID to be added into the CCM frames if VID_ADD bit is enabled.	RW	0x001
18:11	MAID	LSB 1 byte to be inserted into the generated CCM frame as MAID	RW	0x0
10	TX	This bit enables/disables CCM frame generation & transmission of corresponding instance. 0b0: disable, do not generate CCM packets 0b1: enable, enable CCM packet generation	RW	0x0
9:0	INTLV	CCM frame transmission interval in msec for the corresponding instances.	RW	0x0

## CCM\_TX\_INST\_MEM

BASE ADDRESS : 0xBB0005B8

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 7 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (7 bits per field)

Index-based configuration for ports participate in ERP regard of CCM frame generation and transmission.

Index N and Index N+8 represent for MEMBER0 and MEMBER1 of the same instance.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
6:0	P0	Port number which would participate in ERP and would generate & send CCM frames.	RW	0x0

### **CCM\_TX\_INST\_TRK\_PRESENT**

BASE ADDRESS : 0xBB0005C8

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Index-based configuration for ports participate in ERP regard of CCM frame generation and transmission.

Index N and Index N+8 represent for MEMBER0 and MEMBER1 of the same instance.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
0	TRK_PRESENT	CCM_TX_INST_MEM.P0 is trunk ID or port 0x0: port 0x1: trunk ID	RW	0x0

### **CCM\_TX\_INST\_PKT**

BASE ADDRESS : 0xBB0005CC

ARRAY INDEX : 0 - 7

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x48

This is a One-Dimension Common Register Array.

Configuration for CCM frame generation

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18:6	MEPID	MEPID to be used for generating CCM frames	RW	0x0001
5:3	CCM_INTLV	The interval between CCM frame transmissions.	RW	0x1
2:0	MDL	MD Level to be used for generating CCM frames	RW	0x0

Bits	Field	Description	Type	Default
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## ETH\_DM\_PORT\_EN

BASE ADDRESS : 0xBB00A45C

PORT INDEX : 0 - 55

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Enable the Ethernet Delay Measurement function on a port.

Bits	Field	Description	Type	Default
0	EN	Enable ETH-DM function. (If an ETH-DM packet comes from an enabled port, ASIC will do the Rx timestamping and trap this packet to CPU) 0b0: disable 0b1: enable	RW	0x0

## ETH\_DM\_RX\_CTRL

REGISTER ADDRESS : 0xBB00A464

DEFAULT VALUE : 0x0

Action to be taken on receiving Ethernet Delay Measurement frames of different MD levels.

Bits	Field	Description	Type	Default
31:16	RESERVED			
15:14	MD_LV_7	Action on receiving Ethernet Delay Measurement frames with MD Level 7. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
13:12	MD_LV_6	Action on receiving Ethernet Delay Measurement frames with MD Level 6. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
11:10	MD_LV_5	Action on receiving Ethernet Delay Measurement frames with MD Level 5. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
9:8	MD_LV_4	Action on receiving Ethernet Delay Measurement frames with MD Level 4. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
7:6	MD_LV_3	Action on receiving Ethernet Delay Measurement frames with MD Level 3. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
5:4	MD_LV_2	Action on receiving Ethernet Delay Measurement frames with MD Level 2. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
3:2	MD_LV_1	Action on receiving Ethernet Delay Measurement frames with MD Level 1. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0
1:0	MD_LV_0	Action on receiving Ethernet Delay Measurement frames with MD Level 0. 0x0: forward 0x1: drop 0x2: trap to local CPU 0x3: trap to master CPU	RW	0x0

### **ETH\_DM\_TX\_DLY**

REGISTER ADDRESS : 0xBB004F60

DEFAULT VALUE : 0x0

Set the delay time at each link-speed for ETH-DM transmission.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:24	SPD_10G	Specify the delay time to adjust the timestamp at 5G or 10G link-speed for ETH-DM transmission. (Unit: 8 nanoseconds)	RW	0x0
23:16	SPD_1G	Specify the delay time to adjust the timestamp at 1G or 2.5G link-speed for ETH-DM transmission. (Unit: 8 nanoseconds)	RW	0x0
15:8	SPD_100M	Specify the delay time to adjust the timestamp at 100M link-speed for ETH-DM transmission. (Unit: 8 nanoseconds)	RW	0x0

Bits	Field	Description	Type	Default
7:0	SPD_10M	Specify the delay time to adjust the timestamp at 10M link-speed for ETH-DM transmission. (Unit: 8 nanoseconds)	RW	0x0

## ETH\_DM\_RX\_TIME

BASE ADDRESS : 0xBB00A468  
ARRAY INDEX : 0 - 63  
ARRAY OFFSET : 0x8  
DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Get the corresponding Rx timestamp of the ETH-DM packet.  
(total: 64 set, read ‘NSEC’ to clear ‘NSEC’ and ‘SEC’)

Bits	Field	Description	Type	Default
63:62	RESERVED			
61:32	NSEC	The nanosecond of the latched Rx timestamp.	RC	0x0
31:0	SEC	The second of the latched Rx timestamp.	RC	0x0

## ETH\_DM\_CLK\_CTRL

REGISTER ADDRESS : 0xBB00142C  
DEFAULT VALUE : 0x0

ETH-DM system clock free running configuration

Bits	Field	Description	Type	Default
31:1	RESERVED			
0	CLK_EN	Enable the free-running clock of ETH-DM module. 0b0:disable 0b1:enable	RW	0x0

## ETH\_DM\_TIME\_FREQ

REGISTER ADDRESS : 0xBB001430  
DEFAULT VALUE : 0x8000000

Configuration of the frequency of the system time.

Bits	Field	Description	Type	Default
31:28	RESERVED			
27:0	TIME_FREQ	Frequency of the system reference time.	RW	0x8000000

### **ETH\_DM\_TIME\_CTRL**

REGISTER ADDRESS : 0xBB001434

DEFAULT VALUE : 0x0

Execution and command bits for ETH-DM time access

Bits	Field	Description	Type	Default
31:2	RESERVED			
1	EXEC	Trigger hardware to execute an indirect access of System Time.  (Note: this bit is common used by software and hardware. When hardware completes the table access, it will clear this bit automatically.)	RWAC	0x0
0	CMD	Operation of the indirect access of System Time. 0b0: Read 0b1: Write	RW	0x0

### **ETH\_DM\_TIME\_CTRL\_SEC**

REGISTER ADDRESS : 0xBB001438

DEFAULT VALUE : 0x0, 0x0

The second and nano-second fields for ETH-DM time access.

Bits	Field	Description	Type	Default
63:32	SEC	Second in the reference time clock.	RW	0x0
31:30	RESERVED			
29:0	NSEC	Nanosecond in the reference time clock. (Unit: 1 nanoseconds)	RW	0x0

SECTION 15.3

### **UDLD**

UDLD module

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## CHAPTER 16

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# Time Synchronization

The chapter describes features related to time-synchronization

SECTION 16.1

## **PTP (PRECISION TIME PROTOCOL)**

PTP (Precision Time Protocol) module that provides PTP related configurations including per port enable, system time control, time stamp read/ISR, and PTP packet hardware modification control registers.

SECTION 16.2

## **SYNCHRONOUS.ETHERNET**

Synchronous-Ethernet module

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## CHAPTER 17

# SDN

The chapter describes features related to SDN (Software-Defined Network)

SECTION 17.1

## OPENFLOW

Declare OpenFlow related configuration registers and tables

### OF\_PORT\_CTRL

BASE ADDRESS : 0xBB00D000

PORT INDEX : 0 - 56

PORT OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

OpenFlow port configuration register.

Bits	Field	Description	Type	Default
0	EN	Pipeline classification by port. Packet from OpenFlow enabled port is sent to OpenFlow Pipeline. 0b0: disabled 0b1: enabled	RW	0x0

### OF\_VLAN\_EN

BASE ADDRESS : 0xBB00D008

ARRAY INDEX : 0 - 4095

ARRAY OFFSET : 1 bit

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Pipeline classification by VLAN. Packet from OpenFlow enabled VLAN is sent to OpenFlow Pipeline.

Bits	Field	Description	Type	Default
0	EN	Enable VLAN for OpenFlow processing. 0b0: disabled 0b1: enabled	RW	0x0

Bits	Field	Description	Type	Default
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## OF\_VLAN\_AND\_PORT\_EN

BASE ADDRESS : 0xBB00D208  
ARRAY INDEX : 0 - 4095  
ARRAY OFFSET : 1 bit  
DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (1 bit per field)

Pipeline classification by VLAN and Port. Send packet to OpenFlow Pipeline if the packet's corresponding OF\_PORT\_EN and OF\_VLAN\_EN are both enabled.

Bits	Field	Description	Type	Default
0	EN	Enable qualifying VLAN and port for OpenFlow processing. 0b0: disabled 0b1: enabled	RW	0x0

## OF\_ACT\_CTRL

REGISTER ADDRESS : 0xBB00D403  
DEFAULT VALUE : 0x1

Configuration registers related to instructions and actions.

Bits	Field	Description	Type	Default
31:7	RESERVED			
6	TRAP_TARGET	Specify the trap target. 0b0: local CPU 0b1: master CPU	RW	0x0
5:0	LB_LIMIT	Limit maximum loopback times. Note: value 0x0 and 0x3F means unlimited.	RW	0x01

## OF\_L2\_FLOW\_TBL\_CTRL

REGISTER ADDRESS : 0xBB00D40C  
DEFAULT VALUE : 0x20

Configuration registers related to L2 flow table.

Bits	Field	Description	Type	Default
31:6	RESERVED			

Bits	Field	Description	Type	Default
5	BLK1_ALGO	Select the hash algorithm for L2 Hash-based Flow Entry block 1. 0b0: algorithm 0 0b1: algorithm 1	RW	0x1
4	BLK0_ALGO	Select the hash algorithm for L2 Hash-based Flow Entry block 0. 0b0: algorithm 0 0b1: algorithm 1	RW	0x0
3:0	LU_SEL	Select lookup key (Match Fields) for the First and Second lookup. 0x0: (VID,SA) and (0,SA) 0x1: (VID,SA) and (VID,DA) 0x2: (VID,SA) and (0,DA) 0x3: (0,SA) and (VID,DA) 0x4: (0,SA) and (0,DA) 0x5: (VID,DA) and (0,DA) 0x6: (DA,SA) and (VID,SA) 0x7: (DA,SA) and (0,SA) 0x8: (DA,SA) and (VID,DA) 0x9: (DA,SA) and (0,DA) 0xA: (IP COL,L4_DPORT,L4_SPORT,DIP,SIP) and (VID,L4_DPORT,L4_SPORT,DIP,SIP) 0xB~0xF: reserved	RW PROTO- and	0x0

## OF\_L3\_FLOW\_TBL\_CTRL

REGISTER ADDRESS : 0xBB00D410

DEFAULT VALUE : 0x40

Configuration registers related to L3 flow table.

Bits	Field	Description	Type	Default
31:7	RESERVED			
6	BLK1_ALGO	Select the hash algorithm for L3 Hash-based Flow Entry block 1. 0b0: algorithm 0 0b1: algorithm 1	RW	0x1
5	BLK0_ALGO	Select the hash algorithm for L3 Hash-based Flow Entry block 0. 0b0: algorithm 0 0b1: algorithm 1	RW	0x0
4	LU_PRI_SEL	Define the flow entry priority when both SRAM and TCAM are hit. 0b0: SRAM 0b1: TCAM	RW	0x0
3:2	HASH_LU_SEL	Select Hash-based flow table lookup key for the First and Second lookup. 0x0: SIP and DIP 0x1: (SIP,DIP) and SIP 0x2: (SIP,DIP) and DIP 0x3: reserved	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	CAM_LU_SEL	Select CAM-based flow table lookup key for the First and Second lookup. 0x0: (Metadata,SIP) and (Metadata,DIP) 0x1: (Metadata,SIP,DIP) and (Metadata,SIP) 0x2: (Metadata,SIP,DIP) and (Metadata,DIP) 0x3: reserved	RW	0x0

## OF\_GRP\_HASH\_CTRL

REGISTER ADDRESS : 0xBB00D414

DEFAULT VALUE : 0x7F

Specify the participated hash factor for group entry hash calculation. The hash calculation is used to index the specific action bucket entry for the group entry with type select.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:7	RESERVED			
6	L4_DPORT_INC	Include L4(TCP/UDP/SCTP) destination port.	RW	0x1
5	L4_SPORT_INC	Include L4(TCP/UDP/SCTP) source port.	RW	0x1
4	DIP_INC	Include destination IP address.	RW	0x1
3	SIP_INC	Include source IP address.	RW	0x1
2	DMAC_INC	Include destination MAC address.	RW	0x1
1	SMAC_INC	Include source MAC address.	RW	0x1
0	JGR_PORT_INC	include physical ingress port. 0: not include 1: include	RW	0x1

## OF\_IGR\_TBL\_MIS

BASE ADDRESS : 0xBB00D418

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 2 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (2 bits per field)

Specify lookup miss action for ingress flow tables.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
1:0	ACT	Ingress flow table miss action. 0x0: drop 0x1: trap 0x2: forward to next table 0x3: execute current action set	RW	0x0

## **OF\_EGR\_TBL\_MIS**

REGISTER ADDRESS : 0xBB004F64

DEFAULT VALUE : 0x0

Specify lookup miss action for egress flow tables.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	ACT	Egress flow table miss action. 0x0: forward 0x1: drop	RW	0x0

## **OF\_IGR\_TBL\_CNTR**

BASE ADDRESS : 0xBB00D41C

ARRAY INDEX : 0 - 3

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Ingress flow table counters for counting lookup and match packets.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:32	LOOKUP	Packet lookups counter for specific ingress flow table. Read to clear.	RC	0x0
31:0	MATCH	Packet matches counter for specific ingress flow table. Read to clear.	RC	0x0

## **OF\_EGR\_TBL\_CNTR**

REGISTER ADDRESS : 0xBB004F68

DEFAULT VALUE : 0x0, 0x0

Egress flow table counters for counting lookup and match packets.

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:32	LOOKUP	Packet lookups counter for specific egress flow table. Read to clear.	RC	0x0
31:0	MATCH	Packet matches counter for specific egress flow table. Read to clear.	RC	0x0

## OF\_EXCPT\_CTRL

REGISTER ADDRESS : 0xBB004F70

DEFAULT VALUE : 0x0

Configurations for OpenFlow exception.

Bits	Field	Description	Type	Default
31:1	RESERVED			
0	TTL_ACT	Action to take if MPLS/IP TTL exception (TTL=0 after decrement) is asserted. 0b0: drop 0b1: forward	RW	0x0

## ACTION\_BUCKET

TABLE TYPE : 0xB

TABLE INDEX : 0 - 8191

Group table action buckets

Bits	Field	Description
95	WA_COPY_TTL_INWARD	Apply copy TTL inward to the packet.
94	WA_POP_VLAN	Pop the outermost VLAN Tag from the packet.
93	WA_POP_MPLS	Pop MPLS label from the packet. 0b0: disable 0b1: enable
92	WA_POP_MPLS_TYPE	Pop MPLS label from the packet. 0b0: pop outermost label 0b1: pop double label
91	WA_PUSH_MPLS	Push MPLS label to the packet. 0b0: disable 0b1: enable All the WA_PUSH_MPLS_XXX fields following are used only if WA_PUSH_MPLS=enable.
90	WA_PUSH_MPLS_MODE	0b0: push MPLS label to the packet 0b1: push/swap MPLS label according to WA_MPLS_LIB_IDX
89	WA_MPLS_VPN_TYPE	0b0: L3 VPN 0b1: L2 VPN
88:78	WA_MPLS_LIB_IDX	The field is used when WA_PUSH_MPLS=0x2. Index to MPLS Encap. Table for retrieving label related info.
77	WA_MPLS_EHTYPE	The field is used when WA_PUSH_MPLS!=disable. 0b0: 0x8847 0b1: 0x8848
76	WA_PUSH_VLAN	Push VLAN tag to the packet.
75:74	WA_VLAN_EHTYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID)
73	WA_COPY_TTL_OUTWARD	Apply copy TTL outward to the packet.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
72	WA_DEC_MPLS_TTL	Decrement the outermost MPLS TTL from the packet.
71	WA_DEC_IP_TTL	Decrement the outermost IPv4 TTL/IPv6 Hop Limit from the packet.
70:68	WA_SET_FIELD0_TYPE	<p>Apply packet modification for specified Field Type.            0x0: disable            0x1: Source MAC            0x2: VLAN Priority            0x3: MPLS Traffic Class            0x4: MPLS TTL            0x5: IPv4/IPv6 DSCP            0x6: IPv4 TTL/IPv6 Hop Limit            0x7: IPv4 Flag Reserved Bit</p> <p>Note: all the modification should apply to the outermost possible header.</p>
67:58	WA_SET_FIELD0_DATA	<p>Field Data 0.            When WA_SET_FIELD0_TYPE =            0x1: [9:0] as index to Egress L3 INTF Table for retrieving SMAC            0x2: [2:0] as VLAN Priority            0x3: [2:0] as MPLS Traffic Class            0x4: [7:0] as MPLS TTL            0x5: [5:0] as IPv4/IPv6 DSCP            0x6: [7:0] as IPv4 TTL/IPv6 Hop Limit</p>
57:55	WA_SET_FIELD1_TYPE	<p>Apply packet modification for specified Field Type.            0x0: disable            0x1: Destination MAC            0x2: VLAN Priority            0x3: MPLS Label            0x4: MPLS Traffic Class            0x5: MPLS TTL            0x6: IPv4/IPv6 DSCP            0x7: IPv4 TTL/IPv6 Hop Limit</p>
54:39	WA_SET_FIELD1_DATA	<p>Field Data 1.            When WA_SET_FIELD0_TYPE =            0x1: [15:0] as index to L2 table for retrieving DMAC            0x2: [2:0] as VLAN Priority            0x3: [10:0] as index to MPLS ENCAP. table            0x4: [2:0] as MPLS Traffic Class            0x5: [7:0] as MPLS TTL            0x6: [5:0] as IPv4/IPv6 DSCP            0x7: [7:0] as IPv4 TTL/IPv6 Hop Limit</p>
38:36	WA_SET_FIELD2_TYPE	<p>Apply packet modification for specified Field Type.            0x0: disable            0x1: VLAN ID            0x2: VLAN Priority            0x3: MPLS Label            0x4: MPLS Traffic Class            0x5: MPLS TTL            0x6: IPv4/IPv6 DSCP            0x7: IPv4 TTL/IPv6 Hop Limit</p>
35:24	WA_SET_FIELD2_DATA	<p>Field Data 2.            When WA_SET_FIELD0_TYPE =            0x1: [11:0] as VLAN ID            0x2: [2:0] as VLAN Priority            0x3: [10:0] as index to MPLS ENCAP. table            0x4: [2:0] as MPLS Traffic Class            0x5: [7:0] as MPLS TTL            0x6: [5:0] as IPv4/IPv6 DSCP            0x7: [7:0] as IPv4 TTL/IPv6 Hop Limit</p>

<b>Bits</b>	<b>Field</b>	<b>Description</b>
23	WA_SET_Q	Set Queue ID for the packet.
22:20	WA_SET_Q_DATA	Queue ID.
19:16	WA_OUTPUT_TYPE	Output type. 0x0: disable 0x1: physical port with source port filter 0x2: physical port without source port filter 0x3: trunk port with source port filter 0x4: trunk port without source port filter 0x5: multiple egress ports (with source port and trunk filter) 0x6: packet's ingress physical port 0x7: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x8: loopback to transitional pipeline 0x9: tunnel interface 0xA-0xF: reserved
15:4	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [9:6] as device ID, [5:0] as physical egress port 0x3: [6:0] as trunk ID 0x4: [6:0] as trunk ID 0x5: [11:0] as index to portmask table 0x9: [8:0] as index to tunnel start table
3:0	RESERVED	

## FT\_EGR

TABLE TYPE : 0x0

TABLE INDEX : 0 - 4095

Egress Flow Table 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
543:528	FIELD_13	FIELD 13 data
527:512	FIELD_12	FIELD 12 data
511:496	FIELD_11	FIELD 11 data
495:480	FIELD_10	FIELD 10 data
479:464	FIELD_9	FIELD 9 data
463:448	FIELD_8	FIELD 8 data
447:432	FIELD_7	FIELD 7 data
431:416	FIELD_6	FIELD 6 data
415:400	FIELD_5	FIELD 5 data
399:384	FIELD_4	FIELD 4 data
383:368	FIELD_3	FIELD 3 data
367:352	FIELD_2	FIELD 2 data
351:336	FIELD_1	FIELD 1 data
335:320	FIELD_0	FIELD 0 data
319	MGNT_VLAN	Mangement VLAN
318	DEV_MAC	Packet's DA hit device MAC.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
317	NOT_FIRST_FRAG	IPv4/IPv6 fragment offset != 0
316:314	FRAME_TYPE_L4	L4 Protocol: 0b000: UDP 0b001: TCP 0b010: ICMP/ICMPv6 0b011: IGMP 0b100: MLD 0b101: SCTP 0b110: L4 Unknown 0b111: Reserved
313:312	FRAME_TYPE	Frame Type: 0b00: ARP 0b01: L2 only 0b10: IPv4 0b11: IPv6
311	OTAG_FMT	Outer Tag Format 0b1: untag/priority tag 0b0: tag
310	ITAG_FMT	Inner Tag Format 0b1: untag/priority tag 0b0: tag
309	OTAG_EXIST	Packet with outer tag
308	ITAG_EXIST	Packet with inner tag
307:306	FRAME_TYPE_L2	L2 Frame Type 0b00: Reserved 0b01: Ethernet 0b10: LLC_Other 0b11: LLC_SNAP
305	NORMAL_PORT	Egress normal port which exclude CPU port and stacking port.
304	TID	Template ID
303:288	BMSK_FIELD_13	Bitmask field 13
287:272	BMSK_FIELD_12	Bitmask field 12
271:256	BMSK_FIELD_11	Bitmask field 11
255:240	BMSK_FIELD_10	Bitmask field 10
239:224	BMSK_FIELD_9	Bitmask field 9
223:208	BMSK_FIELD_8	Bitmask field 8
207:192	BMSK_FIELD_7	Bitmask field 7
191:176	BMSK_FIELD_6	Bitmask field 6
175:160	BMSK_FIELD_5	Bitmask field 5
159:144	BMSK_FIELD_4	Bitmask field 4
143:128	BMSK_FIELD_3	Bitmask field 3
127:112	BMSK_FIELD_2	Bitmask field 2
111:96	BMSK_FIELD_1	Bitmask field 1
95:80	BMSK_FIELD_0	Bitmask field 0
79	BMSK_MGMT_VLAN	Bitmask Mangement VLAN
78	BMSK_DEVMAC	Bitmask DMAC hit device MAC
77	BMSK_NOT_FIRST_FRAG	Bitmask IPv4/IPv6 fragment offset != 0
76:74	BMSK_FRAME_TYPE_L4	Bitmask L4 protocol
73:72	BMSK_FRAME_TYPE	Bitmask Frame Type

<b>Bits</b>	<b>Field</b>	<b>Description</b>
71	BMSK_OTAG_FMT	Bitmask Outer Tag Format
70	BMSK_ITAG_FMT	Bitmask Inner Tag Format
69	BMSK_OTAG_EXIST	Bitmask Packet with outer tag
68	BMSK_ITAG_EXIST	Bitmask Packet with inner tag
67:66	BMSK_FRAME_TYPE_L2	Bitmask L2 Frame Type
65	BMSK_NML_PORT	Bitmask normal port
64	BMSK_TID	Bitmask Template ID
63	VALID	Valid bit
62	REVERSE	Reverse the matching result
61	RESERVED	
60	AGGR_2	aggregate the result of two entries resided in consecutive blocks (block 0&1, 2&3, ..., 2n&2n+1)
59:58	RESERVED	
57	INS_WRITE_ACT	Write action instruction mask
56	RESERVED	
55	INS_METER	Meter instruction mask
54:46	METER_IDX	Meter index
45	METER_YELLOW_ACT	Take the action if packet is colored to Yellow. 0b0: drop 0b1: DSCP remark
44	METER_RED_ACT	Take the action if packet is colored to Red. 0b0: drop 0b1: DSCP remark
43	WA_POP_VLAN	Pop the outermost VLAN Tag from the packet.
42	WA_PUSH_VLAN	Push VLAN tag to the packet.
41:40	WA_VLAN_EHTYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).
39:38	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN Priority 0x2: IPv4/IPv6 DSCP 0x3: IPv4 Flag Reserved Bit
		Note: all the modification should apply to the outermost possible header.
37:32	WA_SET_FIELD0_DATA	Field Data 0. When WA_SET_FIELD0_TYPE = 0x1: [2:0] as VLAN Priority 0x2: [5:0] as IPv4/IPv6 DSCP
31	WA_SET_FIELD1_TYPE	Apply packet modification for specified Field Type. 0b0: disable 0b1: VLAN ID
30:19	WA_SET_FIELD1_DATA	Field Data 1. When WA_SET_FIELD0_TYPE = 0b1: [11:0] as VLAN ID
18	WA_OUTPUT_TYPE	Output type. 0b0: forward 0b1: drop
17:0	RESERVED	

## FT\_IGR

TABLE TYPE : 0x0

TABLE INDEX : 0 - 4095

Ingress Flow Table 0 and 1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
703:688	FIELD_13	FIELD 13 data
687:672	FIELD_12	FIELD 12 data
671:656	FIELD_11	FIELD 11 data
655:640	FIELD_10	FIELD 10 data
639:624	FIELD_9	FIELD 9 data
623:608	FIELD_8	FIELD 8 data
607:592	FIELD_7	FIELD 7 data
591:576	FIELD_6	FIELD 6 data
575:560	FIELD_5	FIELD 5 data
559:544	FIELD_4	FIELD 4 data
543:528	FIELD_3	FIELD 3 data
527:512	FIELD_2	FIELD 2 data
511:496	FIELD_1	FIELD 1 data
495:480	FIELD_0	FIELD 0 data
479	MGNT_VLAN	Mangement VLAN
478	DEV_MAC	For Ingress Full Match Flow Table 0, the field means Content is longer than parser limitation. For Ingress Full Match Flow Table 1, the field means Packet's DA hit device MAC.
477	NOT_FIRST_FRAG	IPv4/IPv6 fragment offset != 0
476:474	FRAME_TYPE_L4	L4 Protocol: 0b000: UDP 0b001: TCP 0b010: ICMP/ICMPv6 0b011: IGMP 0b100: MLD 0b101: SCTP 0b110: L4 Unknown 0b111: Reserved
473:472	FRAME_TYPE	Frame Type: 0b00: ARP 0b01: L2 only 0b10: IPv4 0b11: IPv6
471	OTAG_FMT	Outer Tag Format 0b1: untag/priority tag 0b0: tag
470	ITAG_FMT	Inner Tag Format 0b1: untag/priority tag 0b0: tag
469	OTAG_EXIST	Packet with outer tag
468	ITAG_EXIST	Packet with inner tag

<b>Bits</b>	<b>Field</b>	<b>Description</b>
467:466	FRAME_TYPE_L2	L2 Frame Type 0b00: Reserved 0b01: Ethernet 0b10: LLC_Other 0b11: LLC_SNAP
465	NORMAL_PORT	Ingress normal port which exclude CPU port and stacking port.
464	TID	Template ID
463:448	BMSK_FIELD_13	Bitmask field 13
447:432	BMSK_FIELD_12	Bitmask field 12
431:416	BMSK_FIELD_11	Bitmask field 11
415:400	BMSK_FIELD_10	Bitmask field 10
399:384	BMSK_FIELD_9	Bitmask field 9
383:368	BMSK_FIELD_8	Bitmask field 8
367:352	BMSK_FIELD_7	Bitmask field 7
351:336	BMSK_FIELD_6	Bitmask field 6
335:320	BMSK_FIELD_5	Bitmask field 5
319:304	BMSK_FIELD_4	Bitmask field 4
303:288	BMSK_FIELD_3	Bitmask field 3
287:272	BMSK_FIELD_2	Bitmask field 2
271:256	BMSK_FIELD_1	Bitmask field 1
255:240	BMSK_FIELD_0	Bitmask field 0
239	BMSK_MGMT_VLAN	Bitmask Mangement VLAN
238	BMSK_DEVMAC	Bitmask DMAC hit device MAC
237	BMSK_NOT_FIRST_FRAG	Bitmask IPv4/IPv6 fragment offset != 0
236:234	BMSK_FRAME_TYPE_L4	Bitmask L4 protocol
233:232	BMSK_FRAME_TYPE	Bitmask Frame Type
231	BMSK_OTAG_FMT	Bitmask Outer Tag Format
230	BMSK_ITAG_FMT	Bitmask Inner Tag Format
229	BMSK_OTAG_EXIST	Bitmask Packet with outer tag
228	BMSK_ITAG_EXIST	Bitmask Packet with inner tag
227:226	BMSK_FRAME_TYPE_L2	Bitmask L2 Frame Type
225	BMSK_NML_PORT	Bitmask normal port
224	BMSK_TID	Bitmask Template ID
223	VALID	Valid bit
222	REVERSE	Reverse the matching result
221	AGGR_1	aggregate the result of two consecutive rules (entry 0&1, 2&3, ..., 2n&2n+1) in the same block
220	AGGR_2	aggregate the result of two entries resided in consecutive blocks (block 0&1, 2&3, ..., 2n&2n+1)
219	INS_GOTO_TBL	Goto table instruction mask
218	INS_WRITE_METADATA	Write Metadata instruction mask
217	INS_WRITE_ACT	Write action instruction mask
216	INS_CLR_ACT	Clear action instruction mask
215	INS_METER	Meter instruction mask

<b>Bits</b>	<b>Field</b>	<b>Description</b>
214:206	METER_IDX	Meter index
205	METER_YELLOW_ACT	Take the action if packet is colored to Yellow. 0b0: drop 0b1: DSCP remark
204	METER_RED_ACT	Take the action if packet is colored to Red. 0b0: drop 0b1: DSCP remark
203	WA_COPY_TTL_INWARD	Apply copy TTL inward to the packet.
202	WA_POP_VLAN	Pop the outermost VLAN Tag from the packet.
201	WA_POP_MPLS	Pop MPLS label from the packet. 0b0: disable 0b1: enable
200	WA_POP_MPLS_TYPE	Pop MPLS label from the packet. 0b0: pop outermost label 0b1: pop double label
199	WA_PUSH_MPLS	Push MPLS label to the packet. 0b0: disable 0b1: enable All the WA_PUSH_MPLS_XXX fields following are used only if WA_PUSH_MPLS=enable.
198	WA_PUSH_MPLS_MODE	0b0: push MPLS label to the packet 0b1: push/swap MPLS label according to WA_MPLS_LIB_IDX
197	WA_MPLS_VPN_TYPE	0b0: L3 VPN 0b1: L2 VPN
196:186	WA_MPLS_LIB_IDX	The field is used when WA_PUSH_MPLS=0x2. Index to MPLS Encap. table for retrieving label related info.
185	WA_MPLS_EHTYPE	The field is used when WA_PUSH_MPLS!=disable. 0b0: 0x8847 0b1: 0x8848
184	WA_PUSH_VLAN	Push VLAN tag to the packet.
183:182	WA_VLAN_EHTYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).
181	WA_COPY_TTL_OUTWARD	Apply copy TTL outward to the packet.
180	WA_DEC_MPLS_TTL	Decrement the outermost MPLS TTL from the packet.
179	WA_DEC_IP_TTL	Decrement the outermost IPv4 TTL/IPv6 Hop Limit from the packet.
178:176	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: Source MAC 0x2: VLAN Priority 0x3: MPLS Traffic Class 0x4: MPLS TTL 0x5: IPv4/IPv6 DSCP 0x6: IPv4 TTL/IPv6 Hop Limit 0x7: IPv4 Flag Reserved Bit
		Note: all the modification should apply to the outermost possible header.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
175:166	WA_SET_FIELD0_DATA	<p>Field Data 0.</p> <p>When WA_SET_FIELD0_TYPE =</p> <ul style="list-style-type: none"> <li>0x1: [9:0] as index to Egress L3 INTF Table for retrieving SMAC</li> <li>0x2: [2:0] as VLAN Priority</li> <li>0x3: [2:0] as MPLS Traffic Class</li> <li>0x4: [7:0] as MPLS TTL</li> <li>0x5: [5:0] as IPv4/IPv6 DSCP</li> <li>0x6: [7:0] as IPv4 TTL/IPv6 Hop Limit</li> </ul>
165:163	WA_SET_FIELD1_TYPE	<p>Apply packet modification for specified Field Type.</p> <ul style="list-style-type: none"> <li>0x0: disable</li> <li>0x1: Destination MAC</li> <li>0x2: VLAN Priority</li> <li>0x3: MPLS Label</li> <li>0x4: MPLS Traffic Class</li> <li>0x5: MPLS TTL</li> <li>0x6: IPv4/IPv6 DSCP</li> <li>0x7: IPv4 TTL/IPv6 Hop Limit</li> </ul>
162:147	WA_SET_FIELD1_DATA	<p>Field Data 1.</p> <p>When WA_SET_FIELD0_TYPE =</p> <ul style="list-style-type: none"> <li>0x1: [15:0] as index to L2 table for retrieving DMAC</li> <li>0x2: [2:0] as VLAN Priority</li> <li>0x3: [10:0] as index to MPLS ENCAP. table</li> <li>0x4: [2:0] as MPLS Traffic Class</li> <li>0x5: [7:0] as MPLS TTL</li> <li>0x6: [5:0] as IPv4/IPv6 DSCP</li> <li>0x7: [7:0] as IPv4 TTL/IPv6 Hop Limit</li> </ul>
146:144	WA_SET_FIELD2_TYPE	<p>Apply packet modification for specified Field Type.</p> <ul style="list-style-type: none"> <li>0x0: disable</li> <li>0x1: VLAN ID</li> <li>0x2: VLAN Priority</li> <li>0x3: MPLS Label</li> <li>0x4: MPLS Traffic Class</li> <li>0x5: MPLS TTL</li> <li>0x6: IPv4/IPv6 DSCP</li> <li>0x7: IPv4 TTL/IPv6 Hop Limit</li> </ul>
143:132	WA_SET_FIELD2_DATA	<p>Field Data 2.</p> <p>When WA_SET_FIELD0_TYPE =</p> <ul style="list-style-type: none"> <li>0x1: [11:0] as VLAN ID</li> <li>0x2: [2:0] as VLAN Priority</li> <li>0x3: [10:0] as index to MPLS ENCAP. table</li> <li>0x4: [2:0] as MPLS Traffic Class</li> <li>0x5: [7:0] as MPLS TTL</li> <li>0x6: [5:0] as IPv4/IPv6 DSCP</li> <li>0x7: [7:0] as IPv4 TTL/IPv6 Hop Limit</li> </ul>
131:129	WA_SET_FIELD3_TYPE	<p>Apply packet modification for specified Field Type.</p> <ul style="list-style-type: none"> <li>0x0: disable</li> <li>0x1: Source IPv4 Address</li> <li>0x2: Destination IPv4 Address</li> <li>0x3: VLAN Priority</li> <li>0x4: MPLS Label</li> <li>0x5: MPLS Traffic Class</li> <li>0x6: MPLS TTL</li> <li>0x7: IPv4/IPv6 DSCP</li> </ul>

<b>Bits</b>	<b>Field</b>	<b>Description</b>
128:97	WA_SET_FIELD3_DATA	Field Data 3. When WA_SET_FIELD3_TYPE = 0x1: [31:0] as Source IPv4 Address 0x2: [31:0] as Destination IPv4 Address 0x3: [2:0] as VLAN Priority 0x4: [10:0] as index to MPLS ENCAP. table 0x5: [2:0] as MPLS Traffic Class 0x6: [7:0] as MPLS TTL 0x7: [5:0] as IPv4/IPv6 DSCP
96:94	WA_SET_FIELD4_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: L4 Source Port 0x2: L4 Destination Port 0x3: VLAN Priority 0x4: MPLS Label 0x5: MPLS Traffic Class 0x6: MPLS TTL 0x7: IPv4/IPv6 DSCP
93:78	WA_SET_FIELD4_DATA	Field Data 4. When WA_SET_FIELD4_TYPE = 0x1: [15:0] as TCP/UDP Source Port 0x2: [15:0] as TCP/UDP Destination Port 0x3: [2:0] as VLAN Priority 0x4: [10:0] as index to MPLS ENCAP. table 0x5: [2:0] as MPLS Traffic Class 0x6: [7:0] as MPLS TTL 0x7: [5:0] as IPv4/IPv6 DSCP
77	WA_SET_Q	Set Queue ID for the packet.
76:74	WA_SET_Q_DATA	Queue ID.
73	WA_GROUP	Apply group action to the packet.
72:62	WA_GROUP_IDX	Index to Group table for retrieving the group entry.
61:58	WA_OUTPUT_TYPE	Output type. 0x0: disable 0x1: physical port with source port filter 0x2: physical port without source port filter 0x3: trunk port with source port filter 0x4: trunk port without source port filter 0x5: multiple egress ports (with source port and trunk filter) 0x6: packet's ingress physical port 0x7: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x8: loopback to transitional pipeline 0x9: tunnel interface 0xA-0xF: reserved
57:46	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [9:6] as device ID, [5:0] as physical egress port 0x3: [6:0] as trunk ID 0x4: [6:0] as trunk ID 0x5: [11:0] as index to portmask table 0x9: [8:0] as index to tunnel start table
45:34	METADATA	Metadata
33:22	METADATA_MSK	Metadata Mask

<b>Bits</b>	<b>Field</b>	<b>Description</b>
21:20	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
19:18	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
17:12	GOTO_TBL_LB_TIME	Target loopback time
11:0	RESERVED	

## FT\_L2\_HASH\_FMT0\_0

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Hash-based Flow Table Format 0 for Entry N

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry Format. 0b0:Ethernet/WLAN 0b1:OpenFlow
125:124	TYPE	Entry Type. 0x0: SMAC 0x1: DMAC 0x2: SMAC+DMAC 0x3: 5-TUPLE
123:112	VID	VLAN ID
111:64	MAC	MAC Address depends on TYPE
63	RESERVED	
62	METADATA_CMP	Whether to compare Metadata
61:56	METADATA_KEY	Metadata match field
55	INS_GOTO_TBL	Goto table instruction mask
54	INS_WRITE_METADATA	Write Metadata instruction mask
53	INS_WRITE_ACT	Write action instruction mask
52	INS_CLR_ACT	Clear action instruction mask
51:50	WA_VLAN_EHTHYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).
49:48	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN ID 0x2: IPv4/IPv6 DSCP 0x3: reserved
		Note: all the modification should apply to the outermost possible header.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
47:36	WA_SET_FIELD0_DATA	Field Data 0. When WA_SET_FIELD0_TYPE = 0x1: [11:0] as VLAN ID 0x2: [5:0] as IPv4/IPv6 DSCP
35	WA_SET_FIELD1_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN Priority
34:32	WA_SET_FIELD1_DATA	VLAN Priority.
31	WA_PUSH_VLAN	Push VLAN tag to the packet.
30	WA_SET_Q	Set Queue ID for the packet.
29:27	WA_SET_Q_DATA	Queue ID.
26:25	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
24:23	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopback. GOTO_TBL_ACT=2, the next table ID after loopback.
22:17	GOTO_TBL_LB_TIME	Target loopback time
16	HIT_STS	Hit state
15:0	RESERVED	

### **FT\_L2\_HASH\_FMT0\_1**

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Hash-based Flow Table Format 0 for Entry N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry Format. 0b0:Ethernet/WLAN 0b1:OpenFlow
125:124	TYPE	Entry Type. 0x0: SMAC 0x1: DMAC 0x2: SMAC+DMAC 0x3: 5-TUPLE
123:112	VID	VLAN ID
111:64	MAC	MAC Address depends on TYPE
63	RESERVED	
62	METADATA_CMP	Whether to compare Metadata
61:56	METADATA_KEY	Metadata match field

<b>Bits</b>	<b>Field</b>	<b>Description</b>
55:53	WA_OUTPUT_TYPE	Output type. 0x0: disable 0x1: physical port with source port filter 0x2: trunk port with source port filter 0x3: multiple egress ports (with source port and trunk filter) 0x4: packet's ingress physical port 0x5: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x6: loopback to transitional pipeline 0x7: tunnel interface
52:41	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [6:0] as trunk ID 0x3: [11:0] as index to portmask table 0x7: [8:0] as index to tunnel start table
40:29	METADATA	Metadata
28:17	METADATA_MSK	Metadata Mask
16:0	RESERVED	

### FT\_L2\_HASH\_FMT1\_0

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Hash-based Flow Table Format 1(5-Tuple IP Protocol) for Entry N

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry Format. 0b0:Ethernet/WLAN 0b1:OpenFlow
125:124	TYPE	Entry Type. 0x0: SMAC 0x1: DMAC 0x2: SMAC+DMAC 0x3: 5-TUPLE
123:112	RESERVED	
111:80	SIP	IPv4 source address
79:64	L4SPORT	L4 source port
63	TUPLE_TYPE	5-Tuple Type 0x0: IP Protocol 0x1: VID
62:56	IPPROTO_7_1	IP Protocol[7:1]
55	INS_GOTO_TBL	Goto table instruction mask
54	INS_WRITE_METADATA	Write Metadata instruction mask
53	INS_WRITE_ACT	Write action instruction mask
52	INS_CLR_ACT	Clear action instruction mask
51:50	WA_VLAN_EHTHYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).

<b>Bits</b>	<b>Field</b>	<b>Description</b>
49:48	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN ID 0x2: IPv4/IPv6 DSCP 0x3: reserved  Note: all the modification should apply to the outermost possible header.
47:36	WA_SET_FIELD0_DATA	Field Data 0. When WA_SET_FIELD0_TYPE = 0x1: [11:0] as VLAN ID 0x2: [5:0] as IPv4/IPv6 DSCP
35	WA_SET_FIELD1_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN Priority
34:32	WA_SET_FIELD1_DATA	VLAN Priority.
31	WA_PUSH_VLAN	Push VLAN tag to the packet.
30	WA_SET_Q	Set Queue ID for the packet.
29:27	WA_SET_Q_DATA	Queue ID.
26:25	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
24:23	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
22:17	GOTO_TBL_LB_TIME	Target loopback time
16	HIT_STS	Hit state
15:0	RESERVED	

### FT\_L2\_HASH\_FMT1\_1

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Hash-based Flow Table Format 1(5-Tuple IP Protocol) for Entry N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry Format. 0b0:Ethernet/WLAN 0b1:OpenFlow
125:124	TYPE	Entry Type. 0x0: SMAC 0x1: DMAC 0x2: SMAC+DMAC 0x3: 5-TUPLE

<b>Bits</b>	<b>Field</b>	<b>Description</b>
123:112	RESERVED	
111:80	DIP	IPv4 destination address
79:64	L4DPORT	L4 destination port
63	TUPLE_TYPE	5-Tuple Type 0x0: IP Protocol 0x1: VID
62	IPPROTO_0	IP Protocol[0]
61:56	RESERVED	
55:53	WA_OUTPUT_TYPE	Output type. 0x0: disable 0x1: physical port with source port filter 0x2: trunk port with source port filter 0x3: multiple egress ports (with source port and trunk filter) 0x4: packet's ingress physical port 0x5: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x6: loopback to transitional pipeline 0x7: tunnel interface
52:41	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [6:0] as trunk ID 0x3: [11:0] as index to portmask table 0x7: [8:0] as index to tunnel start table
40:29	METADATA	Metadata
28:17	METADATA_MSK	Metadata Mask
16:0	RESERVED	

### FT\_L2\_HASH\_FMT2\_0

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Hash-based Flow Table Format 2(5-Tuple VID) for Entry N

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry Format. 0b0:Ethernet/WLAN 0b1:OpenFlow
125:124	TYPE	Entry Type. 0x0: SMAC 0x1: DMAC 0x2: SMAC+DMAC 0x3: 5-TUPLE
123:112	RESERVED	
111:80	SIP	IPv4 source address
79:64	L4SPORT	L4 source port
63	TUPLE_TYPE	5-Tuple Type 0x0: IP Protocol 0x1: VID

<b>Bits</b>	<b>Field</b>	<b>Description</b>
62:56	VID_11_5	VID[11:5]
55	INS_GOTO_TBL	Goto table instruction mask
54	INS_WRITE_METADATA	Write Metadata instruction mask
53	INS_WRITE_ACT	Write action instruction mask
52	INS_CLR_ACT	Clear action instruction mask
51:50	WA_VLAN_EHTHTYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).
49:48	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN ID 0x2: IPv4/IPv6 DSCP 0x3: reserved
		Note: all the modification should apply to the outermost possible header.
47:36	WA_SET_FIELD0_DATA	Field Data 0. When WA_SET_FIELD0_TYPE = 0x1: [11:0] as VLAN ID 0x2: [5:0] as IPv4/IPv6 DSCP
35	WA_SET_FIELD1_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN Priority
34:32	WA_SET_FIELD1_DATA	VLAN Priority.
31	WA_PUSH_VLAN	Push VLAN tag to the packet.
30	WA_SET_Q	Set Queue ID for the packet.
29:27	WA_SET_Q_DATA	Queue ID.
26:25	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
24:23	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
22:17	GOTO_TBL_LB_TIME	Target loopback time
16	HIT_STS	Hit state
15:0	RESERVED	

## FT\_L2\_HASH\_FMT2\_1

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Hash-based Flow Table Format 1(5-Tuple VID) for Entry N+1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry Format. 0b0:Ethernet/WLAN 0b1:OpenFlow
125:124	TYPE	Entry Type. 0x0: SMAC 0x1: DMAC 0x2: SMAC+DMAC 0x3: 5-TUPLE
123:112	RESERVED	
111:80	DIP	IPv4 destination address
79:64	L4DPORT	L4 destination port
63	TUPLE_TYPE	5-Tuple Type 0x0: IP Protocol 0x1: VID
62:58	VID_4_0	VID[4:0]
57:56	RESERVED	
55:53	WA_OUTPUT_TYPE	Output type. 0x0: disable 0x1: physical port with source port filter 0x2: trunk port with source port filter 0x3: multiple egress ports (with source port and trunk filter) 0x4: packet's ingress physical port 0x5: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x6: loopback to transitional pipeline 0x7: tunnel interface
52:41	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [6:0] as trunk ID 0x3: [11:0] as index to portmask table 0x7: [8:0] as index to tunnel start table
40:29	METADATA	Metadata
28:17	METADATA_MSK	Metadata Mask
16:0	RESERVED	

## FT\_L3\_HASH\_0

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

L3 Hash-based Flow Table format 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry format. 0b0: L3 0b1: OpenFlow

<b>Bits</b>	<b>Field</b>	<b>Description</b>
125	IP_VER	IP version 0b0: IPv4 0b1: IPv6
124:123	TYPE	L3 flow entry type. 0x0: SIP 0x1: DIP 0x2: SIP+DIP 0x3: reserved
122:91	IP	IPv4/IPv6[31:0] address
90	MD_CMP	Whether to compare Metadata
89:78	METADATA_KEY	Metadata match field
77:75	RESERVED	
74	INS_GOTO_TBL	Goto table instruction mask
73	INS_WRITE_METADATA	Write Metadata instruction mask
72	INS_WRITE_ACT	Write action instruction mask
71	INS_CLR_ACT	Clear action instruction mask
70	WA_PUSH_VLAN	Push VLAN tag to the packet.
69:68	WA_VLAN_EHTYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).
67	WA_DEC_IP_TTL	Decrement the outermost IPv4 TTL/IPv6 Hop Limit from the packet.
66:65	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: Source MAC 0x2: VLAN Priority 0x3: IPv4/IPv6 DSCP
		Note: all the modification should apply to the outermost possible header.
64:55	WA_SET_FIELD0_DATA	Field Data 0. When WA_SET_FIELD0_TYPE = 0x1: [9:0] as index to Egress L3 INTF Table for retrieving SMAC 0x2: [2:0] as VLAN Priority 0x3: [5:0] as IPv4/IPv6 DSCP
54:53	WA_SET_FIELD1_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: Destination MAC 0x2: VLAN Priority 0x3: IPv4/IPv6 DSCP
52:37	WA_SET_FIELD1_DATA	Field Data 1. When WA_SET_FIELD0_TYPE = 0x1: [14:0] as index to L2 table for retrieving DMAC 0x2: [2:0] as VLAN Priority 0x3: [5:0] as IPv4/IPv6 DSCP
36:35	WA_SET_FIELD2_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN ID 0x2: VLAN Priority 0x3: IPv4/IPv6 DSCP
34:23	WA_SET_FIELD2_DATA	Field Data 2. When WA_SET_FIELD0_TYPE = 0x1: [11:0] as VLAN ID 0x2: [2:0] as VLAN Priority 0x3: [5:0] as IPv4/IPv6 DSCP
22:17	GOTO_TBL_LB_TIME	Target loopback time

<b>Bits</b>	<b>Field</b>	<b>Description</b>
16	WA_SET_Q	Set Queue ID for the packet.
15	HIT_STS	Hit state.
14:0	RESERVED	

### FT\_L3\_HASH\_1

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

L3 Hash-based Flow Table format 1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry format. 0b0: L3 0b1: OpenFlow
125	IP_VER	IP version 0b0: IPv4 0b1: IPv6
124:123	TYPE	L3 flow entry type. 0x0: SIP 0x1: DIP 0x2: SIP+DIP 0x3: reserved
122:91	IP	IPv4 address
90	MD_CMP	Whether to compare Metadata
89:78	METADATA_KEY	Metadata match field
77:75	RESERVED	
74	WA_GROUP	Apply group action to the packet.
73:63	WA_GROUP_IDX	Index to Group table for retrieving the group entry.
62:59	WA_OUTPUT_TYPE	0x0: disable 0x1: physical port with source port filter 0x2: physical port without source port filter 0x3: trunk port with source port filter 0x4: trunk port without source port filter 0x5: multiple egress ports (with source port and trunk filter) 0x6: packet's ingress physical port 0x7: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x8: loopback to transitional pipeline 0x9: tunnel interface 0xA-0xF: reserved
58:47	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [9:6] as device ID, [5:0] as physical egress port 0x3: [6:0] as trunk ID 0x4: [6:0] as trunk ID 0x5: [11:0] as index to portmask table 0x9: [8:0] as index to tunnel start table
46:35	METADATA	Metadata

<b>Bits</b>	<b>Field</b>	<b>Description</b>
34:23	METADATA_MSK	Metadata Mask
22:21	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
20:19	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
18:16	WA_SET_Q_DATA	Queue ID.
15:0	RESERVED	

## FT\_L3\_HASH\_2

TABLE TYPE : 0x3

TABLE INDEX : 0 - 12287

L3 Hash-based Flow Table format 2

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	VALID	Valid bit
126	FMT	Entry format. 0b0: L3 0b1: OpenFlow
125	IP_VER	IP version 0b0: IPv4 0b1: IPv6
124:123	TYPE	L3 flow entry type. 0x0: SIP 0x1: DIP 0x2: SIP+DIP 0x3: reserved
122:75	IP	IPv6[127:80]/IPv6[79:32] address
74	WA_GROUP	Apply group action to the packet.
73:63	WA_GROUP_IDX	Index to Group table for retrieving the group entry.
62:59	WA_OUTPUT_TYPE	0x0: disable 0x1: physical port with source port filter 0x2: physical port without source port filter 0x3: trunk port with source port filter 0x4: trunk port without source port filter 0x5: multiple egress ports (with source port and trunk filter) 0x6: packet's ingress physical port 0x7: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x8: loopback to transitional pipeline 0x9: tunnel interface 0xA-0xF: reserved

<b>Bits</b>	<b>Field</b>	<b>Description</b>
58:47	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [9:6] as device ID, [5:0] as physical egress port 0x3: [6:0] as trunk ID 0x4: [6:0] as trunk ID 0x5: [11:0] as index to portmask table 0x9: [8:0] as index to tunnel start table
46:35	METADATA	Metadata
34:23	METADATA_MSK	Metadata Mask
22:21	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
20:19	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
18:16	WA_SET_Q_DATA	Queue ID.
15:0	RESERVED	

### **FT\_L3\_TCAM\_0**

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

L3 TCAM-based Flow Table format 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	Entry format. 0b0: L3 0b1: OpenFlow
189	IP_VER	IP version 0b0: IPv4 0b1: IPv6
188:187	TYPE	L3 flow entry type. 0x0: SIP 0x1: DIP 0x2: SIP+DIP 0x3: reserved
186:155	IP	IPv4/IPv6[31:0] address
154:143	METADATA_KEY	Metadata match field
142:139	RESERVED	
138	BMSK_FMT	Bitmask entry format
137	BMSK_IP_VER	Bitmask IP version
136:135	BMSK_TYPE	Bitmask L3 flow entry Type
134:103	BMSK_IP	Bitmask IP Address

<b>Bits</b>	<b>Field</b>	<b>Description</b>
102:91	BMSK_METADATA	Bitmask Metadata
90:87	RESERVED	
86	INS_GOTO_TBL	Goto table instruction mask
85	INS_WRITE_METADATA	Write Metadata instruction mask
84	INS_WRITE_ACT	Write action instruction mask
83	INS_CLR_ACT	Clear action instruction mask
82	WA_PUSH_VLAN	Push VLAN tag to the packet.
81:80	WA_VLAN_EHTHTYPE	The field is used when WA_PUSH_VLAN=enable. Index to inner TPID pool (VLAN_TAG_TPID_CTRL.ITPID).
79	WA_DEC_IP_TTL	Decrement the outermost IPv4 TTL/IPv6 Hop Limit from the packet.
78:77	WA_SET_FIELD0_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: Source MAC 0x2: VLAN Priority 0x3: IPv4/IPv6 DSCP
		Note: all the modification should apply to the outermost possible header.
76:67	WA_SET_FIELD0_DATA	Field Data 0. When WA_SET_FIELD0_TYPE = 0x1: [9:0] as index to Egress L3 INTF Table for retrieving SMAC 0x2: [2:0] as VLAN Priority 0x3: [5:0] as IPv4/IPv6 DSCP
66:65	WA_SET_FIELD1_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: Destination MAC 0x2: VLAN Priority 0x3: IPv4/IPv6 DSCP
64:49	WA_SET_FIELD1_DATA	Field Data 1. When WA_SET_FIELD0_TYPE = 0x1: [15:0] as index to L2 table for retrieving DMAC 0x2: [2:0] as VLAN Priority 0x3: [5:0] as IPv4/IPv6 DSCP
48:47	WA_SET_FIELD2_TYPE	Apply packet modification for specified Field Type. 0x0: disable 0x1: VLAN ID 0x2: VLAN Priority 0x3: IPv4/IPv6 DSCP
46:35	WA_SET_FIELD2_DATA	Field Data 2. When WA_SET_FIELD0_TYPE = 0x1: [11:0] as VLAN ID 0x2: [2:0] as VLAN Priority 0x3: [5:0] as IPv4/IPv6 DSCP
34:29	GOTO_TBL_LB_TIME	Target loopback time
28	WA_SET_Q	Set Queue ID for the packet.
27	HIT_STS	Hit state
26:0	RESERVED	

### FT\_L3\_TCAM\_1

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

## L3 TCAM-based Flow Table format 1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	Entry format. 0b0: L3 0b1: OpenFlow
189	IP_VER	IP version 0b0: IPv4 0b1: IPv6
188:187	TYPE	L3 flow entry type. 0x0: SIP 0x1: DIP 0x2: SIP+DIP 0x3: reserved
186:155	IP	IPv4 address
154:143	METADATA_KEY	Metadata match field
142:139	RESERVED	
138	BMSK_FMT	Bitmask entry format
137	BMSK_IP_VER	Bitmask IP version
136:135	BMSK_TYPE	Bitmask L3 flow entry Type
134:103	BMSK_IP	Bitmask IP Address
102:91	BMSK_METADATA	Bitmask Metadata
90:87	RESERVED	
86	WA_GROUP	Apply group action to the packet.
85:75	WA_GROUP_IDX	Index to Group table for retrieving the group entry.
74:71	WA_OUTPUT_TYPE	0x0: disable 0x1: physical port with source port filter 0x2: physical port without source port filter 0x3: trunk port with source port filter 0x4: trunk port without source port filter 0x5: multiple egress ports (with source port and trunk filter) 0x6: packet's ingress physical port 0x7: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x8: loopback to transitional pipeline 0x9: tunnel interface 0xA-0xF: reserved
70:59	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [9:6] as device ID, [5:0] as physical egress port 0x3: [6:0] as trunk ID 0x4: [6:0] as trunk ID 0x5: [11:0] as index to portmask table 0x9: [8:0] as index to tunnel start table
58:47	METADATA	Metadata
46:35	METADATA_MSK	Metadata Mask

<b>Bits</b>	<b>Field</b>	<b>Description</b>
34:33	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
32:31	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
30:28	WA_SET_Q_DATA	Queue ID.
27:0	RESERVED	

## FT\_L3\_TCAM\_2

TABLE TYPE : 0x4

TABLE INDEX : 0 - 12287

L3 TCAM-based Flow Table format 2

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191	VALID	Valid bit
190	FMT	Entry format. 0b0: L3 0b1: OpenFlow
189	IP_VER	IP version 0b0: IPv4 0b1: IPv6
188:187	TYPE	L3 flow entry type. 0x0: SIP 0x1: DIP 0x2: SIP+DIP 0x3: reserved
186:139	IP	IPv6[127:80]/IPv6[79:32] address
138	BMSK_FMT	Bitmask entry format
137	BMSK_IP_VER	Bitmask IP version
136:135	BMSK_TYPE	Bitmask L3 flow entry Type
134:87	BMSK_IP	Bitmask IP Address
86	WA_GROUP	Apply group action to the packet.
85:75	WA_GROUP_IDX	Index to Group table for retrieving the group entry.

Bits	Field	Description
74:71	WA_OUTPUT_TYPE	0x0: disable 0x1: physical port with source port filter 0x2: physical port without source port filter 0x3: trunk port with source port filter 0x4: trunk port without source port filter 0x5: multiple egress ports (with source port and trunk filter) 0x6: packet's ingress physical port 0x7: flood (all ports with source port, trunk, VLAN, Spanning Tree filtering) 0x8: loopback to transitional pipeline 0x9: tunnel interface 0xA-0xF: reserved
70:59	WA_OUTPUT_DATA	When WA_OUTPUT_TYPE = 0x1: [9:6] as device ID, [5:0] as physical egress port 0x2: [9:6] as device ID, [5:0] as physical egress port 0x3: [6:0] as trunk ID 0x4: [6:0] as trunk ID 0x5: [11:0] as index to portmask table 0x9: [8:0] as index to tunnel start table
58:47	METADATA	Metadata
46:35	METADATA_MSK	Metadata Mask
34:33	GOTO_TBL_ACT	0x0: Normal Goto 0x1: Apply current Action Set and loopback packet to OpenFlow pipeline again 0x2: Don't apply current Action Set but just loopback packet to OpenFlow pipeline again 0x3: reserved
32:31	GOTO_TBL_ID	The next table ID. GOTO_TBL_ACT=0, the next table ID. GOTO_TBL_ACT=1, the next table ID after loopbacked. GOTO_TBL_ACT=2, the next table ID after loopbacked.
30:28	WA_SET_Q_DATA	Queue ID.
27:0	RESERVED	

## GROUP\_TBL

TABLE TYPE : 0xA

TABLE INDEX : 0 - 2047

OpenFlow group table

Bits	Field	Description
31	TYPE	Group entry type. 0b0: all 0b1: select
30:24	BUCKET_NUM	Specify action bucket number of the group entry.
23:11	BUCKET_ID	Point to the first action bucket of the group entry.
10:0	RESERVED	

## **OF\_DMAC**

TABLE TYPE : 0xF

TABLE INDEX : 0 - 1023

OpenFlow DMAC table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:16	MAC	Destination MAC address for set filed action.
15:0	RESERVED	

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## CHAPTER 18

# L2 Tunneling

The chapter describes features related to L2 Tunneling

SECTION 18.1

## L2 TUNNEL

L2 Tunnel module

### **L2\_TUNNEL\_CTRL**

REGISTER ADDRESS : 0xBB00CDD0

DEFAULT VALUE : 0xC0F70018

Define forwarding behavior for invalid VXLAN header and VXLAN control packet.

Bits	Field	Description	Type	Default
31:24	VXLAN_GPE_EXCPT_FLAG_S	Defined exception flags for VXLAN-GPE packet	RW	0xC0
23:16	VXLAN_EXCPT_FLAGS	Defined exception flags for VXLAN packet	RW	0xF7
15:14	VXLAN_EXCPT_ACT	Forwarding action for exception packets 0x0: Drop 0x1: Trap (according to TRAP_TARGET) 0x2: Forward 0x3: Copy (Forward + Trap)	RW	0x0
13	VXLAN_CTRL_PKT_ACT	Trap packet with VXLAN Control Packet to CPU: 0b0: Forward 0b1: Trap	RW	0x0
12	VXLAN_INVLD_HDR_ACT	Trap packet with invalid VXLAN header to CPU: 0b0: Drop 0b1: Trap	RW	0x0
11	VXLAN_TRAP_TARGET	Specify the trap target. 0b0: local CPU 0b1: master CPU	RW	0x0
10	VXLAN_VNI_LU_MIS_ACT	{ VTEP + VNI } Lookup miss action 0x0: Drop 0x1: Trap (according to TRAP_TARGET)	RW	0x0
9:0	RESERVED			

### **BSSID**

TABLE TYPE : 0xA

TABLE INDEX : 0 - 2047

## BSSID table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
95	VALID	Valid bit.
94:47	BSSID	BSSID MAC Address [47:0].
46:45	TT_OVID_CMD	When packet is tunnel terminated and payload is 802.3, assign OVID to: 0b00: outer untagged and outer priority tagged packet 0b01: outer untagged packet 0b10: all packet(outer untagged, outer priority-tagged, outer tagged) 0b11: reserved  If payload is 802.11, apply TT_OVID directly.
44:33	TT_OVID	Outer VID assigned to the packet.
32:31	TT_IVID_CMD	When packet is tunnel terminated and payload is 802.3, assign IVID to: 0b00: inner untagged and inner priority tagged packet 0b01: inner untagged packet 0b10: all packet(inner untagged, inner priority-tagged, inner tagged) 0b11: reserved  If payload is 802.11, apply TT_IVID directly.
30:19	TT_IVID	Inner VID assigned to the packet.
18	TT_FWD_VLAN_SEL	Forwarding VLAN which has higher priority than port-based decision. 0b0: use inner VLAN 0b1: use outer VLAN
17:16	TT_PRI_SEL_IDX	Index to the priority selection table.
15:13	TT_INT_PRI	Internal Priority for Tunnel Terminated.
12	RESERVED	
11:3	TS_TUNNEL_IDX	Index to Tunnel Start entry. (for encapsulating)
2:0	RESERVED	

**BSSID\_LIST**

TABLE TYPE : 0xC

TABLE INDEX : 0 - 8191

## BSSID Link List table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
31:20	BSSID_IDX	Index to BSSID entry.
19:7	BSSID_NEXT_IDX	Index to next BSSID list entry.
6:0	RESERVED	

## BSSID\_TUNNEL\_START

TABLE TYPE : 0x3

TABLE INDEX : 0 - 2047

BSSID tunnel start table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127:80	BSSID	BSSID MAC Address [39:0].
79:77	QOS_PROFILE	QoS profile for Tunnel Start.
76	11E_EN	Enable 802.11e QoS Control field encapsulation. The field takes effect if Tunnel Entry[FRAME_FMT]=1.
75:71	RADIO_ID	RID in CAPWAP header for Tunnel Start.
70:7	WIRELESS_INFO	Wireless Specific Info specified by user. Autelan's AP uses the info to speedup forwarding.
6:0	RESERVED	

## CAPWAP\_TUNNEL\_START\_0

TABLE TYPE : 0x9

TABLE INDEX : 0 - 383

CAPWAP tunnel start table format 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
287	IP_VERSION	0b0: IPv4 0b1: IPv6
286:284	TUNNEL_TYPE	0x0: Configured tunnel 0x1: ISATAP 0x2: 6to4 0x3: 6RD 0x4: GRE 0x5: CAPWAP 0x6~0x7: Reserved
283:252	SIP	IPv4/IPv6[31:0] Source IP address.
251:220	DIP	IPv4/IPv6[31:0] Destination IP address.
219	TTL_ASSIGN	0b0: Use TTL for tunnel header 0b1: Use packet's TTL for tunnel header
218:211	TTL	TTL/HopLimit. The field is used if TTL_ASSIGN =0.
210	UDP_TYPE	0b0: UDP 0b1: UDP Lite
209	DONT_FRAG_ASSIGN	0b0: Use DONT_FRAG for tunnel header 0b1: Use packet's don't frag bit for tunnel header The field takes effect if IP_VERSION=0(IPv4).
208	DONT_FRAG	Represents Don't frag bit in IPv4 tunnel. The field takes effect if USE_PKT_DONT_FRAG=0.
207:192	UDP_SPORT	Source UDP port.

<b>Bits</b>	<b>Field</b>	<b>Description</b>
191:176	UDP_DPORT	Destination UDP port.
175:170	QOS_PROFILE_IDX	Index to tunnel start QoS profile for specifying DSCP/inner priority/outer priority in tunnel header.
169	INT_ITAG_STS	Passenger inner tag status, the field takes effect if FRAME_FMT=0. 0b0: untag 0b1: tag
168	INT_OTAG_STS	Passenger outer tag status, the field takes effect if FRAME_FMT=0. 0b0: untag 0b1: tag
167:152	RESERVED	
151	RADIO_MAC_ADD	Insert Radio MAC Address field in CAPWAP header. 0b0: don't insert 0b1: insert
150	WL_INFO_ADD	Insert Wireless Specific Information field in CAPWAP header. 0b0: don't insert 0b1: insert
149	FRAME_FMT	0b0: 802.3 frame payload 0b1: 802.11 frame payload
148:0	RESERVED	

## CAPWAP\_TUNNEL\_START\_1

TABLE TYPE : 0x9

TABLE INDEX : 0 - 383

CAPWAP tunnel start table format 1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	IP_VERSION	0b0: IPv4 0b1: IPv6
126:31	IP	IPv6 address[127:32]
30:0	RESERVED	

## L2\_TNL\_LST

TABLE TYPE : 0xC

TABLE INDEX : 0 - 8191

L2 Tunnel Link List table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
31:20	L2_TNL_IDX	Index to L2 tunnel entry.
19:7	NEXT_IDX	Index to next list entry.
6:0	RESERVED	

Bits	Field	Description
------	-------	-------------

## L2\_TNL\_MCAST

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Tunnel MAC multicast table

Bits	Field	Description
95	VALID	Valid bit.
94	FMT	0b0: Ethernet/WLAN 0b1: OpenFlow
93	PE	0x0: Non-PE
92	RESERVED	
91:80	FID	Filtering database ID.
79:32	MAC	Destination MAC Address
31	LOCAL_FWD	Forward packet to the portmask indexed by MCAST_PMSK_IDX.
30	NEXT_HOP	Nexthop flag
29:18	MCAST_PMSK_IDX	Index to multicast portmask table. The field is used only if LOCAL_FWD=1.
17	REMOTE_FWD	Forward packet to the VXLAN tunnel(s) indexed by L2_TNL_LST_IDX.
16:4	L2_TNL_LST_IDX	Index to L2 Tunnel Link List. The field is used only if REMOTE_FWD=1.
3:0	RESERVED	

## L2\_TNL\_UCAST

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

L2 Tunnel MAC unicast table

Bits	Field	Description
95	VALID	Valid bit.
94	FMT	0b0: Ethernet/WLAN
93	PE	0x0: Non-PE
92	RESERVED	
91:80	FID	Filtering database ID.
79:32	MAC	Source/Destination MAC Address.
31	L2_TNL	L2 Tunnel indicator
30	NEXT_HOP	Nexthop flag

<b>Bits</b>	<b>Field</b>	<b>Description</b>
29	IS_TRK	Individual port (IS_TRK = 0) Trunk ID (IS_TRK = 1)
28:19	SPA	Source Port
18:16	AGE	Aging time
15	SA_BLK	Source MAC address blocking
14	DA_BLK	Destination MAC address blocking
13	STATIC	Static entry bit
12	SUSPEND	Suspend (wait to be removed)
11:0	RESERVED	

## L2\_TNL\_VXLAN

TABLE TYPE : 0xA

TABLE INDEX : 0 - 2047

L2 Tunnel VXLAN table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
95	VALID	Valid bit.
94:80	MAGIC	MAGIC field
79:71	TT_TUNNEL_IDX	Tunnel Termination Index
70:47	VNI	VNI (Virtual Network Identifier)
46:45	TT_OVID_CMD	When packet is tunnel terminated, assign OVID to: 0b00: outer untagged and outer priority tagged packet 0b01: outer untagged packet 0b10: all packet(outer untagged, outer priority-tagged, outer tagged) 0b11: reserved
44:33	TT_OVID	Outer VID assigned to the packet.
32:31	TT_IVID_CMD	When packet is tunnel terminated, assign IVID to: 0b00: inner untagged and inner priority tagged packet 0b01: inner untagged packet 0b10: all packet(inner untagged, inner priority-tagged, inner tagged) 0b11: reserved
30:19	TT_IVID	Inner VID assigned to the packet.
18	TT_FWD_VLAN_SEL	Forwarding VLAN which has higher priority than port-based decision. 0b0: use inner VLAN 0b1: use outer VLAN
17:16	TT_PRI_SEL_IDX	Index to the priority selection table.
15:13	TT_INT_PRI	Internal Priority for Tunnel Terminated.
12	RESERVED	
11:3	TS_TUNNEL_IDX	Index to Tunnel Start entry. (for encapsulating)
2:0	RESERVED	

## L2\_TNL\_VXLAN\_ENCAP

TABLE TYPE : 0x3

TABLE INDEX : 0 - 2111

L2 Tunnel VXLAN encapsulation table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:49	MAGIC	MAGIC field
48:40	TS_TUNNEL_IDX	Tunnel Start entry index (for encapsulating)
39:16	VNI	VNI (Virtual Network Identifier)
15:13	QOS_PROFILE	QoS profile for Tunnel Start.
12:0	RESERVED	

## VXLAN\_TUNNEL\_START\_0

TABLE TYPE : 0x9

TABLE INDEX : 0 - 383

VXLAN tunnel start table format 0

<b>Bits</b>	<b>Field</b>	<b>Description</b>
287	IP_VERSION	0b0: IPv4 0b1: IPv6
286:284	TUNNEL_TYPE	0x5: VXLAN 0x7: VXLAN-GPE
283:252	SIP	IPv4/IPv6[31:0] Source IP address.
251:220	DIP	IPv4/IPv6[31:0] Destination IP address.
219	TTL_ASSIGN	0b0: Use TTL for tunnel header 0b1: Use packet's TTL for tunnel header
218:211	TTL	TTL/HopLimit. The field is used if TTL_ASSIGN =0.
210	UDP_TYPE	Unused for VXLAN tunnel
209	DONT_FRAG_ASSIGN	0b0: Use DONT_FRAG for tunnel header 0b1: Use packet's don't frag bit for tunnel header The field takes effect if IP_VERSION=0(IPv4).
208	DONT_FRAG	Represents Don't frag bit in IPv4 tunnel. The field takes effect if USE_PKT_DONT_FRAG=0.
207:192	UDP_SPORT	Source UDP port.
191:176	UDP_DPORT	Destination UDP port.
175:170	QOS_PROFILE_IDX	Index to tunnel start QoS profile for specifying DSCP/inner priority/outer priority in tunnel header.
169	INT_ITAG_STS	Passenger inner tag status, the field takes effect if FRAME_FMT=0. 0b0: untag 0b1: tag

<b>Bits</b>	<b>Field</b>	<b>Description</b>
168	INT_OTAG_STS	Passenger outer tag status, the field takes effect if FRAME_FMT=0. 0b0: untag 0b1: tag
167:0	RESERVED	

## VXLAN\_TUNNEL\_START\_1

TABLE TYPE : 0x9

TABLE INDEX : 0 - 383

VXLAN tunnel start table format 1

<b>Bits</b>	<b>Field</b>	<b>Description</b>
127	IP_VERSION	0b0: IPv4 0b1: IPv6
126:31	IP	IPv6 address[127:32]
30:0	RESERVED	

## WLC\_MCAST

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

Wireless client multicast table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
95	VALID	Valid bit.
94	FMT	0b0: Ethernet/WLAN 0b1: OpenFlow
93:92	RESERVED	
91:80	FID	Filtering database ID.
79:32	MAC	Destination MAC Address
31	WIRE_FWD	Forward packet to the portmask indexed by MCAST_PMSK_IDX.
30	RESERVED	
29:18	MCAST_PMSK_IDX	Index to multicast portmask table. The field is used only if WIRE_FWD=1.
17	WIRELESS_FWD	Forward packet to the CAPWAP tunnel(s) indexed by BSSID_LL_IDX.
16:4	BSSID_LIST_IDX	Index to BSSID Link List. The field is used only if WIRELESS_FWD=1.
3:0	RESERVED	

**WLC\_UCAST**

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

Wireless client unicast table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
95	VALID	Valid bit.
94	FMT	0b0: Ethernet/WLAN 0b1: OpenFlow
93:92	RESERVED	
91:80	FID	Filtering database ID.
79:32	MAC	Source/Destination MAC Address.
31	WLAN	Wireless host
30	RESERVED	
29:18	BSSID_IDX	Index to BSSID entry.
17:0	RESERVED	

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## CHAPTER 19

# Virtual Network Function

The chapter describes features related to Virtual Network Functions

SECTION 19.1	<b>PORT EXTENSION</b>
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Port Extension module

### **PE\_ETAG\_CTRL**

REGISTER ADDRESS : 0xBB001444

DEFAULT VALUE : 0x1

E-TAG Configuration Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	CPU_KEEP	Keep original E-TAG status if the packet transmitted by CPU. 0b0: follow the ALE decision 0b1: keep E-TAG status	RW	0x1

### **PE\_ETAG\_MAC\_CTRL**

REGISTER ADDRESS : 0xBB00572C

DEFAULT VALUE : 0x893F

E-TAG MAC Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	TPID	Ethertype of E-TAG, used to parse and encapsulate a packet.	RW	0x893F

### **PE\_PORT\_ETAG\_MAC\_CTRL**

**BASE ADDRESS :** 0xBB006018  
**PORT INDEX :** 0 - 56  
**PORT OFFSET :** 0x80  
**DEFAULT VALUE :** 0x1

This is a One-Dimension Port Register Array.

Per-Port E-TAG MAC Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:1	RESERVED			
0	ETAG_EN	Enable to parse and forward E-TAGed packet (referred by Packet Parser, ALE) 0b0: disable (forward E-tagged packet as an unknown ethertype L2 packet) 0b1: enable	RW	0x1

### **PE\_ETAG\_RMK\_CTRL**

**BASE ADDRESS :** 0xBB000228  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 3 bits  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (3 bits per field)

E-TAG Remarking Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
2:0	PRI	Mapped priority value (based on internal priority)	RW	

### **PE\_PORT\_ETAG\_IGR\_CTRL**

**BASE ADDRESS :** 0xBB00CDD4  
**PORT INDEX :** 0 - 55  
**PORT OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Port Register Array.

Per-Port E-TAG Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:12	RESERVED			
11	ECID_RPF_CHK	Enable bit for ECID RPF check when lookup ECID_PVID table 0b0: Disable 0b1: Enable	RW	0x0

Bits	Field	Description	Type	Default
10	RSVD_ECID_FLTR	Filter of E-TAGed packet with reserved E-CID (0x0 and 0x3FFFF). 0b0: forward 0b1: drop the packet with reserved E-CID if received on the port. Note: it should be turned on when it's a Cascade Port.	RW	0x0
9	MC_ECID_FLTR	Filter of E-TAGed packet with multicast E-CID (GRP != 0). 0b0: forward 0b1: drop the packet with MC E-CID if it's received on the port	RW	0x0
8:1	ECID_NSG	Name Space Group of E-CID. (NSG) Use to logically separate the E-channel ID's name space.	RW	0x0
0	LU_KEY_SEL	Lookup key of L2 forwarding table while relaying the packet. 0b0: Normal { FID, DMAC } 0b1: E-CID { NamingSpaceGroup (Rx PORT) + E-CID }	RW	0x0

## PE\_PORT\_ETAG\_EGR\_CTRL

BASE ADDRESS : 0xBB00022C  
 PORT INDEX : 0 - 56  
 PORT OFFSET : 0x4  
 DEFAULT VALUE : 0x1000000

This is a One-Dimension Port Register Array.

Per-Port E-TAG Egress Control Register

Bits	Field	Description	Type	Default
31:27	RESERVED			
26	VLAN_TAG_STS_MODE	Egress VLAN tag status mode 0x0: normal (follow VLAN table untagged set) 0x1: MC tagged (insert VLAN tag if ECID.GRP!=0); Otherwise, follow VLAN table untagged set.	RW	0x0
25	TAG_DEI_RMKT_EN	Enable bit of egress E-TAG's DEI remarking. 0b0: disable (should keep the original value if it exists) 0b1: enable	RW	0x0
24	TAG_PRI_KEEP	To indicate keeping the E-TAG priority value if the original packet has one. 0b0: follow ALE decision (from internal priority) 0b1: force to use the original value if it's present.	RW	0x1

Bits	Field	Description	Type	Default
23:22	TAG_STS_MODE	Egress E-TAG status mode (referred by Encapsulator) 0b0: untagged (always remove E-TAG) 0b1: tagged (always insert E-TAG) 0b2: tagged if UC and (ECID != PCID); Otherwise, untagged. 0b3: untagged if UC and (ECID==PCID); Otherwise, tagged.	RW	0x0
21:0	RESERVED			

## PE\_PORT\_PCID\_CTRL

BASE ADDRESS : 0xBB00CEB4

PORT INDEX : 0 - 56

PORT OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Port Register Array.

Per-Port PCID Control Register

Bits	Field	Description	Type	Default
31:25	RESERVED			
24:23	ECID_EQ_PCID_ACT	Forwarding action of received packet's ECID is equal to Rx Port's PCID. 0x0: forward (do nothing) 0x1: drop 0x2: trap to local CPU 0x3: copy to local CPU	RW	0x0
22	PE_SRC_PORT_FLTR	Source extended port filtering of Port Extension. 0b0: disabled (forward the packet, don't need to check) 0b1: enabled (filter packet by Ingress E-CID check) Note: It should be enabled on Extended Port.	RW	0x0
21	USE_DEFAULT	Used by aggregating bridge port extender mode. Indicate to replace the extension value of packet's E-CID. 0b0: false (use the original extension value) 0b1: true (use the PCID's extension value)	RW	0x0
20	BASE_PE	Indicate the E-CID capability for port extender. 0b0: false (aggregating bridge port extender, support full E-CID) 0b1: true (base bridge port extender, do not support extension value at both incoming and outgoing E-CID process)	RW	0x0
19:12	PCID_EXT	Extension of the port-based default E-channel ID.	RW	0x0
11:0	PCID_BASE	Base of the port-based default E-channel ID.	RW	0x0

## **ECID\_PMSK\_LIST**

TABLE TYPE : 0xC

TABLE INDEX : 0 - 8191

ECID Portmask List table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63:51	NEXT_IDX	Index to the next ECID_PMSK_LIST entry.
50:29	ECID	ECID bit[21:20]: Group of the ECID bit[19:12]: Extension of the ECID bit[11:0]: Base of the ECID
28	PORT_TYPE	Port Type (0: Portmask Index, 1: Offset Portmask)
27:16	PMSK_IDX	Offset Portmask / Portmask Index For Offset Portmask: bit[11]: reserved bit[10:8]: offset bit[7:0]: portmask When offset is 0x0, portmask represents as port 0 to 7. When offset is 0x1, portmask represents as port 8 to 15. When offset is 0x2, portmask represents as port 16 to 23. When offset is 0x3, portmask represents as port 24 to 31. When offset is 0x4, portmask represents as port 32 to 39. When offset is 0x5, portmask represents as port 40 to 47. When offset is 0x6, portmask represents as port 48 to 55. When offset is 0x7, portmask represents as port 56 to 63.
15:0	RESERVED	For Portmask Index: bit[11:0]: MC_PMSK index

## **ECID\_PVID**

TABLE TYPE : 0xA

TABLE INDEX : 0 - 2047

ECID Extended Port VLAN table

<b>Bits</b>	<b>Field</b>	<b>Description</b>
63	VALID	Valid bit
62:55	NSG	Naming Space Group ID
54:35	ECID	ECID bit[19:12]: Extension of the ECID bit[11:0]: Base of the ECID
34:23	VID	VLAN ID
22	PORT_TYPE	Type of the port 0b0: individual port 0b1: trunk port

Bits	Field	Description
21:15	PORT_ID	For individual port (PORT_TYPE = 0), this field indicates as the port ID. For trunk port ((PORT_TYPE = 1), this field indicates as the trunk ID.
14:0	RESERVED	

## PE\_FWD

TABLE TYPE : 0x0

TABLE INDEX : 0 - 32767

Port Extender Forwarding table

Bits	Field	Description
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b00: L2 entry 0b01: OpenFlow
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124:112	RESERVED	
111:104	NSG	Name Space Group ID (compared with ingress port's NSG)
103:82	ECID	ECID bit[21:20]: Group of the ECID bit[19:12]: Extension of the ECID bit[11:0]: Base of the ECID
81:25	DPM	Forwarding Portmask for this ECID channel.
24:0	RESERVED	

## PE\_FWD\_CAM

TABLE TYPE : 0x1

TABLE INDEX : 0 - 127

Port Extender Forwarding CAM table

Bits	Field	Description
127	VALID	Indicates whether this entry is valid 0b0: invalid 0b1: valid
126	FMT	Indicates entry format 0b00: L2 entry 0b01: OpenFlow

<b>Bits</b>	<b>Field</b>	<b>Description</b>
125	ENTRY_TYPE	Entry type 0b0: L2 unicast/multicast entry 0b1: PE forwarding entry (PE_FWD)
124:112	RESERVED	
111:104	NSG	Name Space Group ID (compared with ingress port's NSG)
103:82	ECID	ECID bit[21:20]: Group of the ECID bit[19:12]: Extension of the ECID bit[11:0]: Base of the ECID
81:25	DPM	Forwarding Portmask for this ECID channel.
24:0	RESERVED	

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## CHAPTER 20

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# Proprietary

The chapter describes features related to proprietary function

SECTION 20.1

**RRCP**

RRCP module

SECTION 20.2

**RLDP & RLPP**

RLDP & RLPP module

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## CHAPTER 21

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# Stacking

The chapter describes features related to stacking system

SECTION 21.1

## UNIT CONTROL

Unit Control for Stacking System including stacking port selection, my device ID, master device ID, time to live and blocking ports configurations.

### **STK\_GBL\_CTRL**

REGISTER ADDRESS : 0xB001448

DEFAULT VALUE : 0x0

Specify the stacking global control functions including stacking port selection, my device ID, and master device ID.

Bits	Field	Description	Type	Default
31:9	RESERVED			
8	DROP_MY_DEV	Drop the packet that sp_info.device field in stack-ing header is the same as MY_DEV_ID. Value: 0b0: do not drop the packet 0b1: drop the packet	RW	0x0
7:4	MY_DEV_ID	Specify the device ID of the device.	RW	0x0
3:0	MASTER_DEV_ID	Specify the master device ID of the device.	RW	0x0

### **STK\_PORT\_ID\_CTRL**

BASE ADDRESS : 0xBB00144C

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 6 bits

DEFAULT VALUE : 0x3F

This is a One-Dimension Register Field Array. (6 bits per field)

Specify the port ID of stacking port.

Bits	Field	Description	Type	Default
5:0	STK_PORT_ID	Port ID of stacking port. Only valid when the port is stacking port. Value > 55 means the index does not map to any port.	RW	0x3F

## **STK\_DEV\_PORT\_MAP\_CTRL**

BASE ADDRESS : 0xBB00145C

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 16 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (16 bits per field)

Specify the device to stacking portmask mapping.

Bits	Field	Description	Type	Default
15:0	DEV_PORT_MAP	Specify the device to stacking port mask mapping for packet forwarding decision. Value: 0b0: Packet forwarding to the specific device should not forward to the stacking port. 0b1: Packet forwarding to the specific device should forward to the stacking port.	RW	0x0

## **STK\_NON\_UNICAST\_BLOCK\_CTRL**

BASE ADDRESS : 0xBB00147C

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 16 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (16 bits per field)

Specify the blocking port mask for non-unicast packets per source device.

Bits	Field	Description	Type	Default
15:0	NON_UNICAST_BLOCK_PM	Blocking port mask of non-unicast packets. Value: 0b0: non-blocking port 0b1: blocked port	RW	0x0

### SECTION 21.2

## **REMOTE ACCESS**

Remote register/table/PHY access for stacking system.

### RMT\_REG\_ACCESS\_CTRL

REGISTER ADDRESS : 0xBB0014A0

DEFAULT VALUE : 0x0

Remote REG Access Control Register

Bits	Field	Description	Type	Default
31	RESERVED			
30	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if received NACK or ACK timeout	RO	0x0
29	EXEC	Executing status 0b0: ready 0b1: busy	RWAC	0x0
28	RWOP	Read/write operation 0b0: read 0b1: write	RW	0x0
27:24	DEV	Device number (0~15)	RW	0x0
23:0	ADDR	Register address	RW	0x0

### RMT\_REG\_ACCESS\_DATA

REGISTER ADDRESS : 0xBB0014A4

DEFAULT VALUE : 0x0

Remote REG Access Data Register

Bits	Field	Description	Type	Default
31:0	DATA	Register data value	RW	0x0

### RMT\_REG\_ACCESS\_CFG

REGISTER ADDRESS : 0xBB0014A8

DEFAULT VALUE : 0x0

Remote REG Access Configuration Register

Bits	Field	Description	Type	Default
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RRA packet.	RW	0x0

## **RMT\_ACCESS\_SEM**

**BASE ADDRESS :** 0xBB0014AC  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 0x4  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Common Register Array.

Remote Access Semaphore Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:9	RESERVED			
8	LOCK	Lock status of the semaphore. 0b0: unlocked 0b1: locked	RW	0x0
7:4	DEV	Device number	RW	0x0
3:0	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 512 uSec)	RO	0x0

## **RMT\_ACCESS\_SEM\_CTRL**

**BASE ADDRESS :** 0xBB0014CC  
**ARRAY INDEX :** 0 - 7  
**ARRAY OFFSET :** 4 bits  
**DEFAULT VALUE :** 0x0

This is a One-Dimension Register Field Array. (4 bits per field)

Remote Access Semaphore Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
3:0	TIMEOUT	Period to wait until timeout. (unit: 512 uSec) 0x0: disabled (will not timeout forever and won't auto unlock the semaphore) 0x1: 512uS ... 0xF: 7.68mS.	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_0\_CTRL

REGISTER ADDRESS : 0xBB008548

DEFAULT VALUE : 0x0

Remote Table Access Set 0 Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:26	RESERVED			
25	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if timeout	RO	0x0
24	EXEC	Executing status 0b0: ready (not executing) 0b1: busy (executing)	RWAC	0x0
23	CMD	Table access operation 0b0: read 0b1: write	RW	0x0
22:19	DEV_ID	Device number (0~15)	RW	0x0
18:15	TBL	Indicate table ID 0x0: L2/CAPWAP/OpenFlow table (SRAM) 0x1: L2/CAPWAP/OpenFlow table (Binary CAM) 0x2: Multicast portmask table 0x3: VLAN table 0x4: IVC table 0x5: MSTI table 0x6: MAC binding table (SRAM) 0x7: MAC binding table (TCAM) 0x8: Tunnel terminate table 0x9: Tunnel start table/VxLAN/CAPWAP 0xA: Flexible table 0 (SRAM) (BSSID/ MPLS de-cap/ MAC binding) 0xB: Flexible table 0 (BCAM) 0xC: Flexible table 1 (BSSID list/ MPLS next hop) 0xD: Source port to trunk ID mapping table 0xE~0xF: Reserved	RW	0x0

Bits	Field	Description	Type	Default
14:0	ADDR	Select access address of the table L2/CAPWAP/OpenFlow (SRAM): ADDR [14:0]={ TBL_IDX[0:0], Index [11:0], EntrySel [1:0] } L2/CAPWAP/OpenFlow (BCAM): ADDR [14:0]={ 00000000, Index [6:0] } Multicast Portmask: ADDR [14:0]={ 000, Index [11:0] } VLAN: ADDR [14:0]={000, VID [11:0]} IVC: ADDR [14:0]={ 00000 , Index [9:0] } MSTI: ADDR [14:0]={000, VID [11:0]} MAC Binding(SRAM): ADDR [14:0] = { 00000 , Index [7:0], EntrySel [1:0] } MAC Binding(BCAM): ADDR [14:0] = { 000000000 , Index [5:0] } Tunnel Terminate: ADDR [14:0] = { 000000 , Index [6:0], EntrySel[1:0] } Tunnel Start/VxLAN/CAPWAP: ADDR [14:0] = { 000000 , Index [6:0], EntrySel [1:0] } Flexible table 0 (BSSID/MPLS decap/ MAC binding) (SRAM): ADDR [14:0]={ 0000, Index [10:0] } Flexible table 0 (BSSID/MPLS decap/ MAC binding) (SRAM): ADDR [14:0]={ 000000000, Index [5:0] } Flexible table 1 (BSSID List/ MPLS next hop): ADDR [14:0]={ 00, Index [12:0] } Source port to trunk ID mapping table: ADDR [14:0]={ 00000, Index [9:0] }	RW	0x0

### **RMT\_TBL\_ACCESS\_SET\_0\_DATA**

BASE ADDRESS : 0xBB00854C  
 ARRAY INDEX : 0 - 7  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Table Access Set 0 Data Register

Bits	Field	Description	Type	Default
31:0	DATA	Data content of the entry	RW	0x0

### **RMT\_TBL\_ACCESS\_SET\_0\_CFG**

REGISTER ADDRESS : 0xBB00856C  
 DEFAULT VALUE : 0x0

Remote Table Access Set 0 Configuration Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RTA packet which is destined to this device.	RW	0x0

### **RMT\_TBL\_ACCESS\_SET\_0\_L2\_READ\_MTHD\_CTRL**

REGISTER ADDRESS : 0xBB008570

DEFAULT VALUE : 0x0

Remote Table Access Set 0 L2 Read Method Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:19	RESERVED			
18:16	METHOD	L2 table (SRAM) look up method 0x0: with specified lut address 0x1: with specified next lut address (L2 UC entry) 0x2: with specified next lut address (L2 dynamic UC entry) 0x3: with specified next lut address (L2 static UC entry) 0x4: with specified next lut address (L2 MC entry) 0x5: with specified next lut address (Next-Hot entry)	RW	0x0
15	HIT	If L2 table (SRAM) get next look up hit	RO	0x0
14:0	HIT_ADDR	The address of hit entry found by L2 get next method.	RO	0x0

### **RMT\_TBL\_ACCESS\_SET\_1\_CTRL**

REGISTER ADDRESS : 0xBB0046B8

DEFAULT VALUE : 0x0

Remote Table Access Set 1 Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:23	RESERVED			
22	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if timeout	RO	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
21	EXEC	Executing status 0b0: ready (not executing) 0b1: busy (executing)	RWAC	0x0
20	CMD	Table access operation 0b0: read 0b1: write	RW	0x0
19:16	DEV_ID	Device number (0~15)	RW	0x0
15:14	TBL	Indicate table ID 0x0: ACL/Full match table 0x1: Meter table 0x2: Flow counter table 0x3: Reserved	RW	0x0
13:0	ADDR	Select access address of the table ACL: ADDR [13:0] = { 00, Index [11:0] } Meter: ADDR [13:0] = { 00000, Index [8:0] } Flow counter (Read): ADDR [13:0] = { 00, Index [11:0] } Flow counter (Write = Clear): ADDR [13:0] = { PktCount[0], ByteCount[0], Index [11:0] }	RW	0x0
		Note: The Write command of Flow Counter table would always write CNTR_MODE field. PKTCNTR[0:0] and BYTECNTR[0:0] in ADDR decide if PKT_CNTR or BYTE_CNTR fields would be written.		

### **RMT\_TBL\_ACCESS\_SET\_1\_DATA**

BASE ADDRESS : 0xBB0046BC  
 ARRAY INDEX : 0 - 21  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Table Access Set 1 Data Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:0	DATA	Data content of the entry	RW	0x0

### **RMT\_TBL\_ACCESS\_SET\_1\_CFG**

REGISTER ADDRESS : 0xBB004714  
 DEFAULT VALUE : 0x0

Remote Table Access Set 1 Configuration Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RTA packet.	RW	0x0

### **RMT\_TBL\_ACCESS\_SET\_2\_CTRL**

REGISTER ADDRESS : 0xBB008574

DEFAULT VALUE : 0x0

Remote Table Access Set 2 Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if timeout	RO	0x0
23	EXEC	Executing status 0b0: ready (not executing) 0b1: busy (executing)	RWAC	0x0
22	CMD	Table access operation 0b0: read 0b1: write	RW	0x0
21:18	DEV_ID	Device number (0~15)	RW	0x0
17:14	TBL	Indicate table ID 0x0: Link Aggregation table 0x1: Port Isolation table 0x2: L3 Router MAC table 0x3: Host Routing/OpenFlow table 0x4: Prefix Routing/OpenFlow table 0x5: L3 ECMP table 0x6: L3 Next Hop table 0x7: L3 IGR Interface table 0x8: L3 EGR Interface table 0x9: L3 OIL table 0xA: OpenFlow group table 0xB: OpenFlow action bucket table 0xC: L3 Ingress Interface MIB Counter table 0xD: L3 Egress Interface MIB Counter table 0xE: L3 Ingress Tunnel Interface MIB Counter table 0xF: L3 Egress Tunnel Interface MIB Counter table	RW	0x0

Bits	Field	Description	Type	Default
13:0	ADDR	Select access address of the table Link Aggregation: ADDR [13:0] = { 0000000 , Index [6:0] } Port Isolation: ADDR [13:0] = { 0000 , Index [9:0] } L3 Router MAC: ADDR [13:0]={0000, Index [9:0] } Host Routing/OpenFlow: ADDR [13:0]={ Tbl_ID[0], Index [9:0], Sel[2:0] } Prefix Routing/OpenFlow: ADDR [13:0] = { Index [10:0], Sel[2:0] } L3 ECMP: ADDR [13:0] = { 000000 , Index [7:0] } L3 Next Hop: ADDR [13:0] = { 0, Index [12:0] } L3 IGR Interface: ADDR [13:0] = { 000 , Index [9:0] } L3 EGR Interface: ADDR [13:0] = { 000 , Index [9:0] } L3 OIL: ADDR [13:0] = { Index [13:0] } OpenFlow group: ADDR [13:0] = {000, Index [10:0] } OpenFlow action bucket: ADDR [13:0] = {0, Index [12:0] } L3 Ingress Interface MIB Counter: ADDR [13:0] = { 0000 , Index [9:0] } L3 Egress Interface MIB Counter: ADDR [13:0] = { 0000 , Index [9:0] } L3 Ingress Tunnel Interface MIB Counter: ADDR [13:0] = { 00000 , Index [8:0] } L3 Egress Tunnel Interface MIB Counter: ADDR [13:0] = { 00000 , Index [8:0] }	RW	0x0

### RMT\_TBL\_ACCESS\_SET\_2\_DATA

BASE ADDRESS : 0xBB008578

ARRAY INDEX : 0 - 5

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Table Access Set 2 Data Register

Bits	Field	Description	Type	Default
31:0	DATA	Data content of the entry	RW	0x0

### RMT\_TBL\_ACCESS\_SET\_2\_CFG

REGISTER ADDRESS : 0xBB008590

DEFAULT VALUE : 0x0

Remote Table Access Set 2 Configuration Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RTA packet.	RW	0x0

### **RMT\_TBL\_ACCESS\_SET\_3\_CTRL**

REGISTER ADDRESS : 0xBB004FA0

DEFAULT VALUE : 0x0

Remote Table Access Set 3 Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:22	RESERVED			
21	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if timeout	RO	0x0
20	EXEC	Executing status 0b0: ready (not executing) 0b1: busy (executing)	RWAC	0x0
19	CMD	Table access operation 0b0: read 0b1: write	RW	0x0
18:15	DEV_ID	Device number (0~15)	RW	0x0
14:12	TBL	Indicate table ID 0x0: VLAN Untag table 0x1: EVC table 0x2: MPLS Encap table 0x3: BSSID Tunnel Start table 0x4: Egress Remarking table 0x5: Tunnel start table/VxLAN/CAPWAP (Encapsulator) table 0x6~0x7: Reserved	RW	0x0
11:0	ADDR	Select access address of the table VLAN Untag: ADDR [11:0]={ VID [11:0] } EVC: ADDR [11:0]={ 00 , Index [9:0] } MPLS Encap: ADDR [11:0] = { 0, Index[10:0] } BSSID Tunnel Start table: ADDR[11:0] = { Index[11:0] } Egress Remarking table: ADDR[11:0] = { 00, Index[9:0] }	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_3\_DATA

BASE ADDRESS : 0xBB004FA4

ARRAY INDEX : 0 - 8

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Table Access Set 3 Data Register

Bits	Field	Description	Type	Default
31:0	DATA	Data content of the entry	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_3\_CFG

REGISTER ADDRESS : 0xBB004FC8

DEFAULT VALUE : 0x0

Remote Table Access Set 3 Configuration Register

Bits	Field	Description	Type	Default
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RTA packet.	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_4\_CTRL

REGISTER ADDRESS : 0xBB003060

DEFAULT VALUE : 0x0

Remote Table Access Set 4 Control Register

Bits	Field	Description	Type	Default
31:14	RESERVED			
13	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if timeout	RO	0x0

Bits	Field	Description	Type	Default
12	EXEC	Executing status 0b0: ready (not executing) 0b1: busy (executing)	RWAC	0x0
11	CMD	Table access operation 0b0: read 0b1: write	RW	0x0
10:7	DEV_ID	Device number (0~15)	RW	0x0
6	TBL	Indicate table ID 0b0: Egress Bandwidth Control table 0b1: Output queue table	RW	0x0
5:0	ADDR	Select access address of the table Egress Bandwidth Contrl: ADDR [5:0]={ Index [5:0] (Port ID) } Output queue: ADDR [5:0]={ Index [5:0] (Port ID) }	RW	0x0

### RMT\_TBL\_ACCESS\_SET\_4\_DATA

BASE ADDRESS : 0xBB003064  
 ARRAY INDEX : 0 - 28  
 ARRAY OFFSET : 0x4  
 DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Table Access Set 4 Data Register

Bits	Field	Description	Type	Default
31:0	DATA	Data content of the entry	RW	0x0

### RMT\_TBL\_ACCESS\_SET\_4\_CFG

REGISTER ADDRESS : 0xBB0030D8  
 DEFAULT VALUE : 0x0

Remote Table Access Set 4 Configuration Register

Bits	Field	Description	Type	Default
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0

Bits	Field	Description	Type	Default
4:0	SEQ_ID	Sequence ID for RTA packet.	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_5\_CTRL

REGISTER ADDRESS : 0xBB007F18

DEFAULT VALUE : 0x0

Remote Table Access Set 5 Control Register

Bits	Field	Description	Type	Default
31:15	RESERVED			
14	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if timeout	RO	0x0
13	EXEC	Executing status 0b0: ready (not executing) 0b1: busy (executing)	RWAC	0x0
12	CMD	Table access operation 0b0: read 0b1: write	RW	0x0
11:8	DEV_ID	Device number (0~15)	RW	0x0
7:6	TBL	Indicate table ID 0x0: Port MIB Counter table 0x1: Private MIB Counter table 0x2: Auto recovery tx error MIB counter table 0x3: Reserved	RW	0x0
5:0	ADDR	Select access address of the table Port MIB Counter: ADDR [5:0]={ Index [5:0] (Port ID) } Private MIB Counter: ADDR [5:0]={ 0, Index [4:0] } Auto recovery tx error MIB counter tale: ADDR [5:0] = { Index[5:0] }	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_5\_DATA

BASE ADDRESS : 0xBB007F1C

ARRAY INDEX : 0 - 52

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Table Access Set 5 Data Register

Bits	Field	Description	Type	Default
31:0	DATA	Data content of the entry	RW	0x0

## RMT\_TBL\_ACCESS\_SET\_5\_CFG

REGISTER ADDRESS : 0xBB007FF0

DEFAULT VALUE : 0x0

Remote Table Access Set 5 Configuration Register

Bits	Field	Description	Type	Default
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RTA packet.	RW	0x0

## RMT\_PHY\_ACCESS\_CTRL

REGISTER ADDRESS : 0xBB000FAC

DEFAULT VALUE : 0x0

Remote PHY Access Control Register

Bits	Field	Description	Type	Default
31:11	RESERVED			
10	RESULT	The result of access operation 0b0: success 0b1: failure Note: The result is 1 if received a failure-result or timeout.	RO	0x0
9	CMD	Executing status (write 1 to issue a new operation when it's in normal state) 0: normal (finish access) 1: trigger to execute (or busy)	RWAC	0x0
8	ACCESS_SEL	0: access phy by mdx 1: access phy by inband	RW	0x0
7	BC	Enable broadcast for PHY register write command.	RW	0x0
6	RWOP	Read/write operation 0b0: read 0b1: write	RW	0x0

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
5:4	TYPE	Access register type 0b00: MII register 0b01: 1G MMD register 0b10: 10G MMD	RW	0x0
3:0	DEV_ID	Device number (0~15)	RW	0x0

### **RMT\_PHY\_ACCESS\_DATA**

REGISTER ADDRESS : 0xBB000FB0

DEFAULT VALUE : 0x0

Remote PHY Access Data Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	RESERVED			
15:0	DATA	Data content of the entry	RW	0x0

### **RMT\_PHY\_ACCESS\_CFG**

REGISTER ADDRESS : 0xBB000FB4

DEFAULT VALUE : 0x0

Remote PHY Access Configuration Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31	RESERVED			
30:23	TIMEOUT_TIMER	Timer to count the waiting time. (unit: 32 uSec)	RO	0x0
22:15	TIMEOUT_PERIOD	Period to wait until timeout. (unit: 32 uSec)	RW	0x0
14:10	RX_REQ_SEQ_ID	Sequence ID of the last received RTA request packet which is destined to this device.	RO	0x0
9:5	RX_ACK_SEQ_ID	Sequence ID of the last received RTA ack packet which is destined to this device.	RO	0x0
4:0	SEQ_ID	Sequence ID for RTA packet.	RW	0x0

### **RMT\_PHY\_ACCESS\_PHY\_MSK\_CTRL**

REGISTER ADDRESS : 0xBB000FB8

DEFAULT VALUE : 0x0, 0x0

Remote PHY Access PHY-mask Control Register

Bits	Field	Description	Type	Default
63:56	RESERVED			
55:32	PHY_MSK_HI	Port mask [55:32] for writing operation (not PHY address)	RW	0x0
31:0	PHY_MSK_LO	Port mask [31:0] for writing operation (not PHY address)	RW	0x0

### RMT\_PHY\_ACCESS\_PORT\_ID\_CTRL

REGISTER ADDRESS : 0xBB000FC0

DEFAULT VALUE : 0x0

Remote PHY Access Port ID Control Register

Bits	Field	Description	Type	Default
31:6	RESERVED			
5:0	PORT_ID	Select port ID for read command Note: PHY_ADDR = PORT_ID + {PORT 0 PHY Polling start addr}	RW	0x0

### RMT\_PHY\_ACCESS\_PARAM

REGISTER ADDRESS : 0xBB000FC4

DEFAULT VALUE : 0x0

Remote PHY Access Parameters Register

Bits	Field	Description	Type	Default
31:27	RESERVED			
26:14	MAIN_PAGE	Page of the register (only available when TYPE = 0)	RW	0x0
13:5	EXT_PAGE	Select extension page number to access (only available when TYPE = 0)	RW	0x0
4:0	REG	Select register number to access (only available when TYPE = 0)	RW	0x0

### RMT\_PHY\_ACCESS\_MMD\_PARAM

REGISTER ADDRESS : 0xBB000FC8

DEFAULT VALUE : 0x0

Remote PHY Access MMD Parameters Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:21	RESERVED			
20:16	MMD_DEV_ADDR	MMD device address (only available when TYPE = 1)	RW	0x0
15:0	MMD_REG	MMD register number (only available when TYPE = 1)	RW	0x0

## SECTION 21.3

## REMOTE INTERRUPT

Remote interrupt notification

### **RMT\_INTR\_CTRL**

REGISTER ADDRESS : 0xBB0014D0

DEFAULT VALUE : 0x0

Remote Interrupt Control Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:25	RESERVED			
24:23	TX_DELAY	The delay time of the packet transmission of Interrupt Notification. 0x0: 10uS 0x1: 20uS 0x2: 30uS 0x3: 40uS	RW	0x0
22	TX_EN	Enable bit of Interrupt Notification Transmission/Forwarding	RW	0x0
21	TX_TRIG	Software triggers H/W to send an interrupt notification message 0b0: idle/completed status 0b1: trigger	RWAC	0x0
20:16	CMP_SEQ_MARGIN	The margin for comparing the sequence ID between packet and local	RW	0x0
15:0	RX_FORCE_UPDATE	Force H/W to update the info of the specified device once. 0b0: disable 0b1: force (just one time, H/W will auto clear after updating) Note: Bit [15:0] means device 15 (MSB) to device 0 (LSB).	RWAC	0x0

### **RMT\_INTR\_INFO**

BASE ADDRESS : 0xBB0014D4

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 0x4

DEFAULT VALUE : 0x0

This is a One-Dimension Common Register Array.

Remote Interrupt Info Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:10	RESERVED			
9:5	RX_SEQ_ID	The sequence ID of the last received RIN of all remote devices (read only)	RO	0x0
4:0	SEQ_ID	Sequence ID of all remote devices	RW	0x0

### **RMT\_INTR\_DATA\_CCM**

BASE ADDRESS : 0xBB001514

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 16 bits

DEFAULT VALUE : 0x0

This is a One-Dimension Register Field Array. (16 bits per field)

Remote Interrupt Data (CCM) Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
15:0	ISR_CCM	CCM interrupt status of all remote devices.	RW	0x0

### **RMT\_INTR\_DATA\_LINK\_STS**

BASE ADDRESS : 0xBE001534

ARRAY INDEX : 0 - 15

ARRAY OFFSET : 0x8

DEFAULT VALUE : 0x0, 0x0

This is a One-Dimension Common Register Array.

Remote Interrupt Data (Link Status) Register

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
63:56	RESERVED			
55:32	PORT_LINK_STS_HI	Port [55:32] link status of all remote devices.	RW	0x0
31:0	PORT_LINK_STS_LO	Port [31:0] link status of all remote devices.	RW	0x0

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## CHAPTER 22

# Miscellaneous

The chapter describes miscellaneous features

SECTION 22.1

## **PARSER**

Configurations related to parser including field selector

### **PARSER\_CTRL**

REGISTER ADDRESS : 0xBB00056C

DEFAULT VALUE : 0x12B60004

Configurations related to Parser

<b>Bits</b>	<b>Field</b>	<b>Description</b>	<b>Type</b>	<b>Default</b>
31:16	VXLAN_GPE_UDP_PORT	UDP port number for parsing VXLAN-GPE header. (default: 4790)	RW	0x12B6
15:7	RESERVED			
6:5	MALFORMED_PKT_ACT	Action for malformed packets. 0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU	RW	0x0
4:3	PARSER_CANT_HANDLE_ACT	Action for the packets that parser is unable to handle when parsing the frame. 0x0: Forward 0x1: Drop 0x2: Trap to local CPU 0x3: Trap to master CPU	RW	0x0
2	WL_OFFSET_EN	Ignore 2 more bytes when parsing 802.11 frame. 0b0: disable 0b1: enable	RW	0x1
1	PPPOE_PARSE_EN	Enable PPPoE header parsing or not. 0b0: disable, packets carrying PPPoE header will be treated as layer 3 unknown packets. 0b1: could recognize PPPoE encapsulation and upper protocols encapsulated	RW	0x0

Bits	Field	Description	Type	Default
0	RFC1042_OUI_IGNORE	Ignore comparing the OUI while parsing the RFC1042 header. That is, a frame with format "AA-AA-03-XX-XX-XX" could be deemed a RFC1042 frame. 0b0: don't ignore 0b1: ignore	RW	0x0

## GTP\_PARSE\_CTRL

REGISTER ADDRESS : 0xBB000570

DEFAULT VALUE : 0x868084B

GTP parsing control register

Bits	Field	Description	Type	Default
31:16	USR_DATA_L4_PORT	Layer 4 port number of the user-data packet of GTP tunnel protocol. (default: 2152)	RW	0x0868
15:0	CTRL_L4_PORT	Layer 4 port number of the control packet of GTP tunnel protocol. (default: 2123)	RW	0x084B

## DIAMETER\_PARSE\_CTRL

REGISTER ADDRESS : 0xBB000574

DEFAULT VALUE : 0xF1C

Diameter protocol parsing control register

Bits	Field	Description	Type	Default
31:17	RESERVED			
16	SESSION_ID_EN	Indicate to parse the session of the Diameter Header. Note: It will use the all field-selectors (including the 16-bit validmask) to store the session ID if it's enabled.	RW	0x0
15:0	HDR_L4_PORT	Layer 4 port number of the header of Diameter protocol. (default: 3868)	RW	0x0F1C

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## CHAPTER 23

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### Private

The chapter describes features used internally

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# Index: Alphabetical List

Alphabetical List of Registers and Tables. This section is a quick reference to all the tables and register in this datasheet.

## A

All registers and tables starting with a A.

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
ACL_CTRL	0xBB008C24	0x0	<a href="#">Page 367</a>
ACL_PORT_LOOKUP_CTRL	0xBB0044F8	0x3	<a href="#">Page 366</a>
ACTION_BUCKET	<Indirect> Type:11 Size:8192	—	<a href="#">Page 410</a>
ATK_PRVNT_ACT	0xBB008C34	0x0	<a href="#">Page 380</a>
ATK_PRVNT_ARP_INVLD_PORT_ACT	0xBB008C4C	0x0	<a href="#">Page 384</a>
ATK_PRVNT_CTRL	0xBB008C30	0x0	<a href="#">Page 379</a>
ATK_PRVNT_ICMP_CTRL	0xBB008C3C	0x200	<a href="#">Page 382</a>
ATK_PRVNT_IPV6_CTRL	0xBB008C38	0x4D8	<a href="#">Page 382</a>
ATK_PRVNT_PORT_EN	0xBB008C28	0x0	<a href="#">Page 379</a>
ATK_PRVNT_SMURF_CTRL	0xBB008C44	0x0	<a href="#">Page 383</a>
ATK_PRVNT_STS	0xBB008C48	0x0	<a href="#">Page 383</a>
ATK_PRVNT_TCP_CTRL	0xBB008C40	0x14	<a href="#">Page 383</a>
AVB_CTRL	0xBB008004	0x9DD3	<a href="#">Page 259</a>
AVB_CTRL_MAC	0xBB004F44	0x13	<a href="#">Page 260</a>
AVB_PORT_CLASS_A_EN	0xBB008C14	0x0	<a href="#">Page 258</a>
AVB_PORT_CLASS_A_EN_MAC	0xBB004F34	0x0	<a href="#">Page 258</a>
AVB_PORT_CLASS_B_EN	0xBB008C1C	0x0	<a href="#">Page 258</a>
AVB_PORT_CLASS_B_EN_MAC	0xBB004F3C	0x0	<a href="#">Page 259</a>

## B

All registers and tables starting with a B.

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
BONDING_STS	0xBB00040C	0x1F0	<a href="#">Page 3</a>
BSSID	<Indirect> Type:10 Size:2048	—	<a href="#">Page 437</a>

Register	Address	Default	Reference
BSSID_LIST	<Indirect> Type:12 Size:8192	—	<a href="#">Page 438</a>
BSSID_TUNNEL_START	<Indirect> Type:3 Size:2048	—	<a href="#">Page 438</a>

**C**

All registers and tables starting with a C.

Register	Address	Default	Reference
CAPWAP_TUNNEL_START_0	<Indirect> Type:9 Size:384	—	<a href="#">Page 439</a>
CAPWAP_TUNNEL_START_1	<Indirect> Type:9 Size:384	—	<a href="#">Page 440</a>
CCM_RX_INST_CNT	0xBB00A43C	0x0	<a href="#">Page 396</a>
CCM_RX_INST_CTRL	0xBB00A41C	0x3F3F	<a href="#">Page 395</a>
CCM_RX_LIFETIME_CTRL	0xBB00A410	0xB	<a href="#">Page 395</a>
CCM_TX_INST_CTRL	0xBB000598	0x80080000	<a href="#">Page 397</a>
CCM_TX_INST_MEM	0xBB0005B8	0x0	<a href="#">Page 397</a>
CCM_TX_INST_PKT	0xBB0005CC	0x48	<a href="#">Page 398</a>
CCM_TX_INST_TRK_PRESENT	0xBB0005C8	0x0	<a href="#">Page 398</a>
CCM_TX_TAG_CTRL	0xBB000594	0x0	<a href="#">Page 397</a>
CFM_RX_CCM_CTRL	0xBB00A404	0x0	<a href="#">Page 392</a>
CFM_RX_CTRL	0xBB00A400	0x0	<a href="#">Page 391</a>
CFM_RX_LB_CTRL	0xBB00A408	0x0	<a href="#">Page 393</a>
CFM_RX_LT_CTRL	0xBB00A40C	0x0	<a href="#">Page 394</a>
CHIP_RST_STS	0xBB000404	0x0	<a href="#">Page 2</a>

**D**

All registers and tables starting with a D.

Register	Address	Default	Reference
DBG_CTRL0	0xBB0012A8	0x0	<a href="#">Page 32</a>
DBG_CTRL1	0xBB0012AC	0x0	<a href="#">Page 32</a>
DBG_CTRL2	0xBB0012B0	0x0	<a href="#">Page 33</a>
DIAMETER_PARSE_CTRL	0xBB000574	0xF1C	<a href="#">Page 478</a>
DMA_IF_CTRL	0xBB000928	0x6400000	<a href="#">Page 44</a>
DMA_IF_INTR_RX_DONE_MSK	0xBB000914	0x0	<a href="#">Page 42</a>
DMA_IF_INTR_RX_DONE_STS	0xBB000920	0x0	<a href="#">Page 43</a>
DMA_IF_INTR_RX_RUNOUT_MSK	0xBB000910	0x0	<a href="#">Page 42</a>
DMA_IF_INTR_RX_RUNOUT_STS	0xBB00091C	0x0	<a href="#">Page 43</a>
DMA_IF_INTR_TX_DONE_MSK	0xBB000918	0x0	<a href="#">Page 43</a>

Register	Address	Default	Reference
DMA_IF_INTR_TX_DONE_STS	0xBB000924	0x0	<a href="#">Page 44</a>
DMA_IF_PHYSICAL_ADDR_MSK	0xBB00092C	0x1FFFFFFF	<a href="#">Page 46</a>
DMA_IF_PKT_CTRL	0xBB004858	0x18	<a href="#">Page 45</a>
DMA_IF_RX_BASE_DESC_ADDR_CTRL	0xBB000800	0x0	<a href="#">Page 41</a>
DMA_IF_RX_CUR_DESC_ADDR_CTRL	0xBB000880	0x0	<a href="#">Page 41</a>
DMA_IF_RX_RING_CNTR	0xBB0020AC	0x0	<a href="#">Page 46</a>
DMA_IF_RX_RING_SIZE	0xBB002080	0x0	<a href="#">Page 45</a>
DMA_IF_TX_BASE_DESC_ADDR_CTRL	0xBB000900	0x0	<a href="#">Page 41</a>
DMA_IF_TX_CUR_DESC_ADDR_CTRL	0xBB000908	0x0	<a href="#">Page 42</a>
DP_SEL_PORT_TBL_CTRL	0xBB0090B8	0x1111	<a href="#">Page 240</a>
DP_SEL_REMAP_DSCP	0xBB0090A4	0x0	<a href="#">Page 240</a>
DP_SEL_REMAP_ITAG_CFI0	0xBB009094	0x0	<a href="#">Page 238</a>
DP_SEL_REMAP_ITAG_CFI1	0xBB009098	0x0	<a href="#">Page 239</a>
DP_SEL_REMAP_MPLS	0xBB0090B4	0x0	<a href="#">Page 240</a>
DP_SEL_REMAP_OTAG_DEI0	0xBB00909C	0x0	<a href="#">Page 239</a>
DP_SEL_REMAP_OTAG_DEI1	0xBE0090A0	0x0	<a href="#">Page 239</a>
DYING_GASP_POLARITY_CTRL	0xBB001428	0x0	<a href="#">Page 391</a>

## E

All registers and tables starting with a E.

Register	Address	Default	Reference
EACL	<Indirect> Type:0 Size:4096	—	<a href="#">Page 367</a>
ECID_PMSK_LIST	<Indirect> Type:12 Size:8192	—	<a href="#">Page 450</a>
ECID_PVID	<Indirect> Type:10 Size:2048	—	<a href="#">Page 451</a>
EEE_MISC_CTRL0	0xBB005654	0x8	<a href="#">Page 100</a>
EEE_MISC_CTRL1	0xBB002154	0x2000	<a href="#">Page 101</a>
EEE_MULTIWAKE_CTRL	0xBB00566C	0x61	<a href="#">Page 103</a>
EEE_PORT_RX_EN	0xBB00564C	0x1	<a href="#">Page 100</a>
EEE_PORT_RX_STS	0xBB005690	0x0	<a href="#">Page 107</a>
EEE_PORT_TX_EN	0xBB005644	0x1	<a href="#">Page 100</a>
EEE_PORT_TX_STS	0xBB005688	0x0	<a href="#">Page 107</a>
EEE_TX_MINIFG_CTRL0	0xBB005658	0x200014	<a href="#">Page 102</a>
EEE_TX_MINIFG_CTRL1	0xBB00565C	0x271003F	<a href="#">Page 102</a>
EEE_TX_MINIFG_CTRL2	0xBB005660	0x139009C	<a href="#">Page 102</a>
EEE_TX_TIMER_100M_CTRL	0xBB005670	0x12C1E	<a href="#">Page 104</a>
EEE_TX_TIMER_10G_CTRL	0xBB005684	0xC808	<a href="#">Page 107</a>
EEE_TX_TIMER_2P5G_CTRL	0xBB00567C	0xC81E	<a href="#">Page 106</a>
EEE_TX_TIMER_5G_CTRL	0xBB005680	0xC80F	<a href="#">Page 106</a>

Register	Address	Default	Reference
EEE_TX_TIMER_GELITE_CTRL	0xBB005674	0x512C22	<a href="#">Page 104</a>
EEE_TX_TIMER_GIGA_CTRL	0xBB005678	0x512C11	<a href="#">Page 105</a>
EEE_WAIT_RX_INACTIVE_CTRL0	0xBB005664	0x33A3A3A	<a href="#">Page 103</a>
EGBW_CPU_PPS_LB_CTRL	0xBB00215C	0x26C01	<a href="#">Page 201</a>
EGBW_CPU_Q_MAX_LB_CTRL	0xBB002414	0x1194	<a href="#">Page 204</a>
EGBW_CPU_Q_MAX_LB_RST	0xBB002514	0x0	<a href="#">Page 204</a>
EGBW_CTRL	0xBB002158	0x0	<a href="#">Page 201</a>
EGBW_ENCAP_CTRL	0xBB004D8C	0x0	<a href="#">Page 200</a>
EGBW_LB_CTRL	0xBB002160	0x3500AB	<a href="#">Page 202</a>
EGBW_PORT_CTRL	0xBB002164	0x1194	<a href="#">Page 202</a>
EGBW_PORT_LB_RST	0xBB00232C	0x0	<a href="#">Page 203</a>
EGBW_PORT_Q_ASSURED_LB_RST_SET0	0xBB002518	0x0	<a href="#">Page 205</a>
EGBW_PORT_Q_ASSURED_LB_RST_SET1	0xBB0025E8	0x0	<a href="#">Page 205</a>
EGBW_PORT_Q_MAX_LB_RST_SET0	0xBB002334	0x0	<a href="#">Page 203</a>
EGBW_PORT_Q_MAX_LB_RST_SET1	0xBB002404	0x0	<a href="#">Page 203</a>
EGR_Q_BW	<Indirect> Type:0 Size:56	—	<a href="#">Page 205</a>
ETH_DM_CLK_CTRL	0xBB00142C	0x0	<a href="#">Page 401</a>
ETH_DM_PORT_EN	0xBB00A45C	0x0	<a href="#">Page 399</a>
ETH_DM_RX_CTRL	0xBB00A464	0x0	<a href="#">Page 399</a>
ETH_DM_RX_TIME	0xBB00A468	0x0	<a href="#">Page 401</a>
ETH_DM_TIME_CTRL	0xBB001434	0x0	<a href="#">Page 402</a>
ETH_DM_TIME_CTRL_SEC	0xBB001438	0x0	<a href="#">Page 402</a>
ETH_DM_TIME_FREQ	0xBB001430	0x8000000	<a href="#">Page 401</a>
ETH_DM_TX_DLY	0xBB004F60	0x0	<a href="#">Page 400</a>
EXT_GPIO_DATA_CTRL_1	0xBB0007E8	0x0	<a href="#">Page 25</a>
EXT_GPIO_DATA_CTRL_2	0xBB0007EC	0x0	<a href="#">Page 26</a>
EXT_GPIO_DATA_CTRL_3	0xBB0007F0	0x0	<a href="#">Page 27</a>
EXT_GPIO_DIR_CTRL_1	0xBB0007DC	0x0	<a href="#">Page 24</a>
EXT_GPIO_DIR_CTRL_2	0xBB0007E0	0x0	<a href="#">Page 24</a>
EXT_GPIO_DIR_CTRL_3	0xBB0007E4	0x0	<a href="#">Page 24</a>
EXT_GPIO_GLB_CTRL	0xBB0007D4	0xC	<a href="#">Page 23</a>
EXT_GPIO_INDRT_ACCESS_CTRL	0xBB0007F4	0x0	<a href="#">Page 27</a>
EXT_GPIO_INTR_MODE	0xBB0012D4	0x3	<a href="#">Page 37</a>
EXT_GPIO_TRIG	0xBB0007D8	0x0	<a href="#">Page 24</a>

## F

All registers and tables starting with a F.

Register	Address	Default	Reference
FC_ALE_NON_REPCT_Q_PAGE_CNT	0xBB008184	0x0	<a href="#">Page 224</a>
FC_ALE_REPCT_Q_PAGE_CNT	0xBB008188	0x0	<a href="#">Page 224</a>
FC_CPU_EGR_PAGE_CNT	0xBB00273C	0x0	<a href="#">Page 223</a>
FC_CPU_Q_EGR_DROP_THR	0xBB0026A8	0x4E0044	<a href="#">Page 221</a>

Register	Address	Default	Reference
FC_CPU_Q_EGR_FORCE_DROP_CTRL	0xBB00A9C8	0x0	Page 220
FC_CPU_Q_EGR_PAGE_CNT	0xBB002740	0x0	Page 223
FC_CTRL	0xBB005048	0x1	Page 211
FC_GLB_DROP_THR	0xBB005134	0x16C1	Page 212
FC_GLB_FCOFF_HI THR	0xBB005140	0xDF40DC2	Page 213
FC_GLB_FCOFF_LO THR	0xBB005144	0x9CE099C	Page 213
FC_GLB_HI THR	0xBB005138	0xDF40DC2	Page 212
FC_GLB_LO THR	0xBB00513C	0x9CE099C	Page 213
FC_GLB_PAGE_CNT	0xBB0051BC	0x0	Page 222
FC_GLB_SYS_UTIL_THR	0xBB005130	0x348	Page 212
FC_HOL_PRVNT_CTRL	0xBB00A8E4	0xF	Page 218
FC_HSA_PAGE_CNT	0xBB0027C0	0x0	Page 226
FC_JUMBO_FCOFF_HI THR	0xBB005150	0x7A0076E	Page 214
FC_JUMBO_FCOFF_LO THR	0xBB005154	0x37A0348	Page 215
FC_JUMBO_HI THR	0xBB005148	0x7A0076E	Page 213
FC_JUMBO_LO THR	0xBE00514C	0x37A0348	Page 214
FC_JUMBO_THR_ADJUST	0xBB005158	0x87D00006	Page 215
FC_PB_PAGE_CNT	0xBB0051C0	0x0	Page 222
FC_PORT_ACT_CTRL	0xBB00504C	0x1000008	Page 211
FC_PORT_EGR_DROP_CTRL	0xBB00A800	0x2	Page 218
FC_PORT_EGR_DROP_THR_SET_SEL	0xBB002728	0x0	Page 221
FC_PORT_FCOFF_HI THR	0xBB00517C	0x12C00FA	Page 216
FC_PORT_FCOFF_LO THR	0xBB00518C	0x0	Page 217
FC_PORT_GUAR_THR	0xBB00519C	0xF	Page 217
FC_PORT_HI THR	0xBE00515C	0x12C00FA	Page 215
FC_PORT_LO THR	0xBB00516C	0x14000F	Page 216
FC_PORT_PAGE_CNT	0xBB0051C4	0x0	Page 223
FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0	0xBB00A8E8	0x0	Page 219
FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1	0xBB00A9B8	0x0	Page 219
FC_PORT_THR_SET_SEL	0xBB0051AC	0x0	Page 217
FC_Q_EGR_DROP_THR	0xBB002618	0x0	Page 220
FC REPCT_FCOFF_DROP_CNT	0xBB008194	0x0	Page 225
FC REPCT_FCOFF_PAGE_CNT	0xBB008190	0x0	Page 225
FC REPCT_FCOFF_THR	0xBB008180	0x0	Page 221
FC REPCT_FCON_PAGE_CNT	0xBB00818C	0x0	Page 224
FEFI_STS	0xBB000F18	0x0	Page 88
FIB_UNIDIR_CTRL	0xBB0013C4	0x0	Page 94
FLEX_TBL_CTRL	0xBB008544	0x2	Page 56
FLOW_CNTR	<Indirect> Type:2 Size:4096	—	Page 326
FORCE_MAC_SPD	0xBB000F20	0x0	Page 88
FT_EGR	<Indirect> Type:0 Size:4096	—	Page 412

Register	Address	Default	Reference
FT_IGR	<Indirect> Type:0 Size:4096	—	<a href="#">Page 415</a>
FT_L2_HASH_FMT0_0	<Indirect> Type:0 Size:32768	—	<a href="#">Page 420</a>
FT_L2_HASH_FMT0_1	<Indirect> Type:0 Size:32768	—	<a href="#">Page 421</a>
FT_L2_HASH_FMT1_0	<Indirect> Type:0 Size:32768	—	<a href="#">Page 422</a>
FT_L2_HASH_FMT1_1	<Indirect> Type:0 Size:32768	—	<a href="#">Page 423</a>
FT_L2_HASH_FMT2_0	<Indirect> Type:0 Size:32768	—	<a href="#">Page 424</a>
FT_L2_HASH_FMT2_1	<Indirect> Type:0 Size:32768	—	<a href="#">Page 425</a>
FT_L3_HASH_0	<Indirect> Type:3 Size:12288	—	<a href="#">Page 426</a>
FT_L3_HASH_1	<Indirect> Type:3 Size:12288	—	<a href="#">Page 428</a>
FT_L3_HASH_2	<Indirect> Type:3 Size:12288	—	<a href="#">Page 429</a>
FT_L3_TCAM_0	<Indirect> Type:4 Size:12288	—	<a href="#">Page 430</a>
FT_L3_TCAM_1	<Indirect> Type:4 Size:12288	—	<a href="#">Page 431</a>
FT_L3_TCAM_2	<Indirect> Type:4 Size:12288	—	<a href="#">Page 433</a>

G

All registers and tables starting with a G.

Register	Address	Default	Reference
GROUP_TBL	<Indirect> Type:10 Size:2048	—	<a href="#">Page 434</a>
GTP_PARSE_CTRL	0xBB000570	0x868084B	<a href="#">Page 478</a>

**H**

All registers and tables starting with a H.

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
HALF_CHG_CTRL	0xBB006008	0x400	Page <a href="#">70</a>

**I**

All registers and tables starting with a I.

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
I2C_MST1_CTRL	0xBB00100C	0x28200	Page <a href="#">5</a>
I2C_MST1_DATA_CTRL	0xBB001014	0x0	Page <a href="#">7</a>
I2C_MST1_MEMADDR_CTRL	0xBB001010	0x0	Page <a href="#">7</a>
I2C_MST2_CTRL	0xBB001024	0x28200	Page <a href="#">8</a>
I2C_MST2_DATA_CTRL	0xBB00102C	0x0	Page <a href="#">10</a>
I2C_MST2_MEMADDR_CTRL	0xBB001028	0x0	Page <a href="#">9</a>
I2C_MST_IF_CTRL	0xBB001004	0x0	Page <a href="#">4</a>
I2C_MST_IF_SEI	0xBB001008	0x0	Page <a href="#">5</a>
I2C_SLV_CTRL	0xBB001000	0x0	Page <a href="#">4</a>
IACL	<Indirect> Type:0 Size:4096	—	Page <a href="#">370</a>
IGBW_CTRL	0xBB00E000	0x0	Page <a href="#">197</a>
IGBW_LB_CTRL	0xBB00E004	0x3500AB	Page <a href="#">198</a>
IGBW_PAGE_CNT_CTRL	0xBB00EBE0	0x0	Page <a href="#">200</a>
IGBW_PORT_CTRL	0xBB00E008	0x1194	Page <a href="#">198</a>
IGBW_PORT_EXCEED_FLAG	0xBB00E1D8	0x0	Page <a href="#">199</a>
IGBW_PORT_FC_CTRL	0xBB00E1E0	0x0	Page <a href="#">200</a>
IGBW_PORT_LB_RST	0xBB00E1D0	0x0	Page <a href="#">199</a>
IMR_AUTO_RECOVERY	0xBB00128C	0x0	Page <a href="#">30</a>
IMR_DBGO	0xBB0012A4	0x0	Page <a href="#">32</a>
IMR_EXT_GPIO	0xBB001280	0x0	Page <a href="#">30</a>
IMR_MISC	0xBB00127C	0x0	Page <a href="#">29</a>
IMR_PORT_LINK_STS_CHG	0xBB00126C	0x0	Page <a href="#">28</a>
IMR_REMOTE_INTR_STS_UPD	0xBB001278	0x0	Page <a href="#">29</a>
IMR_RLFD	0xBB001298	0x0	Page <a href="#">31</a>
IMR_SERDES_ERR	0xBB001274	0x0	Page <a href="#">29</a>
IMR_SERDES_RXIDLE	0xBB0012A0	0x0	Page <a href="#">32</a>
IMR_SERDES_UPD_PHYSTS_0	0xBB001290	0x0	Page <a href="#">31</a>
IP_MAC_BIND	<Indirect> Type:6 Size:1024	—	Page <a href="#">386</a>
IP_MAC_BIND_CAM	<Indirect> Type:7 Size:64	—	Page <a href="#">387</a>

Register	Address	Default	Reference
IP_MAC_BIND_DYN	<Indirect> Type:10 Size:2048	—	<a href="#">Page 387</a>
ISR_AUTO_RECOVERY	0xBB0012E4	0x0	<a href="#">Page 37</a>
ISR_DBGO	0xBB0012FC	0x0	<a href="#">Page 39</a>
ISR_EXT_GPIO	0xBB0012CC	0x0	<a href="#">Page 36</a>
ISR_GLB_SRC_STS	0xBB0012B4	0x0	<a href="#">Page 33</a>
ISR_MISC	0xBB0012C8	0x0	<a href="#">Page 36</a>
ISR_PORT_LINK_STS_CHG	0xBB0012B8	0x0	<a href="#">Page 34</a>
ISR_REMOTE_INTR_STS_UPD	0xBB0012C4	0x0	<a href="#">Page 35</a>
ISR_RLFD	0xBB0012F0	0x0	<a href="#">Page 39</a>
ISR_SERDES_ERR	0xBB0012C0	0x0	<a href="#">Page 35</a>
ISR_SERDES_RXIDLE	0xBB0012F8	0x0	<a href="#">Page 39</a>
ISR_SERDES_UPD_PHYSTS	0xBB0012E8	0x0	<a href="#">Page 38</a>
ISR_STAT_TRIGGER	0xBB001800	0x0	<a href="#">Page 38</a>

## L

All registers and tables starting with a L.

Register	Address	Default	Reference
L2_AGE_CTRL	0xBB00C804	0x1C00177	<a href="#">Page 160</a>
L2_BC_FLD_PMSK	0xBB00C8FC	0xFFFFFFFF	<a href="#">Page 165</a>
L2_CAM_MC	<Indirect> Type:1 Size:128	—	<a href="#">Page 172</a>
L2_CAM_UC	<Indirect> Type:1 Size:128	—	<a href="#">Page 172</a>
L2_CB_MC	<Indirect> Type:0 Size:32768	—	<a href="#">Page 173</a>
L2_CB_UC	<Indirect> Type:0 Size:32768	—	<a href="#">Page 174</a>
L2_CTRL	0xBB00C800	0x4006	<a href="#">Page 158</a>
L2_GLB_STT_PORT_MV_ACT	0xBB00C8CC	0x0	<a href="#">Page 162</a>
L2_GLB_STT_PORT_MV_LRN	0xBB00C8D0	0x0	<a href="#">Page 163</a>
L2_HASH_FULL_CNT	0xBB00CDB4	0x0	<a href="#">Page 172</a>
L2_LRN_CONSTRT_CNT	0xBB00C968	0x0	<a href="#">Page 166</a>
L2_LRN_CONSTRT_CTRL	0xBB00C964	0x7FFF8	<a href="#">Page 166</a>
L2_LRN_PORT_CONSTRT_CNT	0xBB00CA50	0x0	<a href="#">Page 167</a>
L2_LRN_PORT_CONSTRT_CTRL	0xBB00C96C	0x7FFF8	<a href="#">Page 167</a>
L2_LRN_TRK_CONSTRT_CNT	0xBB00CB9C	0x0	<a href="#">Page 168</a>
L2_LRN_TRK_CONSTRT_CTRL	0xBB00CB34	0x7FFF8	<a href="#">Page 168</a>
L2_LRN_VLAN_CONSTRT_CNT	0xBB00CD04	0x0	<a href="#">Page 169</a>
L2_LRN_VLAN_CONSTRT_ENTRY	0xBB00CC04	0xFFFF	<a href="#">Page 168</a>

Register	Address	Default	Reference
L2_MC	<Indirect> Type:0 Size:32768	—	<a href="#">Page 175</a>
L2_NTFY_CTRL	0xBB00CDC8	0xE21FFE	<a href="#">Page 177</a>
L2_NTFY_IF_INTR_MSK	0xBB0009E4	0x0	<a href="#">Page 180</a>
L2_NTFY_IF_INTR_STS	0xBB0009E8	0x0	<a href="#">Page 181</a>
L2_NTFY_NIC_FIFO_STS	0xBB0009EC	0x1000000	<a href="#">Page 181</a>
L2_NTFY_NIC_TIMEOUT	0xBB0009F0	0x1000A	<a href="#">Page 182</a>
L2_NTFY_PKT_CTRL	0xBB005704	0x1040	<a href="#">Page 178</a>
L2_NTFY_PKT_FIFO_STS	0xBB000584	0x1000000	<a href="#">Page 182</a>
L2_NTFY_PKT_ITAG	0xBB00571C	0x0	<a href="#">Page 179</a>
L2_NTFY_PKT_MAC	0xBB00570C	0x0	<a href="#">Page 179</a>
L2_NTFY_PKT_MAGIC_NUM	0xBB000580	0x0	<a href="#">Page 179</a>
L2_NTFY_PKT_QUE_ID	0xBB000588	0x0	<a href="#">Page 182</a>
L2_NTFY_PKT_TIMEOUT	0xBB005708	0xA	<a href="#">Page 178</a>
L2_NTFY_RING_BASE_ADDR	0xBB0009DC	0x0	<a href="#">Page 180</a>
L2_NTFY_RING_CUR_ADDR	0xBB0009E0	0x0	<a href="#">Page 180</a>
L2_NTFY_RST	0xBB00140C	0x0	<a href="#">Page 179</a>
L2_PORT_AGE_CTRL	0xBB00C808	0x1	<a href="#">Page 160</a>
L2_PORT_DABLK_CTRL	0xBB00C8EC	0x0	<a href="#">Page 165</a>
L2_PORT_DYN_MV_ACT	0xBB00C848	0x0	<a href="#">Page 162</a>
L2_PORT_DYN_MV_LRN	0xBB00C860	0x1	<a href="#">Page 162</a>
L2_PORT_MV_FORBID	0xBB00C8D4	0x0	<a href="#">Page 163</a>
L2_PORT_MV_FORBID_CTRL	0xBB00C8E0	0x0	<a href="#">Page 164</a>
L2_PORT_NEW_SA_FWD	0xBB00C830	0x0	<a href="#">Page 161</a>
L2_PORT_SABLK_CTRL	0xBB00C8E4	0x0	<a href="#">Page 164</a>
L2_PORT_SALRN	0xBB00C820	0x0	<a href="#">Page 161</a>
L2_PORT_UC_LM_ACT	0xBB00C904	0x0	<a href="#">Page 165</a>
L2_SA_ACT_REF	0xBB00CDAC	0x0	<a href="#">Page 171</a>
L2_SRC_P_FLTR	0xBB00CDA4	0x1	<a href="#">Page 171</a>
L2_TBL_FLUSH_CTRL	0xBB00CD9C	0x0	<a href="#">Page 170</a>
L2_TNL_LST	<Indirect> Type:12 Size:8192	—	<a href="#">Page 440</a>
L2_TNL_MCAST	<Indirect> Type:0 Size:32768	—	<a href="#">Page 441</a>
L2_TNL_UCAST	<Indirect> Type:0 Size:32768	—	<a href="#">Page 441</a>
L2_TNL_VXLAN	<Indirect> Type:10 Size:2048	—	<a href="#">Page 442</a>
L2_TNL_VXLAN_ENCAP	<Indirect> Type:3 Size:2112	—	<a href="#">Page 442</a>
L2_TRK_AGE_CTRL	0xBB00C810	0x1	<a href="#">Page 160</a>
L2_TRK_MV_FORBID	0xBB00C8DC	0x0	<a href="#">Page 164</a>

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
L2_TUNNEL_CTRL	0xBB00CDD0	0xC0F70018	<a href="#">Page 437</a>
L2_UC	<Indirect> Type:0 Size:32768	—	<a href="#">Page 175</a>
L2_UNKN_UC_FLD_PMSK	0xBB00C8F4	0xFFFFFFFF	<a href="#">Page 165</a>
L2_VLAN_CONSTRT_CTRL	0xBB00CD84	0x0	<a href="#">Page 169</a>
L3_ECMP	<Indirect> Type:5 Size:256	—	<a href="#">Page 274</a>
L3_ECMP_HASH_CTRL	0xBB00F268	0x0	<a href="#">Page 271</a>
L3_EGR_INTF	<Indirect> Type:8 Size:1024	—	<a href="#">Page 276</a>
L3_EGR_INTF_CNTR	<Indirect> Type:13 Size:1024	—	<a href="#">Page 277</a>
L3_EGR_INTF_LIST	<Indirect> Type:9 Size:16384	—	<a href="#">Page 277</a>
L3_EGR_TUNNEL_INTF_CNTR	<Indirect> Type:15 Size:384	—	<a href="#">Page 278</a>
L3_ENTRY_MV_CTRL	0xBB00F260	0x0	<a href="#">Page 270</a>
L3_ENTRY_MV_PARAM	0xBB00F264	0x0	<a href="#">Page 270</a>
L3_HOST_ROUTE_IP6MC_0	<Indirect> Type:3 Size:12288	—	<a href="#">Page 278</a>
L3_HOST_ROUTE_IP6MC_1	<Indirect> Type:3 Size:12288	—	<a href="#">Page 280</a>
L3_HOST_ROUTE_IP6MC_2	<Indirect> Type:3 Size:12288	—	<a href="#">Page 281</a>
L3_HOST_ROUTE_IP6MC_3	<Indirect> Type:3 Size:12288	—	<a href="#">Page 281</a>
L3_HOST_ROUTE_IP6MC_4	<Indirect> Type:3 Size:12288	—	<a href="#">Page 282</a>
L3_HOST_ROUTE_IP6MC_5	<Indirect> Type:3 Size:12288	—	<a href="#">Page 282</a>
L3_HOST_ROUTE_IP6UC_0	<Indirect> Type:3 Size:12288	—	<a href="#">Page 283</a>
L3_HOST_ROUTE_IP6UC_1	<Indirect> Type:3 Size:12288	—	<a href="#">Page 284</a>
L3_HOST_ROUTE_IP6UC_2	<Indirect> Type:3 Size:12288	—	<a href="#">Page 284</a>

Register	Address	Default	Reference
L3_HOST_ROUTE_IPMC_0	<Indirect> Type:3 Size:12288	—	<a href="#">Page 285</a>
L3_HOST_ROUTE_IPMC_1	<Indirect> Type:3 Size:12288	—	<a href="#">Page 286</a>
L3_HOST_ROUTE_IPUC	<Indirect> Type:3 Size:12288	—	<a href="#">Page 287</a>
L3_HOST_TBL_CTRL	0xBB00F004	0x0	<a href="#">Page 262</a>
L3_HW_LU_CTRL	0xBB00F2C0	0x0	<a href="#">Page 274</a>
L3_HW_LU_KEY_CTRL	0xBB00F29C	0x0	<a href="#">Page 273</a>
L3_HW_LU_KEY_DIP_CTRL	0xBB00F2B0	0x0	<a href="#">Page 274</a>
L3_HW_LU_KEY_SIP_CTRL	0xBB00F2A0	0x0	<a href="#">Page 273</a>
L3_IGR_INTF	<Indirect> Type:7 Size:1024	—	<a href="#">Page 288</a>
L3_IGR_INTF_CNTR	<Indirect> Type:12 Size:1024	—	<a href="#">Page 290</a>
L3_IGR_TUNNEL_INTF_CNTR	<Indirect>	—	<a href="#">Page 290</a>
L3_INTF_IP6_MTU	0xBB00F220	0x5DC	<a href="#">Page 269</a>
L3_INTF_IP_MTU	0xBB00F1E0	0x5DC	<a href="#">Page 269</a>
L3_IP6MC_ROUTE_CTRL	0xBB00F014	0x1DB3E	<a href="#">Page 266</a>
L3_IP6UC_ROUTE_CTRL	0xBB00F00C	0xA200	<a href="#">Page 264</a>
L3_IPMC_ROUTE_CTRL	0xBB00F010	0x73E	<a href="#">Page 265</a>
L3_IPUC_ROUTE_CTRL	0xBB00F008	0x1000	<a href="#">Page 262</a>
L3_IP_ROUTE_CTRL	0xBB00F000	0xF	<a href="#">Page 261</a>
L3_MONT_CNTR_CTRL	0xBB00F26C	0x0	<a href="#">Page 272</a>
L3_MONT_CNTR_DATA	0xBB00F27C	0x0	<a href="#">Page 272</a>
L3_NEXTHOP	<Indirect> Type:6 Size:8192	—	<a href="#">Page 291</a>
L3_PORT_IP6_ROUTE_CTRL	0xBB00F0FC	0x0	<a href="#">Page 268</a>
L3_PORT_IP_ROUTE_CTRL	0xBB00F018	0x0	<a href="#">Page 268</a>
L3_PREFIX_ROUTE_IP6MC_0	<Indirect> Type:4 Size:12288	—	<a href="#">Page 291</a>
L3_PREFIX_ROUTE_IP6MC_1	<Indirect> Type:4 Size:12288	—	<a href="#">Page 293</a>
L3_PREFIX_ROUTE_IP6MC_2	<Indirect> Type:4 Size:12288	—	<a href="#">Page 293</a>
L3_PREFIX_ROUTE_IP6MC_3	<Indirect> Type:4 Size:12288	—	<a href="#">Page 294</a>

Register	Address	Default	Reference
L3_PREFIX_ROUTE_IP6MC_4	<Indirect> Type:4 Size:12288	—	<a href="#">Page 295</a>
L3_PREFIX_ROUTE_IP6MC_5	<Indirect> Type:4 Size:12288	—	<a href="#">Page 295</a>
L3_PREFIX_ROUTE_IP6UC_0	<Indirect> Type:4 Size:12288	—	<a href="#">Page 296</a>
L3_PREFIX_ROUTE_IP6UC_1	<Indirect> Type:4 Size:12288	—	<a href="#">Page 297</a>
L3_PREFIX_ROUTE_IP6UC_2	<Indirect> Type:4 Size:12288	—	<a href="#">Page 297</a>
L3_PREFIX_ROUTE_IPMC_0	<Indirect> Type:4 Size:12288	—	<a href="#">Page 298</a>
L3_PREFIX_ROUTE_IPMC_1	<Indirect> Type:4 Size:12288	—	<a href="#">Page 300</a>
L3_PREFIX_ROUTE_IPUC	<Indirect> Type:4 Size:12288	—	<a href="#">Page 301</a>
L3_ROUTER_MAC	<Indirect> Type:2 Size:1024	—	<a href="#">Page 302</a>
LAG	<Indirect> Type:0 Size:128	—	<a href="#">Page 153</a>
LED_GLB_CTRL	0xBB000600	0x429654	<a href="#">Page 13</a>
LED_INDRT_ACCESS_CTRL	0xBB00076C	0x0	<a href="#">Page 22</a>
LED_PORT_COMBO_MASK_CTRL	0xBB000664	0x0	<a href="#">Page 19</a>
LED_PORT_COPR_MASK_CTRL	0xBB000654	0x0	<a href="#">Page 18</a>
LED_PORT_COPR_SET_SEL_CTRL	0xBB000634	0x0	<a href="#">Page 18</a>
LED_PORT_FIB_MASK_CTRL	0xBB00065C	0x0	<a href="#">Page 19</a>
LED_PORT_FIB_SET_SEL_CTRL	0xBB000644	0x0	<a href="#">Page 18</a>
LED_PORT_NUM_CTRL	0xBB000604	0x2	<a href="#">Page 15</a>
LED_PORT_SW_CTRL	0xBB00068C	0x0	<a href="#">Page 20</a>
LED_PORT_SW_EN_CTRL	0xBB000670	0x0	<a href="#">Page 20</a>
LED_SET_0_CTRL0	0xBB000630	0xA200B80	<a href="#">Page 17</a>
LED_SET_0_CTRL1	0xBB00062C	0xA408000	<a href="#">Page 17</a>
LED_SET_1_CTRL0	0xBB000628	0xA800B00	<a href="#">Page 17</a>
LED_SET_1_CTRL1	0xBB000624	0xC0008000	<a href="#">Page 16</a>
LED_SET_2_CTRL0	0xBB000620	0x2200280	<a href="#">Page 16</a>
LED_SET_2_CTRL1	0xBB00061C	0x2010BFF	<a href="#">Page 16</a>
LED_SET_3_CTRL0	0xBB000618	0xA200A01	<a href="#">Page 15</a>
LED_SET_3_CTRL1	0xBB000614	0xA400A80	<a href="#">Page 15</a>
LINK_DELAY_CTRL	0xBB000CD8	0x56	<a href="#">Page 77</a>
LOCAL_PORT_TRK_MAP	0xBB004CAC	0x0	<a href="#">Page 153</a>

**M**

All registers and tables starting with a M.

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
MAC_CPU_TAG_ID_CTRL	0xBB001364	0x8899	Page 70
MAC_EEE_ABLTY	0xBB000F08	0x0	Page 87
MAC_FORCE_MODE_CTRL	0xBB000DCC	0x32800	Page 82
MAC_GLB_CTRL	0xBB005550	0x2312	Page 68
MAC_L2_ADDR_CTRL	0xBB00135C	0x4C000000	Page 64
MAC_L2_CPU_CRC	0xBB00485C	0x1	Page 65
MAC_L2_CPU_MAX_LEN_CTRL	0xBB001368	0x18F863E	Page 90
MAC_L2_GLOBAL_CTRL1	0xBB005548	0x4D41A14A	Page 59
MAC_L2_GLOBAL_CTRL2	0xBB001358	0x1E208108	Page 61
MAC_L2_PADDING_SEL	0xBB00554C	0x0	Page 64
MAC_L2_PORT_CTRL	0xBB006000	0x193	Page 63
MAC_L2_PORT_MAX_LEN_CTRL	0xBB005554	0xC003000	Page 89
MAC_LINK_DUP_STS	0xBB000EF0	0x0	Page 86
MAC_LINK_MEDIA_STS	0xBB000EC8	0x0	Page 85
MAC_LINK_SPD_STS	0xBB000ED0	0x0	Page 86
MAC_LINK_STS	0xBB000EC0	0x0	Page 85
MAC_MSTR_SLV_STS	0xBB000F10	0x0	Page 88
MAC_PORT_CTRL	0xBB006004	0x3	Page 69
MAC_RLF_D_EN	0xBB000EB0	0x0	Page 84
MAC_RLF_D_STS	0xBB000EB8	0x0	Page 84
MAC_RX_PAUSE_STS	0xBB000F00	0x0	Page 87
MAC_SERDES_MODE_CTRL	0xBB00136C	0xFFFF	Page 90
MAC_SLV_TIMEOUT	0xBB00124C	0x7FFE	Page 13
MAC_TX_PAUSE_STS	0xBB000EF8	0x0	Page 86
MBIST_NIC_R	0xBB000934	0x0	Page 47
MBIST_NIC_W	0xBB000930	0x0	Page 46
MC_PMSK	<Indirect> Type:2 Size:4096	—	Page 176
MDIO_FREE_CNT_CTRL	0xBB000CDC	0x259	Page 78
METER	<Indirect> Type:1 Size:512	—	Page 210
METER_GLB_CTRL	0xBB00411C	0x0	Page 209
METER_LB_EXCEED_STS	0xBB004120	0x0	Page 209
METER_LB_GLB_EXCEED_STS	0xBB004160	0x0	Page 210
MIR_CTRL	0xBB00AF00	0x0	Page 353
MIR_DPM_CTRL	0xBB00AF30	0x0	Page 354
MIR_QID_CTRL	0xBB00AF58	0x0	Page 355
MIR_RSPAN_RX_TAG_RM_CTRL	0xBB00AF5C	0x0	Page 356
MIR_RSPAN_TX_CTRL	0xBB001420	0x0	Page 356
MIR_RSPAN_VLAN_CTRL	0xBB001410	0x81000000	Page 356
MIR_SAMPLE_RATE_CTRL	0xBB00AF50	0x0	Page 355

Register	Address	Default	Reference
MIR_SPM_CTRL	0xBB00AF10	0x0	<a href="#">Page 354</a>
MODEL_NAME_INFO	0xBB000004	0x93110000	<a href="#">Page 1</a>
MPLS_DECAP	<Indirect> Type:10 Size:2048	—	<a href="#">Page 314</a>
MPLS_DECAP_CAM	<Indirect> Type:11 Size:64	—	<a href="#">Page 315</a>
MPLS_DPINTPRI2TC_CTRL	0xBB004F50	0x0	<a href="#">Page 314</a>
MPLS_ENCAP	<Indirect> Type:2 Size:2048	—	<a href="#">Page 316</a>
MPLS_ENCAP_CTRL	0xBB004F4C	0x1	<a href="#">Page 314</a>
MPLS_GLB_CTRL	0xBB00F2E4	0x0	<a href="#">Page 313</a>
MPLS_NH	<Indirect> Type:12 Size:8192	—	<a href="#">Page 316</a>
MPLS_PARSE_CTRL	0xBB00052C	0x1	<a href="#">Page 313</a>
MSTI	<Indirect> Type:5 Size:128	—	<a href="#">Page 155</a>

## O

All registers and tables starting with a O.

Register	Address	Default	Reference
OAM_CTRL	0xBB008D40	0x0	<a href="#">Page 389</a>
OAM_GLB_DYING_GASP_CTRL	0xBB001424	0x260053	<a href="#">Page 390</a>
OAM_PORT_ACT_CTRL	0xBB008D44	0x3	<a href="#">Page 389</a>
OAM_PORT_DYING_GASP_CTRL	0xBB003058	0x0	<a href="#">Page 390</a>
OF_ACT_CTRL	0xBB00D408	0x1	<a href="#">Page 406</a>
OF_DMAC	<Indirect> Type:15 Size:1024	—	<a href="#">Page 434</a>
OF_EGR_TBL_CNTR	0xBB004F68	0x0	<a href="#">Page 409</a>
OF_EGR_TBL_MIS	0xBB004F64	0x0	<a href="#">Page 408</a>
OF_EXCPT_CTRL	0xBB004F70	0x0	<a href="#">Page 409</a>
OF_GRP_HASH_CTRL	0xBB00D414	0x7F	<a href="#">Page 408</a>
OF_IGR_TBL_CNTR	0xBB00D41C	0x0	<a href="#">Page 409</a>
OF_IGR_TBL_MIS	0xBB00D418	0x0	<a href="#">Page 408</a>
OF_L2_FLOW_TBL_CTRL	0xBB00D40C	0x20	<a href="#">Page 406</a>
OF_L3_FLOW_TBL_CTRL	0xBB00D410	0x40	<a href="#">Page 407</a>
OF_PORT_CTRL	0xBB00D000	0x0	<a href="#">Page 405</a>
OF_VLAN_AND_PORT_EN	0xBB00D208	0x0	<a href="#">Page 406</a>
OF_VLAN_EN	0xBB00D008	0x0	<a href="#">Page 405</a>
OUT_Q	<Indirect> Type:1 Size:56	—	<a href="#">Page 226</a>

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All registers and tables starting with a P.

<b>Register</b>	<b>Address</b>	<b>Default</b>	<b>Reference</b>
PARSER_CTRL	0xBB00056C	0x12B60004	<a href="#">Page 477</a>
PE_ETAG_CTRL	0xBB001444	0x1	<a href="#">Page 447</a>
PE_ETAG_MAC_CTRL	0xBB00572C	0x893F	<a href="#">Page 447</a>
PE_ETAG_RMK_CTRL	0xBB000228	0x0	<a href="#">Page 448</a>
PE_FWD	<Indirect> Type:0 Size:32768	—	<a href="#">Page 452</a>
PE_FWD_CAM	<Indirect> Type:1 Size:128	—	<a href="#">Page 452</a>
PE_PORT_ETAG_EGR_CTRL	0xBB00022C	0x1000000	<a href="#">Page 449</a>
PE_PORT_ETAG_IGR_CTRL	0xBB00CDD4	0x0	<a href="#">Page 448</a>
PE_PORT_ETAG_MAC_CTRL	0xBE006018	0x1	<a href="#">Page 447</a>
PE_PORT_PCID_CTRL	0xBE00CEB4	0x0	<a href="#">Page 450</a>
PIE_BLK_GROUP_CTRL	0xBB004194	0x0	<a href="#">Page 360</a>
PIE_BLK_LOOKUP_CTRL	0xBB004180	0x1	<a href="#">Page 359</a>
PIE_BLK_PHASE_CTRL	0xBB004184	0x0	<a href="#">Page 359</a>
PIE_BLK_TMPLTE_CTRL	0xBB004214	0x0	<a href="#">Page 360</a>
PIE_CLR_CTRL	0xBB0042D8	0x0	<a href="#">Page 361</a>
PIE_CTRL	0xBB0042DC	0x0	<a href="#">Page 362</a>
PIE_ENCAP_CTRL	0xBB004F5C	0x0	<a href="#">Page 365</a>
PIE_FIELD_SELTOR_CTRL	0xBB000530	0x0	<a href="#">Page 365</a>
PIE_CLB_HIT_INDICATION	0xBB0044E4	0x0	<a href="#">Page 364</a>
PIE_MISC	0xBB0044F4	0x0	<a href="#">Page 364</a>
PIE_MISC2	0xBB008008	0x0	<a href="#">Page 366</a>
PIE_MV_CTRL	0xBB0042D0	0x0	<a href="#">Page 361</a>
PIE_MV_LEN_CTRL	0xBB0042D4	0x0	<a href="#">Page 361</a>
PIE_RULE_HIT_INDICATION	0xBB0042E4	0x0	<a href="#">Page 363</a>
PIE_TAG_CTRL	0xBB0042E0	0x0	<a href="#">Page 363</a>
PIE_TMPLTE_CTRL	0xBB004294	0x0	<a href="#">Page 360</a>
PORT_ISO_CTRL	<Indirect> Type:1 Size:1024	—	<a href="#">Page 158</a>
PORT_ISO_RESTRICT_ROUTE_CTRL	0xBB008100	0x0	<a href="#">Page 157</a>
PORT_ISO_VB_EGR_CTRL	0xBB008B80	0x0	<a href="#">Page 158</a>
PORT_ISO_VB_ISO_PMSK_CTRL	0xBB008A00	0x0	<a href="#">Page 157</a>
PRI_SEL_CTRL	0xBB009000	0x0	<a href="#">Page 233</a>
PRI_SEL_PORT_CTRL	0xBB009004	0x0	<a href="#">Page 233</a>
PRI_SEL_PORT_TBL_IDX_CTRL	0xBB009064	0x0	<a href="#">Page 237</a>
PRI_SEL_REMAP_11E	0xBB009050	0x0	<a href="#">Page 236</a>
PRI_SEL_REMAP_1BR	0xBB00905C	0x0	<a href="#">Page 237</a>
PRI_SEL_REMAP_DSCP	0xBB009034	0x0	<a href="#">Page 236</a>
PRI_SEL_REMAP_IPRI_CFI0	0xBB009024	0x0	<a href="#">Page 234</a>
PRI_SEL_REMAP_IPRI_CFI1	0xBB009028	0x0	<a href="#">Page 235</a>

Register	Address	Default	Reference
PRI_SEL_REMAP_MPLS	0xBB009058	0x0	<a href="#">Page 236</a>
PRI_SEL_REMAP_OPRI_DEI0	0xBB00902C	0x0	<a href="#">Page 235</a>
PRI_SEL_REMAP_OPRI_DEI1	0xBB009030	0x0	<a href="#">Page 235</a>
PRI_SEL_REMAP_PORT	0xBB00900C	0x0	<a href="#">Page 234</a>
PRI_SEL_TBL_CTRL	0xBB009074	0x11112111	<a href="#">Page 237</a>
PROT_SERDSE_MUX_CTRL_0	0xBB0013BC	0x0	<a href="#">Page 91</a>
PROT_SERDSE_MUX_CTRL_1	0xBB0013C0	0x0	<a href="#">Page 93</a>

**Q**

All registers and tables starting with a Q.

Register	Address	Default	Reference
QM_CPUQID2QID_CTRL	0xBB00A9D4	0x0	<a href="#">Page 241</a>
QM_CPUQID2XGSQID_CTRL	0xBB00A9E4	0x0	<a href="#">Page 242</a>
QM_FLAG2CPUQID_CTRL_0	0xBB00AA20	0x0	<a href="#">Page 246</a>
QM_FLAG2CPUQID_CTRL_1	0xBB00AA24	0x0	<a href="#">Page 247</a>
QM_FLAG2CPUQID_CTRL_2	0xBB00AA28	0x0	<a href="#">Page 247</a>
QM_INTPRI2QID_CTRL	0xBB00A9D0	0x0	<a href="#">Page 241</a>
QM_RSN2CPUQID_CTRL_0	0xBB00A9F4	0x0	<a href="#">Page 242</a>
QM_RSN2CPUQID_CTRL_1	0xBB00A9F8	0x0	<a href="#">Page 243</a>
QM_RSN2CPUQID_CTRL_10	0xBB00AA1C	0x0	<a href="#">Page 246</a>
QM_RSN2CPUQID_CTRL_2	0xBB00A9FC	0x0	<a href="#">Page 243</a>
QM_RSN2CPUQID_CTRL_3	0xBB00AA00	0x0	<a href="#">Page 243</a>
QM_RSN2CPUQID_CTRL_4	0xBB00AA04	0x0	<a href="#">Page 244</a>
QM_RSN2CPUQID_CTRL_5	0xBB00AA08	0x0	<a href="#">Page 244</a>
QM_RSN2CPUQID_CTRL_6	0xBB00AA0C	0x0	<a href="#">Page 245</a>
QM_RSN2CPUQID_CTRL_7	0xBB00AA10	0x0	<a href="#">Page 245</a>
QM_RSN2CPUQID_CTRL_8	0xBB00AA14	0x0	<a href="#">Page 245</a>
QM_RSN2CPUQID_CTRL_9	0xBB00AA18	0x0	<a href="#">Page 246</a>

**R**

All registers and tables starting with a R.

Register	Address	Default	Reference
REMARK	<Indirect> Type:4 Size:1024	—	<a href="#">Page 257</a>
RMA_BPDU_FLD_PMSK	0xBB008950	0xFFFFFFFF	<a href="#">Page 145</a>
RMA_CTRL_0	0xBB008800	0x14	<a href="#">Page 135</a>
RMA_CTRL_1	0xBB008804	0x0	<a href="#">Page 137</a>
RMA_CTRL_2	0xBB008808	0x0	<a href="#">Page 139</a>
RMA_EAPOL_FLD_PMSK	0xBB008958	0xFFFFFFFF	<a href="#">Page 145</a>

Register	Address	Default	Reference
RMA_FLD_PMSK	0xBB008948	0xFFFFFFFF	<a href="#">Page 144</a>
RMA_LLDP_FLD_PMSK	0xBB008960	0xFFFFFFFF	<a href="#">Page 145</a>
RMA_MGN_LRNLN_CTRL	0xBB008818	0x7	<a href="#">Page 142</a>
RMA_MIRROR_CTRL	0xBB00880C	0x0	<a href="#">Page 141</a>
RMA_PORT_BPDU_CTRL	0xBB00881C	0x0	<a href="#">Page 142</a>
RMA_PORT_EAPOL_CTRL	0xBB008930	0x0	<a href="#">Page 144</a>
RMA_PORT_LLDP_CTRL	0xBB008918	0x0	<a href="#">Page 143</a>
RMA_PORT_PTP_CTRL	0xBB008834	0x0	<a href="#">Page 142</a>
RMA_SMAC_LRNLN_CTRL	0xBB008810	0x1	<a href="#">Page 141</a>
RMA_USR_DEF_CTRL	0xBB008970	0x80	<a href="#">Page 146</a>
RMA_USR_DEF_FLD_PMSK	0xBB008968	0xFFFFFFFF	<a href="#">Page 146</a>
RMK_CTRL	0xBB004D90	0x3	<a href="#">Page 250</a>
RMK_DP2DEI_CTRL	0xBB004ECC	0x0	<a href="#">Page 255</a>
RMK_DPINTPRI2DSCP_CTRL	0xBB004F1C	0x0	<a href="#">Page 257</a>
RMK_DSCP2DSCP_CTRL	0xBB004FE8	0x0	<a href="#">Page 256</a>
RMK_DSCP2IPRI_CTRL	0xBE004E84	0x0	<a href="#">Page 253</a>
RMK_DSCP2OPRI_CTRL	0xBB004EAC	0x0	<a href="#">Page 254</a>
RMK_INTPRI2DEI_CTRL	0xBB004EC8	0x0	<a href="#">Page 255</a>
RMK_INTPRI2DSCP_CTRL	0xBB004ED0	0x0	<a href="#">Page 255</a>
RMK_INTPRI2IPRI_CTRL	0xBB004E78	0x0	<a href="#">Page 252</a>
RMK_INTPRI2OPRI_CTRL	0xBB004EA0	0x0	<a href="#">Page 253</a>
RMK_IPRI2DSCP_CTRL	0xBB004ED8	0x0	<a href="#">Page 256</a>
RMK_IPRI2IPRI_CTRL	0xBB004E7C	0x0	<a href="#">Page 252</a>
RMK_IPRI2OPRI_CTRL	0xBB004EA4	0x0	<a href="#">Page 254</a>
RMK_OPRI2DSCP_CTRL	0xBE004EE0	0x0	<a href="#">Page 256</a>
RMK_OPRI2IPRI_CTRL	0xBB004E80	0x0	<a href="#">Page 253</a>
RMK_OPRI2OPRI_CTRL	0xBB004EA8	0x0	<a href="#">Page 254</a>
RMK_PORT_CTRL	0xBB004D94	0x0	<a href="#">Page 251</a>
RMT_ACCESS_SEM	0xBB0014AC	0x0	<a href="#">Page 460</a>
RMT_ACCESS_SEM_CTRL	0xBB0014CC	0x0	<a href="#">Page 460</a>
RMT_INTR_CTRL	0xBB0014D0	0x0	<a href="#">Page 474</a>
RMT_INTR_DATA_CCM	0xBB001514	0x0	<a href="#">Page 475</a>
RMT_INTR_DATA_LINK_STS	0xBB001534	0x0	<a href="#">Page 475</a>
RMT_INTR_INFO	0xBB0014D4	0x0	<a href="#">Page 474</a>
RMT_PHY_ACCESS_CFG	0xBB000FB4	0x0	<a href="#">Page 472</a>
RMT_PHY_ACCESS_CTRL	0xBB000FAC	0x0	<a href="#">Page 471</a>
RMT_PHY_ACCESS_DATA	0xBB000FB0	0x0	<a href="#">Page 472</a>
RMT_PHY_ACCESS_MMD_PARAM	0xBB000FC8	0x0	<a href="#">Page 473</a>
RMT_PHY_ACCESS_PARAM	0xBB000FC4	0x0	<a href="#">Page 473</a>
RMT_PHY_ACCESS_PHY_MSK_CTRL	0xBB000FB8	0x0	<a href="#">Page 472</a>
RMT_PHY_ACCESS_PORT_ID_CTRL	0xBB000FC0	0x0	<a href="#">Page 473</a>
RMT_REG_ACCESS_CFG	0xBB0014A8	0x0	<a href="#">Page 459</a>
RMT_REG_ACCESS_CTRL	0xBB0014A0	0x0	<a href="#">Page 458</a>
RMT_REG_ACCESS_DATA	0xBB0014A4	0x0	<a href="#">Page 459</a>
RMT_TBL_ACCESS_SET_0_CFG	0xBB00856C	0x0	<a href="#">Page 462</a>
RMT_TBL_ACCESS_SET_0_CTRL	0xBB008548	0x0	<a href="#">Page 460</a>

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RMT_TBL_ACCESS_SET_0_DATA	0xBB00854C	0x0	<a href="#">Page 462</a>
RMT_TBL_ACCESS_SET_0_L2_READ_MTHD_CTRL	0xBB008570	0x0	<a href="#">Page 463</a>
RMT_TBL_ACCESS_SET_1_CFG	0xBB004714	0x0	<a href="#">Page 464</a>
RMT_TBL_ACCESS_SET_1_CTRL	0xBB0046B8	0x0	<a href="#">Page 463</a>
RMT_TBL_ACCESS_SET_1_DATA	0xBB0046BC	0x0	<a href="#">Page 464</a>
RMT_TBL_ACCESS_SET_2_CFG	0xBB008590	0x0	<a href="#">Page 466</a>
RMT_TBL_ACCESS_SET_2_CTRL	0xBB008574	0x0	<a href="#">Page 465</a>
RMT_TBL_ACCESS_SET_2_DATA	0xBB008578	0x0	<a href="#">Page 466</a>
RMT_TBL_ACCESS_SET_3_CFG	0xBB004FC8	0x0	<a href="#">Page 468</a>
RMT_TBL_ACCESS_SET_3_CTRL	0xBB004FA0	0x0	<a href="#">Page 467</a>
RMT_TBL_ACCESS_SET_3_DATA	0xBB004FA4	0x0	<a href="#">Page 467</a>
RMT_TBL_ACCESS_SET_4_CFG	0xBB0030D8	0x0	<a href="#">Page 469</a>
RMT_TBL_ACCESS_SET_4_CTRL	0xBB003060	0x0	<a href="#">Page 468</a>
RMT_TBL_ACCESS_SET_4_DATA	0xBB003064	0x0	<a href="#">Page 469</a>
RMT_TBL_ACCESS_SET_5_CFG	0xBE007FF0	0x0	<a href="#">Page 471</a>
RMT_TBL_ACCESS_SET_5_CTRL	0xBE007F18	0x0	<a href="#">Page 470</a>
RMT_TBL_ACCESS_SET_5_DATA	0xBB007F1C	0x0	<a href="#">Page 470</a>
RNG_CHK_CTRL	0xBB0045D8	0x0	<a href="#">Page 377</a>
RNG_CHK_IP_CTRL	0xBB004658	0x0	<a href="#">Page 377</a>
RNG_CHK_IP_RNG	0xBB004678	0x0	<a href="#">Page 378</a>
RST_GLB_CTRL	0xBB000400	0xA60	<a href="#">Page 1</a>

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SCHED_CPU_Q_CTRL	0xBB002FC8	0x1	<a href="#">Page 249</a>
SCHED_CTRL	0xBB003050	0x8	<a href="#">Page 249</a>
SCHED_PORT_ALGO_CTRL	0xBB003048	0x0	<a href="#">Page 249</a>
SCHED_PORT_Q_CTRL_SET0	0xBB002888	0x1	<a href="#">Page 248</a>
SCHED_PORT_Q_CTRL_SET1	0xBB002F08	0x1	<a href="#">Page 248</a>
SC_DRAIN_OUT_THR	0xBB002884	0x140014	<a href="#">Page 230</a>
SC_PORT_TIMER	0xBB006014	0x20002	<a href="#">Page 230</a>
SEC_IP_MAC_BIND_CTRL	0xBB008C5C	0x0	<a href="#">Page 385</a>
SEC_PORT_IP_MAC_BIND_CTRL	0xBB008C60	0x0	<a href="#">Page 386</a>
SERDES_BC_CTRL	0xBB005640	0x1F	<a href="#">Page 98</a>
SERDES_INDRT_ACCESS_CTRL	0xBB005638	0x0	<a href="#">Page 96</a>
SERDES_INDRT_DATA_CTRL	0xBB00563C	0x0	<a href="#">Page 97</a>
SERDES_MISC_CTRL0	0xBB0013E0	0x0	<a href="#">Page 99</a>
SERDES_MISC_CTRL1	0xBB0013E4	0x0	<a href="#">Page 99</a>
SERDES_MODE_CTRL	0xBB0013CC	0x9F	<a href="#">Page 95</a>
SERDES_UNIDIR_CTRL	0xBB0013DC	0x0	<a href="#">Page 98</a>
SFLOW_CTRL	0xBB008400	0x0	<a href="#">Page 357</a>

Register	Address	Default	Reference
SFLOW_PORT_RATE_CTRL	0xBB008404	0x0	<a href="#">Page 357</a>
SMI_10GPHY_POLLING_SEL0	0xBB000CF0	0x1FA434	<a href="#">Page 80</a>
SMI_10GPHY_POLLING_SEL1	0xBB000CF4	0x1FA435	<a href="#">Page 80</a>
SMI_10GPHY_POLLING_SEL2	0xBB000CF8	0x11FA400	<a href="#">Page 80</a>
SMI_10GPHY_POLLING_SEL3	0xBB000CFC	0x13FA412	<a href="#">Page 81</a>
SMI_10GPHY_POLLING_SEL4	0xBB000D00	0x17FA414	<a href="#">Page 81</a>
SMI_10GPHY_RLFD_POLLING_SEL	0xBB000CE8	0x0	<a href="#">Page 79</a>
SMI_BYPASS_ABLTY_LOCK_CTRL	0xBB000CC4	0x0	<a href="#">Page 76</a>
SMI_GLB_CTRL0	0xBB000CC0	0x550	<a href="#">Page 74</a>
SMI_GLB_CTRL1	0xBB000CBC	0x40000	<a href="#">Page 72</a>
SMI_GPHY_POLLING_SEL	0xBB000CEC	0x1A	<a href="#">Page 79</a>
SMI_GPHY_RLFD_POLLING_SEL	0xBB000CE0	0xA470015	<a href="#">Page 78</a>
SMI_INDRT_ACCESS_BC_PHYID_CTRL	0xBB000C14	0x1F	<a href="#">Page 67</a>
SMI_INDRT_ACCESS_CTRL_0	0xBB000C00	0xFFFF800	<a href="#">Page 65</a>
SMI_INDRT_ACCESS_CTRL_1	0xBB000C04	0x1FF	<a href="#">Page 66</a>
SMI_INDRT_ACCESS_CTRL_2	0xBE000C08	0x0	<a href="#">Page 66</a>
SMI_INDRT_ACCESS_CTRL_3	0xBB000C10	0x0	<a href="#">Page 67</a>
SMI_INDRT_ACCESS_MMD_CTRL	0xBB000C18	0x0	<a href="#">Page 67</a>
SMI_PHY_ABLTY_GET_SEL	0xBB000CAC	0x0	<a href="#">Page 72</a>
SMI_PORT_ADDR_CTRL	0xBB000C74	0x0	<a href="#">Page 71</a>
SMI_PORT_POLLING_CTRL	0xBB000CCC	0x0	<a href="#">Page 77</a>
SMI_PORT_POLLING_SEL	0xBB000C9C	0x0	<a href="#">Page 71</a>
SMI_RLFD_POLLING_BIT_SEL	0xBB000CE4	0x0	<a href="#">Page 79</a>
SPD_FORCE_VAL	0xBB000F23	0x0	<a href="#">Page 89</a>
SPI_ADDR	0xBE001248	0x0	<a href="#">Page 12</a>
SPI_CTRL0	0xBB00103C	0x68024	<a href="#">Page 10</a>
SPI_CTRL1	0xBB001040	0x0	<a href="#">Page 11</a>
SPI_CTRL2	0xBB001044	0x0	<a href="#">Page 11</a>
SPI_DATA	0xBB001048	0x0	<a href="#">Page 12</a>
SRC_TRK_MAP	<Indirect> Type:13 Size:1024	—	<a href="#">Page 154</a>
STAT_BRIDGE_DOT1DTPLEARNEDENTRYDISCARDS	0xBB007F14	0x0	<a href="#">Page 321</a>
STAT_CNT_SET0_CTRL	0xBB005728	0x30000040	<a href="#">Page 321</a>
STAT_CNT_SET1_CTRL	0xBB005724	0x30000040	<a href="#">Page 320</a>
STAT_CTRL	0xBB005720	0xF	<a href="#">Page 320</a>
STAT_PORT_MIB_CNTR	<Indirect> Type:0 Size:57	—	<a href="#">Page 322</a>
STAT_PORT_PRVTE_CNTR	<Indirect> Type:1 Size:57	—	<a href="#">Page 327</a>
STAT_PORT_PRVTE_E_Q_CNTR	<Indirect> Type:14 Size:57	—	<a href="#">Page 329</a>
STAT_PORT_RST	0xBB007EF8	0x0	<a href="#">Page 319</a>
STAT_PRVTE_DBG_CNTR0	0xBB00D800	0x0	<a href="#">Page 330</a>
STAT_PRVTE_DBG_CNTR1	0xBB00D804	0x0	<a href="#">Page 330</a>
STAT_PRVTE_DBG_CNTR10	0xBB00D828	0x0	<a href="#">Page 332</a>

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STAT_PRVTE_DBG_CNTR11	0xBB00D82C	0x0	Page 333
STAT_PRVTE_DBG_CNTR12	0xBB00D830	0x0	Page 333
STAT_PRVTE_DBG_CNTR13	0xBB00D834	0x0	Page 333
STAT_PRVTE_DBG_CNTR14	0xBB00D838	0x0	Page 334
STAT_PRVTE_DBG_CNTR15	0xBB00D83C	0x0	Page 334
STAT_PRVTE_DBG_CNTR16	0xBB00D840	0x0	Page 334
STAT_PRVTE_DBG_CNTR17	0xBB00D844	0x0	Page 334
STAT_PRVTE_DBG_CNTR18	0xBB00D848	0x0	Page 335
STAT_PRVTE_DBG_CNTR19	0xBB00D84C	0x0	Page 335
STAT_PRVTE_DBG_CNTR2	0xBB00D808	0x0	Page 330
STAT_PRVTE_DBG_CNTR20	0xBB00D850	0x0	Page 335
STAT_PRVTE_DBG_CNTR21	0xBB00D854	0x0	Page 335
STAT_PRVTE_DBG_CNTR22	0xBB00D858	0x0	Page 336
STAT_PRVTE_DBG_CNTR23	0xBB00D85C	0x0	Page 336
STAT_PRVTE_DBG_CNTR24	0xBB00D860	0x0	Page 336
STAT_PRVTE_DBG_CNTR25	0xBE00D364	0x0	Page 337
STAT_PRVTE_DBG_CNTR26	0xBB00D868	0x0	Page 337
STAT_PRVTE_DBG_CNTR27	0xBB00D86C	0x0	Page 337
STAT_PRVTE_DBG_CNTR28	0xBB00D870	0x0	Page 337
STAT_PRVTE_DBG_CNTR29	0xBB00D874	0x0	Page 338
STAT_PRVTE_DBG_CNTR3	0xBB00D80C	0x0	Page 330
STAT_PRVTE_DBG_CNTR30	0xBB00D878	0x0	Page 338
STAT_PRVTE_DBG_CNTR31	0xBB00D87C	0x0	Page 338
STAT_PRVTE_DBG_CNTR32	0xBB00D880	0x0	Page 339
STAT_PRVTE_DBG_CNTR33	0xBE00D884	0x0	Page 339
STAT_PRVTE_DBG_CNTR34	0xBB00D888	0x0	Page 339
STAT_PRVTE_DBG_CNTR35	0xBB00D88C	0x0	Page 339
STAT_PRVTE_DBG_CNTR36	0xBB00D890	0x0	Page 340
STAT_PRVTE_DBG_CNTR37	0xBB00D894	0x0	Page 340
STAT_PRVTE_DBG_CNTR38	0xBB00D898	0x0	Page 340
STAT_PRVTE_DBG_CNTR39	0xBB00D89C	0x0	Page 340
STAT_PRVTE_DBG_CNTR4	0xBB00D810	0x0	Page 331
STAT_PRVTE_DBG_CNTR40	0xBB00D8A0	0x0	Page 341
STAT_PRVTE_DBG_CNTR41	0xBB00D8A4	0x0	Page 341
STAT_PRVTE_DBG_CNTR42	0xBB00D8A8	0x0	Page 341
STAT_PRVTE_DBG_CNTR43	0xBB00D8AC	0x0	Page 342
STAT_PRVTE_DBG_CNTR44	0xBB00D8B0	0x0	Page 342
STAT_PRVTE_DBG_CNTR45	0xBB00D8B4	0x0	Page 342
STAT_PRVTE_DBG_CNTR46	0xBB00D8B8	0x0	Page 342
STAT_PRVTE_DBG_CNTR47	0xBB00D8BC	0x0	Page 343
STAT_PRVTE_DBG_CNTR48	0xBB00D8C0	0x0	Page 343
STAT_PRVTE_DBG_CNTR49	0xBB00D8C4	0x0	Page 343
STAT_PRVTE_DBG_CNTR5	0xBB00D814	0x0	Page 331
STAT_PRVTE_DBG_CNTR50	0xBB00D8C8	0x0	Page 344
STAT_PRVTE_DBG_CNTR51	0xBB00D8CC	0x0	Page 344
STAT_PRVTE_DBG_CNTR52	0xBB00D8D0	0x0	Page 344

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STAT_PRVTE_DBG_CNTR53	0xBB00D8D4	0x0	Page 344
STAT_PRVTE_DBG_CNTR54	0xBB00D8D8	0x0	Page 345
STAT_PRVTE_DBG_CNTR55	0xBB00D8DC	0x0	Page 345
STAT_PRVTE_DBG_CNTR56	0xBB00D8E0	0x0	Page 345
STAT_PRVTE_DBG_CNTR57	0xBB00D8E4	0x0	Page 345
STAT_PRVTE_DBG_CNTR58	0xBB00D8E8	0x0	Page 346
STAT_PRVTE_DBG_CNTR59	0xBB00D8EC	0x0	Page 346
STAT_PRVTE_DBG_CNTR6	0xBB00D818	0x0	Page 331
STAT_PRVTE_DBG_CNTR60	0xBB00D8F0	0x0	Page 346
STAT_PRVTE_DBG_CNTR61	0xBB00D8F4	0x0	Page 347
STAT_PRVTE_DBG_CNTR62	0xBB00D8F8	0x0	Page 347
STAT_PRVTE_DBG_CNTR63	0xBB00D8FC	0x0	Page 347
STAT_PRVTE_DBG_CNTR64	0xBB00D900	0x0	Page 347
STAT_PRVTE_DBG_CNTR65	0xBB00D904	0x0	Page 348
STAT_PRVTE_DBG_CNTR66	0xBB00D908	0x0	Page 348
STAT_PRVTE_DBG_CNTR67	0xBE00D90C	0x0	Page 348
STAT_PRVTE_DBG_CNTR68	0xBB00D910	0x0	Page 349
STAT_PRVTE_DBG_CNTR69	0xBB00D914	0x0	Page 349
STAT_PRVTE_DBG_CNTR7	0xBB00D81C	0x0	Page 332
STAT_PRVTE_DBG_CNTR70	0xBB00D918	0x0	Page 349
STAT_PRVTE_DBG_CNTR71	0xBB00D91C	0x0	Page 349
STAT_PRVTE_DBG_CNTR72	0xBB00D920	0x0	Page 350
STAT_PRVTE_DBG_CNTR73	0xBB00D924	0x0	Page 350
STAT_PRVTE_DBG_CNTR74	0xBB00D928	0x0	Page 350
STAT_PRVTE_DBG_CNTR75	0xBE00D92C	0x0	Page 351
STAT_PRVTE_DBG_CNTR76	0xBB00D930	0x0	Page 351
STAT_PRVTE_DBG_CNTR77	0xBB00D934	0x0	Page 351
STAT_PRVTE_DBG_CNTR78	0xBB00D938	0x0	Page 351
STAT_PRVTE_DBG_CNTR79	0xBB00D93C	0x0	Page 352
STAT_PRVTE_DBG_CNTR8	0xBB00D820	0x0	Page 332
STAT_PRVTE_DBG_CNTR80	0xBB00D940	0x0	Page 352
STAT_PRVTE_DBG_CNTR9	0xBB00D824	0x0	Page 332
STAT_RST	0xBB007EF4	0x0	Page 319
STK_DEV_PORT_MAP_CTRL	0xBB00145C	0x0	Page 458
STK_GBL_CTRL	0xBB001448	0x0	Page 457
STK_NON_UNICAST_BLOCK_CTRL	0xBB00147C	0x0	Page 458
STK_PORT_ID_CTRL	0xBB00144C	0x3F	Page 457
STORM_CTRL	0xBB00B000	0x0	Page 185
STORM_LB_CTRL	0xBB00B004	0x3500AB	Page 186
STORM_LB_PPS_CTRL	0xBB00B008	0x271	Page 187
STORM_LB_PROTO_CTRL	0xBB00A000	0x135F1B	Page 187
STORM_PORT_BC_CTRL	0xBB00B3C4	0x8000	Page 191
STORM_PORT_BC_EXCEED_FLAG	0xBB00B594	0x0	Page 192
STORM_PORT_BC_LB_RST	0xBB00B58C	0x0	Page 191
STORM_PORT_CTRL	0xBB00B00C	0x0	Page 187
STORM_PORT_MC_CTRL	0xBB00B1EC	0x8000	Page 189

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STORM_PORT_MC_EXCEED_FLAG	0xBB00B3BC	0x0	<a href="#">Page 190</a>
STORM_PORT_MC_LB_RST	0xBB00B3B4	0x0	<a href="#">Page 190</a>
STORM_PORT_PROTO_ARP_CTRL	0xBB00A2E0	0x1FF20	<a href="#">Page 196</a>
STORM_PORT_PROTO_ARP_EXCEED_FLAG	0xBB00A3CC	0x0	<a href="#">Page 196</a>
STORM_PORT_PROTO_ARP_LB_RST	0xBB00A3C4	0x0	<a href="#">Page 196</a>
STORM_PORT_PROTO_BPDU_CTRL	0xBB00A0F8	0x1FF20	<a href="#">Page 193</a>
STORM_PORT_PROTO_BPDU_EXCEED_FLAG	0xBB00A1E4	0x0	<a href="#">Page 194</a>
STORM_PORT_PROTO_BPDU_LB_RST	0xBB00A1DC	0x0	<a href="#">Page 194</a>
STORM_PORT_PROTO_DHCP_CTRL	0xBB00A004	0x1FF20	<a href="#">Page 192</a>
STORM_PORT_PROTO_DHCP_EXCEED_FLAG	0xBB00A0F0	0x0	<a href="#">Page 193</a>
STORM_PORT_PROTO_DHCP_LB_RST	0xBB00A0E8	0x0	<a href="#">Page 192</a>
STORM_PORT_PROTO_IGMP_CTRL	0xBB00A1EC	0x1FF20	<a href="#">Page 194</a>
STORM_PORT_PROTO_IGMP_EXCEED_FLAG	0xBB00A2D8	0x0	<a href="#">Page 195</a>
STORM_PORT_PROTO_IGMP_LB_RST	0xBB00A2D0	0x0	<a href="#">Page 195</a>
STORM_PORT_UC_CTRL	0xBB00B014	0x8000	<a href="#">Page 188</a>
STORM_PORT_UC_EXCEED_FLAG	0xBE00B1E4	0x0	<a href="#">Page 189</a>
STORM_PORT_UC_LB_RST	0xBB00B1DC	0x0	<a href="#">Page 188</a>
ST_CTRL	0xBB008000	0x0	<a href="#">Page 155</a>
SWRED_DROP_CNTR	0xBB008608	0x0	<a href="#">Page 229</a>
SWRED_DROP_CNTR_CIDX	0xBB008604	0x0	<a href="#">Page 229</a>
SWRED_DROP_CNTR_PIDX	0xBB008600	0x0	<a href="#">Page 228</a>
SWRED_DROP_CNTR_RST	0xBB008610	0x0	<a href="#">Page 230</a>
SWRED_Q_DROP_RATE	0xBB0027C4	0x0	<a href="#">Page 228</a>
SWRED_Q_THR	0xBB0027F4	0x0	<a href="#">Page 228</a>
SW_LED_LOAD	0xBE00066C	0x0	<a href="#">Page 19</a>

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TBL_ACCESS_CTRL_0	0xBB008500	0x0	<a href="#">Page 48</a>
TBL_ACCESS_CTRL_1	0xBB0040C0	0x0	<a href="#">Page 51</a>
TBL_ACCESS_CTRL_2	0xBB008528	0x0	<a href="#">Page 52</a>
TBL_ACCESS_CTRL_3	0xBB000200	0x0	<a href="#">Page 54</a>
TBL_ACCESS_CTRL_4	0xBB0020DC	0x0	<a href="#">Page 55</a>
TBL_ACCESS_CTRL_5	0xBB007E1C	0x0	<a href="#">Page 56</a>
TBL_ACCESS_DATA_0	0xBB008508	0x0	<a href="#">Page 51</a>
TBL_ACCESS_DATA_1	0xBB0040C4	0x0	<a href="#">Page 52</a>
TBL_ACCESS_DATA_2	0xBB00852C	0x0	<a href="#">Page 53</a>
TBL_ACCESS_DATA_3	0xBB000204	0x0	<a href="#">Page 54</a>
TBL_ACCESS_DATA_4	0xBB0020E0	0x0	<a href="#">Page 55</a>
TBL_ACCESS_DATA_5	0xBB007E20	0x0	<a href="#">Page 56</a>
TBL_ACCESS_L2_METHOD_CTRL	0xBB008504	0x0	<a href="#">Page 50</a>
TRAP_ARP_GRAT_PORT_ACT	0xBB008C04	0x0	<a href="#">Page 48</a>

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TRAP_CTRL	0xBB008C00	0x1	<a href="#">Page 48</a>
TRK_CTRL	0xBB00BA78	0x44	<a href="#">Page 149</a>
TRK_HASH_CTRL	0xBB00BA70	0x3FFF	<a href="#">Page 148</a>
TRK_ID_CTRL	0xBB00B800	0x0	<a href="#">Page 147</a>
TRK_LOCAL_TBL	0xBB00BA84	0x3FFFFFFBE	<a href="#">Page 151</a>
TRK_LOCAL_TBL_REFRESH	0xBB00BA80	0x0	<a href="#">Page 150</a>
TRK_MBR_CTRL	0xBB00B8D0	0x0	<a href="#">Page 148</a>
TRK_SHFT_CTRL	0xBB00BA7C	0x0	<a href="#">Page 150</a>
TRK_STK_CTRL	0xBB00BE94	0x0	<a href="#">Page 152</a>
TUNNEL_6RD_DOMAIN	0xBB00F2CC	0x0	<a href="#">Page 307</a>
TUNNEL_IP_ADDR_CHK_CTRL	0xBB00F2C4	0x0	<a href="#">Page 305</a>
TUNNEL_IP_IDENTIFICATION	0xBB004F48	0x0	<a href="#">Page 306</a>
TUNNEL_QOS_PROFILE	0xBB000100	0x0	<a href="#">Page 307</a>
TUNNEL_ROUTE_CTRL	0xBB00F2C8	0x0	<a href="#">Page 306</a>
TUNNEL_START	<Indirect> Type:9 Size:384	—	<a href="#">Page 308</a>
TUNNEL_START_ENCAP	<Indirect> Type:5 Size:384	—	<a href="#">Page 309</a>
TUNNEL_START_ENCAP_1	<Indirect> Type:5 Size:384	—	<a href="#">Page 310</a>
TUNNEL_START_ENCAP_2	<Indirect> Type:5 Size:384	—	<a href="#">Page 310</a>
TUNNEL_TERMINATION	<Indirect> Type:8 Size:384	—	<a href="#">Page 310</a>
TUNNEL_TERMINATION_1	<Indirect> Type:8 Size:384	—	<a href="#">Page 312</a>
TUNNEL_TERMINATION_2	<Indirect> Type:8 Size:384	—	<a href="#">Page 312</a>
TUNNEL_VXLAN_CTRL	0xBB000528	0x12B5	<a href="#">Page 308</a>

## U

All registers and tables starting with a U.

Register	Address	Default	Reference
USXGMII_SUBMODE_CTRL	0xBB0013E8	0x0	<a href="#">Page 99</a>

## V

All registers and tables starting with a V.

Register	Address	Default	Reference
VACL	<Indirect> Type:0 Size:4096	—	<a href="#">Page 373</a>

Register	Address	Default	Reference
VLAN	<Indirect> Type:3 Size:4096	—	<a href="#">Page 116</a>
VLAN_APP_PKT_CTRL	0xBB0096B0	0x0	<a href="#">Page 113</a>
VLAN_CTRL	0xBB0094E4	0x0	<a href="#">Page 111</a>
VLAN_EGR_VID RNG_CHK_SET_0	0xBB004944	0x0	<a href="#">Page 122</a>
VLAN_EGR_VID RNG_CHK_SET_1	0xBB0049C4	0x0	<a href="#">Page 122</a>
VLAN_EGR_VID RNG_CHK_SET_2	0xBB004A44	0x0	<a href="#">Page 123</a>
VLAN_EGR_VID RNG_CHK_SET_3	0xBB004AC4	0x0	<a href="#">Page 123</a>
VLAN_ETAG_TPID_CTRL	0xBB00051C	0x8100	<a href="#">Page 109</a>
VLAN_EVC	<Indirect> Type:1 Size:1024	—	<a href="#">Page 129</a>
VLAN_EVC_ENTRY_INDICATION	0xBB004C28	0x0	<a href="#">Page 128</a>
VLAN_EVC_ENTRY_INDICATION_AGGR	0xBB004CA8	0x0	<a href="#">Page 128</a>
VLAN_IGR_VID RNG_CHK_SET_0	0xBB009C00	0x0	<a href="#">Page 120</a>
VLAN_IGR_VID RNG_CHK_SET_1	0xBB009C80	0x0	<a href="#">Page 120</a>
VLAN_IGR_VID RNG_CHK_SET_2	0xBB009D00	0x0	<a href="#">Page 121</a>
VLAN_IGR_VID RNG_CHK_SET_3	0xBB009D80	0x0	<a href="#">Page 121</a>
VLAN_IP_BASED	<Indirect> Type:4 Size:2048	—	<a href="#">Page 130</a>
VLAN_IVC	<Indirect> Type:4 Size:2048	—	<a href="#">Page 131</a>
VLAN_IVC_BLK_CTRL	0xBB009E00	0x0	<a href="#">Page 124</a>
VLAN_IVC_ENTRY_INDICATION	0xBB009EE8	0x0	<a href="#">Page 128</a>
VLAN_IVC_ENTRY_INDICATION_AGGR	0xBB009FE8	0x0	<a href="#">Page 128</a>
VLAN_L2TBL_CNVT_CTRL	0xBB0096CC	0x0	<a href="#">Page 124</a>
VLAN_MAC_BASED	<Indirect> Type:4 Size:2048	—	<a href="#">Page 132</a>
VLAN_PORT_AFT	0xBB009400	0xF	<a href="#">Page 111</a>
VLAN_PORT_EGR_FLTR	0xBB0096C4	0x1	<a href="#">Page 115</a>
VLAN_PORT_ETAG_TPID_CMP	0xBB000520	0x0	<a href="#">Page 110</a>
VLAN_PORT_EVC_CTRL	0xBB004B44	0x0	<a href="#">Page 127</a>
VLAN_PORT_FWD_CTRL	0xBB0095CC	0xC	<a href="#">Page 113</a>
VLAN_PORT_IGR_CTRL	0xBB0094E8	0x8006001	<a href="#">Page 112</a>
VLAN_PORT_IGR_FLTR	0xBB0096B4	0x1	<a href="#">Page 114</a>
VLAN_PORT_ITAG_TPID_CMP_MSK	0xBB006010	0x1	<a href="#">Page 110</a>
VLAN_PORT_IVC_CTRL	0xBB009E04	0x0	<a href="#">Page 126</a>
VLAN_PORT_L2TBL_CNVT_CTRL	0xBB0096D0	0x0	<a href="#">Page 124</a>
VLAN_PORT_OTAG_TPID_CMP_MSK	0xBB00600C	0x0	<a href="#">Page 109</a>
VLAN_PORT_PPB_VLAN_SET	0xBB00C020	0x0	<a href="#">Page 119</a>
VLAN_PORT_TAG_CTRL	0xBB004860	0x3C9	<a href="#">Page 115</a>
VLAN_PPB_VLAN_SET	0xBB00C000	0x0	<a href="#">Page 119</a>
VLAN_PROFILE_SET	0xBB009800	0xFFFFFFFF	<a href="#">Page 117</a>
VLAN_TAG_TPID_CTRL	0xBB0013FC	0x88A88100	<a href="#">Page 109</a>

Register	Address	Default	Reference
VLAN_TRUNK_L2TBL_CNVT_CTRL_0	0xBB0097E4	0x0	<a href="#">Page 126</a>
VLAN_TRUNK_L2TBL_CNVT_CTRL_1	0xBB0097D4	0x0	<a href="#">Page 126</a>
VLAN_TRUNK_L2TBL_CNVT_CTRL_2	0xBB0097C4	0x0	<a href="#">Page 125</a>
VLAN_TRUNK_L2TBL_CNVT_CTRL_3	0xBB0097B4	0x0	<a href="#">Page 125</a>
VLAN_UNTAG	<Indirect> Type:0 Size:4096	—	<a href="#">Page 117</a>
VXLAN_TUNNEL_START_0	<Indirect> Type:9 Size:384	—	<a href="#">Page 443</a>
VXLAN_TUNNEL_START_1	<Indirect> Type:9 Size:384	—	<a href="#">Page 444</a>

## W

All registers and tables starting with a W.

Register	Address	Default	Reference
WDOG_RST_STS	0xBB000408	0x0	<a href="#">Page 3</a>
WLC_MCAST	<Indirect> Type:0 Size:32768	—	<a href="#">Page 444</a>
WLC_UCAST	<Indirect> Type:0 Size:32768	—	<a href="#">Page 444</a>

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## Appendix A: Memory Map

Register memory map of the chip design. This section is a quick reference to all the tables and register in this datasheet.

<b>0xBB000000</b>
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Address	Register	Len
0xBB008C24	ACL_CTRL.RESERVED[31:3]	29
0xBB008C24	ACL_CTRL.LB_LIMIT[2:0]	3
0xBB0044F8	ACL_PORT_LOOKUP_CTRL [0].RESERVED[31:2]	30
0xBB0044F8	ACL_PORT_LOOKUP_CTRL [0].EGR_LUT[1:1]	1
0xBB0044F8	ACL_PORT_LOOKUP_CTRL [0].IGR_LUT[0:0]	1
0xBB0044FC	ACL_PORT_LOOKUP_CTRL [1].RESERVED[31:2]	30
0xBB0044FC	ACL_PORT_LOOKUP_CTRL [1].EGR_LUT[1:1]	1
0xBB0044FC	ACL_PORT_LOOKUP_CTRL [1].IGR_LUT[0:0]	1
0xBB004500	ACL_PORT_LOOKUP_CTRL [2].RESERVED[31:2]	30
0xBB004500	ACL_PORT_LOOKUP_CTRL [2].EGR_LUT[1:1]	1
0xBB004500	ACL_PORT_LOOKUP_CTRL [2].IGR_LUT[0:0]	1
0xBB004504	ACL_PORT_LOOKUP_CTRL [3].RESERVED[31:2]	30
0xBB004504	ACL_PORT_LOOKUP_CTRL [3].EGR_LUT[1:1]	1
0xBB004504	ACL_PORT_LOOKUP_CTRL [3].IGR_LUT[0:0]	1
0xBB004508	ACL_PORT_LOOKUP_CTRL [4].RESERVED[31:2]	30
0xBB004508	ACL_PORT_LOOKUP_CTRL [4].EGR_LUT[1:1]	1
0xBB004508	ACL_PORT_LOOKUP_CTRL [4].IGR_LUT[0:0]	1
0xBB00450C	ACL_PORT_LOOKUP_CTRL [5].RESERVED[31:2]	30
0xBB00450C	ACL_PORT_LOOKUP_CTRL [5].EGR_LUT[1:1]	1
0xBB00450C	ACL_PORT_LOOKUP_CTRL [5].IGR_LUT[0:0]	1
0xBB004510	ACL_PORT_LOOKUP_CTRL [6].RESERVED[31:2]	30
0xBB004510	ACL_PORT_LOOKUP_CTRL [6].EGR_LUT[1:1]	1
0xBB004510	ACL_PORT_LOOKUP_CTRL [6].IGR_LUT[0:0]	1
0xBB004514	ACL_PORT_LOOKUP_CTRL [7].RESERVED[31:2]	30
0xBB004514	ACL_PORT_LOOKUP_CTRL [7].EGR_LUT[1:1]	1
0xBB004514	ACL_PORT_LOOKUP_CTRL [7].IGR_LUT[0:0]	1
0xBB004518	ACL_PORT_LOOKUP_CTRL [8].RESERVED[31:2]	30
0xBB004518	ACL_PORT_LOOKUP_CTRL [8].EGR_LUT[1:1]	1
0xBB004518	ACL_PORT_LOOKUP_CTRL [8].IGR_LUT[0:0]	1
0xBB00451C	ACL_PORT_LOOKUP_CTRL [9].RESERVED[31:2]	30

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00451C	ACL_PORT_LOOKUP_CTRL [9].EGR_LUT[1:1]	1
0xBB00451C	ACL_PORT_LOOKUP_CTRL [9].IGR_LUT[0:0]	1
0xBB004520	ACL_PORT_LOOKUP_CTRL [10].RESERVED[31:2]	30
0xBB004520	ACL_PORT_LOOKUP_CTRL [10].EGR_LUT[1:1]	1
0xBB004520	ACL_PORT_LOOKUP_CTRL [10].IGR_LUT[0:0]	1
0xBB004524	ACL_PORT_LOOKUP_CTRL [11].RESERVED[31:2]	30
0xBB004524	ACL_PORT_LOOKUP_CTRL [11].EGR_LUT[1:1]	1
0xBB004524	ACL_PORT_LOOKUP_CTRL [11].IGR_LUT[0:0]	1
0xBB004528	ACL_PORT_LOOKUP_CTRL [12].RESERVED[31:2]	30
0xBB004528	ACL_PORT_LOOKUP_CTRL [12].EGR_LUT[1:1]	1
0xBB004528	ACL_PORT_LOOKUP_CTRL [12].IGR_LUT[0:0]	1
0xBB00452C	ACL_PORT_LOOKUP_CTRL [13].RESERVED[31:2]	30
0xBB00452C	ACL_PORT_LOOKUP_CTRL [13].EGR_LUT[1:1]	1
0xBB00452C	ACL_PORT_LOOKUP_CTRL [13].IGR_LUT[0:0]	1
0xBB004530	ACL_PORT_LOOKUP_CTRL [14].RESERVED[31:2]	30
0xBB004530	ACL_PORT_LOOKUP_CTRL [14].EGR_LUT[1:1]	1
0xBB004530	ACL_PORT_LOOKUP_CTRL [14].IGR_LUT[0:0]	1
0xBB004534	ACL_PORT_LOOKUP_CTRL [15].RESERVED[31:2]	30
0xBB004534	ACL_PORT_LOOKUP_CTRL [15].EGR_LUT[1:1]	1
0xBB004534	ACL_PORT_LOOKUP_CTRL [15].IGR_LUT[0:0]	1
0xBB004538	ACL_PORT_LOOKUP_CTRL [16].RESERVED[31:2]	30
0xBB004538	ACL_PORT_LOOKUP_CTRL [16].EGR_LUT[1:1]	1
0xBB004538	ACL_PORT_LOOKUP_CTRL [16].IGR_LUT[0:0]	1
0xBB00453C	ACL_PORT_LOOKUP_CTRL [17].RESERVED[31:2]	30
0xBB00453C	ACL_PORT_LOOKUP_CTRL [17].EGR_LUT[1:1]	1
0xBB00453C	ACL_PORT_LOOKUP_CTRL [17].IGR_LUT[0:0]	1
0xBB004540	ACL_PORT_LOOKUP_CTRL [18].RESERVED[31:2]	30
0xBB004540	ACL_PORT_LOOKUP_CTRL [18].EGR_LUT[1:1]	1
0xBB004540	ACL_PORT_LOOKUP_CTRL [18].IGR_LUT[0:0]	1
0xBB004544	ACL_PORT_LOOKUP_CTRL [19].RESERVED[31:2]	30
0xBB004544	ACL_PORT_LOOKUP_CTRL [19].EGR_LUT[1:1]	1
0xBB004544	ACL_PORT_LOOKUP_CTRL [19].IGR_LUT[0:0]	1
0xBB004548	ACL_PORT_LOOKUP_CTRL [20].RESERVED[31:2]	30
0xBB004548	ACL_PORT_LOOKUP_CTRL [20].EGR_LUT[1:1]	1
0xBB004548	ACL_PORT_LOOKUP_CTRL [20].IGR_LUT[0:0]	1
0xBB00454C	ACL_PORT_LOOKUP_CTRL [21].RESERVED[31:2]	30
0xBB00454C	ACL_PORT_LOOKUP_CTRL [21].EGR_LUT[1:1]	1
0xBB00454C	ACL_PORT_LOOKUP_CTRL [21].IGR_LUT[0:0]	1
0xBB004550	ACL_PORT_LOOKUP_CTRL [22].RESERVED[31:2]	30
0xBB004550	ACL_PORT_LOOKUP_CTRL [22].EGR_LUT[1:1]	1
0xBB004550	ACL_PORT_LOOKUP_CTRL [22].IGR_LUT[0:0]	1
0xBB004554	ACL_PORT_LOOKUP_CTRL [23].RESERVED[31:2]	30
0xBB004554	ACL_PORT_LOOKUP_CTRL [23].EGR_LUT[1:1]	1
0xBB004554	ACL_PORT_LOOKUP_CTRL [23].IGR_LUT[0:0]	1
0xBB004558	ACL_PORT_LOOKUP_CTRL [24].RESERVED[31:2]	30
0xBB004558	ACL_PORT_LOOKUP_CTRL [24].EGR_LUT[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004558	ACL_PORT_LOOKUP_CTRL [24].IGR_LUT[0:0]	1
0xBB00455C	ACL_PORT_LOOKUP_CTRL [25].RESERVED[31:2]	30
0xBB00455C	ACL_PORT_LOOKUP_CTRL [25].EGR_LUT[1:1]	1
0xBB00455C	ACL_PORT_LOOKUP_CTRL [25].IGR_LUT[0:0]	1
0xBB004560	ACL_PORT_LOOKUP_CTRL [26].RESERVED[31:2]	30
0xBB004560	ACL_PORT_LOOKUP_CTRL [26].EGR_LUT[1:1]	1
0xBB004560	ACL_PORT_LOOKUP_CTRL [26].IGR_LUT[0:0]	1
0xBB004564	ACL_PORT_LOOKUP_CTRL [27].RESERVED[31:2]	30
0xBB004564	ACL_PORT_LOOKUP_CTRL [27].EGR_LUT[1:1]	1
0xBB004564	ACL_PORT_LOOKUP_CTRL [27].IGR_LUT[0:0]	1
0xBB004568	ACL_PORT_LOOKUP_CTRL [28].RESERVED[31:2]	30
0xBB004568	ACL_PORT_LOOKUP_CTRL [28].EGR_LUT[1:1]	1
0xBB004568	ACL_PORT_LOOKUP_CTRL [28].IGR_LUT[0:0]	1
0xBB00456C	ACL_PORT_LOOKUP_CTRL [29].RESERVED[31:2]	30
0xBB00456C	ACL_PORT_LOOKUP_CTRL [29].EGR_LUT[1:1]	1
0xBB00456C	ACL_PORT_LOOKUP_CTRL [29].IGR_LUT[0:0]	1
0xBB004570	ACL_PORT_LOOKUP_CTRL [30].RESERVED[31:2]	30
0xBB004570	ACL_PORT_LOOKUP_CTRL [30].EGR_LUT[1:1]	1
0xBB004570	ACL_PORT_LOOKUP_CTRL [30].IGR_LUT[0:0]	1
0xBB004574	ACL_PORT_LOOKUP_CTRL [31].RESERVED[31:2]	30
0xBB004574	ACL_PORT_LOOKUP_CTRL [31].EGR_LUT[1:1]	1
0xBB004574	ACL_PORT_LOOKUP_CTRL [31].IGR_LUT[0:0]	1
0xBB004578	ACL_PORT_LOOKUP_CTRL [32].RESERVED[31:2]	30
0xBB004578	ACL_PORT_LOOKUP_CTRL [32].EGR_LUT[1:1]	1
0xBB004578	ACL_PORT_LOOKUP_CTRL [32].IGR_LUT[0:0]	1
0xBB00457C	ACL_PORT_LOOKUP_CTRL [33].RESERVED[31:2]	30
0xBB00457C	ACL_PORT_LOOKUP_CTRL [33].EGR_LUT[1:1]	1
0xBB00457C	ACL_PORT_LOOKUP_CTRL [33].IGR_LUT[0:0]	1
0xBB004580	ACL_PORT_LOOKUP_CTRL [34].RESERVED[31:2]	30
0xBB004580	ACL_PORT_LOOKUP_CTRL [34].EGR_LUT[1:1]	1
0xBB004580	ACL_PORT_LOOKUP_CTRL [34].IGR_LUT[0:0]	1
0xBB004584	ACL_PORT_LOOKUP_CTRL [35].RESERVED[31:2]	30
0xBB004584	ACL_PORT_LOOKUP_CTRL [35].EGR_LUT[1:1]	1
0xBB004584	ACL_PORT_LOOKUP_CTRL [35].IGR_LUT[0:0]	1
0xBB004588	ACL_PORT_LOOKUP_CTRL [36].RESERVED[31:2]	30
0xBB004588	ACL_PORT_LOOKUP_CTRL [36].EGR_LUT[1:1]	1
0xBB004588	ACL_PORT_LOOKUP_CTRL [36].IGR_LUT[0:0]	1
0xBB00458C	ACL_PORT_LOOKUP_CTRL [37].RESERVED[31:2]	30
0xBB00458C	ACL_PORT_LOOKUP_CTRL [37].EGR_LUT[1:1]	1
0xBB00458C	ACL_PORT_LOOKUP_CTRL [37].IGR_LUT[0:0]	1
0xBB004590	ACL_PORT_LOOKUP_CTRL [38].RESERVED[31:2]	30
0xBB004590	ACL_PORT_LOOKUP_CTRL [38].EGR_LUT[1:1]	1
0xBB004590	ACL_PORT_LOOKUP_CTRL [38].IGR_LUT[0:0]	1
0xBB004594	ACL_PORT_LOOKUP_CTRL [39].RESERVED[31:2]	30
0xBB004594	ACL_PORT_LOOKUP_CTRL [39].EGR_LUT[1:1]	1
0xBB004594	ACL_PORT_LOOKUP_CTRL [39].IGR_LUT[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004598	ACL_PORT_LOOKUP_CTRL [40].RESERVED[31:2]	30
0xBB004598	ACL_PORT_LOOKUP_CTRL [40].EGR_LUT[1:1]	1
0xBB004598	ACL_PORT_LOOKUP_CTRL [40].IGR_LUT[0:0]	1
0xBB00459C	ACL_PORT_LOOKUP_CTRL [41].RESERVED[31:2]	30
0xBB00459C	ACL_PORT_LOOKUP_CTRL [41].EGR_LUT[1:1]	1
0xBB00459C	ACL_PORT_LOOKUP_CTRL [41].IGR_LUT[0:0]	1
0xBB0045A0	ACL_PORT_LOOKUP_CTRL [42].RESERVED[31:2]	30
0xBB0045A0	ACL_PORT_LOOKUP_CTRL [42].EGR_LUT[1:1]	1
0xBB0045A0	ACL_PORT_LOOKUP_CTRL [42].IGR_LUT[0:0]	1
0xBB0045A4	ACL_PORT_LOOKUP_CTRL [43].RESERVED[31:2]	30
0xBB0045A4	ACL_PORT_LOOKUP_CTRL [43].EGR_LUT[1:1]	1
0xBB0045A4	ACL_PORT_LOOKUP_CTRL [43].IGR_LUT[0:0]	1
0xBB0045A8	ACL_PORT_LOOKUP_CTRL [44].RESERVED[31:2]	30
0xBB0045A8	ACL_PORT_LOOKUP_CTRL [44].EGR_LUT[1:1]	1
0xBB0045A8	ACL_PORT_LOOKUP_CTRL [44].IGR_LUT[0:0]	1
0xBB0045AC	ACL_PORT_LOOKUP_CTRL [45].RESERVED[31:2]	30
0xBB0045AC	ACL_PORT_LOOKUP_CTRL [45].EGR_LUT[1:1]	1
0xBB0045AC	ACL_PORT_LOOKUP_CTRL [45].IGR_LUT[0:0]	1
0xBB0045B0	ACL_PORT_LOOKUP_CTRL [46].RESERVED[31:2]	30
0xBB0045B0	ACL_PORT_LOOKUP_CTRL [46].EGR_LUT[1:1]	1
0xBB0045B0	ACL_PORT_LOOKUP_CTRL [46].IGR_LUT[0:0]	1
0xBB0045B4	ACL_PORT_LOOKUP_CTRL [47].RESERVED[31:2]	30
0xBB0045B4	ACL_PORT_LOOKUP_CTRL [47].EGR_LUT[1:1]	1
0xBB0045B4	ACL_PORT_LOOKUP_CTRL [47].IGR_LUT[0:0]	1
0xBB0045B8	ACL_PORT_LOOKUP_CTRL [48].RESERVED[31:2]	30
0xBB0045B8	ACL_PORT_LOOKUP_CTRL [48].EGR_LUT[1:1]	1
0xBB0045B8	ACL_PORT_LOOKUP_CTRL [48].IGR_LUT[0:0]	1
0xBB0045BC	ACL_PORT_LOOKUP_CTRL [49].RESERVED[31:2]	30
0xBB0045BC	ACL_PORT_LOOKUP_CTRL [49].EGR_LUT[1:1]	1
0xBB0045BC	ACL_PORT_LOOKUP_CTRL [49].IGR_LUT[0:0]	1
0xBB0045C0	ACL_PORT_LOOKUP_CTRL [50].RESERVED[31:2]	30
0xBB0045C0	ACL_PORT_LOOKUP_CTRL [50].EGR_LUT[1:1]	1
0xBB0045C0	ACL_PORT_LOOKUP_CTRL [50].IGR_LUT[0:0]	1
0xBB0045C4	ACL_PORT_LOOKUP_CTRL [51].RESERVED[31:2]	30
0xBB0045C4	ACL_PORT_LOOKUP_CTRL [51].EGR_LUT[1:1]	1
0xBB0045C4	ACL_PORT_LOOKUP_CTRL [51].IGR_LUT[0:0]	1
0xBB0045C8	ACL_PORT_LOOKUP_CTRL [52].RESERVED[31:2]	30
0xBB0045C8	ACL_PORT_LOOKUP_CTRL [52].EGR_LUT[1:1]	1
0xBB0045C8	ACL_PORT_LOOKUP_CTRL [52].IGR_LUT[0:0]	1
0xBB0045CC	ACL_PORT_LOOKUP_CTRL [53].RESERVED[31:2]	30
0xBB0045CC	ACL_PORT_LOOKUP_CTRL [53].EGR_LUT[1:1]	1
0xBB0045CC	ACL_PORT_LOOKUP_CTRL [53].IGR_LUT[0:0]	1
0xBB0045D0	ACL_PORT_LOOKUP_CTRL [54].RESERVED[31:2]	30
0xBB0045D0	ACL_PORT_LOOKUP_CTRL [54].EGR_LUT[1:1]	1
0xBB0045D0	ACL_PORT_LOOKUP_CTRL [54].IGR_LUT[0:0]	1
0xBB0045D4	ACL_PORT_LOOKUP_CTRL [55].RESERVED[31:2]	30

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0045D4	ACL_PORT_LOOKUP_CTRL [55].EGR_LUT[1:1]	1
0xBB0045D4	ACL_PORT_LOOKUP_CTRL [55].IGR_LUT[0:0]	1
0xBB008C34	ATK_PRVNT_ACT.RESERVED[31:20]	12
0xBB008C34	ATK_PRVNT_ACT.TCP_FRAG_OFF_MIN[19:19]	1
0xBB008C34	ATK_PRVNT_ACT.SYN_RST[18:18]	1
0xBB008C34	ATK_PRVNT_ACT.SYN_FIN[17:17]	1
0xBB008C34	ATK_PRVNT_ACT.XMAS[16:16]	1
0xBB008C34	ATK_PRVNT_ACT.NULL_SCAN[15:15]	1
0xBB008C34	ATK_PRVNT_ACT.SYN_SPORT_LESS_1024[14:14]	1
0xBB008C34	ATK_PRVNT_ACT.TCP_HDR_LEN_MIN[13:13]	1
0xBB008C34	ATK_PRVNT_ACT.SMURF[12:12]	1
0xBB008C34	ATK_PRVNT_ACT.ICMPV6_PING_MAX[11:11]	1
0xBB008C34	ATK_PRVNT_ACT.ICMPV4_PING_MAX[10:10]	1
0xBB008C34	ATK_PRVNT_ACT.ICMP_FRAG_PKT[9:9]	1
0xBB008C34	ATK_PRVNT_ACT.IPV6_FRAG_LEN_MIN[8:8]	1
0xBB008C34	ATK_PRVNT_ACT.POD[7:7]	1
0xBB008C34	ATK_PRVNT_ACT.TCP_BLAT[6:6]	1
0xBB008C34	ATK_PRVNT_ACT.UDP_BLAT[5:5]	1
0xBB008C34	ATK_PRVNT_ACT.LAND[4:4]	1
0xBB008C34	ATK_PRVNT_ACT.INVALID_HEADER[3:3]	1
0xBB008C34	ATK_PRVNT_ACT.MCSA[2:2]	1
0xBB008C34	ATK_PRVNT_ACT.ZERO_MAC[1:1]	1
0xBB008C34	ATK_PRVNT_ACT.DA_EQUAL_SA[0:0]	1
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [0].ACT[1:0]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [1].ACT[3:2]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [2].ACT[5:4]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [3].ACT[7:6]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [4].ACT[9:8]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [5].ACT[11:10]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [6].ACT[13:12]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [7].ACT[15:14]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [8].ACT[17:16]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [9].ACT[19:18]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [10].ACT[21:20]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [11].ACT[23:22]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [12].ACT[25:24]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [13].ACT[27:26]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [14].ACT[29:28]	2
0xBB008C4C	ATK_PRVNT_ARP_INVLD_PORT_ACT [15].ACT[31:30]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [16].ACT[1:0]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [17].ACT[3:2]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [18].ACT[5:4]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [19].ACT[7:6]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [20].ACT[9:8]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [21].ACT[11:10]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [22].ACT[13:12]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [23].ACT[15:14]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [24].ACT[17:16]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [25].ACT[19:18]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [26].ACT[21:20]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [27].ACT[23:22]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [28].ACT[25:24]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [29].ACT[27:26]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [30].ACT[29:28]	2
0xBB008C50	ATK_PRVNT_ARP_INVLD_PORT_ACT [31].ACT[31:30]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [32].ACT[1:0]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [33].ACT[3:2]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [34].ACT[5:4]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [35].ACT[7:6]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [36].ACT[9:8]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [37].ACT[11:10]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [38].ACT[13:12]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [39].ACT[15:14]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [40].ACT[17:16]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [41].ACT[19:18]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [42].ACT[21:20]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [43].ACT[23:22]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [44].ACT[25:24]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [45].ACT[27:26]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [46].ACT[29:28]	2
0xBB008C54	ATK_PRVNT_ARP_INVLD_PORT_ACT [47].ACT[31:30]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [48].ACT[1:0]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [49].ACT[3:2]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [50].ACT[5:4]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [51].ACT[7:6]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [52].ACT[9:8]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [53].ACT[11:10]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [54].ACT[13:12]	2
0xBB008C58	ATK_PRVNT_ARP_INVLD_PORT_ACT [55].ACT[15:14]	2
0xBB008C30	ATK_PRVNT_CTRL.RESERVED[31:21]	11
0xBB008C30	ATK_PRVNT_CTRL.CPU_SEL[20:20]	1
0xBB008C30	ATK_PRVNT_CTRL.TCP_FRAG_OFF_MIN[19:19]	1
0xBB008C30	ATK_PRVNT_CTRL.SYN_RST[18:18]	1
0xBB008C30	ATK_PRVNT_CTRL.SYN_FIN[17:17]	1
0xBB008C30	ATK_PRVNT_CTRL.XMAS[16:16]	1
0xBB008C30	ATK_PRVNT_CTRL.NULL_SCAN[15:15]	1
0xBB008C30	ATK_PRVNT_CTRL.SYN_SPORT_LESS_1024[14:14]	1
0xBB008C30	ATK_PRVNT_CTRL.TCP_HDR_LEN_MIN[13:13]	1
0xBB008C30	ATK_PRVNT_CTRL.SMURF[12:12]	1
0xBB008C30	ATK_PRVNT_CTRL.ICMPV6_PING_MAX[11:11]	1
0xBB008C30	ATK_PRVNT_CTRL.ICMPV4_PING_MAX[10:10]	1
0xBB008C30	ATK_PRVNT_CTRL.ICMP_FRAG_PKT[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C30	ATK_PRVNT_CTRL.IPV6_FRAG_LEN_MIN[8:8]	1
0xBB008C30	ATK_PRVNT_CTRL.POD[7:7]	1
0xBB008C30	ATK_PRVNT_CTRL.TCP_BLAT[6:6]	1
0xBB008C30	ATK_PRVNT_CTRL.UDP_BLAT[5:5]	1
0xBB008C30	ATK_PRVNT_CTRL.LAND[4:4]	1
0xBB008C30	ATK_PRVNT_CTRL.INVALID_HEADER[3:3]	1
0xBB008C30	ATK_PRVNT_CTRL.MCSA[2:2]	1
0xBB008C30	ATK_PRVNT_CTRL.ZERO_MAC[1:1]	1
0xBB008C30	ATK_PRVNT_CTRL.DA_EQUAL_SA[0:0]	1
0xBB008C3C	ATK_PRVNT_ICMP_CTRL.RESERVED[31:16]	16
0xBB008C3C	ATK_PRVNT_ICMP_CTRL.PKT_LEN_MAX[15:0]	16
0xBB008C38	ATK_PRVNT_IPV6_CTRL.RESERVED[31:16]	16
0xBB008C38	ATK_PRVNT_IPV6_CTRL.FRAG_LEN_MIN[15:0]	16
0xBB008C28	ATK_PRVNT_PORT_EN [0].EN[0:0]	1
0xBB008C28	ATK_PRVNT_PORT_EN [1].EN[1:1]	1
0xBB008C28	ATK_PRVNT_PORT_EN [2].EN[2:2]	1
0xBB008C28	ATK_PRVNT_PORT_EN [3].EN[3:3]	1
0xBB008C28	ATK_PRVNT_PORT_EN [4].EN[4:4]	1
0xBB008C28	ATK_PRVNT_PORT_EN [5].EN[5:5]	1
0xBB008C28	ATK_PRVNT_PORT_EN [6].EN[6:6]	1
0xBB008C28	ATK_PRVNT_PORT_EN [7].EN[7:7]	1
0xBB008C28	ATK_PRVNT_PORT_EN [8].EN[8:8]	1
0xBB008C28	ATK_PRVNT_PORT_EN [9].EN[9:9]	1
0xBB008C28	ATK_PRVNT_PORT_EN [10].EN[10:10]	1
0xBB008C28	ATK_PRVNT_PORT_EN [11].EN[11:11]	1
0xBB008C28	ATK_PRVNT_PORT_EN [12].EN[12:12]	1
0xBB008C28	ATK_PRVNT_PORT_EN [13].EN[13:13]	1
0xBB008C28	ATK_PRVNT_PORT_EN [14].EN[14:14]	1
0xBB008C28	ATK_PRVNT_PORT_EN [15].EN[15:15]	1
0xBB008C28	ATK_PRVNT_PORT_EN [16].EN[16:16]	1
0xBB008C28	ATK_PRVNT_PORT_EN [17].EN[17:17]	1
0xBB008C28	ATK_PRVNT_PORT_EN [18].EN[18:18]	1
0xBB008C28	ATK_PRVNT_PORT_EN [19].EN[19:19]	1
0xBB008C28	ATK_PRVNT_PORT_EN [20].EN[20:20]	1
0xBB008C28	ATK_PRVNT_PORT_EN [21].EN[21:21]	1
0xBB008C28	ATK_PRVNT_PORT_EN [22].EN[22:22]	1
0xBB008C28	ATK_PRVNT_PORT_EN [23].EN[23:23]	1
0xBB008C28	ATK_PRVNT_PORT_EN [24].EN[24:24]	1
0xBB008C28	ATK_PRVNT_PORT_EN [25].EN[25:25]	1
0xBB008C28	ATK_PRVNT_PORT_EN [26].EN[26:26]	1
0xBB008C28	ATK_PRVNT_PORT_EN [27].EN[27:27]	1
0xBB008C28	ATK_PRVNT_PORT_EN [28].EN[28:28]	1
0xBB008C28	ATK_PRVNT_PORT_EN [29].EN[29:29]	1
0xBB008C28	ATK_PRVNT_PORT_EN [30].EN[30:30]	1
0xBB008C28	ATK_PRVNT_PORT_EN [31].EN[31:31]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [32].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C2C	ATK_PRVNT_PORT_EN [33].EN[1:1]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [34].EN[2:2]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [35].EN[3:3]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [36].EN[4:4]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [37].EN[5:5]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [38].EN[6:6]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [39].EN[7:7]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [40].EN[8:8]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [41].EN[9:9]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [42].EN[10:10]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [43].EN[11:11]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [44].EN[12:12]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [45].EN[13:13]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [46].EN[14:14]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [47].EN[15:15]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [48].EN[16:16]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [49].EN[17:17]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [50].EN[18:18]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [51].EN[19:19]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [52].EN[20:20]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [53].EN[21:21]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [54].EN[22:22]	1
0xBB008C2C	ATK_PRVNT_PORT_EN [55].EN[23:23]	1
0xBB008C44	ATK_PRVNT_SMURF_CTRL.NETMASK[31:0]	32
0xBB008C48	ATK_PRVNT_STS.RESERVED[31:21]	11
0xBB008C48	ATK_PRVNT_STS.ARP_INVLD[20:20]	1
0xBB008C48	ATK_PRVNT_STS.TCP_FRAG_OFF_MIN[19:19]	1
0xBB008C48	ATK_PRVNT_STS.SYN_RST[18:18]	1
0xBB008C48	ATK_PRVNT_STS.SYN_FIN[17:17]	1
0xBB008C48	ATK_PRVNT_STS.XMAS[16:16]	1
0xBB008C48	ATK_PRVNT_STS.NULL_SCAN[15:15]	1
0xBB008C48	ATK_PRVNT_STS.SYN_SPORT_LESS_1024[14:14]	1
0xBB008C48	ATK_PRVNT_STS.TCP_HDR_MIN[13:13]	1
0xBB008C48	ATK_PRVNT_STS.SMURF[12:12]	1
0xBB008C48	ATK_PRVNT_STS.ICMPV6_PING_MAX[11:11]	1
0xBB008C48	ATK_PRVNT_STS.ICMPV4_PING_MAX[10:10]	1
0xBB008C48	ATK_PRVNT_STS.ICMP_FRAG_PKT[9:9]	1
0xBB008C48	ATK_PRVNT_STS.IPV6_FRAG_LEN_MIN[8:8]	1
0xBB008C48	ATK_PRVNT_STS.POD[7:7]	1
0xBB008C48	ATK_PRVNT_STS.TCP_BLAT[6:6]	1
0xBB008C48	ATK_PRVNT_STS.UDP_BLAT[5:5]	1
0xBB008C48	ATK_PRVNT_STS.LAND[4:4]	1
0xBB008C48	ATK_PRVNT_STS.INVALID_HEADER[3:3]	1
0xBB008C48	ATK_PRVNT_STS.MCSA[2:2]	1
0xBB008C48	ATK_PRVNT_STS.ZERO_MAC[1:1]	1
0xBB008C48	ATK_PRVNT_STS.DA_EQUAL_SA[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C40	ATK_PRVNT_TCP_CTRL.RESERVED[31:5]	27
0xBB008C40	ATK_PRVNT_TCP_CTRL.HDR_LEN_MIN[4:0]	5
0xBB008004	AVB_CTRL.RESERVED[31:18]	14
0xBB008004	AVB_CTRL.CLASS_NON_B_REDIRECT_QID[17:15]	3
0xBB008004	AVB_CTRL.CLASS_NON_A_REDIRECT_QID[14:12]	3
0xBB008004	AVB_CTRL.CLASS_B_QID[11:9]	3
0xBB008004	AVB_CTRL.CLASS_A_QID[8:6]	3
0xBB008004	AVB_CTRL.CLASS_B_PRI[5:3]	3
0xBB008004	AVB_CTRL.CLASS_A_PRI[2:0]	3
0xBB004F44	AVB_CTRL_MAC.RESERVED[31:6]	26
0xBB004F44	AVB_CTRL_MAC.CLASS_NON_B_RMK_PRI[5:3]	3
0xBB004F44	AVB_CTRL_MAC.CLASS_NON_A_RMK_PRI[2:0]	3
0xBB008C14	AVB_PORT_CLASS_A_EN [0].EN[0:0]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [1].EN[1:1]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [2].EN[2:2]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [3].EN[3:3]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [4].EN[4:4]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [5].EN[5:5]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [6].EN[6:6]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [7].EN[7:7]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [8].EN[8:8]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [9].EN[9:9]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [10].EN[10:10]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [11].EN[11:11]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [12].EN[12:12]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [13].EN[13:13]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [14].EN[14:14]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [15].EN[15:15]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [16].EN[16:16]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [17].EN[17:17]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [18].EN[18:18]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [19].EN[19:19]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [20].EN[20:20]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [21].EN[21:21]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [22].EN[22:22]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [23].EN[23:23]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [24].EN[24:24]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [25].EN[25:25]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [26].EN[26:26]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [27].EN[27:27]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [28].EN[28:28]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [29].EN[29:29]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [30].EN[30:30]	1
0xBB008C14	AVB_PORT_CLASS_A_EN [31].EN[31:31]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [32].EN[0:0]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [33].EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C18	AVB_PORT_CLASS_A_EN [34].EN[2:2]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [35].EN[3:3]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [36].EN[4:4]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [37].EN[5:5]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [38].EN[6:6]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [39].EN[7:7]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [40].EN[8:8]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [41].EN[9:9]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [42].EN[10:10]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [43].EN[11:11]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [44].EN[12:12]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [45].EN[13:13]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [46].EN[14:14]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [47].EN[15:15]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [48].EN[16:16]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [49].EN[17:17]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [50].EN[18:18]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [51].EN[19:19]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [52].EN[20:20]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [53].EN[21:21]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [54].EN[22:22]	1
0xBB008C18	AVB_PORT_CLASS_A_EN [55].EN[23:23]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [0].EN[0:0]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [1].EN[1:1]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [2].EN[2:2]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [3].EN[3:3]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [4].EN[4:4]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [5].EN[5:5]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [6].EN[6:6]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [7].EN[7:7]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [8].EN[8:8]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [9].EN[9:9]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [10].EN[10:10]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [11].EN[11:11]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [12].EN[12:12]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [13].EN[13:13]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [14].EN[14:14]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [15].EN[15:15]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [16].EN[16:16]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [17].EN[17:17]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [18].EN[18:18]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [19].EN[19:19]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [20].EN[20:20]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [21].EN[21:21]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [22].EN[22:22]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [23].EN[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [24].EN[24:24]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [25].EN[25:25]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [26].EN[26:26]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [27].EN[27:27]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [28].EN[28:28]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [29].EN[29:29]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [30].EN[30:30]	1
0xBB004F34	AVB_PORT_CLASS_A_EN_MAC [31].EN[31:31]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [32].EN[0:0]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [33].EN[1:1]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [34].EN[2:2]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [35].EN[3:3]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [36].EN[4:4]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [37].EN[5:5]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [38].EN[6:6]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [39].EN[7:7]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [40].EN[8:8]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [41].EN[9:9]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [42].EN[10:10]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [43].EN[11:11]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [44].EN[12:12]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [45].EN[13:13]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [46].EN[14:14]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [47].EN[15:15]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [48].EN[16:16]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [49].EN[17:17]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [50].EN[18:18]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [51].EN[19:19]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [52].EN[20:20]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [53].EN[21:21]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [54].EN[22:22]	1
0xBB004F38	AVB_PORT_CLASS_A_EN_MAC [55].EN[23:23]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [0].EN[0:0]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [1].EN[1:1]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [2].EN[2:2]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [3].EN[3:3]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [4].EN[4:4]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [5].EN[5:5]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [6].EN[6:6]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [7].EN[7:7]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [8].EN[8:8]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [9].EN[9:9]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [10].EN[10:10]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [11].EN[11:11]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [12].EN[12:12]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [13].EN[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C1C	AVB_PORT_CLASS_B_EN [14].EN[14:14]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [15].EN[15:15]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [16].EN[16:16]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [17].EN[17:17]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [18].EN[18:18]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [19].EN[19:19]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [20].EN[20:20]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [21].EN[21:21]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [22].EN[22:22]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [23].EN[23:23]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [24].EN[24:24]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [25].EN[25:25]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [26].EN[26:26]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [27].EN[27:27]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [28].EN[28:28]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [29].EN[29:29]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [30].EN[30:30]	1
0xBB008C1C	AVB_PORT_CLASS_B_EN [31].EN[31:31]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [32].EN[0:0]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [33].EN[1:1]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [34].EN[2:2]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [35].EN[3:3]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [36].EN[4:4]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [37].EN[5:5]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [38].EN[6:6]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [39].EN[7:7]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [40].EN[8:8]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [41].EN[9:9]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [42].EN[10:10]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [43].EN[11:11]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [44].EN[12:12]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [45].EN[13:13]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [46].EN[14:14]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [47].EN[15:15]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [48].EN[16:16]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [49].EN[17:17]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [50].EN[18:18]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [51].EN[19:19]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [52].EN[20:20]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [53].EN[21:21]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [54].EN[22:22]	1
0xBB008C20	AVB_PORT_CLASS_B_EN [55].EN[23:23]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [0].EN[0:0]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [1].EN[1:1]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [2].EN[2:2]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [3].EN[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [4].EN[4:4]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [5].EN[5:5]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [6].EN[6:6]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [7].EN[7:7]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [8].EN[8:8]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [9].EN[9:9]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [10].EN[10:10]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [11].EN[11:11]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [12].EN[12:12]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [13].EN[13:13]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [14].EN[14:14]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [15].EN[15:15]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [16].EN[16:16]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [17].EN[17:17]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [18].EN[18:18]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [19].EN[19:19]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [20].EN[20:20]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [21].EN[21:21]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [22].EN[22:22]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [23].EN[23:23]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [24].EN[24:24]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [25].EN[25:25]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [26].EN[26:26]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [27].EN[27:27]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [28].EN[28:28]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [29].EN[29:29]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [30].EN[30:30]	1
0xBB004F3C	AVB_PORT_CLASS_B_EN_MAC [31].EN[31:31]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [32].EN[0:0]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [33].EN[1:1]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [34].EN[2:2]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [35].EN[3:3]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [36].EN[4:4]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [37].EN[5:5]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [38].EN[6:6]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [39].EN[7:7]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [40].EN[8:8]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [41].EN[9:9]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [42].EN[10:10]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [43].EN[11:11]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [44].EN[12:12]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [45].EN[13:13]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [46].EN[14:14]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [47].EN[15:15]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [48].EN[16:16]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [49].EN[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [50].EN[18:18]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [51].EN[19:19]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [52].EN[20:20]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [53].EN[21:21]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [54].EN[22:22]	1
0xBB004F40	AVB_PORT_CLASS_B_EN_MAC [55].EN[23:23]	1
0xBB00040C	BONDING_STS.RESERVED[31:0]	32
0xBB00A43C	CCM_RX_INST_CNT [0].RESERVED[31:20]	12
0xBB00A43C	CCM_RX_INST_CNT [0].CNTR1[19:10]	10
0xBB00A43C	CCM_RX_INST_CNT [0].CNTR0[9:0]	10
0xBB00A440	CCM_RX_INST_CNT [1].RESERVED[31:20]	12
0xBB00A440	CCM_RX_INST_CNT [1].CNTR1[19:10]	10
0xBB00A440	CCM_RX_INST_CNT [1].CNTR0[9:0]	10
0xBB00A444	CCM_RX_INST_CNT [2].RESERVED[31:20]	12
0xBB00A444	CCM_RX_INST_CNT [2].CNTR1[19:10]	10
0xBB00A444	CCM_RX_INST_CNT [2].CNTR0[9:0]	10
0xBB00A448	CCM_RX_INST_CNT [3].RESERVED[31:20]	12
0xBB00A448	CCM_RX_INST_CNT [3].CNTR1[19:10]	10
0xBB00A448	CCM_RX_INST_CNT [3].CNTR0[9:0]	10
0xBB00A44C	CCM_RX_INST_CNT [4].RESERVED[31:20]	12
0xBB00A44C	CCM_RX_INST_CNT [4].CNTR1[19:10]	10
0xBB00A44C	CCM_RX_INST_CNT [4].CNTR0[9:0]	10
0xBB00A450	CCM_RX_INST_CNT [5].RESERVED[31:20]	12
0xBB00A450	CCM_RX_INST_CNT [5].CNTR1[19:10]	10
0xBB00A450	CCM_RX_INST_CNT [5].CNTR0[9:0]	10
0xBB00A454	CCM_RX_INST_CNT [6].RESERVED[31:20]	12
0xBB00A454	CCM_RX_INST_CNT [6].CNTR1[19:10]	10
0xBB00A454	CCM_RX_INST_CNT [6].CNTR0[9:0]	10
0xBB00A458	CCM_RX_INST_CNT [7].RESERVED[31:20]	12
0xBB00A458	CCM_RX_INST_CNT [7].CNTR1[19:10]	10
0xBB00A458	CCM_RX_INST_CNT [7].CNTR0[9:0]	10
0xBB00A41C	CCM_RX_INST_CTRL [0].RESERVED[31:28]	4
0xBB00A41C	CCM_RX_INST_CTRL [0].VID[27:16]	12
0xBB00A41C	CCM_RX_INST_CTRL [0].TRK_PRESENT1[15:15]	1
0xBB00A41C	CCM_RX_INST_CTRL [0].MEMBER1[14:8]	7
0xBB00A41C	CCM_RX_INST_CTRL [0].TRK_PRESENT0[7:7]	1
0xBB00A41C	CCM_RX_INST_CTRL [0].MEMBER0[6:0]	7
0xBB00A420	CCM_RX_INST_CTRL [1].RESERVED[31:28]	4
0xBB00A420	CCM_RX_INST_CTRL [1].VID[27:16]	12
0xBB00A420	CCM_RX_INST_CTRL [1].TRK_PRESENT1[15:15]	1
0xBB00A420	CCM_RX_INST_CTRL [1].MEMBER1[14:8]	7
0xBB00A420	CCM_RX_INST_CTRL [1].TRK_PRESENT0[7:7]	1
0xBB00A420	CCM_RX_INST_CTRL [1].MEMBER0[6:0]	7
0xBB00A424	CCM_RX_INST_CTRL [2].RESERVED[31:28]	4
0xBB00A424	CCM_RX_INST_CTRL [2].VID[27:16]	12
0xBB00A424	CCM_RX_INST_CTRL [2].TRK_PRESENT1[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A424	CCM_RX_INST_CTRL [2].MEMBER1[14:8]	7
0xBB00A424	CCM_RX_INST_CTRL [2].TRK_PRESENT0[7:7]	1
0xBB00A424	CCM_RX_INST_CTRL [2].MEMBER0[6:0]	7
0xBB00A428	CCM_RX_INST_CTRL [3].RESERVED[31:28]	4
0xBB00A428	CCM_RX_INST_CTRL [3].VID[27:16]	12
0xBB00A428	CCM_RX_INST_CTRL [3].TRK_PRESENT1[15:15]	1
0xBB00A428	CCM_RX_INST_CTRL [3].MEMBER1[14:8]	7
0xBB00A428	CCM_RX_INST_CTRL [3].TRK_PRESENT0[7:7]	1
0xBB00A428	CCM_RX_INST_CTRL [3].MEMBER0[6:0]	7
0xBB00A42C	CCM_RX_INST_CTRL [4].RESERVED[31:28]	4
0xBB00A42C	CCM_RX_INST_CTRL [4].VID[27:16]	12
0xBB00A42C	CCM_RX_INST_CTRL [4].TRK_PRESENT1[15:15]	1
0xBB00A42C	CCM_RX_INST_CTRL [4].MEMBER1[14:8]	7
0xBB00A42C	CCM_RX_INST_CTRL [4].TRK_PRESENT0[7:7]	1
0xBB00A42C	CCM_RX_INST_CTRL [4].MEMBER0[6:0]	7
0xBB00A430	CCM_RX_INST_CTRL [5].RESERVED[31:28]	4
0xBB00A430	CCM_RX_INST_CTRL [5].VID[27:16]	12
0xBB00A430	CCM_RX_INST_CTRL [5].TRK_PRESENT1[15:15]	1
0xBB00A430	CCM_RX_INST_CTRL [5].MEMBER1[14:8]	7
0xBB00A430	CCM_RX_INST_CTRL [5].TRK_PRESENT0[7:7]	1
0xBB00A430	CCM_RX_INST_CTRL [5].MEMBER0[6:0]	7
0xBB00A434	CCM_RX_INST_CTRL [6].RESERVED[31:28]	4
0xBB00A434	CCM_RX_INST_CTRL [6].VID[27:16]	12
0xBB00A434	CCM_RX_INST_CTRL [6].TRK_PRESENT1[15:15]	1
0xBB00A434	CCM_RX_INST_CTRL [6].MEMBER1[14:8]	7
0xBB00A434	CCM_RX_INST_CTRL [6].TRK_PRESENT0[7:7]	1
0xBB00A434	CCM_RX_INST_CTRL [6].MEMBER0[6:0]	7
0xBB00A438	CCM_RX_INST_CTRL [7].RESERVED[31:28]	4
0xBB00A438	CCM_RX_INST_CTRL [7].VID[27:16]	12
0xBB00A438	CCM_RX_INST_CTRL [7].TRK_PRESENT1[15:15]	1
0xBB00A438	CCM_RX_INST_CTRL [7].MEMBER1[14:8]	7
0xBB00A438	CCM_RX_INST_CTRL [7].TRK_PRESENT0[7:7]	1
0xBB00A438	CCM_RX_INST_CTRL [7].MEMBER0[6:0]	7
0xBB00A410	CCM_RX_LIFETIME_CTRL [0].LIFETIME[9:0]	10
0xBB00A410	CCM_RX_LIFETIME_CTRL [1].LIFETIME[19:10]	10
0xBB00A410	CCM_RX_LIFETIME_CTRL [2].LIFETIME[29:20]	10
0xBB00A414	CCM_RX_LIFETIME_CTRL [3].LIFETIME[9:0]	10
0xBB00A414	CCM_RX_LIFETIME_CTRL [4].LIFETIME[19:10]	10
0xBB00A414	CCM_RX_LIFETIME_CTRL [5].LIFETIME[29:20]	10
0xBB00A418	CCM_RX_LIFETIME_CTRL [6].LIFETIME[9:0]	10
0xBB00A418	CCM_RX_LIFETIME_CTRL [7].LIFETIME[19:10]	10
0xBB000598	CCM_TX_INST_CTRL [0].VID_ADD[31:31]	1
0xBB000598	CCM_TX_INST_CTRL [0].VID[30:19]	12
0xBB000598	CCM_TX_INST_CTRL [0].MAID[18:11]	8
0xBB000598	CCM_TX_INST_CTRL [0].TX[10:10]	1
0xBB000598	CCM_TX_INST_CTRL [0].INTLV[9:0]	10

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00059C	CCM_TX_INST_CTRL [1].VID_ADD[31:31]	1
0xBB00059C	CCM_TX_INST_CTRL [1].VID[30:19]	12
0xBB00059C	CCM_TX_INST_CTRL [1].MAID[18:11]	8
0xBB00059C	CCM_TX_INST_CTRL [1].TX[10:10]	1
0xBB00059C	CCM_TX_INST_CTRL [1].INTLV[9:0]	10
0xBB0005A0	CCM_TX_INST_CTRL [2].VID_ADD[31:31]	1
0xBB0005A0	CCM_TX_INST_CTRL [2].VID[30:19]	12
0xBB0005A0	CCM_TX_INST_CTRL [2].MAID[18:11]	8
0xBB0005A0	CCM_TX_INST_CTRL [2].TX[10:10]	1
0xBB0005A0	CCM_TX_INST_CTRL [2].INTLV[9:0]	10
0xBB0005A4	CCM_TX_INST_CTRL [3].VID_ADD[31:31]	1
0xBB0005A4	CCM_TX_INST_CTRL [3].VID[30:19]	12
0xBB0005A4	CCM_TX_INST_CTRL [3].MAID[18:11]	8
0xBB0005A4	CCM_TX_INST_CTRL [3].TX[10:10]	1
0xBB0005A4	CCM_TX_INST_CTRL [3].INTLV[9:0]	10
0xBB0005A8	CCM_TX_INST_CTRL [4].VID_ADD[31:31]	1
0xBB0005A8	CCM_TX_INST_CTRL [4].VID[30:19]	12
0xBB0005A8	CCM_TX_INST_CTRL [4].MAID[18:11]	8
0xBB0005A8	CCM_TX_INST_CTRL [4].TX[10:10]	1
0xBB0005A8	CCM_TX_INST_CTRL [4].INTLV[9:0]	10
0xBB0005AC	CCM_TX_INST_CTRL [5].VID_ADD[31:31]	1
0xBB0005AC	CCM_TX_INST_CTRL [5].VID[30:19]	12
0xBB0005AC	CCM_TX_INST_CTRL [5].MAID[18:11]	8
0xBB0005AC	CCM_TX_INST_CTRL [5].TX[10:10]	1
0xBB0005AC	CCM_TX_INST_CTRL [5].INTLV[9:0]	10
0xBB0005B0	CCM_TX_INST_CTRL [6].VID_ADD[31:31]	1
0xBB0005B0	CCM_TX_INST_CTRL [6].VID[30:19]	12
0xBB0005B0	CCM_TX_INST_CTRL [6].MAID[18:11]	8
0xBB0005B0	CCM_TX_INST_CTRL [6].TX[10:10]	1
0xBB0005B0	CCM_TX_INST_CTRL [6].INTLV[9:0]	10
0xBB0005B4	CCM_TX_INST_CTRL [7].VID_ADD[31:31]	1
0xBB0005B4	CCM_TX_INST_CTRL [7].VID[30:19]	12
0xBB0005B4	CCM_TX_INST_CTRL [7].MAID[18:11]	8
0xBB0005B4	CCM_TX_INST_CTRL [7].TX[10:10]	1
0xBB0005B4	CCM_TX_INST_CTRL [7].INTLV[9:0]	10
0xBB0005B8	CCM_TX_INST_MEM [0].P0[6:0]	7
0xBB0005B8	CCM_TX_INST_MEM [1].P0[13:7]	7
0xBB0005B8	CCM_TX_INST_MEM [2].P0[20:14]	7
0xBB0005B8	CCM_TX_INST_MEM [3].P0[27:21]	7
0xBB0005BC	CCM_TX_INST_MEM [4].P0[6:0]	7
0xBB0005BC	CCM_TX_INST_MEM [5].P0[13:7]	7
0xBB0005BC	CCM_TX_INST_MEM [6].P0[20:14]	7
0xBB0005BC	CCM_TX_INST_MEM [7].P0[27:21]	7
0xBB0005C0	CCM_TX_INST_MEM [8].P0[6:0]	7
0xBB0005C0	CCM_TX_INST_MEM [9].P0[13:7]	7
0xBB0005C0	CCM_TX_INST_MEM [10].P0[20:14]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0005C0	CCM_TX_INST_MEM [11].P0[27:21]	7
0xBB0005C4	CCM_TX_INST_MEM [12].P0[6:0]	7
0xBB0005C4	CCM_TX_INST_MEM [13].P0[13:7]	7
0xBB0005C4	CCM_TX_INST_MEM [14].P0[20:14]	7
0xBB0005C4	CCM_TX_INST_MEM [15].P0[27:21]	7
0xBB0005CC	CCM_TX_INST_PKT [0].RESERVED[31:19]	13
0xBB0005CC	CCM_TX_INST_PKT [0].MEPID[18:6]	13
0xBB0005CC	CCM_TX_INST_PKT [0].CCM_INTLV[5:3]	3
0xBB0005CC	CCM_TX_INST_PKT [0].MDL[2:0]	3
0xBB0005D0	CCM_TX_INST_PKT [1].RESERVED[31:19]	13
0xBB0005D0	CCM_TX_INST_PKT [1].MEPID[18:6]	13
0xBB0005D0	CCM_TX_INST_PKT [1].CCM_INTLV[5:3]	3
0xBB0005D0	CCM_TX_INST_PKT [1].MDL[2:0]	3
0xBB0005D4	CCM_TX_INST_PKT [2].RESERVED[31:19]	13
0xBB0005D4	CCM_TX_INST_PKT [2].MEPID[18:6]	13
0xBB0005D4	CCM_TX_INST_PKT [2].CCM_INTLV[5:3]	3
0xBB0005D4	CCM_TX_INST_PKT [2].MDL[2:0]	3
0xBB0005D8	CCM_TX_INST_PKT [3].RESERVED[31:19]	13
0xBB0005D8	CCM_TX_INST_PKT [3].MEPID[18:6]	13
0xBB0005D8	CCM_TX_INST_PKT [3].CCM_INTLV[5:3]	3
0xBB0005D8	CCM_TX_INST_PKT [3].MDL[2:0]	3
0xBB0005DC	CCM_TX_INST_PKT [4].RESERVED[31:19]	13
0xBB0005DC	CCM_TX_INST_PKT [4].MEPID[18:6]	13
0xBB0005DC	CCM_TX_INST_PKT [4].CCM_INTLV[5:3]	3
0xBB0005DC	CCM_TX_INST_PKT [4].MDL[2:0]	3
0xBB0005E0	CCM_TX_INST_PKT [5].RESERVED[31:19]	13
0xBB0005E0	CCM_TX_INST_PKT [5].MEPID[18:6]	13
0xBB0005E0	CCM_TX_INST_PKT [5].CCM_INTLV[5:3]	3
0xBB0005E0	CCM_TX_INST_PKT [5].MDL[2:0]	3
0xBB0005E4	CCM_TX_INST_PKT [6].RESERVED[31:19]	13
0xBB0005E4	CCM_TX_INST_PKT [6].MEPID[18:6]	13
0xBB0005E4	CCM_TX_INST_PKT [6].CCM_INTLV[5:3]	3
0xBB0005E4	CCM_TX_INST_PKT [6].MDL[2:0]	3
0xBB0005E8	CCM_TX_INST_PKT [7].RESERVED[31:19]	13
0xBB0005E8	CCM_TX_INST_PKT [7].MEPID[18:6]	13
0xBB0005E8	CCM_TX_INST_PKT [7].CCM_INTLV[5:3]	3
0xBB0005E8	CCM_TX_INST_PKT [7].MDL[2:0]	3
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [0].TRK_PRESENT[0:0]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [1].TRK_PRESENT[1:1]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [2].TRK_PRESENT[2:2]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [3].TRK_PRESENT[3:3]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [4].TRK_PRESENT[4:4]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [5].TRK_PRESENT[5:5]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [6].TRK_PRESENT[6:6]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [7].TRK_PRESENT[7:7]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [8].TRK_PRESENT[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [9].TRK_PRESENT[9:9]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [10].TRK_PRESENT[10:10]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [11].TRK_PRESENT[11:11]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [12].TRK_PRESENT[12:12]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [13].TRK_PRESENT[13:13]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [14].TRK_PRESENT[14:14]	1
0xBB0005C8	CCM_TX_INST_TRK_PRESENT [15].TRK_PRESENT[15:15]	1
0xBB000594	CCM_TX_TAG_CTRL.RESERVED[31:6]	26
0xBB000594	CCM_TX_TAG_CTRL.PCP[5:3]	3
0xBB000594	CCM_TX_TAG_CTRL.CFI[2:2]	1
0xBB000594	CCM_TX_TAG_CTRL.TPID[1:0]	2
0xBB00A404	CFM_RX_CCM_CTRL.RESERVED[31:16]	16
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_7[15:14]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_6[13:12]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_5[11:10]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_4[9:8]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_3[7:6]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_2[5:4]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_1[3:2]	2
0xBB00A404	CFM_RX_CCM_CTRL.MD_LV_0[1:0]	2
0xBB00A400	CFM_RX_CTRL.RESERVED[31:3]	29
0xBB00A400	CFM_RX_CTRL.CPU_SEL[2:2]	1
0xBB00A400	CFM_RX_CTRL.UNKN_RX[1:0]	2
0xBB00A408	CFM_RX_LB_CTRL.RESERVED[31:16]	16
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_7[15:14]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_6[13:12]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_5[11:10]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_4[9:8]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_3[7:6]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_2[5:4]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_1[3:2]	2
0xBB00A408	CFM_RX_LB_CTRL.MD_LV_0[1:0]	2
0xBB00A40C	CFM_RX_LT_CTRL.RESERVED[31:16]	16
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_7[15:14]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_6[13:12]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_5[11:10]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_4[9:8]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_3[7:6]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_2[5:4]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_1[3:2]	2
0xBB00A40C	CFM_RX_LT_CTRL.MD_LV_0[1:0]	2
0xBB000404	CHIP_RST_STS.CHIP_RST_STS[31:0]	32
0xBB0012A8	DBG_CTRL0.CFG_DBGO_MSK[31:0]	32
0xBB0012AC	DBG_CTRL1.CFG_DBGO_CMP_VAL[31:0]	32
0xBB0012B0	DBG_CTRL2.RESERVED[31:3]	29
0xBB0012B0	DBG_CTRL2.CFG_DBGO_MG[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012B0	DBG_CTRL2.CFG_DBGO_TYPE[1:0]	2
0xBB000574	DIAMETER_PARSE_CTRL.RESERVED[31:17]	15
0xBB000574	DIAMETER_PARSE_CTRL.SESSION_ID_EN[16:16]	1
0xBB000574	DIAMETER_PARSE_CTRL.HDR_L4_PORT[15:0]	16
0xBB000928	DMA_IF_CTRL.RESERVED[31:30]	2
0xBB000928	DMA_IF_CTRL.RX_TRUNCATE_LEN[29:16]	14
0xBB000928	DMA_IF_CTRL.RESERVED[15:7]	9
0xBB000928	DMA_IF_CTRL.RX_TRUNCATE_EN[6:6]	1
0xBB000928	DMA_IF_CTRL.TX_EN[5:5]	1
0xBB000928	DMA_IF_CTRL.RX_EN[4:4]	1
0xBB000928	DMA_IF_CTRL.TX_HIGH_FETCH[3:3]	1
0xBB000928	DMA_IF_CTRL.TX_LOW_FETCH[2:2]	1
0xBB000928	DMA_IF_CTRL.TX_HIGH_BUSY[1:1]	1
0xBB000928	DMA_IF_CTRL.TX_LOW_BUSY[0:0]	1
0xBB000914	DMA_IF_INTR_RX_DONE_MSK.RX_DONE[31:0]	32
0xBB000920	DMA_IF_INTR_RX_DONE_STS.RX_DONE[31:0]	32
0xBB000910	DMA_IF_INTR_RX_RUNOUT_MSK.RX_RUN_OUT[31:0]	32
0xBB00091C	DMA_IF_INTR_RX_RUNOUT_STS.RX_RUN_OUT[31:0]	32
0xBB000918	DMA_IF_INTR_TX_DONE_MSK.RESERVED[31:4]	28
0xBB000918	DMA_IF_INTR_TX_DONE_MSK.TX_ALL_DONE[3:2]	2
0xBB000918	DMA_IF_INTR_TX_DONE_MSK.TX_DONE[1:0]	2
0xBB000924	DMA_IF_INTR_TX_DONE_STS.RESERVED[31:4]	28
0xBB000924	DMA_IF_INTR_TX_DONE_STS.TX_ALL_DONE[3:2]	2
0xBB000924	DMA_IF_INTR_TX_DONE_STS.TX_DONE[1:0]	2
0xBB00092C	DMA_IF_PHYSICAL_ADDR_MSK.MSK[31:0]	32
0xBB004858	DMA_IF_PKT_CTRL.RESERVED[31:7]	25
0xBB004858	DMA_IF_PKT_CTRL.TRAP_ETAG_PKT_FMT[6:6]	1
0xBB004858	DMA_IF_PKT_CTRL.FWD_ETAG_PKT_FMT[5:5]	1
0xBB004858	DMA_IF_PKT_CTRL.TRAP_PKT_TAG[4:4]	1
0xBB004858	DMA_IF_PKT_CTRL.FWD_PKT_TAC[3:3]	1
0xBB004858	DMA_IF_PKT_CTRL.TRAP_PKT_FMT[2:2]	1
0xBB004858	DMA_IF_PKT_CTRL.FWD_PKT_FMT[1:1]	1
0xBB004858	DMA_IF_PKT_CTRL.PKT_VLAN_FMT[0:0]	1
0xBB000800	DMA_IF_RX_BASE_DESC_ADDR_CTRL[0].ADDR[31:0]	32
0xBB000804	DMA_IF_RX_BASE_DESC_ADDR_CTRL[1].ADDR[31:0]	32
0xBB000808	DMA_IF_RX_BASE_DESC_ADDR_CTRL[2].ADDR[31:0]	32
0xBB00080C	DMA_IF_RX_BASE_DESC_ADDR_CTRL[3].ADDR[31:0]	32
0xBB000810	DMA_IF_RX_BASE_DESC_ADDR_CTRL[4].ADDR[31:0]	32
0xBB000814	DMA_IF_RX_BASE_DESC_ADDR_CTRL[5].ADDR[31:0]	32
0xBB000818	DMA_IF_RX_BASE_DESC_ADDR_CTRL[6].ADDR[31:0]	32
0xBB00081C	DMA_IF_RX_BASE_DESC_ADDR_CTRL[7].ADDR[31:0]	32
0xBB000820	DMA_IF_RX_BASE_DESC_ADDR_CTRL[8].ADDR[31:0]	32
0xBB000824	DMA_IF_RX_BASE_DESC_ADDR_CTRL[9].ADDR[31:0]	32
0xBB000828	DMA_IF_RX_BASE_DESC_ADDR_CTRL[10].ADDR[31:0]	32
0xBB00082C	DMA_IF_RX_BASE_DESC_ADDR_CTRL[11].ADDR[31:0]	32
0xBB000830	DMA_IF_RX_BASE_DESC_ADDR_CTRL[12].ADDR[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000834	DMA_IF_RX_BASE_DESC_ADDR_CTRL [13].ADDR[31:0]	32
0xBB000838	DMA_IF_RX_BASE_DESC_ADDR_CTRL [14].ADDR[31:0]	32
0xBB00083C	DMA_IF_RX_BASE_DESC_ADDR_CTRL [15].ADDR[31:0]	32
0xBB000840	DMA_IF_RX_BASE_DESC_ADDR_CTRL [16].ADDR[31:0]	32
0xBB000844	DMA_IF_RX_BASE_DESC_ADDR_CTRL [17].ADDR[31:0]	32
0xBB000848	DMA_IF_RX_BASE_DESC_ADDR_CTRL [18].ADDR[31:0]	32
0xBB00084C	DMA_IF_RX_BASE_DESC_ADDR_CTRL [19].ADDR[31:0]	32
0xBB000850	DMA_IF_RX_BASE_DESC_ADDR_CTRL [20].ADDR[31:0]	32
0xBB000854	DMA_IF_RX_BASE_DESC_ADDR_CTRL [21].ADDR[31:0]	32
0xBB000858	DMA_IF_RX_BASE_DESC_ADDR_CTRL [22].ADDR[31:0]	32
0xBB00085C	DMA_IF_RX_BASE_DESC_ADDR_CTRL [23].ADDR[31:0]	32
0xBB000860	DMA_IF_RX_BASE_DESC_ADDR_CTRL [24].ADDR[31:0]	32
0xBB000864	DMA_IF_RX_BASE_DESC_ADDR_CTRL [25].ADDR[31:0]	32
0xBB000868	DMA_IF_RX_BASE_DESC_ADDR_CTRL [26].ADDR[31:0]	32
0xBB00086C	DMA_IF_RX_BASE_DESC_ADDR_CTRL [27].ADDR[31:0]	32
0xBB000870	DMA_IF_RX_BASE_DESC_ADDR_CTRL [28].ADDR[31:0]	32
0xBB000874	DMA_IF_RX_BASE_DESC_ADDR_CTRL [29].ADDR[31:0]	32
0xBB000878	DMA_IF_RX_BASE_DESC_ADDR_CTRL [30].ADDR[31:0]	32
0xBB00087C	DMA_IF_RX_BASE_DESC_ADDR_CTRL [31].ADDR[31:0]	32
0xBB000880	DMA_IF_RX_CUR_DESC_ADDR_CTRL [0].ADDR[31:0]	32
0xBB000884	DMA_IF_RX_CUR_DESC_ADDR_CTRL [1].ADDR[31:0]	32
0xBB000888	DMA_IF_RX_CUR_DESC_ADDR_CTRL [2].ADDR[31:0]	32
0xBB00088C	DMA_IF_RX_CUR_DESC_ADDR_CTRL [3].ADDR[31:0]	32
0xBB000890	DMA_IF_RX_CUR_DESC_ADDR_CTRL [4].ADDR[31:0]	32
0xBB000894	DMA_IF_RX_CUR_DESC_ADDR_CTRL [5].ADDR[31:0]	32
0xBB000898	DMA_IF_RX_CUR_DESC_ADDR_CTRL [6].ADDR[31:0]	32
0xBB00089C	DMA_IF_RX_CUR_DESC_ADDR_CTRL [7].ADDR[31:0]	32
0xBB0008A0	DMA_IF_RX_CUR_DESC_ADDR_CTRL [8].ADDR[31:0]	32
0xBB0008A4	DMA_IF_RX_CUR_DESC_ADDR_CTRL [9].ADDR[31:0]	32
0xBB0008A8	DMA_IF_RX_CUR_DESC_ADDR_CTRL [10].ADDR[31:0]	32
0xBB0008AC	DMA_IF_RX_CUR_DESC_ADDR_CTRL [11].ADDR[31:0]	32
0xBB0008B0	DMA_IF_RX_CUR_DESC_ADDR_CTRL [12].ADDR[31:0]	32
0xBB0008B4	DMA_IF_RX_CUR_DESC_ADDR_CTRL [13].ADDR[31:0]	32
0xBB0008B8	DMA_IF_RX_CUR_DESC_ADDR_CTRL [14].ADDR[31:0]	32
0xBB0008BC	DMA_IF_RX_CUR_DESC_ADDR_CTRL [15].ADDR[31:0]	32
0xBB0008C0	DMA_IF_RX_CUR_DESC_ADDR_CTRL [16].ADDR[31:0]	32
0xBB0008C4	DMA_IF_RX_CUR_DESC_ADDR_CTRL [17].ADDR[31:0]	32
0xBB0008C8	DMA_IF_RX_CUR_DESC_ADDR_CTRL [18].ADDR[31:0]	32
0xBB0008CC	DMA_IF_RX_CUR_DESC_ADDR_CTRL [19].ADDR[31:0]	32
0xBB0008D0	DMA_IF_RX_CUR_DESC_ADDR_CTRL [20].ADDR[31:0]	32
0xBB0008D4	DMA_IF_RX_CUR_DESC_ADDR_CTRL [21].ADDR[31:0]	32
0xBB0008D8	DMA_IF_RX_CUR_DESC_ADDR_CTRL [22].ADDR[31:0]	32
0xBB0008DC	DMA_IF_RX_CUR_DESC_ADDR_CTRL [23].ADDR[31:0]	32
0xBB0008E0	DMA_IF_RX_CUR_DESC_ADDR_CTRL [24].ADDR[31:0]	32
0xBB0008E4	DMA_IF_RX_CUR_DESC_ADDR_CTRL [25].ADDR[31:0]	32
0xBB0008E8	DMA_IF_RX_CUR_DESC_ADDR_CTRL [26].ADDR[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0008EC	DMA_IF_RX_CUR_DESC_ADDR_CTRL [27].ADDR[31:0]	32
0xBB0008F0	DMA_IF_RX_CUR_DESC_ADDR_CTRL [28].ADDR[31:0]	32
0xBB0008F4	DMA_IF_RX_CUR_DESC_ADDR_CTRL [29].ADDR[31:0]	32
0xBB0008F8	DMA_IF_RX_CUR_DESC_ADDR_CTRL [30].ADDR[31:0]	32
0xBB0008FC	DMA_IF_RX_CUR_DESC_ADDR_CTRL [31].ADDR[31:0]	32
0xBB0020AC	DMA_IF_RX_RING_CNTR [0].CNTR[9:0]	10
0xBB0020AC	DMA_IF_RX_RING_CNTR [1].CNTR[19:10]	10
0xBB0020AC	DMA_IF_RX_RING_CNTR [2].CNTR[29:20]	10
0xBB0020B0	DMA_IF_RX_RING_CNTR [3].CNTR[9:0]	10
0xBB0020B0	DMA_IF_RX_RING_CNTR [4].CNTR[19:10]	10
0xBB0020B0	DMA_IF_RX_RING_CNTR [5].CNTR[29:20]	10
0xBB0020B4	DMA_IF_RX_RING_CNTR [6].CNTR[9:0]	10
0xBB0020B4	DMA_IF_RX_RING_CNTR [7].CNTR[19:10]	10
0xBB0020B4	DMA_IF_RX_RING_CNTR [8].CNTR[29:20]	10
0xBB0020B8	DMA_IF_RX_RING_CNTR [9].CNTR[9:0]	10
0xBB0020B8	DMA_IF_RX_RING_CNTR [10].CNTR[19:10]	10
0xBB0020B8	DMA_IF_RX_RING_CNTR [11].CNTR[29:20]	10
0xBB0020BC	DMA_IF_RX_RING_CNTR [12].CNTR[9:0]	10
0xBB0020BC	DMA_IF_RX_RING_CNTR [13].CNTR[19:10]	10
0xBB0020BC	DMA_IF_RX_RING_CNTR [14].CNTR[29:20]	10
0xBB0020C0	DMA_IF_RX_RING_CNTR [15].CNTR[9:0]	10
0xBB0020C0	DMA_IF_RX_RING_CNTR [16].CNTR[19:10]	10
0xBB0020C0	DMA_IF_RX_RING_CNTR [17].CNTR[29:20]	10
0xBB0020C4	DMA_IF_RX_RING_CNTR [18].CNTR[9:0]	10
0xBB0020C4	DMA_IF_RX_RING_CNTR [19].CNTR[19:10]	10
0xBB0020C4	DMA_IF_RX_RING_CNTR [20].CNTR[29:20]	10
0xBB0020C8	DMA_IF_RX_RING_CNTR [21].CNTR[9:0]	10
0xBB0020C8	DMA_IF_RX_RING_CNTR [22].CNTR[19:10]	10
0xBB0020C8	DMA_IF_RX_RING_CNTR [23].CNTR[29:20]	10
0xBB0020CC	DMA_IF_RX_RING_CNTR [24].CNTR[9:0]	10
0xBB0020CC	DMA_IF_RX_RING_CNTR [25].CNTR[19:10]	10
0xBB0020CC	DMA_IF_RX_RING_CNTR [26].CNTR[29:20]	10
0xBB0020D0	DMA_IF_RX_RING_CNTR [27].CNTR[9:0]	10
0xBB0020D0	DMA_IF_RX_RING_CNTR [28].CNTR[19:10]	10
0xBB0020D0	DMA_IF_RX_RING_CNTR [29].CNTR[29:20]	10
0xBB0020D4	DMA_IF_RX_RING_CNTR [30].CNTR[9:0]	10
0xBB0020D4	DMA_IF_RX_RING_CNTR [31].CNTR[19:10]	10
0xBB002080	DMA_IF_RX_RING_SIZE [0].SIZE[9:0]	10
0xBB002080	DMA_IF_RX_RING_SIZE [1].SIZE[19:10]	10
0xBB002080	DMA_IF_RX_RING_SIZE [2].SIZE[29:20]	10
0xBB002084	DMA_IF_RX_RING_SIZE [3].SIZE[9:0]	10
0xBB002084	DMA_IF_RX_RING_SIZE [4].SIZE[19:10]	10
0xBB002084	DMA_IF_RX_RING_SIZE [5].SIZE[29:20]	10
0xBB002088	DMA_IF_RX_RING_SIZE [6].SIZE[9:0]	10
0xBB002088	DMA_IF_RX_RING_SIZE [7].SIZE[19:10]	10
0xBB002088	DMA_IF_RX_RING_SIZE [8].SIZE[29:20]	10

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00208C	DMA_IF_RX_RING_SIZE [9].SIZE[9:0]	10
0xBB00208C	DMA_IF_RX_RING_SIZE [10].SIZE[19:10]	10
0xBB00208C	DMA_IF_RX_RING_SIZE [11].SIZE[29:20]	10
0xBB002090	DMA_IF_RX_RING_SIZE [12].SIZE[9:0]	10
0xBB002090	DMA_IF_RX_RING_SIZE [13].SIZE[19:10]	10
0xBB002090	DMA_IF_RX_RING_SIZE [14].SIZE[29:20]	10
0xBB002094	DMA_IF_RX_RING_SIZE [15].SIZE[9:0]	10
0xBB002094	DMA_IF_RX_RING_SIZE [16].SIZE[19:10]	10
0xBB002094	DMA_IF_RX_RING_SIZE [17].SIZE[29:20]	10
0xBB002098	DMA_IF_RX_RING_SIZE [18].SIZE[9:0]	10
0xBB002098	DMA_IF_RX_RING_SIZE [19].SIZE[19:10]	10
0xBB002098	DMA_IF_RX_RING_SIZE [20].SIZE[29:20]	10
0xBB00209C	DMA_IF_RX_RING_SIZE [21].SIZE[9:0]	10
0xBB00209C	DMA_IF_RX_RING_SIZE [22].SIZE[19:10]	10
0xBB00209C	DMA_IF_RX_RING_SIZE [23].SIZE[29:20]	10
0xBB0020A0	DMA_IF_RX_RING_SIZE [24].SIZE[9:0]	10
0xBB0020A0	DMA_IF_RX_RING_SIZE [25].SIZE[19:10]	10
0xBB0020A0	DMA_IF_RX_RING_SIZE [26].SIZE[29:20]	10
0xBB0020A4	DMA_IF_RX_RING_SIZE [27].SIZE[9:0]	10
0xBB0020A4	DMA_IF_RX_RING_SIZE [28].SIZE[19:10]	10
0xBB0020A4	DMA_IF_RX_RING_SIZE [29].SIZE[29:20]	10
0xBB0020A8	DMA_IF_RX_RING_SIZE [30].SIZE[9:0]	10
0xBB0020A8	DMA_IF_RX_RING_SIZE [31].SIZE[19:10]	10
0xBB000900	DMA_IF_TX_BASE_DESC_ADDR_CTRL [0].ADDR[31:0]	32
0xBB000904	DMA_IF_TX_BASE_DESC_ADDR_CTRL [1].ADDR[31:0]	32
0xBB000908	DMA_IF_TX_CUR_DESC_ADDR_CTRL [0].ADDR[31:0]	32
0xBB00090C	DMA_IF_TX_CUR_DESC_ADDR_CTRL [1].ADDR[31:0]	32
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].RESERVED[31:14]	18
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].WT_MPLS[13:12]	2
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].RESERVED[11:10]	2
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].WT_OTAG[9:8]	2
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].RESERVED[7:6]	2
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].WT_ITAG[5:4]	2
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].RESERVED[3:2]	2
0xBB0090B8	DP_SEL_PORT_TBL_CTRL [0].WT_DSCP[1:0]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].RESERVED[31:14]	18
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].WT_MPLS[13:12]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].RESERVED[11:10]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].WT_OTAG[9:8]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].RESERVED[7:6]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].WT_ITAG[5:4]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].RESERVED[3:2]	2
0xBB0090BC	DP_SEL_PORT_TBL_CTRL [1].WT_DSCP[1:0]	2
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].RESERVED[31:14]	18
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].WT_MPLS[13:12]	2
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].RESERVED[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].WT_OTAG[9:8]	2
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].RESERVED[7:6]	2
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].WT_ITAG[5:4]	2
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].RESERVED[3:2]	2
0xBB0090C0	DP_SEL_PORT_TBL_CTRL [2].WT_DSCP[1:0]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].RESERVED[31:14]	18
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].WT_MPLS[13:12]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].RESERVED[11:10]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].WT_OTAG[9:8]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].RESERVED[7:6]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].WT_ITAG[5:4]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].RESERVED[3:2]	2
0xBB0090C4	DP_SEL_PORT_TBL_CTRL [3].WT_DSCP[1:0]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].RESERVED[31:14]	18
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].WT_MPLS[13:12]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].RESERVED[11:10]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].WT_OTAG[9:8]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].RESERVED[7:6]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].WT_ITAG[5:4]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].RESERVED[3:2]	2
0xBB0090C8	DP_SEL_PORT_TBL_CTRL [4].WT_DSCP[1:0]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].RESERVED[31:14]	18
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].WT_MPLS[13:12]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].RESERVED[11:10]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].WT_OTAG[9:8]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].RESERVED[7:6]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].WT_ITAG[5:4]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].RESERVED[3:2]	2
0xBB0090CC	DP_SEL_PORT_TBL_CTRL [5].WT_DSCP[1:0]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].RESERVED[31:14]	18
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].WT_MPLS[13:12]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].RESERVED[11:10]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].WT_OTAG[9:8]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].RESERVED[7:6]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].WT_ITAG[5:4]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].RESERVED[3:2]	2
0xBB0090D0	DP_SEL_PORT_TBL_CTRL [6].WT_DSCP[1:0]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].RESERVED[31:14]	18
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].WT_MPLS[13:12]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].RESERVED[11:10]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].WT_OTAG[9:8]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].RESERVED[7:6]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].WT_ITAG[5:4]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].RESERVED[3:2]	2
0xBB0090D4	DP_SEL_PORT_TBL_CTRL [7].WT_DSCP[1:0]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].RESERVED[31:14]	18

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].WT_MPLS[13:12]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].RESERVED[11:10]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].WT_OTAG[9:8]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].RESERVED[7:6]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].WT_ITAG[5:4]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].RESERVED[3:2]	2
0xBB0090D8	DP_SEL_PORT_TBL_CTRL [8].WT_DSCP[1:0]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].RESERVED[31:14]	18
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].WT_MPLS[13:12]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].RESERVED[11:10]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].WT_OTAG[9:8]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].RESERVED[7:6]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].WT_ITAG[5:4]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].RESERVED[3:2]	2
0xBB0090DC	DP_SEL_PORT_TBL_CTRL [9].WT_DSCP[1:0]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].RESERVED[31:14]	18
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].WT_MPLS[13:12]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].RESERVED[11:10]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].WT_OTAG[9:8]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].RESERVED[7:6]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].WT_ITAG[5:4]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].RESERVED[3:2]	2
0xBB0090E0	DP_SEL_PORT_TBL_CTRL [10].WT_DSCP[1:0]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].RESERVED[31:14]	18
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].WT_MPLS[13:12]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].RESERVED[11:10]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].WT_OTAG[9:8]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].RESERVED[7:6]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].WT_ITAG[5:4]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].RESERVED[3:2]	2
0xBB0090E4	DP_SEL_PORT_TBL_CTRL [11].WT_DSCP[1:0]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].RESERVED[31:14]	18
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].WT_MPLS[13:12]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].RESERVED[11:10]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].WT_OTAG[9:8]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].RESERVED[7:6]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].WT_ITAG[5:4]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].RESERVED[3:2]	2
0xBB0090E8	DP_SEL_PORT_TBL_CTRL [12].WT_DSCP[1:0]	2
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].RESERVED[31:14]	18
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].WT_MPLS[13:12]	2
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].RESERVED[11:10]	2
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].WT_OTAG[9:8]	2
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].RESERVED[7:6]	2
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].WT_ITAG[5:4]	2
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].RESERVED[3:2]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0090EC	DP_SEL_PORT_TBL_CTRL [13].WT_DSCP[1:0]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].RESERVED[31:14]	18
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].WT_MPLS[13:12]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].RESERVED[11:10]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].WT_OTAG[9:8]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].RESERVED[7:6]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].WT_ITAG[5:4]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].RESERVED[3:2]	2
0xBB0090F0	DP_SEL_PORT_TBL_CTRL [14].WT_DSCP[1:0]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].RESERVED[31:14]	18
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].WT_MPLS[13:12]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].RESERVED[11:10]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].WT_OTAG[9:8]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].RESERVED[7:6]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].WT_ITAG[5:4]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].RESERVED[3:2]	2
0xBB0090F4	DP_SEL_PORT_TBL_CTRL [15].WT_DSCP[1:0]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].RESERVED[31:14]	18
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].WT_MPLS[13:12]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].RESERVED[11:10]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].WT_OTAG[9:8]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].RESERVED[7:6]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].WT_ITAG[5:4]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].RESERVED[3:2]	2
0xBB0090F8	DP_SEL_PORT_TBL_CTRL [16].WT_DSCP[1:0]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].RESERVED[31:14]	18
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].WT_MPLS[13:12]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].RESERVED[11:10]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].WT_OTAG[9:8]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].RESERVED[7:6]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].WT_ITAG[5:4]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].RESERVED[3:2]	2
0xBB0090FC	DP_SEL_PORT_TBL_CTRL [17].WT_DSCP[1:0]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].RESERVED[31:14]	18
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].WT_MPLS[13:12]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].RESERVED[11:10]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].WT_OTAG[9:8]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].RESERVED[7:6]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].WT_ITAG[5:4]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].RESERVED[3:2]	2
0xBB009100	DP_SEL_PORT_TBL_CTRL [18].WT_DSCP[1:0]	2
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].RESERVED[31:14]	18
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].WT_MPLS[13:12]	2
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].RESERVED[11:10]	2
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].WT_OTAG[9:8]	2
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].RESERVED[7:6]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].WT_ITAG[5:4]	2
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].RESERVED[3:2]	2
0xBB009104	DP_SEL_PORT_TBL_CTRL [19].WT_DSCP[1:0]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].RESERVED[31:14]	18
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].WT_MPLS[13:12]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].RESERVED[11:10]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].WT_OTAG[9:8]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].RESERVED[7:6]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].WT_ITAG[5:4]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].RESERVED[3:2]	2
0xBB009108	DP_SEL_PORT_TBL_CTRL [20].WT_DSCP[1:0]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].RESERVED[31:14]	18
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].WT_MPLS[13:12]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].RESERVED[11:10]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].WT_OTAG[9:8]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].RESERVED[7:6]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].WT_ITAG[5:4]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].RESERVED[3:2]	2
0xBB00910C	DP_SEL_PORT_TBL_CTRL [21].WT_DSCP[1:0]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].RESERVED[31:14]	18
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].WT_MPLS[13:12]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].RESERVED[11:10]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].WT_OTAG[9:8]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].RESERVED[7:6]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].WT_ITAG[5:4]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].RESERVED[3:2]	2
0xBB009110	DP_SEL_PORT_TBL_CTRL [22].WT_DSCP[1:0]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].RESERVED[31:14]	18
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].WT_MPLS[13:12]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].RESERVED[11:10]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].WT_OTAG[9:8]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].RESERVED[7:6]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].WT_ITAG[5:4]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].RESERVED[3:2]	2
0xBB009114	DP_SEL_PORT_TBL_CTRL [23].WT_DSCP[1:0]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].RESERVED[31:14]	18
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].WT_MPLS[13:12]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].RESERVED[11:10]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].WT_OTAG[9:8]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].RESERVED[7:6]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].WT_ITAG[5:4]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].RESERVED[3:2]	2
0xBB009118	DP_SEL_PORT_TBL_CTRL [24].WT_DSCP[1:0]	2
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].RESERVED[31:14]	18
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].WT_MPLS[13:12]	2
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].RESERVED[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].WT_OTAG[9:8]	2
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].RESERVED[7:6]	2
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].WT_ITAG[5:4]	2
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].RESERVED[3:2]	2
0xBB00911C	DP_SEL_PORT_TBL_CTRL [25].WT_DSCP[1:0]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].RESERVED[31:14]	18
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].WT_MPLS[13:12]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].RESERVED[11:10]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].WT_OTAG[9:8]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].RESERVED[7:6]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].WT_ITAG[5:4]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].RESERVED[3:2]	2
0xBB009120	DP_SEL_PORT_TBL_CTRL [26].WT_DSCP[1:0]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].RESERVED[31:14]	18
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].WT_MPLS[13:12]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].RESERVED[11:10]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].WT_OTAG[9:8]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].RESERVED[7:6]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].WT_ITAG[5:4]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].RESERVED[3:2]	2
0xBB009124	DP_SEL_PORT_TBL_CTRL [27].WT_DSCP[1:0]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].RESERVED[31:14]	18
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].WT_MPLS[13:12]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].RESERVED[11:10]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].WT_OTAG[9:8]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].RESERVED[7:6]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].WT_ITAG[5:4]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].RESERVED[3:2]	2
0xBB009128	DP_SEL_PORT_TBL_CTRL [28].WT_DSCP[1:0]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].RESERVED[31:14]	18
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].WT_MPLS[13:12]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].RESERVED[11:10]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].WT_OTAG[9:8]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].RESERVED[7:6]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].WT_ITAG[5:4]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].RESERVED[3:2]	2
0xBB00912C	DP_SEL_PORT_TBL_CTRL [29].WT_DSCP[1:0]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].RESERVED[31:14]	18
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].WT_MPLS[13:12]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].RESERVED[11:10]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].WT_OTAG[9:8]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].RESERVED[7:6]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].WT_ITAG[5:4]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].RESERVED[3:2]	2
0xBB009130	DP_SEL_PORT_TBL_CTRL [30].WT_DSCP[1:0]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].RESERVED[31:14]	18

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].WT_MPLS[13:12]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].RESERVED[11:10]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].WT_OTAG[9:8]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].RESERVED[7:6]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].WT_ITAG[5:4]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].RESERVED[3:2]	2
0xBB009134	DP_SEL_PORT_TBL_CTRL [31].WT_DSCP[1:0]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].RESERVED[31:14]	18
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].WT_MPLS[13:12]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].RESERVED[11:10]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].WT_OTAG[9:8]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].RESERVED[7:6]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].WT_ITAG[5:4]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].RESERVED[3:2]	2
0xBB009138	DP_SEL_PORT_TBL_CTRL [32].WT_DSCP[1:0]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].RESERVED[31:14]	18
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].WT_MPLS[13:12]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].RESERVED[11:10]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].WT_OTAG[9:8]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].RESERVED[7:6]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].WT_ITAG[5:4]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].RESERVED[3:2]	2
0xBB00913C	DP_SEL_PORT_TBL_CTRL [33].WT_DSCP[1:0]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].RESERVED[31:14]	18
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].WT_MPLS[13:12]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].RESERVED[11:10]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].WT_OTAG[9:8]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].RESERVED[7:6]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].WT_ITAG[5:4]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].RESERVED[3:2]	2
0xBB009140	DP_SEL_PORT_TBL_CTRL [34].WT_DSCP[1:0]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].RESERVED[31:14]	18
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].WT_MPLS[13:12]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].RESERVED[11:10]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].WT_OTAG[9:8]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].RESERVED[7:6]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].WT_ITAG[5:4]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].RESERVED[3:2]	2
0xBB009144	DP_SEL_PORT_TBL_CTRL [35].WT_DSCP[1:0]	2
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].RESERVED[31:14]	18
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].WT_MPLS[13:12]	2
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].RESERVED[11:10]	2
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].WT_OTAG[9:8]	2
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].RESERVED[7:6]	2
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].WT_ITAG[5:4]	2
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].RESERVED[3:2]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009148	DP_SEL_PORT_TBL_CTRL [36].WT_DSCP[1:0]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].RESERVED[31:14]	18
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].WT_MPLS[13:12]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].RESERVED[11:10]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].WT_OTAG[9:8]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].RESERVED[7:6]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].WT_ITAG[5:4]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].RESERVED[3:2]	2
0xBB00914C	DP_SEL_PORT_TBL_CTRL [37].WT_DSCP[1:0]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].RESERVED[31:14]	18
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].WT_MPLS[13:12]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].RESERVED[11:10]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].WT_OTAG[9:8]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].RESERVED[7:6]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].WT_ITAG[5:4]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].RESERVED[3:2]	2
0xBB009150	DP_SEL_PORT_TBL_CTRL [38].WT_DSCP[1:0]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].RESERVED[31:14]	18
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].WT_MPLS[13:12]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].RESERVED[11:10]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].WT_OTAG[9:8]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].RESERVED[7:6]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].WT_ITAG[5:4]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].RESERVED[3:2]	2
0xBB009154	DP_SEL_PORT_TBL_CTRL [39].WT_DSCP[1:0]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].RESERVED[31:14]	18
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].WT_MPLS[13:12]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].RESERVED[11:10]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].WT_OTAG[9:8]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].RESERVED[7:6]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].WT_ITAG[5:4]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].RESERVED[3:2]	2
0xBB009158	DP_SEL_PORT_TBL_CTRL [40].WT_DSCP[1:0]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].RESERVED[31:14]	18
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].WT_MPLS[13:12]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].RESERVED[11:10]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].WT_OTAG[9:8]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].RESERVED[7:6]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].WT_ITAG[5:4]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].RESERVED[3:2]	2
0xBB00915C	DP_SEL_PORT_TBL_CTRL [41].WT_DSCP[1:0]	2
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].RESERVED[31:14]	18
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].WT_MPLS[13:12]	2
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].RESERVED[11:10]	2
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].WT_OTAG[9:8]	2
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].RESERVED[7:6]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].WT_ITAG[5:4]	2
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].RESERVED[3:2]	2
0xBB009160	DP_SEL_PORT_TBL_CTRL [42].WT_DSCP[1:0]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].RESERVED[31:14]	18
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].WT_MPLS[13:12]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].RESERVED[11:10]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].WT_OTAG[9:8]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].RESERVED[7:6]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].WT_ITAG[5:4]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].RESERVED[3:2]	2
0xBB009164	DP_SEL_PORT_TBL_CTRL [43].WT_DSCP[1:0]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].RESERVED[31:14]	18
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].WT_MPLS[13:12]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].RESERVED[11:10]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].WT_OTAG[9:8]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].RESERVED[7:6]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].WT_ITAG[5:4]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].RESERVED[3:2]	2
0xBB009168	DP_SEL_PORT_TBL_CTRL [44].WT_DSCP[1:0]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].RESERVED[31:14]	18
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].WT_MPLS[13:12]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].RESERVED[11:10]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].WT_OTAG[9:8]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].RESERVED[7:6]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].WT_ITAG[5:4]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].RESERVED[3:2]	2
0xBB00916C	DP_SEL_PORT_TBL_CTRL [45].WT_DSCP[1:0]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].RESERVED[31:14]	18
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].WT_MPLS[13:12]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].RESERVED[11:10]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].WT_OTAG[9:8]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].RESERVED[7:6]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].WT_ITAG[5:4]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].RESERVED[3:2]	2
0xBB009170	DP_SEL_PORT_TBL_CTRL [46].WT_DSCP[1:0]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].RESERVED[31:14]	18
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].WT_MPLS[13:12]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].RESERVED[11:10]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].WT_OTAG[9:8]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].RESERVED[7:6]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].WT_ITAG[5:4]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].RESERVED[3:2]	2
0xBB009174	DP_SEL_PORT_TBL_CTRL [47].WT_DSCP[1:0]	2
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].RESERVED[31:14]	18
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].WT_MPLS[13:12]	2
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].RESERVED[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].WT_OTAG[9:8]	2
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].RESERVED[7:6]	2
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].WT_ITAG[5:4]	2
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].RESERVED[3:2]	2
0xBB009178	DP_SEL_PORT_TBL_CTRL [48].WT_DSCP[1:0]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].RESERVED[31:14]	18
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].WT_MPLS[13:12]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].RESERVED[11:10]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].WT_OTAG[9:8]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].RESERVED[7:6]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].WT_ITAG[5:4]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].RESERVED[3:2]	2
0xBB00917C	DP_SEL_PORT_TBL_CTRL [49].WT_DSCP[1:0]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].RESERVED[31:14]	18
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].WT_MPLS[13:12]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].RESERVED[11:10]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].WT_OTAG[9:8]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].RESERVED[7:6]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].WT_ITAG[5:4]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].RESERVED[3:2]	2
0xBB009180	DP_SEL_PORT_TBL_CTRL [50].WT_DSCP[1:0]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].RESERVED[31:14]	18
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].WT_MPLS[13:12]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].RESERVED[11:10]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].WT_OTAG[9:8]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].RESERVED[7:6]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].WT_ITAG[5:4]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].RESERVED[3:2]	2
0xBB009184	DP_SEL_PORT_TBL_CTRL [51].WT_DSCP[1:0]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].RESERVED[31:14]	18
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].WT_MPLS[13:12]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].RESERVED[11:10]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].WT_OTAG[9:8]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].RESERVED[7:6]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].WT_ITAG[5:4]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].RESERVED[3:2]	2
0xBB009188	DP_SEL_PORT_TBL_CTRL [52].WT_DSCP[1:0]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].RESERVED[31:14]	18
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].WT_MPLS[13:12]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].RESERVED[11:10]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].WT_OTAG[9:8]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].RESERVED[7:6]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].WT_ITAG[5:4]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].RESERVED[3:2]	2
0xBB00918C	DP_SEL_PORT_TBL_CTRL [53].WT_DSCP[1:0]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].RESERVED[31:14]	18

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].WT_MPLS[13:12]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].RESERVED[11:10]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].WT_OTAG[9:8]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].RESERVED[7:6]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].WT_ITAG[5:4]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].RESERVED[3:2]	2
0xBB009190	DP_SEL_PORT_TBL_CTRL [54].WT_DSCP[1:0]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].RESERVED[31:14]	18
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].WT_MPLS[13:12]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].RESERVED[11:10]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].WT_OTAG[9:8]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].RESERVED[7:6]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].WT_ITAG[5:4]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].RESERVED[3:2]	2
0xBB009194	DP_SEL_PORT_TBL_CTRL [55].WT_DSCP[1:0]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].RESERVED[31:14]	18
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].WT_MPLS[13:12]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].RESERVED[11:10]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].WT_OTAG[9:8]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].RESERVED[7:6]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].WT_ITAG[5:4]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].RESERVED[3:2]	2
0xBB009198	DP_SEL_PORT_TBL_CTRL [56].WT_DSCP[1:0]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [0].DP_DSCP[1:0]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [1].DP_DSCP[3:2]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [2].DP_DSCP[5:4]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [3].DP_DSCP[7:6]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [4].DP_DSCP[9:8]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [5].DP_DSCP[11:10]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [6].DP_DSCP[13:12]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [7].DP_DSCP[15:14]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [8].DP_DSCP[17:16]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [9].DP_DSCP[19:18]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [10].DP_DSCP[21:20]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [11].DP_DSCP[23:22]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [12].DP_DSCP[25:24]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [13].DP_DSCP[27:26]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [14].DP_DSCP[29:28]	2
0xBB0090A4	DP_SEL_REMAP_DSCP [15].DP_DSCP[31:30]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [16].DP_DSCP[1:0]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [17].DP_DSCP[3:2]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [18].DP_DSCP[5:4]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [19].DP_DSCP[7:6]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [20].DP_DSCP[9:8]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [21].DP_DSCP[11:10]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [22].DP_DSCP[13:12]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0090A8	DP_SEL_REMAP_DSCP [23].DP_DSCP[15:14]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [24].DP_DSCP[17:16]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [25].DP_DSCP[19:18]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [26].DP_DSCP[21:20]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [27].DP_DSCP[23:22]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [28].DP_DSCP[25:24]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [29].DP_DSCP[27:26]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [30].DP_DSCP[29:28]	2
0xBB0090A8	DP_SEL_REMAP_DSCP [31].DP_DSCP[31:30]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [32].DP_DSCP[1:0]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [33].DP_DSCP[3:2]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [34].DP_DSCP[5:4]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [35].DP_DSCP[7:6]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [36].DP_DSCP[9:8]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [37].DP_DSCP[11:10]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [38].DP_DSCP[13:12]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [39].DP_DSCP[15:14]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [40].DP_DSCP[17:16]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [41].DP_DSCP[19:18]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [42].DP_DSCP[21:20]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [43].DP_DSCP[23:22]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [44].DP_DSCP[25:24]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [45].DP_DSCP[27:26]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [46].DP_DSCP[29:28]	2
0xBB0090AC	DP_SEL_REMAP_DSCP [47].DP_DSCP[31:30]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [48].DP_DSCP[1:0]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [49].DP_DSCP[3:2]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [50].DP_DSCP[5:4]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [51].DP_DSCP[7:6]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [52].DP_DSCP[9:8]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [53].DP_DSCP[11:10]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [54].DP_DSCP[13:12]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [55].DP_DSCP[15:14]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [56].DP_DSCP[17:16]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [57].DP_DSCP[19:18]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [58].DP_DSCP[21:20]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [59].DP_DSCP[23:22]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [60].DP_DSCP[25:24]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [61].DP_DSCP[27:26]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [62].DP_DSCP[29:28]	2
0xBB0090B0	DP_SEL_REMAP_DSCP [63].DP_DSCP[31:30]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [0].DP_CFI0_IPRI[1:0]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [1].DP_CFI0_IPRI[3:2]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [2].DP_CFI0_IPRI[5:4]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [3].DP_CFI0_IPRI[7:6]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [4].DP_CFI0_IPRI[9:8]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [5].DP_CFI0_IPRI[11:10]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [6].DP_CFI0_IPRI[13:12]	2
0xBB009094	DP_SEL_REMAP_ITAG_CFI0 [7].DP_CFI0_IPRI[15:14]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [0].DP_CFI1_IPRI[1:0]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [1].DP_CFI1_IPRI[3:2]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [2].DP_CFI1_IPRI[5:4]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [3].DP_CFI1_IPRI[7:6]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [4].DP_CFI1_IPRI[9:8]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [5].DP_CFI1_IPRI[11:10]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [6].DP_CFI1_IPRI[13:12]	2
0xBB009098	DP_SEL_REMAP_ITAG_CFI1 [7].DP_CFI1_IPRI[15:14]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [0].DP_TC[1:0]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [1].DP_TC[3:2]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [2].DP_TC[5:4]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [3].DP_TC[7:6]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [4].DP_TC[9:8]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [5].DP_TC[11:10]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [6].DP_TC[13:12]	2
0xBB0090B4	DP_SEL_REMAP_MPLS [7].DP_TC[15:14]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [0].DP_DEI0_OPRI[1:0]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [1].DP_DEI0_OPRI[3:2]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [2].DP_DEI0_OPRI[5:4]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [3].DP_DEI0_OPRI[7:6]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [4].DP_DEI0_OPRI[9:8]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [5].DP_DEI0_OPRI[11:10]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [6].DP_DEI0_OPRI[13:12]	2
0xBB00909C	DP_SEL_REMAP_OTAG_DEI0 [7].DP_DEI0_OPRI[15:14]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [0].DP_DEI1_OPRI[1:0]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [1].DP_DEI1_OPRI[3:2]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [2].DP_DEI1_OPRI[5:4]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [3].DP_DEI1_OPRI[7:6]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [4].DP_DEI1_OPRI[9:8]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [5].DP_DEI1_OPRI[11:10]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [6].DP_DEI1_OPRI[13:12]	2
0xBB0090A0	DP_SEL_REMAP_OTAG_DEI1 [7].DP_DEI1_OPRI[15:14]	2
0xBB001428	DYING_GASP_POLARITY_CTRL.RESERVED[31:1]	31
0xBB001428	DYING_GASP_POLARITY_CTRL.DYGASP_POLARITY[0:0]	1
0xBB005654	EEE_MISC_CTRL0.RESERVED[31:5]	27
0xBB005654	EEE_MISC_CTRL0.EEE_EN_FC_EFCT[4:4]	1
0xBB005654	EEE_MISC_CTRL0.EEE_REF_RXLPI[3:3]	1
0xBB005654	EEE_MISC_CTRL0.EEE_LINK_UP_DELAY[2:1]	2
0xBB005654	EEE_MISC_CTRL0.EEE_TX_WAKE_SEL[0:0]	1
0xBB002154	EEE_MISC_CTRL1.RESERVED[31:20]	12
0xBB002154	EEE_MISC_CTRL1.EEE_LOW_Q_THR[19:8]	12
0xBB002154	EEE_MISC_CTRL1.EEE_HIGH_Q[7:0]	8
0xBB00566C	EEE_MULTIWAKE_CTRL.RESERVED[31:8]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00566C	EEE_MULTIWAKE_CTRL.EEE_MULTIWAKE_PORTS[7:5]	3
0xBB00566C	EEE_MULTIWAKE_CTRL.EEE_MULTIWAKE_INTLV[4:3]	2
0xBB00566C	EEE_MULTIWAKE_CTRL.EEE_MULTIWAKE_TIME_UNIT[2:1]	2
0xBB00566C	EEE_MULTIWAKE_CTRL.EEE_MULTIWAKE_EN[0:0]	1
0xBB00564C	EEE_PORT_RX_EN [0].EEE_PORT_RX_EN[0:0]	1
0xBB00564C	EEE_PORT_RX_EN [1].EEE_PORT_RX_EN[1:1]	1
0xBB00564C	EEE_PORT_RX_EN [2].EEE_PORT_RX_EN[2:2]	1
0xBB00564C	EEE_PORT_RX_EN [3].EEE_PORT_RX_EN[3:3]	1
0xBB00564C	EEE_PORT_RX_EN [4].EEE_PORT_RX_EN[4:4]	1
0xBB00564C	EEE_PORT_RX_EN [5].EEE_PORT_RX_EN[5:5]	1
0xBB00564C	EEE_PORT_RX_EN [6].EEE_PORT_RX_EN[6:6]	1
0xBB00564C	EEE_PORT_RX_EN [7].EEE_PORT_RX_EN[7:7]	1
0xBB00564C	EEE_PORT_RX_EN [8].EEE_PORT_RX_EN[8:8]	1
0xBB00564C	EEE_PORT_RX_EN [9].EEE_PORT_RX_EN[9:9]	1
0xBB00564C	EEE_PORT_RX_EN [10].EEE_PORT_RX_EN[10:10]	1
0xBB00564C	EEE_PORT_RX_EN [11].EEE_PORT_RX_EN[11:11]	1
0xBB00564C	EEE_PORT_RX_EN [12].EEE_PORT_RX_EN[12:12]	1
0xBB00564C	EEE_PORT_RX_EN [13].EEE_PORT_RX_EN[13:13]	1
0xBB00564C	EEE_PORT_RX_EN [14].EEE_PORT_RX_EN[14:14]	1
0xBB00564C	EEE_PORT_RX_EN [15].EEE_PORT_RX_EN[15:15]	1
0xBB00564C	EEE_PORT_RX_EN [16].EEE_PORT_RX_EN[16:16]	1
0xBB00564C	EEE_PORT_RX_EN [17].EEE_PORT_RX_EN[17:17]	1
0xBB00564C	EEE_PORT_RX_EN [18].EEE_PORT_RX_EN[18:18]	1
0xBB00564C	EEE_PORT_RX_EN [19].EEE_PORT_RX_EN[19:19]	1
0xBB00564C	EEE_PORT_RX_EN [20].EEE_PORT_RX_EN[20:20]	1
0xBB00564C	EEE_PORT_RX_EN [21].EEE_PORT_RX_EN[21:21]	1
0xBB00564C	EEE_PORT_RX_EN [22].EEE_PORT_RX_EN[22:22]	1
0xBB00564C	EEE_PORT_RX_EN [23].EEE_PORT_RX_EN[23:23]	1
0xBB00564C	EEE_PORT_RX_EN [24].EEE_PORT_RX_EN[24:24]	1
0xBB00564C	EEE_PORT_RX_EN [25].EEE_PORT_RX_EN[25:25]	1
0xBB00564C	EEE_PORT_RX_EN [26].EEE_PORT_RX_EN[26:26]	1
0xBB00564C	EEE_PORT_RX_EN [27].EEE_PORT_RX_EN[27:27]	1
0xBB00564C	EEE_PORT_RX_EN [28].EEE_PORT_RX_EN[28:28]	1
0xBB00564C	EEE_PORT_RX_EN [29].EEE_PORT_RX_EN[29:29]	1
0xBB00564C	EEE_PORT_RX_EN [30].EEE_PORT_RX_EN[30:30]	1
0xBB00564C	EEE_PORT_RX_EN [31].EEE_PORT_RX_EN[31:31]	1
0xBB005650	EEE_PORT_RX_EN [32].EEE_PORT_RX_EN[0:0]	1
0xBB005650	EEE_PORT_RX_EN [33].EEE_PORT_RX_EN[1:1]	1
0xBB005650	EEE_PORT_RX_EN [34].EEE_PORT_RX_EN[2:2]	1
0xBB005650	EEE_PORT_RX_EN [35].EEE_PORT_RX_EN[3:3]	1
0xBB005650	EEE_PORT_RX_EN [36].EEE_PORT_RX_EN[4:4]	1
0xBB005650	EEE_PORT_RX_EN [37].EEE_PORT_RX_EN[5:5]	1
0xBB005650	EEE_PORT_RX_EN [38].EEE_PORT_RX_EN[6:6]	1
0xBB005650	EEE_PORT_RX_EN [39].EEE_PORT_RX_EN[7:7]	1
0xBB005650	EEE_PORT_RX_EN [40].EEE_PORT_RX_EN[8:8]	1
0xBB005650	EEE_PORT_RX_EN [41].EEE_PORT_RX_EN[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005650	EEE_PORT_RX_EN [42].EEE_PORT_RX_EN[10:10]	1
0xBB005650	EEE_PORT_RX_EN [43].EEE_PORT_RX_EN[11:11]	1
0xBB005650	EEE_PORT_RX_EN [44].EEE_PORT_RX_EN[12:12]	1
0xBB005650	EEE_PORT_RX_EN [45].EEE_PORT_RX_EN[13:13]	1
0xBB005650	EEE_PORT_RX_EN [46].EEE_PORT_RX_EN[14:14]	1
0xBB005650	EEE_PORT_RX_EN [47].EEE_PORT_RX_EN[15:15]	1
0xBB005650	EEE_PORT_RX_EN [48].EEE_PORT_RX_EN[16:16]	1
0xBB005650	EEE_PORT_RX_EN [49].EEE_PORT_RX_EN[17:17]	1
0xBB005650	EEE_PORT_RX_EN [50].EEE_PORT_RX_EN[18:18]	1
0xBB005650	EEE_PORT_RX_EN [51].EEE_PORT_RX_EN[19:19]	1
0xBB005650	EEE_PORT_RX_EN [52].EEE_PORT_RX_EN[20:20]	1
0xBB005650	EEE_PORT_RX_EN [53].EEE_PORT_RX_EN[21:21]	1
0xBB005650	EEE_PORT_RX_EN [54].EEE_PORT_RX_EN[22:22]	1
0xBB005650	EEE_PORT_RX_EN [55].EEE_PORT_RX_EN[23:23]	1
0xBB005690	EEE_PORT_RX_STS [0].EEE_EEEP_RX_STS[0:0]	1
0xBB005690	EEE_PORT_RX_STS [1].EEE_EEEP_RX_STS[1:1]	1
0xBB005690	EEE_PORT_RX_STS [2].EEE_EEEP_RX_STS[2:2]	1
0xBB005690	EEE_PORT_RX_STS [3].EEE_EEEP_RX_STS[3:3]	1
0xBB005690	EEE_PORT_RX_STS [4].EEE_EEEP_RX_STS[4:4]	1
0xBB005690	EEE_PORT_RX_STS [5].EEE_EEEP_RX_STS[5:5]	1
0xBB005690	EEE_PORT_RX_STS [6].EEE_EEEP_RX_STS[6:6]	1
0xBB005690	EEE_PORT_RX_STS [7].EEE_EEEP_RX_STS[7:7]	1
0xBB005690	EEE_PORT_RX_STS [8].EEE_EEEP_RX_STS[8:8]	1
0xBB005690	EEE_PORT_RX_STS [9].EEE_EEEP_RX_STS[9:9]	1
0xBB005690	EEE_PORT_RX_STS [10].EEE_EEEP_RX_STS[10:10]	1
0xBB005690	EEE_PORT_RX_STS [11].EEE_EEEP_RX_STS[11:11]	1
0xBB005690	EEE_PORT_RX_STS [12].EEE_EEEP_RX_STS[12:12]	1
0xBB005690	EEE_PORT_RX_STS [13].EEE_EEEP_RX_STS[13:13]	1
0xBB005690	EEE_PORT_RX_STS [14].EEE_EEEP_RX_STS[14:14]	1
0xBB005690	EEE_PORT_RX_STS [15].EEE_EEEP_RX_STS[15:15]	1
0xBB005690	EEE_PORT_RX_STS [16].EEE_EEEP_RX_STS[16:16]	1
0xBB005690	EEE_PORT_RX_STS [17].EEE_EEEP_RX_STS[17:17]	1
0xBB005690	EEE_PORT_RX_STS [18].EEE_EEEP_RX_STS[18:18]	1
0xBB005690	EEE_PORT_RX_STS [19].EEE_EEEP_RX_STS[19:19]	1
0xBB005690	EEE_PORT_RX_STS [20].EEE_EEEP_RX_STS[20:20]	1
0xBB005690	EEE_PORT_RX_STS [21].EEE_EEEP_RX_STS[21:21]	1
0xBB005690	EEE_PORT_RX_STS [22].EEE_EEEP_RX_STS[22:22]	1
0xBB005690	EEE_PORT_RX_STS [23].EEE_EEEP_RX_STS[23:23]	1
0xBB005690	EEE_PORT_RX_STS [24].EEE_EEEP_RX_STS[24:24]	1
0xBB005690	EEE_PORT_RX_STS [25].EEE_EEEP_RX_STS[25:25]	1
0xBB005690	EEE_PORT_RX_STS [26].EEE_EEEP_RX_STS[26:26]	1
0xBB005690	EEE_PORT_RX_STS [27].EEE_EEEP_RX_STS[27:27]	1
0xBB005690	EEE_PORT_RX_STS [28].EEE_EEEP_RX_STS[28:28]	1
0xBB005690	EEE_PORT_RX_STS [29].EEE_EEEP_RX_STS[29:29]	1
0xBB005690	EEE_PORT_RX_STS [30].EEE_EEEP_RX_STS[30:30]	1
0xBB005690	EEE_PORT_RX_STS [31].EEE_EEEP_RX_STS[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005694	EEE_PORT_RX_STS [32].EEE_EEEP_RX_STS[0:0]	1
0xBB005694	EEE_PORT_RX_STS [33].EEE_EEEP_RX_STS[1:1]	1
0xBB005694	EEE_PORT_RX_STS [34].EEE_EEEP_RX_STS[2:2]	1
0xBB005694	EEE_PORT_RX_STS [35].EEE_EEEP_RX_STS[3:3]	1
0xBB005694	EEE_PORT_RX_STS [36].EEE_EEEP_RX_STS[4:4]	1
0xBB005694	EEE_PORT_RX_STS [37].EEE_EEEP_RX_STS[5:5]	1
0xBB005694	EEE_PORT_RX_STS [38].EEE_EEEP_RX_STS[6:6]	1
0xBB005694	EEE_PORT_RX_STS [39].EEE_EEEP_RX_STS[7:7]	1
0xBB005694	EEE_PORT_RX_STS [40].EEE_EEEP_RX_STS[8:8]	1
0xBB005694	EEE_PORT_RX_STS [41].EEE_EEEP_RX_STS[9:9]	1
0xBB005694	EEE_PORT_RX_STS [42].EEE_EEEP_RX_STS[10:10]	1
0xBB005694	EEE_PORT_RX_STS [43].EEE_EEEP_RX_STS[11:11]	1
0xBB005694	EEE_PORT_RX_STS [44].EEE_EEEP_RX_STS[12:12]	1
0xBB005694	EEE_PORT_RX_STS [45].EEE_EEEP_RX_STS[13:13]	1
0xBB005694	EEE_PORT_RX_STS [46].EEE_EEEP_RX_STS[14:14]	1
0xBB005694	EEE_PORT_RX_STS [47].EEE_EEEP_RX_STS[15:15]	1
0xBB005694	EEE_PORT_RX_STS [48].EEE_EEEP_RX_STS[16:16]	1
0xBB005694	EEE_PORT_RX_STS [49].EEE_EEEP_RX_STS[17:17]	1
0xBB005694	EEE_PORT_RX_STS [50].EEE_EEEP_RX_STS[18:18]	1
0xBB005694	EEE_PORT_RX_STS [51].EEE_EEEP_RX_STS[19:19]	1
0xBB005694	EEE_PORT_RX_STS [52].EEE_EEEP_RX_STS[20:20]	1
0xBB005694	EEE_PORT_RX_STS [53].EEE_EEEP_RX_STS[21:21]	1
0xBB005694	EEE_PORT_RX_STS [54].EEE_EEEP_RX_STS[22:22]	1
0xBB005694	EEE_PORT_RX_STS [55].EEE_EEEP_RX_STS[23:23]	1
0xBB005644	EEE_PORT_TX_EN [0].EEE_PORT_TX_EN[0:0]	1
0xBB005644	EEE_PORT_TX_EN [1].EEE_PORT_TX_EN[1:1]	1
0xBB005644	EEE_PORT_TX_EN [2].EEE_PORT_TX_EN[2:2]	1
0xBB005644	EEE_PORT_TX_EN [3].EEE_PORT_TX_EN[3:3]	1
0xBB005644	EEE_PORT_TX_EN [4].EEE_PORT_TX_EN[4:4]	1
0xBB005644	EEE_PORT_TX_EN [5].EEE_PORT_TX_EN[5:5]	1
0xBB005644	EEE_PORT_TX_EN [6].EEE_PORT_TX_EN[6:6]	1
0xBB005644	EEE_PORT_TX_EN [7].EEE_PORT_TX_EN[7:7]	1
0xBB005644	EEE_PORT_TX_EN [8].EEE_PORT_TX_EN[8:8]	1
0xBB005644	EEE_PORT_TX_EN [9].EEE_PORT_TX_EN[9:9]	1
0xBB005644	EEE_PORT_TX_EN [10].EEE_PORT_TX_EN[10:10]	1
0xBB005644	EEE_PORT_TX_EN [11].EEE_PORT_TX_EN[11:11]	1
0xBB005644	EEE_PORT_TX_EN [12].EEE_PORT_TX_EN[12:12]	1
0xBB005644	EEE_PORT_TX_EN [13].EEE_PORT_TX_EN[13:13]	1
0xBB005644	EEE_PORT_TX_EN [14].EEE_PORT_TX_EN[14:14]	1
0xBB005644	EEE_PORT_TX_EN [15].EEE_PORT_TX_EN[15:15]	1
0xBB005644	EEE_PORT_TX_EN [16].EEE_PORT_TX_EN[16:16]	1
0xBB005644	EEE_PORT_TX_EN [17].EEE_PORT_TX_EN[17:17]	1
0xBB005644	EEE_PORT_TX_EN [18].EEE_PORT_TX_EN[18:18]	1
0xBB005644	EEE_PORT_TX_EN [19].EEE_PORT_TX_EN[19:19]	1
0xBB005644	EEE_PORT_TX_EN [20].EEE_PORT_TX_EN[20:20]	1
0xBB005644	EEE_PORT_TX_EN [21].EEE_PORT_TX_EN[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005644	EEE_PORT_TX_EN [22].EEE_PORT_TX_EN[22:22]	1
0xBB005644	EEE_PORT_TX_EN [23].EEE_PORT_TX_EN[23:23]	1
0xBB005644	EEE_PORT_TX_EN [24].EEE_PORT_TX_EN[24:24]	1
0xBB005644	EEE_PORT_TX_EN [25].EEE_PORT_TX_EN[25:25]	1
0xBB005644	EEE_PORT_TX_EN [26].EEE_PORT_TX_EN[26:26]	1
0xBB005644	EEE_PORT_TX_EN [27].EEE_PORT_TX_EN[27:27]	1
0xBB005644	EEE_PORT_TX_EN [28].EEE_PORT_TX_EN[28:28]	1
0xBB005644	EEE_PORT_TX_EN [29].EEE_PORT_TX_EN[29:29]	1
0xBB005644	EEE_PORT_TX_EN [30].EEE_PORT_TX_EN[30:30]	1
0xBB005644	EEE_PORT_TX_EN [31].EEE_PORT_TX_EN[31:31]	1
0xBB005648	EEE_PORT_TX_EN [32].EEE_PORT_TX_EN[0:0]	1
0xBB005648	EEE_PORT_TX_EN [33].EEE_PORT_TX_EN[1:1]	1
0xBB005648	EEE_PORT_TX_EN [34].EEE_PORT_TX_EN[2:2]	1
0xBB005648	EEE_PORT_TX_EN [35].EEE_PORT_TX_EN[3:3]	1
0xBB005648	EEE_PORT_TX_EN [36].EEE_PORT_TX_EN[4:4]	1
0xBB005648	EEE_PORT_TX_EN [37].EEE_PORT_TX_EN[5:5]	1
0xBB005648	EEE_PORT_TX_EN [38].EEE_PORT_TX_EN[6:6]	1
0xBB005648	EEE_PORT_TX_EN [39].EEE_PORT_TX_EN[7:7]	1
0xBB005648	EEE_PORT_TX_EN [40].EEE_PORT_TX_EN[8:8]	1
0xBB005648	EEE_PORT_TX_EN [41].EEE_PORT_TX_EN[9:9]	1
0xBB005648	EEE_PORT_TX_EN [42].EEE_PORT_TX_EN[10:10]	1
0xBB005648	EEE_PORT_TX_EN [43].EEE_PORT_TX_EN[11:11]	1
0xBB005648	EEE_PORT_TX_EN [44].EEE_PORT_TX_EN[12:12]	1
0xBB005648	EEE_PORT_TX_EN [45].EEE_PORT_TX_EN[13:13]	1
0xBB005648	EEE_PORT_TX_EN [46].EEE_PORT_TX_EN[14:14]	1
0xBB005648	EEE_PORT_TX_EN [47].EEE_PORT_TX_EN[15:15]	1
0xBB005648	EEE_PORT_TX_EN [48].EEE_PORT_TX_EN[16:16]	1
0xBB005648	EEE_PORT_TX_EN [49].EEE_PORT_TX_EN[17:17]	1
0xBB005648	EEE_PORT_TX_EN [50].EEE_PORT_TX_EN[18:18]	1
0xBB005648	EEE_PORT_TX_EN [51].EEE_PORT_TX_EN[19:19]	1
0xBB005648	EEE_PORT_TX_EN [52].EEE_PORT_TX_EN[20:20]	1
0xBB005648	EEE_PORT_TX_EN [53].EEE_PORT_TX_EN[21:21]	1
0xBB005648	EEE_PORT_TX_EN [54].EEE_PORT_TX_EN[22:22]	1
0xBB005648	EEE_PORT_TX_EN [55].EEE_PORT_TX_EN[23:23]	1
0xBB005688	EEE_PORT_TX_STS [0].EEE_EEEP_TX_STS[0:0]	1
0xBB005688	EEE_PORT_TX_STS [1].EEE_EEEP_TX_STS[1:1]	1
0xBB005688	EEE_PORT_TX_STS [2].EEE_EEEP_TX_STS[2:2]	1
0xBB005688	EEE_PORT_TX_STS [3].EEE_EEEP_TX_STS[3:3]	1
0xBB005688	EEE_PORT_TX_STS [4].EEE_EEEP_TX_STS[4:4]	1
0xBB005688	EEE_PORT_TX_STS [5].EEE_EEEP_TX_STS[5:5]	1
0xBB005688	EEE_PORT_TX_STS [6].EEE_EEEP_TX_STS[6:6]	1
0xBB005688	EEE_PORT_TX_STS [7].EEE_EEEP_TX_STS[7:7]	1
0xBB005688	EEE_PORT_TX_STS [8].EEE_EEEP_TX_STS[8:8]	1
0xBB005688	EEE_PORT_TX_STS [9].EEE_EEEP_TX_STS[9:9]	1
0xBB005688	EEE_PORT_TX_STS [10].EEE_EEEP_TX_STS[10:10]	1
0xBB005688	EEE_PORT_TX_STS [11].EEE_EEEP_TX_STS[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005688	EEE_PORT_TX_STS [12].EEE_EEEP_TX_STS[12:12]	1
0xBB005688	EEE_PORT_TX_STS [13].EEE_EEEP_TX_STS[13:13]	1
0xBB005688	EEE_PORT_TX_STS [14].EEE_EEEP_TX_STS[14:14]	1
0xBB005688	EEE_PORT_TX_STS [15].EEE_EEEP_TX_STS[15:15]	1
0xBB005688	EEE_PORT_TX_STS [16].EEE_EEEP_TX_STS[16:16]	1
0xBB005688	EEE_PORT_TX_STS [17].EEE_EEEP_TX_STS[17:17]	1
0xBB005688	EEE_PORT_TX_STS [18].EEE_EEEP_TX_STS[18:18]	1
0xBB005688	EEE_PORT_TX_STS [19].EEE_EEEP_TX_STS[19:19]	1
0xBB005688	EEE_PORT_TX_STS [20].EEE_EEEP_TX_STS[20:20]	1
0xBB005688	EEE_PORT_TX_STS [21].EEE_EEEP_TX_STS[21:21]	1
0xBB005688	EEE_PORT_TX_STS [22].EEE_EEEP_TX_STS[22:22]	1
0xBB005688	EEE_PORT_TX_STS [23].EEE_EEEP_TX_STS[23:23]	1
0xBB005688	EEE_PORT_TX_STS [24].EEE_EEEP_TX_STS[24:24]	1
0xBB005688	EEE_PORT_TX_STS [25].EEE_EEEP_TX_STS[25:25]	1
0xBB005688	EEE_PORT_TX_STS [26].EEE_EEEP_TX_STS[26:26]	1
0xBB005688	EEE_PORT_TX_STS [27].EEE_EEEP_TX_STS[27:27]	1
0xBB005688	EEE_PORT_TX_STS [28].EEE_EEEP_TX_STS[28:28]	1
0xBB005688	EEE_PORT_TX_STS [29].EEE_EEEP_TX_STS[29:29]	1
0xBB005688	EEE_PORT_TX_STS [30].EEE_EEEP_TX_STS[30:30]	1
0xBB005688	EEE_PORT_TX_STS [31].EEE_EEEP_TX_STS[31:31]	1
0xBB00568C	EEE_PORT_TX_STS [32].EEE_EEEP_TX_STS[0:0]	1
0xBB00568C	EEE_PORT_TX_STS [33].EEE_EEEP_TX_STS[1:1]	1
0xBB00568C	EEE_PORT_TX_STS [34].EEE_EEEP_TX_STS[2:2]	1
0xBB00568C	EEE_PORT_TX_STS [35].EEE_EEEP_TX_STS[3:3]	1
0xBB00568C	EEE_PORT_TX_STS [36].EEE_EEEP_TX_STS[4:4]	1
0xBB00568C	EEE_PORT_TX_STS [37].EEE_EEEP_TX_STS[5:5]	1
0xBB00568C	EEE_PORT_TX_STS [38].EEE_EEEP_TX_STS[6:6]	1
0xBB00568C	EEE_PORT_TX_STS [39].EEE_EEEP_TX_STS[7:7]	1
0xBB00568C	EEE_PORT_TX_STS [40].EEE_EEEP_TX_STS[8:8]	1
0xBB00568C	EEE_PORT_TX_STS [41].EEE_EEEP_TX_STS[9:9]	1
0xBB00568C	EEE_PORT_TX_STS [42].EEE_EEEP_TX_STS[10:10]	1
0xBB00568C	EEE_PORT_TX_STS [43].EEE_EEEP_TX_STS[11:11]	1
0xBB00568C	EEE_PORT_TX_STS [44].EEE_EEEP_TX_STS[12:12]	1
0xBB00568C	EEE_PORT_TX_STS [45].EEE_EEEP_TX_STS[13:13]	1
0xBB00568C	EEE_PORT_TX_STS [46].EEE_EEEP_TX_STS[14:14]	1
0xBB00568C	EEE_PORT_TX_STS [47].EEE_EEEP_TX_STS[15:15]	1
0xBB00568C	EEE_PORT_TX_STS [48].EEE_EEEP_TX_STS[16:16]	1
0xBB00568C	EEE_PORT_TX_STS [49].EEE_EEEP_TX_STS[17:17]	1
0xBB00568C	EEE_PORT_TX_STS [50].EEE_EEEP_TX_STS[18:18]	1
0xBB00568C	EEE_PORT_TX_STS [51].EEE_EEEP_TX_STS[19:19]	1
0xBB00568C	EEE_PORT_TX_STS [52].EEE_EEEP_TX_STS[20:20]	1
0xBB00568C	EEE_PORT_TX_STS [53].EEE_EEEP_TX_STS[21:21]	1
0xBB00568C	EEE_PORT_TX_STS [54].EEE_EEEP_TX_STS[22:22]	1
0xBB00568C	EEE_PORT_TX_STS [55].EEE_EEEP_TX_STS[23:23]	1
0xBB005658	EEE_TX_MINIFG_CTRL0.EEE_TX_LPI_MINIPG_GELITE[31:16]	16
0xBB005658	EEE_TX_MINIFG_CTRL0.EEE_TX_LPI_MINIPG_FE[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00565C	EEE_TX_MINIFG_CTRL1.EEE_TX_LPI_MINIPG_10G[31:16]	16
0xBB00565C	EEE_TX_MINIFG_CTRL1.EEE_TX_LPI_MINIPG_GE[15:0]	16
0xBB005660	EEE_TX_MINIFG_CTRL2.EEE_TX_LPI_MINIPG_5G[31:16]	16
0xBB005660	EEE_TX_MINIFG_CTRL2.EEE_TX_LPI_MINIPG_2P5G[15:0]	16
0xBB005670	EEE_TX_TIMER_100M_CTRL.RESERVED[31:20]	12
0xBB005670	EEE_TX_TIMER_100M_CTRL.EEE_LOW_Q_TX_DELAY_FE[19:8]	12
0xBB005670	EEE_TX_TIMER_100M_CTRL.EEE_TX_WAKE_TIMER_FE[7:0]	8
0xBB005684	EEE_TX_TIMER_10G_CTRL.RESERVED[31:20]	12
0xBB005684	EEE_TX_TIMER_10G_CTRL.EEE_LOW_Q_TX_DELAY_10G[19:8]	12
0xBB005684	EEE_TX_TIMER_10G_CTRL.EEE_TX_WAKE_TIMER_10G[7:0]	8
0xBB00567C	EEE_TX_TIMER_2P5G_CTRL.RESERVED[31:20]	12
0xBB00567C	EEE_TX_TIMER_2P5G_CTRL.EEE_LOW_Q_TX_DELAY_2P5G[19:8]	12
0xBB00567C	EEE_TX_TIMER_2P5G_CTRL.TX_WAKE_TIMER_2P5G[7:0]	8
0xBB005680	EEE_TX_TIMER_5G_CTRL.RESERVED[31:20]	12
0xBB005680	EEE_TX_TIMER_5G_CTRL.EEE_LOW_Q_TX_DELAY_5G[19:8]	12
0xBB005680	EEE_TX_TIMER_5G_CTRL.TX_WAKE_TIMER_5G[7:0]	8
0xBB005674	EEE_TX_TIMER_GELITE_CTRL.RESERVED[31:28]	4
0xBB005674	EEE_TX_TIMER_GELITE_CTRL.EEE_TX_PAUSE_WAKE_TIMER_GELITE[27:20]	8
0xBB005674	EEE_TX_TIMER_GELITE_CTRL.EEE_LOW_Q_TX_DELAY_GELITE[19:8]	12
0xBB005674	EEE_TX_TIMER_GELITE_CTRL.EEE_TX_WAKE_TIMER_GELITE[7:0]	8
0xBB005678	EEE_TX_TIMER_GIGA_CTRL.RESERVED[31:28]	4
0xBB005678	EEE_TX_TIMER_GIGA_CTRL.EEE_TX_PAUSE_WAKE_TIMER_GE[27:20]	8
0xBB005678	EEE_TX_TIMER_GIGA_CTRL.EEE_LOW_Q_TX_DELAY_GE[19:8]	12
0xBB005678	EEE_TX_TIMER_GIGA_CTRL.TX_WAKE_TIMER_GE[7:0]	8
0xBB005664	EEE_WAIT_RX_INACTIVE_CTRL0.RESERVED[31:26]	6
0xBB005664	EEE_WAIT_RX_INACTIVE_CTRL0.EEE_WAIT_RX_INACTIVE_GE[25:25]	1
0xBB005664	EEE_WAIT_RX_INACTIVE_CTRL0.EEE_WAIT_RX_INACTIVE_GELITE[24:24]	1
0xBB005664	EEE_WAIT_RX_INACTIVE_CTRL0.RESERVED[23:16]	8
0xBB005664	EEE_WAIT_RX_INACTIVE_CTRL0.EEE_WAIT_RX_INACTIVE_TIMER_GE[15:8]	8
0xBB005664	EEE_WAIT_RX_INACTIVE_CTRL0.EEE_WAIT_RX_INACTIVE_TIMER_GELITE[7:0]	8
0xBB00215C	EGBW_CPU_PPS_LB_CTRL.TICK[31:8]	24
0xBB00215C	EGBW_CPU_PPS_LB_CTRL.TKN[7:0]	8
0xBB002414	EGBW_CPU_Q_MAX_LB_CTRL [0].RESERVED[31:21]	11
0xBB002414	EGBW_CPU_Q_MAX_LB_CTRL [0].EN[20:20]	1
0xBB002414	EGBW_CPU_Q_MAX_LB_CTRL [0].RATE[19:0]	20
0xBB002418	EGBW_CPU_Q_MAX_LB_CTRL [0].RESERVED[31:16]	16
0xBB002418	EGBW_CPU_Q_MAX_LB_CTRL [0].BURST[15:0]	16
0xBB00241C	EGBW_CPU_Q_MAX_LB_CTRL [1].RESERVED[31:21]	11
0xBB00241C	EGBW_CPU_Q_MAX_LB_CTRL [1].EN[20:20]	1
0xBB00241C	EGBW_CPU_Q_MAX_LB_CTRL [1].RATE[19:0]	20
0xBB002420	EGBW_CPU_Q_MAX_LB_CTRL [1].RESERVED[31:16]	16
0xBB002420	EGBW_CPU_Q_MAX_LB_CTRL [1].BURST[15:0]	16
0xBB002424	EGBW_CPU_Q_MAX_LB_CTRL [2].RESERVED[31:21]	11
0xBB002424	EGBW_CPU_Q_MAX_LB_CTRL [2].EN[20:20]	1
0xBB002424	EGBW_CPU_Q_MAX_LB_CTRL [2].RATE[19:0]	20
0xBB002428	EGBW_CPU_Q_MAX_LB_CTRL [2].RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002428	EGBW_CPU_Q_MAX_LB_CTRL [2].BURST[15:0]	16
0xBB00242C	EGBW_CPU_Q_MAX_LB_CTRL [3].RESERVED[31:21]	11
0xBB00242C	EGBW_CPU_Q_MAX_LB_CTRL [3].EN[20:20]	1
0xBB00242C	EGBW_CPU_Q_MAX_LB_CTRL [3].RATE[19:0]	20
0xBB002430	EGBW_CPU_Q_MAX_LB_CTRL [3].RESERVED[31:16]	16
0xBB002430	EGBW_CPU_Q_MAX_LB_CTRL [3].BURST[15:0]	16
0xBB002434	EGBW_CPU_Q_MAX_LB_CTRL [4].RESERVED[31:21]	11
0xBB002434	EGBW_CPU_Q_MAX_LB_CTRL [4].EN[20:20]	1
0xBB002434	EGBW_CPU_Q_MAX_LB_CTRL [4].RATE[19:0]	20
0xBB002438	EGBW_CPU_Q_MAX_LB_CTRL [4].RESERVED[31:16]	16
0xBB002438	EGBW_CPU_Q_MAX_LB_CTRL [4].BURST[15:0]	16
0xBB00243C	EGBW_CPU_Q_MAX_LB_CTRL [5].RESERVED[31:21]	11
0xBB00243C	EGBW_CPU_Q_MAX_LB_CTRL [5].EN[20:20]	1
0xBB00243C	EGBW_CPU_Q_MAX_LB_CTRL [5].RATE[19:0]	20
0xBB002440	EGBW_CPU_Q_MAX_LB_CTRL [5].RESERVED[31:16]	16
0xBB002440	EGBW_CPU_Q_MAX_LB_CTRL [5].BURST[15:0]	16
0xBB002444	EGBW_CPU_Q_MAX_LB_CTRL [6].RESERVED[31:21]	11
0xBB002444	EGBW_CPU_Q_MAX_LB_CTRL [6].EN[20:20]	1
0xBB002444	EGBW_CPU_Q_MAX_LB_CTRL [6].RATE[19:0]	20
0xBB002448	EGBW_CPU_Q_MAX_LB_CTRL [6].RESERVED[31:16]	16
0xBB002448	EGBW_CPU_Q_MAX_LB_CTRL [6].BURST[15:0]	16
0xBB00244C	EGBW_CPU_Q_MAX_LB_CTRL [7].RESERVED[31:21]	11
0xBB00244C	EGBW_CPU_Q_MAX_LB_CTRL [7].EN[20:20]	1
0xBB00244C	EGBW_CPU_Q_MAX_LB_CTRL [7].RATE[19:0]	20
0xBB002450	EGBW_CPU_Q_MAX_LB_CTRL [7].RESERVED[31:16]	16
0xBB002450	EGBW_CPU_Q_MAX_LB_CTRL [7].BURST[15:0]	16
0xBB002454	EGBW_CPU_Q_MAX_LB_CTRL [8].RESERVED[31:21]	11
0xBB002454	EGBW_CPU_Q_MAX_LB_CTRL [8].EN[20:20]	1
0xBB002454	EGBW_CPU_Q_MAX_LB_CTRL [8].RATE[19:0]	20
0xBB002458	EGBW_CPU_Q_MAX_LB_CTRL [8].RESERVED[31:16]	16
0xBB002458	EGBW_CPU_Q_MAX_LB_CTRL [8].BURST[15:0]	16
0xBB00245C	EGBW_CPU_Q_MAX_LB_CTRL [9].RESERVED[31:21]	11
0xBB00245C	EGBW_CPU_Q_MAX_LB_CTRL [9].EN[20:20]	1
0xBB00245C	EGBW_CPU_Q_MAX_LB_CTRL [9].RATE[19:0]	20
0xBB002460	EGBW_CPU_Q_MAX_LB_CTRL [9].RESERVED[31:16]	16
0xBB002460	EGBW_CPU_Q_MAX_LB_CTRL [9].BURST[15:0]	16
0xBB002464	EGBW_CPU_Q_MAX_LB_CTRL [10].RESERVED[31:21]	11
0xBB002464	EGBW_CPU_Q_MAX_LB_CTRL [10].EN[20:20]	1
0xBB002464	EGBW_CPU_Q_MAX_LB_CTRL [10].RATE[19:0]	20
0xBB002468	EGBW_CPU_Q_MAX_LB_CTRL [10].RESERVED[31:16]	16
0xBB002468	EGBW_CPU_Q_MAX_LB_CTRL [10].BURST[15:0]	16
0xBB00246C	EGBW_CPU_Q_MAX_LB_CTRL [11].RESERVED[31:21]	11
0xBB00246C	EGBW_CPU_Q_MAX_LB_CTRL [11].EN[20:20]	1
0xBB00246C	EGBW_CPU_Q_MAX_LB_CTRL [11].RATE[19:0]	20
0xBB002470	EGBW_CPU_Q_MAX_LB_CTRL [11].RESERVED[31:16]	16
0xBB002470	EGBW_CPU_Q_MAX_LB_CTRL [11].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002474	EGBW_CPU_Q_MAX_LB_CTRL [12].RESERVED[31:21]	11
0xBB002474	EGBW_CPU_Q_MAX_LB_CTRL [12].EN[20:20]	1
0xBB002474	EGBW_CPU_Q_MAX_LB_CTRL [12].RATE[19:0]	20
0xBB002478	EGBW_CPU_Q_MAX_LB_CTRL [12].RESERVED[31:16]	16
0xBB002478	EGBW_CPU_Q_MAX_LB_CTRL [12].BURST[15:0]	16
0xBB00247C	EGBW_CPU_Q_MAX_LB_CTRL [13].RESERVED[31:21]	11
0xBB00247C	EGBW_CPU_Q_MAX_LB_CTRL [13].EN[20:20]	1
0xBB00247C	EGBW_CPU_Q_MAX_LB_CTRL [13].RATE[19:0]	20
0xBB002480	EGBW_CPU_Q_MAX_LB_CTRL [13].RESERVED[31:16]	16
0xBB002480	EGBW_CPU_Q_MAX_LB_CTRL [13].BURST[15:0]	16
0xBB002484	EGBW_CPU_Q_MAX_LB_CTRL [14].RESERVED[31:21]	11
0xBB002484	EGBW_CPU_Q_MAX_LB_CTRL [14].EN[20:20]	1
0xBB002484	EGBW_CPU_Q_MAX_LB_CTRL [14].RATE[19:0]	20
0xBB002488	EGBW_CPU_Q_MAX_LB_CTRL [14].RESERVED[31:16]	16
0xBB002488	EGBW_CPU_Q_MAX_LB_CTRL [14].BURST[15:0]	16
0xBB00248C	EGBW_CPU_Q_MAX_LB_CTRL [15].RESERVED[31:21]	11
0xBB00248C	EGBW_CPU_Q_MAX_LB_CTRL [15].EN[20:20]	1
0xBB00248C	EGBW_CPU_Q_MAX_LB_CTRL [15].RATE[19:0]	20
0xBB002490	EGBW_CPU_Q_MAX_LB_CTRL [15].RESERVED[31:16]	16
0xBB002490	EGBW_CPU_Q_MAX_LB_CTRL [15].BURST[15:0]	16
0xBB002494	EGBW_CPU_Q_MAX_LB_CTRL [16].RESERVED[31:21]	11
0xBB002494	EGBW_CPU_Q_MAX_LB_CTRL [16].EN[20:20]	1
0xBB002494	EGBW_CPU_Q_MAX_LB_CTRL [16].RATE[19:0]	20
0xBB002498	EGBW_CPU_Q_MAX_LB_CTRL [16].RESERVED[31:16]	16
0xBB002498	EGBW_CPU_Q_MAX_LB_CTRL [16].BURST[15:0]	16
0xBB00249C	EGBW_CPU_Q_MAX_LB_CTRL [17].RESERVED[31:21]	11
0xBB00249C	EGBW_CPU_Q_MAX_LB_CTRL [17].EN[20:20]	1
0xBB00249C	EGBW_CPU_Q_MAX_LB_CTRL [17].RATE[19:0]	20
0xBB0024A0	EGBW_CPU_Q_MAX_LB_CTRL [17].RESERVED[31:16]	16
0xBB0024A0	EGBW_CPU_Q_MAX_LB_CTRL [17].BURST[15:0]	16
0xBB0024A4	EGBW_CPU_Q_MAX_LB_CTRL [18].RESERVED[31:21]	11
0xBB0024A4	EGBW_CPU_Q_MAX_LB_CTRL [18].EN[20:20]	1
0xBB0024A4	EGBW_CPU_Q_MAX_LB_CTRL [18].RATE[19:0]	20
0xBB0024A8	EGBW_CPU_Q_MAX_LB_CTRL [18].RESERVED[31:16]	16
0xBB0024A8	EGBW_CPU_Q_MAX_LB_CTRL [18].BURST[15:0]	16
0xBB0024AC	EGBW_CPU_Q_MAX_LB_CTRL [19].RESERVED[31:21]	11
0xBB0024AC	EGBW_CPU_Q_MAX_LB_CTRL [19].EN[20:20]	1
0xBB0024AC	EGBW_CPU_Q_MAX_LB_CTRL [19].RATE[19:0]	20
0xBB0024B0	EGBW_CPU_Q_MAX_LB_CTRL [19].RESERVED[31:16]	16
0xBB0024B0	EGBW_CPU_Q_MAX_LB_CTRL [19].BURST[15:0]	16
0xBB0024B4	EGBW_CPU_Q_MAX_LB_CTRL [20].RESERVED[31:21]	11
0xBB0024B4	EGBW_CPU_Q_MAX_LB_CTRL [20].EN[20:20]	1
0xBB0024B4	EGBW_CPU_Q_MAX_LB_CTRL [20].RATE[19:0]	20
0xBB0024B8	EGBW_CPU_Q_MAX_LB_CTRL [20].RESERVED[31:16]	16
0xBB0024B8	EGBW_CPU_Q_MAX_LB_CTRL [20].BURST[15:0]	16
0xBB0024BC	EGBW_CPU_Q_MAX_LB_CTRL [21].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0024BC	EGBW_CPU_Q_MAX_LB_CTRL [21].EN[20:20]	1
0xBB0024BC	EGBW_CPU_Q_MAX_LB_CTRL [21].RATE[19:0]	20
0xBB0024C0	EGBW_CPU_Q_MAX_LB_CTRL [21].RESERVED[31:16]	16
0xBB0024C0	EGBW_CPU_Q_MAX_LB_CTRL [21].BURST[15:0]	16
0xBB0024C4	EGBW_CPU_Q_MAX_LB_CTRL [22].RESERVED[31:21]	11
0xBB0024C4	EGBW_CPU_Q_MAX_LB_CTRL [22].EN[20:20]	1
0xBB0024C4	EGBW_CPU_Q_MAX_LB_CTRL [22].RATE[19:0]	20
0xBB0024C8	EGBW_CPU_Q_MAX_LB_CTRL [22].RESERVED[31:16]	16
0xBB0024C8	EGBW_CPU_Q_MAX_LB_CTRL [22].BURST[15:0]	16
0xBB0024CC	EGBW_CPU_Q_MAX_LB_CTRL [23].RESERVED[31:21]	11
0xBB0024CC	EGBW_CPU_Q_MAX_LB_CTRL [23].EN[20:20]	1
0xBB0024CC	EGBW_CPU_Q_MAX_LB_CTRL [23].RATE[19:0]	20
0xBB0024D0	EGBW_CPU_Q_MAX_LB_CTRL [23].RESERVED[31:16]	16
0xBB0024D0	EGBW_CPU_Q_MAX_LB_CTRL [23].BURST[15:0]	16
0xBB0024D4	EGBW_CPU_Q_MAX_LB_CTRL [24].RESERVED[31:21]	11
0xBB0024D4	EGBW_CPU_Q_MAX_LB_CTRL [24].EN[20:20]	1
0xBB0024D4	EGBW_CPU_Q_MAX_LB_CTRL [24].RATE[19:0]	20
0xBB0024D8	EGBW_CPU_Q_MAX_LB_CTRL [24].RESERVED[31:16]	16
0xBB0024D8	EGBW_CPU_Q_MAX_LB_CTRL [24].BURST[15:0]	16
0xBB0024DC	EGBW_CPU_Q_MAX_LB_CTRL [25].RESERVED[31:21]	11
0xBB0024DC	EGBW_CPU_Q_MAX_LB_CTRL [25].EN[20:20]	1
0xBB0024DC	EGBW_CPU_Q_MAX_LB_CTRL [25].RATE[19:0]	20
0xBB0024E0	EGBW_CPU_Q_MAX_LB_CTRL [25].RESERVED[31:16]	16
0xBB0024E0	EGBW_CPU_Q_MAX_LB_CTRL [25].BURST[15:0]	16
0xBB0024E4	EGBW_CPU_Q_MAX_LB_CTRL [26].RESERVED[31:21]	11
0xBB0024E4	EGBW_CPU_Q_MAX_LB_CTRL [26].EN[20:20]	1
0xBB0024E4	EGBW_CPU_Q_MAX_LB_CTRL [26].RATE[19:0]	20
0xBB0024E8	EGBW_CPU_Q_MAX_LB_CTRL [26].RESERVED[31:16]	16
0xBB0024E8	EGBW_CPU_Q_MAX_LB_CTRL [26].BURST[15:0]	16
0xBB0024EC	EGBW_CPU_Q_MAX_LB_CTRL [27].RESERVED[31:21]	11
0xBB0024EC	EGBW_CPU_Q_MAX_LB_CTRL [27].EN[20:20]	1
0xBB0024EC	EGBW_CPU_Q_MAX_LB_CTRL [27].RATE[19:0]	20
0xBB0024F0	EGBW_CPU_Q_MAX_LB_CTRL [27].RESERVED[31:16]	16
0xBB0024F0	EGBW_CPU_Q_MAX_LB_CTRL [27].BURST[15:0]	16
0xBB0024F4	EGBW_CPU_Q_MAX_LB_CTRL [28].RESERVED[31:21]	11
0xBB0024F4	EGBW_CPU_Q_MAX_LB_CTRL [28].EN[20:20]	1
0xBB0024F4	EGBW_CPU_Q_MAX_LB_CTRL [28].RATE[19:0]	20
0xBB0024F8	EGBW_CPU_Q_MAX_LB_CTRL [28].RESERVED[31:16]	16
0xBB0024F8	EGBW_CPU_Q_MAX_LB_CTRL [28].BURST[15:0]	16
0xBB0024FC	EGBW_CPU_Q_MAX_LB_CTRL [29].RESERVED[31:21]	11
0xBB0024FC	EGBW_CPU_Q_MAX_LB_CTRL [29].EN[20:20]	1
0xBB0024FC	EGBW_CPU_Q_MAX_LB_CTRL [29].RATE[19:0]	20
0xBB002500	EGBW_CPU_Q_MAX_LB_CTRL [29].RESERVED[31:16]	16
0xBB002500	EGBW_CPU_Q_MAX_LB_CTRL [29].BURST[15:0]	16
0xBB002504	EGBW_CPU_Q_MAX_LB_CTRL [30].RESERVED[31:21]	11
0xBB002504	EGBW_CPU_Q_MAX_LB_CTRL [30].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002504	EGBW_CPU_Q_MAX_LB_CTRL [30].RATE[19:0]	20
0xBB002508	EGBW_CPU_Q_MAX_LB_CTRL [30].RESERVED[31:16]	16
0xBB002508	EGBW_CPU_Q_MAX_LB_CTRL [30].BURST[15:0]	16
0xBB00250C	EGBW_CPU_Q_MAX_LB_CTRL [31].RESERVED[31:21]	11
0xBB00250C	EGBW_CPU_Q_MAX_LB_CTRL [31].EN[20:20]	1
0xBB00250C	EGBW_CPU_Q_MAX_LB_CTRL [31].RATE[19:0]	20
0xBB002510	EGBW_CPU_Q_MAX_LB_CTRL [31].RESERVED[31:16]	16
0xBB002510	EGBW_CPU_Q_MAX_LB_CTRL [31].BURST[15:0]	16
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [0].RST[0:0]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [1].RST[1:1]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [2].RST[2:2]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [3].RST[3:3]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [4].RST[4:4]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [5].RST[5:5]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [6].RST[6:6]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [7].RST[7:7]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [8].RST[8:8]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [9].RST[9:9]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [10].RST[10:10]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [11].RST[11:11]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [12].RST[12:12]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [13].RST[13:13]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [14].RST[14:14]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [15].RST[15:15]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [16].RST[16:16]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [17].RST[17:17]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [18].RST[18:18]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [19].RST[19:19]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [20].RST[20:20]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [21].RST[21:21]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [22].RST[22:22]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [23].RST[23:23]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [24].RST[24:24]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [25].RST[25:25]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [26].RST[26:26]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [27].RST[27:27]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [28].RST[28:28]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [29].RST[29:29]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [30].RST[30:30]	1
0xBB002514	EGBW_CPU_Q_MAX_LB_RST [31].RST[31:31]	1
0xBB002158	EGBW_CTRL.RESERVED[31:2]	30
0xBB002158	EGBW_CTRL.INC_IFG[1:1]	1
0xBB002158	EGBW_CTRL.RATE_MODE_CPU[0:0]	1
0xBB004D8C	EGBW_ENCAP_CTRL.RESERVED[31:1]	31
0xBB004D8C	EGBW_ENCAP_CTRL.CPU_TAG_FEED_BACK[0:0]	1
0xBB002160	EGBW_LB_CTRL.TICK[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002160	EGBW_LB_CTRL.TKN[15:0]	16
0xBB002164	EGBW_PORT_CTRL [0].RESERVED[31:21]	11
0xBB002164	EGBW_PORT_CTRL [0].EN[20:20]	1
0xBB002164	EGBW_PORT_CTRL [0].RATE[19:0]	20
0xBB002168	EGBW_PORT_CTRL [0].RESERVED[31:16]	16
0xBB002168	EGBW_PORT_CTRL [0].BURST[15:0]	16
0xBB00216C	EGBW_PORT_CTRL [1].RESERVED[31:21]	11
0xBB00216C	EGBW_PORT_CTRL [1].EN[20:20]	1
0xBB00216C	EGBW_PORT_CTRL [1].RATE[19:0]	20
0xBB002170	EGBW_PORT_CTRL [1].RESERVED[31:16]	16
0xBB002170	EGBW_PORT_CTRL [1].BURST[15:0]	16
0xBB002174	EGBW_PORT_CTRL [2].RESERVED[31:21]	11
0xBB002174	EGBW_PORT_CTRL [2].EN[20:20]	1
0xBB002174	EGBW_PORT_CTRL [2].RATE[19:0]	20
0xBB002178	EGBW_PORT_CTRL [2] RESERVED[31:16]	16
0xBB002178	EGBW_PORT_CTRL [2].BURST[15:0]	16
0xBB00217C	EGBW_PORT_CTRL [3].RESERVED[31:21]	11
0xBB00217C	EGBW_PORT_CTRL [3].EN[20:20]	1
0xBB00217C	EGBW_PORT_CTRL [3].RATE[19:0]	20
0xBB002180	EGBW_PORT_CTRL [3].RESERVED[31:16]	16
0xBB002180	EGBW_PORT_CTRL [3].BURST[15:0]	16
0xBB002184	EGBW_PORT_CTRL [4].RESERVED[31:21]	11
0xBB002184	EGBW_PORT_CTRL [4].EN[20:20]	1
0xBB002184	EGBW_PORT_CTRL [4].RATE[19:0]	20
0xBB002188	EGBW_PORT_CTRL [4].RESERVED[31:16]	16
0xBB002188	EGBW_PORT_CTRL [4].BURST[15:0]	16
0xBB00218C	EGBW_PORT_CTRL [5].RESERVED[31:21]	11
0xBB00218C	EGBW_PORT_CTRL [5].EN[20:20]	1
0xBB00218C	EGBW_PORT_CTRL [5].RATE[19:0]	20
0xBB002190	EGBW_PORT_CTRL [5].RESERVED[31:16]	16
0xBB002190	EGBW_PORT_CTRL [5].BURST[15:0]	16
0xBB002194	EGBW_PORT_CTRL [6] RESERVED[31:21]	11
0xBB002194	EGBW_PORT_CTRL [6].EN[20:20]	1
0xBB002194	EGBW_PORT_CTRL [6].RATE[19:0]	20
0xBB002198	EGBW_PORT_CTRL [6].RESERVED[31:16]	16
0xBB002198	EGBW_PORT_CTRL [6].BURST[15:0]	16
0xBB00219C	EGBW_PORT_CTRL [7].RESERVED[31:21]	11
0xBB00219C	EGBW_PORT_CTRL [7].EN[20:20]	1
0xBB00219C	EGBW_PORT_CTRL [7].RATE[19:0]	20
0xBB0021A0	EGBW_PORT_CTRL [7].RESERVED[31:16]	16
0xBB0021A0	EGBW_PORT_CTRL [7].BURST[15:0]	16
0xBB0021A4	EGBW_PORT_CTRL [8].RESERVED[31:21]	11
0xBB0021A4	EGBW_PORT_CTRL [8].EN[20:20]	1
0xBB0021A4	EGBW_PORT_CTRL [8].RATE[19:0]	20
0xBB0021A8	EGBW_PORT_CTRL [8].RESERVED[31:16]	16
0xBB0021A8	EGBW_PORT_CTRL [8].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0021AC	EGBW_PORT_CTRL [9].RESERVED[31:21]	11
0xBB0021AC	EGBW_PORT_CTRL [9].EN[20:20]	1
0xBB0021AC	EGBW_PORT_CTRL [9].RATE[19:0]	20
0xBB0021B0	EGBW_PORT_CTRL [9].RESERVED[31:16]	16
0xBB0021B0	EGBW_PORT_CTRL [9].BURST[15:0]	16
0xBB0021B4	EGBW_PORT_CTRL [10].RESERVED[31:21]	11
0xBB0021B4	EGBW_PORT_CTRL [10].EN[20:20]	1
0xBB0021B4	EGBW_PORT_CTRL [10].RATE[19:0]	20
0xBB0021B8	EGBW_PORT_CTRL [10].RESERVED[31:16]	16
0xBB0021B8	EGBW_PORT_CTRL [10].BURST[15:0]	16
0xBB0021BC	EGBW_PORT_CTRL [11].RESERVED[31:21]	11
0xBB0021BC	EGBW_PORT_CTRL [11].EN[20:20]	1
0xBB0021BC	EGBW_PORT_CTRL [11].RATE[19:0]	20
0xBB0021C0	EGBW_PORT_CTRL [11].RESERVED[31:16]	16
0xBB0021C0	EGBW_PORT_CTRL [11].BURST[15:0]	16
0xBB0021C4	EGBW_PORT_CTRL [12].RESERVED[31:21]	11
0xBB0021C4	EGBW_PORT_CTRL [12].EN[20:20]	1
0xBB0021C4	EGBW_PORT_CTRL [12].RATE[19:0]	20
0xBB0021C8	EGBW_PORT_CTRL [12].RESERVED[31:16]	16
0xBB0021C8	EGBW_PORT_CTRL [12].BURST[15:0]	16
0xBB0021CC	EGBW_PORT_CTRL [13].RESERVED[31:21]	11
0xBB0021CC	EGBW_PORT_CTRL [13].EN[20:20]	1
0xBB0021CC	EGBW_PORT_CTRL [13].RATE[19:0]	20
0xBB0021D0	EGBW_PORT_CTRL [13].RESERVED[31:16]	16
0xBB0021D0	EGBW_PORT_CTRL [13].BURST[15:0]	16
0xBB0021D4	EGBW_PORT_CTRL [14].RESERVED[31:21]	11
0xBB0021D4	EGBW_PORT_CTRL [14].EN[20:20]	1
0xBB0021D4	EGBW_PORT_CTRL [14].RATE[19:0]	20
0xBB0021D8	EGBW_PORT_CTRL [14].RESERVED[31:16]	16
0xBB0021D8	EGBW_PORT_CTRL [14].BURST[15:0]	16
0xBB0021DC	EGBW_PORT_CTRL [15].RESERVED[31:21]	11
0xBB0021DC	EGBW_PORT_CTRL [15].EN[20:20]	1
0xBB0021DC	EGBW_PORT_CTRL [15].RATE[19:0]	20
0xBB0021E0	EGBW_PORT_CTRL [15].RESERVED[31:16]	16
0xBB0021E0	EGBW_PORT_CTRL [15].BURST[15:0]	16
0xBB0021E4	EGBW_PORT_CTRL [16].RESERVED[31:21]	11
0xBB0021E4	EGBW_PORT_CTRL [16].EN[20:20]	1
0xBB0021E4	EGBW_PORT_CTRL [16].RATE[19:0]	20
0xBB0021E8	EGBW_PORT_CTRL [16].RESERVED[31:16]	16
0xBB0021E8	EGBW_PORT_CTRL [16].BURST[15:0]	16
0xBB0021EC	EGBW_PORT_CTRL [17].RESERVED[31:21]	11
0xBB0021EC	EGBW_PORT_CTRL [17].EN[20:20]	1
0xBB0021EC	EGBW_PORT_CTRL [17].RATE[19:0]	20
0xBB0021F0	EGBW_PORT_CTRL [17].RESERVED[31:16]	16
0xBB0021F0	EGBW_PORT_CTRL [17].BURST[15:0]	16
0xBB0021F4	EGBW_PORT_CTRL [18].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0021F4	EGBW_PORT_CTRL [18].EN[20:20]	1
0xBB0021F4	EGBW_PORT_CTRL [18].RATE[19:0]	20
0xBB0021F8	EGBW_PORT_CTRL [18].RESERVED[31:16]	16
0xBB0021F8	EGBW_PORT_CTRL [18].BURST[15:0]	16
0xBB0021FC	EGBW_PORT_CTRL [19].RESERVED[31:21]	11
0xBB0021FC	EGBW_PORT_CTRL [19].EN[20:20]	1
0xBB0021FC	EGBW_PORT_CTRL [19].RATE[19:0]	20
0xBB002200	EGBW_PORT_CTRL [19].RESERVED[31:16]	16
0xBB002200	EGBW_PORT_CTRL [19].BURST[15:0]	16
0xBB002204	EGBW_PORT_CTRL [20].RESERVED[31:21]	11
0xBB002204	EGBW_PORT_CTRL [20].EN[20:20]	1
0xBB002204	EGBW_PORT_CTRL [20].RATE[19:0]	20
0xBB002208	EGBW_PORT_CTRL [20].RESERVED[31:16]	16
0xBB002208	EGBW_PORT_CTRL [20].BURST[15:0]	16
0xBB00220C	EGBW_PORT_CTRL [21].RESERVED[31:21]	11
0xBB00220C	EGBW_PORT_CTRL [21].EN[20:20]	1
0xBB00220C	EGBW_PORT_CTRL [21].RATE[19:0]	20
0xBB002210	EGBW_PORT_CTRL [21].RESERVED[31:16]	16
0xBB002210	EGBW_PORT_CTRL [21].BURST[15:0]	16
0xBB002214	EGBW_PORT_CTRL [22].RESERVED[31:21]	11
0xBB002214	EGBW_PORT_CTRL [22].EN[20:20]	1
0xBB002214	EGBW_PORT_CTRL [22].RATE[19:0]	20
0xBB002218	EGBW_PORT_CTRL [22].RESERVED[31:16]	16
0xBB002218	EGBW_PORT_CTRL [22].BURST[15:0]	16
0xBB00221C	EGBW_PORT_CTRL [23].RESERVED[31:21]	11
0xBB00221C	EGBW_PORT_CTRL [23].EN[20:20]	1
0xBB00221C	EGBW_PORT_CTRL [23].RATE[19:0]	20
0xBB002220	EGBW_PORT_CTRL [23].RESERVED[31:16]	16
0xBB002220	EGBW_PORT_CTRL [23].BURST[15:0]	16
0xBB002224	EGBW_PORT_CTRL [24].RESERVED[31:21]	11
0xBB002224	EGBW_PORT_CTRL [24].EN[20:20]	1
0xBB002224	EGBW_PORT_CTRL [24].RATE[19:0]	20
0xBB002228	EGBW_PORT_CTRL [24].RESERVED[31:16]	16
0xBB002228	EGBW_PORT_CTRL [24].BURST[15:0]	16
0xBB00222C	EGBW_PORT_CTRL [25].RESERVED[31:21]	11
0xBB00222C	EGBW_PORT_CTRL [25].EN[20:20]	1
0xBB00222C	EGBW_PORT_CTRL [25].RATE[19:0]	20
0xBB002230	EGBW_PORT_CTRL [25].RESERVED[31:16]	16
0xBB002230	EGBW_PORT_CTRL [25].BURST[15:0]	16
0xBB002234	EGBW_PORT_CTRL [26].RESERVED[31:21]	11
0xBB002234	EGBW_PORT_CTRL [26].EN[20:20]	1
0xBB002234	EGBW_PORT_CTRL [26].RATE[19:0]	20
0xBB002238	EGBW_PORT_CTRL [26].RESERVED[31:16]	16
0xBB002238	EGBW_PORT_CTRL [26].BURST[15:0]	16
0xBB00223C	EGBW_PORT_CTRL [27].RESERVED[31:21]	11
0xBB00223C	EGBW_PORT_CTRL [27].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00223C	EGBW_PORT_CTRL [27].RATE[19:0]	20
0xBB002240	EGBW_PORT_CTRL [27].RESERVED[31:16]	16
0xBB002240	EGBW_PORT_CTRL [27].BURST[15:0]	16
0xBB002244	EGBW_PORT_CTRL [28].RESERVED[31:21]	11
0xBB002244	EGBW_PORT_CTRL [28].EN[20:20]	1
0xBB002244	EGBW_PORT_CTRL [28].RATE[19:0]	20
0xBB002248	EGBW_PORT_CTRL [28].RESERVED[31:16]	16
0xBB002248	EGBW_PORT_CTRL [28].BURST[15:0]	16
0xBB00224C	EGBW_PORT_CTRL [29].RESERVED[31:21]	11
0xBB00224C	EGBW_PORT_CTRL [29].EN[20:20]	1
0xBB00224C	EGBW_PORT_CTRL [29].RATE[19:0]	20
0xBB002250	EGBW_PORT_CTRL [29].RESERVED[31:16]	16
0xBB002250	EGBW_PORT_CTRL [29].BURST[15:0]	16
0xBB002254	EGBW_PORT_CTRL [30].RESERVED[31:21]	11
0xBB002254	EGBW_PORT_CTRL [30].EN[20:20]	1
0xBB002254	EGBW_PORT_CTRL [30].RATE[19:0]	20
0xBB002258	EGBW_PORT_CTRL [30].RESERVED[31:16]	16
0xBB002258	EGBW_PORT_CTRL [30].BURST[15:0]	16
0xBB00225C	EGBW_PORT_CTRL [31].RESERVED[31:21]	11
0xBB00225C	EGBW_PORT_CTRL [31].EN[20:20]	1
0xBB00225C	EGBW_PORT_CTRL [31].RATE[19:0]	20
0xBB002260	EGBW_PORT_CTRL [31].RESERVED[31:16]	16
0xBB002260	EGBW_PORT_CTRL [31].BURST[15:0]	16
0xBB002264	EGBW_PORT_CTRL [32].RESERVED[31:21]	11
0xBB002264	EGBW_PORT_CTRL [32].EN[20:20]	1
0xBB002264	EGBW_PORT_CTRL [32].RATE[19:0]	20
0xBB002268	EGBW_PORT_CTRL [32].RESERVED[31:16]	16
0xBB002268	EGBW_PORT_CTRL [32].BURST[15:0]	16
0xBB00226C	EGBW_PORT_CTRL [33].RESERVED[31:21]	11
0xBB00226C	EGBW_PORT_CTRL [33].EN[20:20]	1
0xBB00226C	EGBW_PORT_CTRL [33].RATE[19:0]	20
0xBB002270	EGBW_PORT_CTRL [33].RESERVED[31:16]	16
0xBB002270	EGBW_PORT_CTRL [33].BURST[15:0]	16
0xBB002274	EGBW_PORT_CTRL [34].RESERVED[31:21]	11
0xBB002274	EGBW_PORT_CTRL [34].EN[20:20]	1
0xBB002274	EGBW_PORT_CTRL [34].RATE[19:0]	20
0xBB002278	EGBW_PORT_CTRL [34].RESERVED[31:16]	16
0xBB002278	EGBW_PORT_CTRL [34].BURST[15:0]	16
0xBB00227C	EGBW_PORT_CTRL [35].RESERVED[31:21]	11
0xBB00227C	EGBW_PORT_CTRL [35].EN[20:20]	1
0xBB00227C	EGBW_PORT_CTRL [35].RATE[19:0]	20
0xBB002280	EGBW_PORT_CTRL [35].RESERVED[31:16]	16
0xBB002280	EGBW_PORT_CTRL [35].BURST[15:0]	16
0xBB002284	EGBW_PORT_CTRL [36].RESERVED[31:21]	11
0xBB002284	EGBW_PORT_CTRL [36].EN[20:20]	1
0xBB002284	EGBW_PORT_CTRL [36].RATE[19:0]	20

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002288	EGBW_PORT_CTRL [36].RESERVED[31:16]	16
0xBB002288	EGBW_PORT_CTRL [36].BURST[15:0]	16
0xBB00228C	EGBW_PORT_CTRL [37].RESERVED[31:21]	11
0xBB00228C	EGBW_PORT_CTRL [37].EN[20:20]	1
0xBB00228C	EGBW_PORT_CTRL [37].RATE[19:0]	20
0xBB002290	EGBW_PORT_CTRL [37].RESERVED[31:16]	16
0xBB002290	EGBW_PORT_CTRL [37].BURST[15:0]	16
0xBB002294	EGBW_PORT_CTRL [38].RESERVED[31:21]	11
0xBB002294	EGBW_PORT_CTRL [38].EN[20:20]	1
0xBB002294	EGBW_PORT_CTRL [38].RATE[19:0]	20
0xBB002298	EGBW_PORT_CTRL [38].RESERVED[31:16]	16
0xBB002298	EGBW_PORT_CTRL [38].BURST[15:0]	16
0xBB00229C	EGBW_PORT_CTRL [39].RESERVED[31:21]	11
0xBB00229C	EGBW_PORT_CTRL [39].EN[20:20]	1
0xBB00229C	EGBW_PORT_CTRL [39].RATE[19:0]	20
0xBB0022A0	EGBW_PORT_CTRL [39].RESERVED[31:16]	16
0xBB0022A0	EGBW_PORT_CTRL [39].BURST[15:0]	16
0xBB0022A4	EGBW_PORT_CTRL [40].RESERVED[31:21]	11
0xBB0022A4	EGBW_PORT_CTRL [40].EN[20:20]	1
0xBB0022A4	EGBW_PORT_CTRL [40].RATE[19:0]	20
0xBB0022A8	EGBW_PORT_CTRL [40].RESERVED[31:16]	16
0xBB0022A8	EGBW_PORT_CTRL [40].BURST[15:0]	16
0xBB0022AC	EGBW_PORT_CTRL [41].RESERVED[31:21]	11
0xBB0022AC	EGBW_PORT_CTRL [41].EN[20:20]	1
0xBB0022AC	EGBW_PORT_CTRL [41].RATE[19:0]	20
0xBB0022B0	EGBW_PORT_CTRL [41].RESERVED[31:16]	16
0xBB0022B0	EGBW_PORT_CTRL [41].BURST[15:0]	16
0xBB0022B4	EGBW_PORT_CTRL [42].RESERVED[31:21]	11
0xBB0022B4	EGBW_PORT_CTRL [42].EN[20:20]	1
0xBB0022B4	EGBW_PORT_CTRL [42].RATE[19:0]	20
0xBB0022B8	EGBW_PORT_CTRL [42].RESERVED[31:16]	16
0xBB0022B8	EGBW_PORT_CTRL [42].BURST[15:0]	16
0xBB0022BC	EGBW_PORT_CTRL [43].RESERVED[31:21]	11
0xBB0022BC	EGBW_PORT_CTRL [43].EN[20:20]	1
0xBB0022BC	EGBW_PORT_CTRL [43].RATE[19:0]	20
0xBB0022C0	EGBW_PORT_CTRL [43].RESERVED[31:16]	16
0xBB0022C0	EGBW_PORT_CTRL [43].BURST[15:0]	16
0xBB0022C4	EGBW_PORT_CTRL [44].RESERVED[31:21]	11
0xBB0022C4	EGBW_PORT_CTRL [44].EN[20:20]	1
0xBB0022C4	EGBW_PORT_CTRL [44].RATE[19:0]	20
0xBB0022C8	EGBW_PORT_CTRL [44].RESERVED[31:16]	16
0xBB0022C8	EGBW_PORT_CTRL [44].BURST[15:0]	16
0xBB0022CC	EGBW_PORT_CTRL [45].RESERVED[31:21]	11
0xBB0022CC	EGBW_PORT_CTRL [45].EN[20:20]	1
0xBB0022CC	EGBW_PORT_CTRL [45].RATE[19:0]	20
0xBB0022D0	EGBW_PORT_CTRL [45].RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0022D0	EGBW_PORT_CTRL [45].BURST[15:0]	16
0xBB0022D4	EGBW_PORT_CTRL [46].RESERVED[31:21]	11
0xBB0022D4	EGBW_PORT_CTRL [46].EN[20:20]	1
0xBB0022D4	EGBW_PORT_CTRL [46].RATE[19:0]	20
0xBB0022D8	EGBW_PORT_CTRL [46].RESERVED[31:16]	16
0xBB0022D8	EGBW_PORT_CTRL [46].BURST[15:0]	16
0xBB0022DC	EGBW_PORT_CTRL [47].RESERVED[31:21]	11
0xBB0022DC	EGBW_PORT_CTRL [47].EN[20:20]	1
0xBB0022DC	EGBW_PORT_CTRL [47].RATE[19:0]	20
0xBB0022E0	EGBW_PORT_CTRL [47].RESERVED[31:16]	16
0xBB0022E0	EGBW_PORT_CTRL [47].BURST[15:0]	16
0xBB0022E4	EGBW_PORT_CTRL [48].RESERVED[31:21]	11
0xBB0022E4	EGBW_PORT_CTRL [48].EN[20:20]	1
0xBB0022E4	EGBW_PORT_CTRL [48].RATE[19:0]	20
0xBB0022E8	EGBW_PORT_CTRL [48].RESERVED[31:16]	16
0xBB0022E8	EGBW_PORT_CTRL [48].BURST[15:0]	16
0xBB0022EC	EGBW_PORT_CTRL [49].RESERVED[31:21]	11
0xBB0022EC	EGBW_PORT_CTRL [49].EN[20:20]	1
0xBB0022EC	EGBW_PORT_CTRL [49].RATE[19:0]	20
0xBB0022F0	EGBW_PORT_CTRL [49].RESERVED[31:16]	16
0xBB0022F0	EGBW_PORT_CTRL [49].BURST[15:0]	16
0xBB0022F4	EGBW_PORT_CTRL [50].RESERVED[31:21]	11
0xBB0022F4	EGBW_PORT_CTRL [50].EN[20:20]	1
0xBB0022F4	EGBW_PORT_CTRL [50].RATE[19:0]	20
0xBB0022F8	EGBW_PORT_CTRL [50].RESERVED[31:16]	16
0xBB0022F8	EGBW_PORT_CTRL [50].BURST[15:0]	16
0xBB0022FC	EGBW_PORT_CTRL [51].RESERVED[31:21]	11
0xBB0022FC	EGBW_PORT_CTRL [51].EN[20:20]	1
0xBB0022FC	EGBW_PORT_CTRL [51].RATE[19:0]	20
0xBB002300	EGBW_PORT_CTRL [51].RESERVED[31:16]	16
0xBB002300	EGBW_PORT_CTRL [51].BURST[15:0]	16
0xBB002304	EGBW_PORT_CTRL [52].RESERVED[31:21]	11
0xBB002304	EGBW_PORT_CTRL [52].EN[20:20]	1
0xBB002304	EGBW_PORT_CTRL [52].RATE[19:0]	20
0xBB002308	EGBW_PORT_CTRL [52].RESERVED[31:16]	16
0xBB002308	EGBW_PORT_CTRL [52].BURST[15:0]	16
0xBB00230C	EGBW_PORT_CTRL [53].RESERVED[31:21]	11
0xBB00230C	EGBW_PORT_CTRL [53].EN[20:20]	1
0xBB00230C	EGBW_PORT_CTRL [53].RATE[19:0]	20
0xBB002310	EGBW_PORT_CTRL [53].RESERVED[31:16]	16
0xBB002310	EGBW_PORT_CTRL [53].BURST[15:0]	16
0xBB002314	EGBW_PORT_CTRL [54].RESERVED[31:21]	11
0xBB002314	EGBW_PORT_CTRL [54].EN[20:20]	1
0xBB002314	EGBW_PORT_CTRL [54].RATE[19:0]	20
0xBB002318	EGBW_PORT_CTRL [54].RESERVED[31:16]	16
0xBB002318	EGBW_PORT_CTRL [54].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00231C	EGBW_PORT_CTRL [55].RESERVED[31:21]	11
0xBB00231C	EGBW_PORT_CTRL [55].EN[20:20]	1
0xBB00231C	EGBW_PORT_CTRL [55].RATE[19:0]	20
0xBB002320	EGBW_PORT_CTRL [55].RESERVED[31:16]	16
0xBB002320	EGBW_PORT_CTRL [55].BURST[15:0]	16
0xBB002324	EGBW_PORT_CTRL [56].RESERVED[31:21]	11
0xBB002324	EGBW_PORT_CTRL [56].EN[20:20]	1
0xBB002324	EGBW_PORT_CTRL [56].RATE[19:0]	20
0xBB002328	EGBW_PORT_CTRL [56].RESERVED[31:16]	16
0xBB002328	EGBW_PORT_CTRL [56].BURST[15:0]	16
0xBB00232C	EGBW_PORT_LB_RST [0].RST[0:0]	1
0xBB00232C	EGBW_PORT_LB_RST [1].RST[1:1]	1
0xBB00232C	EGBW_PORT_LB_RST [2].RST[2:2]	1
0xBB00232C	EGBW_PORT_LB_RST [3].RST[3:3]	1
0xBB00232C	EGBW_PORT_LB_RST [4].RST[4:4]	1
0xBB00232C	EGBW_PORT_LB_RST [5].RST[5:5]	1
0xBB00232C	EGBW_PORT_LB_RST [6].RST[6:6]	1
0xBB00232C	EGBW_PORT_LB_RST [7].RST[7:7]	1
0xBB00232C	EGBW_PORT_LB_RST [8].RST[8:8]	1
0xBB00232C	EGBW_PORT_LB_RST [9].RST[9:9]	1
0xBB00232C	EGBW_PORT_LB_RST [10].RST[10:10]	1
0xBB00232C	EGBW_PORT_LB_RST [11].RST[11:11]	1
0xBB00232C	EGBW_PORT_LB_RST [12].RST[12:12]	1
0xBB00232C	EGBW_PORT_LB_RST [13].RST[13:13]	1
0xBB00232C	EGBW_PORT_LB_RST [14].RST[14:14]	1
0xBB00232C	EGBW_PORT_LB_RST [15].RST[15:15]	1
0xBB00232C	EGBW_PORT_LB_RST [16].RST[16:16]	1
0xBB00232C	EGBW_PORT_LB_RST [17].RST[17:17]	1
0xBB00232C	EGBW_PORT_LB_RST [18].RST[18:18]	1
0xBB00232C	EGBW_PORT_LB_RST [19].RST[19:19]	1
0xBB00232C	EGBW_PORT_LB_RST [20].RST[20:20]	1
0xBB00232C	EGBW_PORT_LB_RST [21].RST[21:21]	1
0xBB00232C	EGBW_PORT_LB_RST [22].RST[22:22]	1
0xBB00232C	EGBW_PORT_LB_RST [23].RST[23:23]	1
0xBB00232C	EGBW_PORT_LB_RST [24].RST[24:24]	1
0xBB00232C	EGBW_PORT_LB_RST [25].RST[25:25]	1
0xBB00232C	EGBW_PORT_LB_RST [26].RST[26:26]	1
0xBB00232C	EGBW_PORT_LB_RST [27].RST[27:27]	1
0xBB00232C	EGBW_PORT_LB_RST [28].RST[28:28]	1
0xBB00232C	EGBW_PORT_LB_RST [29].RST[29:29]	1
0xBB00232C	EGBW_PORT_LB_RST [30].RST[30:30]	1
0xBB00232C	EGBW_PORT_LB_RST [31].RST[31:31]	1
0xBB002330	EGBW_PORT_LB_RST [32].RST[0:0]	1
0xBB002330	EGBW_PORT_LB_RST [33].RST[1:1]	1
0xBB002330	EGBW_PORT_LB_RST [34].RST[2:2]	1
0xBB002330	EGBW_PORT_LB_RST [35].RST[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002330	EGBW_PORT_LB_RST [36].RST[4:4]	1
0xBB002330	EGBW_PORT_LB_RST [37].RST[5:5]	1
0xBB002330	EGBW_PORT_LB_RST [38].RST[6:6]	1
0xBB002330	EGBW_PORT_LB_RST [39].RST[7:7]	1
0xBB002330	EGBW_PORT_LB_RST [40].RST[8:8]	1
0xBB002330	EGBW_PORT_LB_RST [41].RST[9:9]	1
0xBB002330	EGBW_PORT_LB_RST [42].RST[10:10]	1
0xBB002330	EGBW_PORT_LB_RST [43].RST[11:11]	1
0xBB002330	EGBW_PORT_LB_RST [44].RST[12:12]	1
0xBB002330	EGBW_PORT_LB_RST [45].RST[13:13]	1
0xBB002330	EGBW_PORT_LB_RST [46].RST[14:14]	1
0xBB002330	EGBW_PORT_LB_RST [47].RST[15:15]	1
0xBB002330	EGBW_PORT_LB_RST [48].RST[16:16]	1
0xBB002330	EGBW_PORT_LB_RST [49].RST[17:17]	1
0xBB002330	EGBW_PORT_LB_RST [50].RST[18:18]	1
0xBB002330	EGBW_PORT_LB_RST [51].RST[19:19]	1
0xBB002330	EGBW_PORT_LB_RST [52].RST[20:20]	1
0xBB002330	EGBW_PORT_LB_RST [53].RST[21:21]	1
0xBB002330	EGBW_PORT_LB_RST [54].RST[22:22]	1
0xBB002330	EGBW_PORT_LB_RST [55].RST[23:23]	1
0xBB002330	EGBW_PORT_LB_RST [56].RST[24:24]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][0].RST[0:0]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][1].RST[1:1]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][2].RST[2:2]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][3].RST[3:3]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][4].RST[4:4]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][5].RST[5:5]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][6].RST[6:6]	1
0xBB002518	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [0][7].RST[7:7]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][0].RST[0:0]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][1].RST[1:1]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][2].RST[2:2]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][3].RST[3:3]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][4].RST[4:4]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][5].RST[5:5]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][6].RST[6:6]	1
0xBB00251C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [1][7].RST[7:7]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][0].RST[0:0]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][1].RST[1:1]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][2].RST[2:2]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][3].RST[3:3]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][4].RST[4:4]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][5].RST[5:5]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][6].RST[6:6]	1
0xBB002520	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [2][7].RST[7:7]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][0].RST[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][1].RST[1:1]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][2].RST[2:2]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][3].RST[3:3]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][4].RST[4:4]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][5].RST[5:5]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][6].RST[6:6]	1
0xBB002524	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [3][7].RST[7:7]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][0].RST[0:0]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][1].RST[1:1]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][2].RST[2:2]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][3].RST[3:3]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][4].RST[4:4]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][5].RST[5:5]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][6].RST[6:6]	1
0xBB002528	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [4][7].RST[7:7]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][0].RST[0:0]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][1].RST[1:1]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][2].RST[2:2]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][3].RST[3:3]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][4].RST[4:4]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][5].RST[5:5]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][6].RST[6:6]	1
0xBB00252C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [5][7].RST[7:7]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][0].RST[0:0]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][1].RST[1:1]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][2].RST[2:2]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][3].RST[3:3]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][4].RST[4:4]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][5].RST[5:5]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][6].RST[6:6]	1
0xBB002530	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [6][7].RST[7:7]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][0].RST[0:0]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][1].RST[1:1]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][2].RST[2:2]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][3].RST[3:3]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][4].RST[4:4]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][5].RST[5:5]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][6].RST[6:6]	1
0xBB002534	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [7][7].RST[7:7]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][0].RST[0:0]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][1].RST[1:1]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][2].RST[2:2]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][3].RST[3:3]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][4].RST[4:4]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][5].RST[5:5]	1
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [8][6].RST[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002538	EGBW_PORT_Q_ASSURED_LB_RST_SET0[8][7].RST[7:7]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][0].RST[0:0]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][1].RST[1:1]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][2].RST[2:2]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][3].RST[3:3]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][4].RST[4:4]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][5].RST[5:5]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][6].RST[6:6]	1
0xBB00253C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[9][7].RST[7:7]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][0].RST[0:0]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][1].RST[1:1]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][2].RST[2:2]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][3].RST[3:3]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][4].RST[4:4]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][5].RST[5:5]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][6].RST[6:6]	1
0xBB002540	EGBW_PORT_Q_ASSURED_LB_RST_SET0[10][7].RST[7:7]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][0].RST[0:0]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][1].RST[1:1]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][2].RST[2:2]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][3].RST[3:3]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][4].RST[4:4]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][5].RST[5:5]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][6].RST[6:6]	1
0xBB002544	EGBW_PORT_Q_ASSURED_LB_RST_SET0[11][7].RST[7:7]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][0].RST[0:0]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][1].RST[1:1]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][2].RST[2:2]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][3].RST[3:3]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][4].RST[4:4]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][5].RST[5:5]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][6].RST[6:6]	1
0xBB002548	EGBW_PORT_Q_ASSURED_LB_RST_SET0[12][7].RST[7:7]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][0].RST[0:0]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][1].RST[1:1]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][2].RST[2:2]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][3].RST[3:3]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][4].RST[4:4]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][5].RST[5:5]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][6].RST[6:6]	1
0xBB00254C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[13][7].RST[7:7]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0[14][0].RST[0:0]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0[14][1].RST[1:1]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0[14][2].RST[2:2]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0[14][3].RST[3:3]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0[14][4].RST[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [14][5].RST[5:5]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [14][6].RST[6:6]	1
0xBB002550	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [14][7].RST[7:7]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][0].RST[0:0]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][1].RST[1:1]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][2].RST[2:2]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][3].RST[3:3]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][4].RST[4:4]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][5].RST[5:5]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][6].RST[6:6]	1
0xBB002554	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [15][7].RST[7:7]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][0].RST[0:0]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][1].RST[1:1]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][2].RST[2:2]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][3].RST[3:3]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][4].RST[4:4]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][5].RST[5:5]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][6].RST[6:6]	1
0xBB002558	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [16][7].RST[7:7]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][0].RST[0:0]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][1].RST[1:1]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][2].RST[2:2]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][3].RST[3:3]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][4].RST[4:4]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][5].RST[5:5]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][6].RST[6:6]	1
0xBB00255C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [17][7].RST[7:7]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][0].RST[0:0]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][1].RST[1:1]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][2].RST[2:2]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][3].RST[3:3]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][4].RST[4:4]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][5].RST[5:5]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][6].RST[6:6]	1
0xBB002560	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [18][7].RST[7:7]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][0].RST[0:0]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][1].RST[1:1]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][2].RST[2:2]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][3].RST[3:3]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][4].RST[4:4]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][5].RST[5:5]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][6].RST[6:6]	1
0xBB002564	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [19][7].RST[7:7]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][0].RST[0:0]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][1].RST[1:1]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][2].RST[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][3].RST[3:3]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][4].RST[4:4]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][5].RST[5:5]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][6].RST[6:6]	1
0xBB002568	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [20][7].RST[7:7]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][0].RST[0:0]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][1].RST[1:1]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][2].RST[2:2]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][3].RST[3:3]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][4].RST[4:4]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][5].RST[5:5]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][6].RST[6:6]	1
0xBB00256C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [21][7].RST[7:7]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][0].RST[0:0]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][1].RST[1:1]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][2].RST[2:2]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][3].RST[3:3]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][4].RST[4:4]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][5].RST[5:5]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][6].RST[6:6]	1
0xBB002570	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [22][7].RST[7:7]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][0].RST[0:0]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][1].RST[1:1]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][2].RST[2:2]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][3].RST[3:3]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][4].RST[4:4]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][5].RST[5:5]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][6].RST[6:6]	1
0xBB002574	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [23][7].RST[7:7]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][0].RST[0:0]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][1].RST[1:1]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][2].RST[2:2]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][3].RST[3:3]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][4].RST[4:4]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][5].RST[5:5]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][6].RST[6:6]	1
0xBB002578	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [24][7].RST[7:7]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][0].RST[0:0]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][1].RST[1:1]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][2].RST[2:2]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][3].RST[3:3]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][4].RST[4:4]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][5].RST[5:5]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][6].RST[6:6]	1
0xBB00257C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [25][7].RST[7:7]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][0].RST[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][1].RST[1:1]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][2].RST[2:2]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][3].RST[3:3]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][4].RST[4:4]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][5].RST[5:5]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][6].RST[6:6]	1
0xBB002580	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [26][7].RST[7:7]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][0].RST[0:0]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][1].RST[1:1]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][2].RST[2:2]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][3].RST[3:3]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][4].RST[4:4]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][5].RST[5:5]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][6].RST[6:6]	1
0xBB002584	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [27][7].RST[7:7]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][0].RST[0:0]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][1].RST[1:1]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][2].RST[2:2]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][3].RST[3:3]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][4].RST[4:4]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][5].RST[5:5]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][6].RST[6:6]	1
0xBB002588	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [28][7].RST[7:7]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][0].RST[0:0]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][1].RST[1:1]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][2].RST[2:2]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][3].RST[3:3]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][4].RST[4:4]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][5].RST[5:5]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][6].RST[6:6]	1
0xBB00258C	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [29][7].RST[7:7]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][0].RST[0:0]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][1].RST[1:1]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][2].RST[2:2]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][3].RST[3:3]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][4].RST[4:4]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][5].RST[5:5]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][6].RST[6:6]	1
0xBB002590	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [30][7].RST[7:7]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][0].RST[0:0]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][1].RST[1:1]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][2].RST[2:2]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][3].RST[3:3]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][4].RST[4:4]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][5].RST[5:5]	1
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [31][6].RST[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002594	EGBW_PORT_Q_ASSURED_LB_RST_SET0[31][7].RST[7:7]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][0].RST[0:0]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][1].RST[1:1]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][2].RST[2:2]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][3].RST[3:3]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][4].RST[4:4]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][5].RST[5:5]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][6].RST[6:6]	1
0xBB002598	EGBW_PORT_Q_ASSURED_LB_RST_SET0[32][7].RST[7:7]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][0].RST[0:0]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][1].RST[1:1]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][2].RST[2:2]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][3].RST[3:3]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][4].RST[4:4]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][5].RST[5:5]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][6].RST[6:6]	1
0xBB00259C	EGBW_PORT_Q_ASSURED_LB_RST_SET0[33][7].RST[7:7]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][0].RST[0:0]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][1].RST[1:1]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][2].RST[2:2]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][3].RST[3:3]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][4].RST[4:4]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][5].RST[5:5]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][6].RST[6:6]	1
0xBB0025A0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[34][7].RST[7:7]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][0].RST[0:0]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][1].RST[1:1]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][2].RST[2:2]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][3].RST[3:3]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][4].RST[4:4]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][5].RST[5:5]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][6].RST[6:6]	1
0xBB0025A4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[35][7].RST[7:7]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][0].RST[0:0]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][1].RST[1:1]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][2].RST[2:2]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][3].RST[3:3]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][4].RST[4:4]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][5].RST[5:5]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][6].RST[6:6]	1
0xBB0025A8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[36][7].RST[7:7]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[37][0].RST[0:0]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[37][1].RST[1:1]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[37][2].RST[2:2]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[37][3].RST[3:3]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[37][4].RST[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [37][5].RST[5:5]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [37][6].RST[6:6]	1
0xBB0025AC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [37][7].RST[7:7]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][0].RST[0:0]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][1].RST[1:1]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][2].RST[2:2]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][3].RST[3:3]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][4].RST[4:4]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][5].RST[5:5]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][6].RST[6:6]	1
0xBB0025B0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [38][7].RST[7:7]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][0].RST[0:0]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][1].RST[1:1]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][2].RST[2:2]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][3].RST[3:3]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][4].RST[4:4]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][5].RST[5:5]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][6].RST[6:6]	1
0xBB0025B4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [39][7].RST[7:7]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][0].RST[0:0]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][1].RST[1:1]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][2].RST[2:2]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][3].RST[3:3]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][4].RST[4:4]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][5].RST[5:5]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][6].RST[6:6]	1
0xBB0025B8	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [40][7].RST[7:7]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][0].RST[0:0]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][1].RST[1:1]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][2].RST[2:2]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][3].RST[3:3]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][4].RST[4:4]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][5].RST[5:5]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][6].RST[6:6]	1
0xBB0025BC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [41][7].RST[7:7]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][0].RST[0:0]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][1].RST[1:1]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][2].RST[2:2]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][3].RST[3:3]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][4].RST[4:4]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][5].RST[5:5]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][6].RST[6:6]	1
0xBB0025C0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [42][7].RST[7:7]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [43][0].RST[0:0]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [43][1].RST[1:1]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [43][2].RST[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[43][3].RST[3:3]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[43][4].RST[4:4]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[43][5].RST[5:5]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[43][6].RST[6:6]	1
0xBB0025C4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[43][7].RST[7:7]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][0].RST[0:0]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][1].RST[1:1]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][2].RST[2:2]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][3].RST[3:3]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][4].RST[4:4]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][5].RST[5:5]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][6].RST[6:6]	1
0xBB0025C8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[44][7].RST[7:7]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][0].RST[0:0]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][1].RST[1:1]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][2].RST[2:2]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][3].RST[3:3]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][4].RST[4:4]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][5].RST[5:5]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][6].RST[6:6]	1
0xBB0025CC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[45][7].RST[7:7]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][0].RST[0:0]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][1].RST[1:1]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][2].RST[2:2]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][3].RST[3:3]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][4].RST[4:4]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][5].RST[5:5]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][6].RST[6:6]	1
0xBB0025D0	EGBW_PORT_Q_ASSURED_LB_RST_SET0[46][7].RST[7:7]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][0].RST[0:0]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][1].RST[1:1]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][2].RST[2:2]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][3].RST[3:3]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][4].RST[4:4]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][5].RST[5:5]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][6].RST[6:6]	1
0xBB0025D4	EGBW_PORT_Q_ASSURED_LB_RST_SET0[47][7].RST[7:7]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][0].RST[0:0]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][1].RST[1:1]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][2].RST[2:2]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][3].RST[3:3]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][4].RST[4:4]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][5].RST[5:5]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][6].RST[6:6]	1
0xBB0025D8	EGBW_PORT_Q_ASSURED_LB_RST_SET0[48][7].RST[7:7]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0[49][0].RST[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][1].RST[1:1]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][2].RST[2:2]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][3].RST[3:3]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][4].RST[4:4]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][5].RST[5:5]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][6].RST[6:6]	1
0xBB0025DC	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [49][7].RST[7:7]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][0].RST[0:0]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][1].RST[1:1]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][2].RST[2:2]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][3].RST[3:3]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][4].RST[4:4]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][5].RST[5:5]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][6].RST[6:6]	1
0xBB0025E0	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [50][7].RST[7:7]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][0].RST[0:0]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][1].RST[1:1]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][2].RST[2:2]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][3].RST[3:3]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][4].RST[4:4]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][5].RST[5:5]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][6].RST[6:6]	1
0xBB0025E4	EGBW_PORT_Q_ASSURED_LB_RST_SET0 [51][7].RST[7:7]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][0].RST[0:0]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][1].RST[1:1]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][2].RST[2:2]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][3].RST[3:3]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][4].RST[4:4]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][5].RST[5:5]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][6].RST[6:6]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][7].RST[7:7]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][8].RST[8:8]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][9].RST[9:9]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][10].RST[10:10]	1
0xBB0025E8	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [52][11].RST[11:11]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][0].RST[0:0]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][1].RST[1:1]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][2].RST[2:2]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][3].RST[3:3]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][4].RST[4:4]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][5].RST[5:5]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][6].RST[6:6]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][7].RST[7:7]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][8].RST[8:8]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][9].RST[9:9]	1
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][10].RST[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0025EC	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [53][11].RST[11:11]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][0].RST[0:0]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][1].RST[1:1]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][2].RST[2:2]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][3].RST[3:3]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][4].RST[4:4]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][5].RST[5:5]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][6].RST[6:6]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][7].RST[7:7]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][8].RST[8:8]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][9].RST[9:9]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][10].RST[10:10]	1
0xBB0025F0	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [54][11].RST[11:11]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][0].RST[0:0]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][1].RST[1:1]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][2].RST[2:2]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][3].RST[3:3]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][4].RST[4:4]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][5].RST[5:5]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][6].RST[6:6]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][7].RST[7:7]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][8].RST[8:8]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][9].RST[9:9]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][10].RST[10:10]	1
0xBB0025F4	EGBW_PORT_Q_ASSURED_LB_RST_SET1 [55][11].RST[11:11]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][0].RST[0:0]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][1].RST[1:1]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][2].RST[2:2]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][3].RST[3:3]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][4].RST[4:4]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][5].RST[5:5]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][6].RST[6:6]	1
0xBB002334	EGBW_PORT_Q_MAX_LB_RST_SET0 [0][7].RST[7:7]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][0].RST[0:0]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][1].RST[1:1]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][2].RST[2:2]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][3].RST[3:3]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][4].RST[4:4]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][5].RST[5:5]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][6].RST[6:6]	1
0xBB002338	EGBW_PORT_Q_MAX_LB_RST_SET0 [1][7].RST[7:7]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][0].RST[0:0]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][1].RST[1:1]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][2].RST[2:2]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][3].RST[3:3]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][4].RST[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][5].RST[5:5]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][6].RST[6:6]	1
0xBB00233C	EGBW_PORT_Q_MAX_LB_RST_SET0 [2][7].RST[7:7]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][0].RST[0:0]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][1].RST[1:1]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][2].RST[2:2]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][3].RST[3:3]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][4].RST[4:4]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][5].RST[5:5]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][6].RST[6:6]	1
0xBB002340	EGBW_PORT_Q_MAX_LB_RST_SET0 [3][7].RST[7:7]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][0].RST[0:0]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][1].RST[1:1]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][2].RST[2:2]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][3].RST[3:3]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][4].RST[4:4]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][5].RST[5:5]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][6].RST[6:6]	1
0xBB002344	EGBW_PORT_Q_MAX_LB_RST_SET0 [4][7].RST[7:7]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][0].RST[0:0]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][1].RST[1:1]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][2].RST[2:2]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][3].RST[3:3]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][4].RST[4:4]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][5].RST[5:5]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][6].RST[6:6]	1
0xBB002348	EGBW_PORT_Q_MAX_LB_RST_SET0 [5][7].RST[7:7]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][0].RST[0:0]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][1].RST[1:1]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][2].RST[2:2]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][3].RST[3:3]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][4].RST[4:4]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][5].RST[5:5]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][6].RST[6:6]	1
0xBB00234C	EGBW_PORT_Q_MAX_LB_RST_SET0 [6][7].RST[7:7]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][0].RST[0:0]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][1].RST[1:1]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][2].RST[2:2]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][3].RST[3:3]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][4].RST[4:4]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][5].RST[5:5]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][6].RST[6:6]	1
0xBB002350	EGBW_PORT_Q_MAX_LB_RST_SET0 [7][7].RST[7:7]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][0].RST[0:0]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][1].RST[1:1]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][2].RST[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][3].RST[3:3]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][4].RST[4:4]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][5].RST[5:5]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][6].RST[6:6]	1
0xBB002354	EGBW_PORT_Q_MAX_LB_RST_SET0 [8][7].RST[7:7]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][0].RST[0:0]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][1].RST[1:1]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][2].RST[2:2]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][3].RST[3:3]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][4].RST[4:4]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][5].RST[5:5]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][6].RST[6:6]	1
0xBB002358	EGBW_PORT_Q_MAX_LB_RST_SET0 [9][7].RST[7:7]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][0].RST[0:0]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][1].RST[1:1]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][2].RST[2:2]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][3].RST[3:3]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][4].RST[4:4]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][5].RST[5:5]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][6].RST[6:6]	1
0xBB00235C	EGBW_PORT_Q_MAX_LB_RST_SET0 [10][7].RST[7:7]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][0].RST[0:0]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][1].RST[1:1]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][2].RST[2:2]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][3].RST[3:3]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][4].RST[4:4]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][5].RST[5:5]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][6].RST[6:6]	1
0xBB002360	EGBW_PORT_Q_MAX_LB_RST_SET0 [11][7].RST[7:7]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][0].RST[0:0]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][1].RST[1:1]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][2].RST[2:2]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][3].RST[3:3]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][4].RST[4:4]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][5].RST[5:5]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][6].RST[6:6]	1
0xBB002364	EGBW_PORT_Q_MAX_LB_RST_SET0 [12][7].RST[7:7]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][0].RST[0:0]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][1].RST[1:1]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][2].RST[2:2]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][3].RST[3:3]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][4].RST[4:4]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][5].RST[5:5]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][6].RST[6:6]	1
0xBB002368	EGBW_PORT_Q_MAX_LB_RST_SET0 [13][7].RST[7:7]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][0].RST[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][1].RST[1:1]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][2].RST[2:2]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][3].RST[3:3]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][4].RST[4:4]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][5].RST[5:5]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][6].RST[6:6]	1
0xBB00236C	EGBW_PORT_Q_MAX_LB_RST_SET0 [14][7].RST[7:7]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][0].RST[0:0]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][1].RST[1:1]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][2].RST[2:2]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][3].RST[3:3]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][4].RST[4:4]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][5].RST[5:5]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][6].RST[6:6]	1
0xBB002370	EGBW_PORT_Q_MAX_LB_RST_SET0 [15][7].RST[7:7]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][0].RST[0:0]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][1].RST[1:1]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][2].RST[2:2]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][3].RST[3:3]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][4].RST[4:4]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][5].RST[5:5]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][6].RST[6:6]	1
0xBB002374	EGBW_PORT_Q_MAX_LB_RST_SET0 [16][7].RST[7:7]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][0].RST[0:0]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][1].RST[1:1]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][2].RST[2:2]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][3].RST[3:3]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][4].RST[4:4]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][5].RST[5:5]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][6].RST[6:6]	1
0xBB002378	EGBW_PORT_Q_MAX_LB_RST_SET0 [17][7].RST[7:7]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][0].RST[0:0]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][1].RST[1:1]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][2].RST[2:2]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][3].RST[3:3]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][4].RST[4:4]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][5].RST[5:5]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][6].RST[6:6]	1
0xBB00237C	EGBW_PORT_Q_MAX_LB_RST_SET0 [18][7].RST[7:7]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][0].RST[0:0]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][1].RST[1:1]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][2].RST[2:2]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][3].RST[3:3]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][4].RST[4:4]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][5].RST[5:5]	1
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][6].RST[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002380	EGBW_PORT_Q_MAX_LB_RST_SET0 [19][7].RST[7:7]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][0].RST[0:0]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][1].RST[1:1]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][2].RST[2:2]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][3].RST[3:3]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][4].RST[4:4]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][5].RST[5:5]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][6].RST[6:6]	1
0xBB002384	EGBW_PORT_Q_MAX_LB_RST_SET0 [20][7].RST[7:7]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][0].RST[0:0]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][1].RST[1:1]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][2].RST[2:2]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][3].RST[3:3]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][4].RST[4:4]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][5].RST[5:5]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][6].RST[6:6]	1
0xBB002388	EGBW_PORT_Q_MAX_LB_RST_SET0 [21][7].RST[7:7]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][0].RST[0:0]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][1].RST[1:1]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][2].RST[2:2]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][3].RST[3:3]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][4].RST[4:4]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][5].RST[5:5]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][6].RST[6:6]	1
0xBB00238C	EGBW_PORT_Q_MAX_LB_RST_SET0 [22][7].RST[7:7]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][0].RST[0:0]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][1].RST[1:1]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][2].RST[2:2]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][3].RST[3:3]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][4].RST[4:4]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][5].RST[5:5]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][6].RST[6:6]	1
0xBB002390	EGBW_PORT_Q_MAX_LB_RST_SET0 [23][7].RST[7:7]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][0].RST[0:0]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][1].RST[1:1]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][2].RST[2:2]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][3].RST[3:3]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][4].RST[4:4]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][5].RST[5:5]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][6].RST[6:6]	1
0xBB002394	EGBW_PORT_Q_MAX_LB_RST_SET0 [24][7].RST[7:7]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][0].RST[0:0]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][1].RST[1:1]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][2].RST[2:2]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][3].RST[3:3]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][4].RST[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][5].RST[5:5]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][6].RST[6:6]	1
0xBB002398	EGBW_PORT_Q_MAX_LB_RST_SET0 [25][7].RST[7:7]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][0].RST[0:0]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][1].RST[1:1]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][2].RST[2:2]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][3].RST[3:3]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][4].RST[4:4]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][5].RST[5:5]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][6].RST[6:6]	1
0xBB00239C	EGBW_PORT_Q_MAX_LB_RST_SET0 [26][7].RST[7:7]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][0].RST[0:0]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][1].RST[1:1]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][2].RST[2:2]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][3].RST[3:3]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][4].RST[4:4]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][5].RST[5:5]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][6].RST[6:6]	1
0xBB0023A0	EGBW_PORT_Q_MAX_LB_RST_SET0 [27][7].RST[7:7]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][0].RST[0:0]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][1].RST[1:1]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][2].RST[2:2]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][3].RST[3:3]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][4].RST[4:4]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][5].RST[5:5]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][6].RST[6:6]	1
0xBB0023A4	EGBW_PORT_Q_MAX_LB_RST_SET0 [28][7].RST[7:7]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][0].RST[0:0]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][1].RST[1:1]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][2].RST[2:2]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][3].RST[3:3]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][4].RST[4:4]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][5].RST[5:5]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][6].RST[6:6]	1
0xBB0023A8	EGBW_PORT_Q_MAX_LB_RST_SET0 [29][7].RST[7:7]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][0].RST[0:0]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][1].RST[1:1]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][2].RST[2:2]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][3].RST[3:3]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][4].RST[4:4]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][5].RST[5:5]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][6].RST[6:6]	1
0xBB0023AC	EGBW_PORT_Q_MAX_LB_RST_SET0 [30][7].RST[7:7]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][0].RST[0:0]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][1].RST[1:1]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][2].RST[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][3].RST[3:3]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][4].RST[4:4]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][5].RST[5:5]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][6].RST[6:6]	1
0xBB0023B0	EGBW_PORT_Q_MAX_LB_RST_SET0 [31][7].RST[7:7]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][0].RST[0:0]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][1].RST[1:1]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][2].RST[2:2]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][3].RST[3:3]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][4].RST[4:4]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][5].RST[5:5]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][6].RST[6:6]	1
0xBB0023B4	EGBW_PORT_Q_MAX_LB_RST_SET0 [32][7].RST[7:7]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][0].RST[0:0]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][1].RST[1:1]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][2].RST[2:2]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][3].RST[3:3]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][4].RST[4:4]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][5].RST[5:5]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][6].RST[6:6]	1
0xBB0023B8	EGBW_PORT_Q_MAX_LB_RST_SET0 [33][7].RST[7:7]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][0].RST[0:0]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][1].RST[1:1]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][2].RST[2:2]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][3].RST[3:3]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][4].RST[4:4]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][5].RST[5:5]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][6].RST[6:6]	1
0xBB0023BC	EGBW_PORT_Q_MAX_LB_RST_SET0 [34][7].RST[7:7]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][0].RST[0:0]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][1].RST[1:1]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][2].RST[2:2]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][3].RST[3:3]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][4].RST[4:4]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][5].RST[5:5]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][6].RST[6:6]	1
0xBB0023C0	EGBW_PORT_Q_MAX_LB_RST_SET0 [35][7].RST[7:7]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][0].RST[0:0]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][1].RST[1:1]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][2].RST[2:2]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][3].RST[3:3]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][4].RST[4:4]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][5].RST[5:5]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][6].RST[6:6]	1
0xBB0023C4	EGBW_PORT_Q_MAX_LB_RST_SET0 [36][7].RST[7:7]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][0].RST[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][1].RST[1:1]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][2].RST[2:2]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][3].RST[3:3]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][4].RST[4:4]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][5].RST[5:5]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][6].RST[6:6]	1
0xBB0023C8	EGBW_PORT_Q_MAX_LB_RST_SET0 [37][7].RST[7:7]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][0].RST[0:0]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][1].RST[1:1]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][2].RST[2:2]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][3].RST[3:3]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][4].RST[4:4]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][5].RST[5:5]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][6].RST[6:6]	1
0xBB0023CC	EGBW_PORT_Q_MAX_LB_RST_SET0 [38][7].RST[7:7]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][0].RST[0:0]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][1].RST[1:1]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][2].RST[2:2]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][3].RST[3:3]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][4].RST[4:4]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][5].RST[5:5]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][6].RST[6:6]	1
0xBB0023D0	EGBW_PORT_Q_MAX_LB_RST_SET0 [39][7].RST[7:7]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][0].RST[0:0]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][1].RST[1:1]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][2].RST[2:2]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][3].RST[3:3]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][4].RST[4:4]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][5].RST[5:5]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][6].RST[6:6]	1
0xBB0023D4	EGBW_PORT_Q_MAX_LB_RST_SET0 [40][7].RST[7:7]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][0].RST[0:0]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][1].RST[1:1]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][2].RST[2:2]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][3].RST[3:3]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][4].RST[4:4]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][5].RST[5:5]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][6].RST[6:6]	1
0xBB0023D8	EGBW_PORT_Q_MAX_LB_RST_SET0 [41][7].RST[7:7]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][0].RST[0:0]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][1].RST[1:1]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][2].RST[2:2]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][3].RST[3:3]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][4].RST[4:4]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][5].RST[5:5]	1
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0 [42][6].RST[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0023DC	EGBW_PORT_Q_MAX_LB_RST_SET0[42][7].RST[7:7]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][0].RST[0:0]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][1].RST[1:1]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][2].RST[2:2]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][3].RST[3:3]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][4].RST[4:4]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][5].RST[5:5]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][6].RST[6:6]	1
0xBB0023E0	EGBW_PORT_Q_MAX_LB_RST_SET0[43][7].RST[7:7]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][0].RST[0:0]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][1].RST[1:1]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][2].RST[2:2]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][3].RST[3:3]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][4].RST[4:4]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][5].RST[5:5]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][6].RST[6:6]	1
0xBB0023E4	EGBW_PORT_Q_MAX_LB_RST_SET0[44][7].RST[7:7]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][0].RST[0:0]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][1].RST[1:1]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][2].RST[2:2]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][3].RST[3:3]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][4].RST[4:4]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][5].RST[5:5]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][6].RST[6:6]	1
0xBB0023E8	EGBW_PORT_Q_MAX_LB_RST_SET0[45][7].RST[7:7]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][0].RST[0:0]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][1].RST[1:1]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][2].RST[2:2]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][3].RST[3:3]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][4].RST[4:4]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][5].RST[5:5]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][6].RST[6:6]	1
0xBB0023EC	EGBW_PORT_Q_MAX_LB_RST_SET0[46][7].RST[7:7]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][0].RST[0:0]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][1].RST[1:1]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][2].RST[2:2]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][3].RST[3:3]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][4].RST[4:4]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][5].RST[5:5]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][6].RST[6:6]	1
0xBB0023F0	EGBW_PORT_Q_MAX_LB_RST_SET0[47][7].RST[7:7]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0[48][0].RST[0:0]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0[48][1].RST[1:1]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0[48][2].RST[2:2]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0[48][3].RST[3:3]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0[48][4].RST[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0 [48][5].RST[5:5]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0 [48][6].RST[6:6]	1
0xBB0023F4	EGBW_PORT_Q_MAX_LB_RST_SET0 [48][7].RST[7:7]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][0].RST[0:0]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][1].RST[1:1]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][2].RST[2:2]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][3].RST[3:3]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][4].RST[4:4]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][5].RST[5:5]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][6].RST[6:6]	1
0xBB0023F8	EGBW_PORT_Q_MAX_LB_RST_SET0 [49][7].RST[7:7]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][0].RST[0:0]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][1].RST[1:1]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][2].RST[2:2]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][3].RST[3:3]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][4].RST[4:4]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][5].RST[5:5]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][6].RST[6:6]	1
0xBB0023FC	EGBW_PORT_Q_MAX_LB_RST_SET0 [50][7].RST[7:7]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][0].RST[0:0]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][1].RST[1:1]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][2].RST[2:2]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][3].RST[3:3]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][4].RST[4:4]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][5].RST[5:5]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][6].RST[6:6]	1
0xBB002400	EGBW_PORT_Q_MAX_LB_RST_SET0 [51][7].RST[7:7]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][0].RST[0:0]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][1].RST[1:1]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][2].RST[2:2]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][3].RST[3:3]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][4].RST[4:4]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][5].RST[5:5]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][6].RST[6:6]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][7].RST[7:7]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][8].RST[8:8]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][9].RST[9:9]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][10].RST[10:10]	1
0xBB002404	EGBW_PORT_Q_MAX_LB_RST_SET1 [52][11].RST[11:11]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][0].RST[0:0]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][1].RST[1:1]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][2].RST[2:2]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][3].RST[3:3]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][4].RST[4:4]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][5].RST[5:5]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][6].RST[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][7].RST[7:7]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][8].RST[8:8]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][9].RST[9:9]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][10].RST[10:10]	1
0xBB002408	EGBW_PORT_Q_MAX_LB_RST_SET1 [53][11].RST[11:11]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][0].RST[0:0]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][1].RST[1:1]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][2].RST[2:2]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][3].RST[3:3]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][4].RST[4:4]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][5].RST[5:5]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][6].RST[6:6]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][7].RST[7:7]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][8].RST[8:8]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][9].RST[9:9]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][10].RST[10:10]	1
0xBB00240C	EGBW_PORT_Q_MAX_LB_RST_SET1 [54][11].RST[11:11]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][0].RST[0:0]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][1].RST[1:1]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][2].RST[2:2]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][3].RST[3:3]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][4].RST[4:4]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][5].RST[5:5]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][6].RST[6:6]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][7].RST[7:7]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][8].RST[8:8]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][9].RST[9:9]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][10].RST[10:10]	1
0xBB002410	EGBW_PORT_Q_MAX_LB_RST_SET1 [55][11].RST[11:11]	1
0xBB00142C	ETH_DM_CLK_CTRL.RESERVED[31:1]	31
0xBB00142C	ETH_DM_CLK_CTRL.CLK_EN[0:0]	1
0xBB00A45C	ETH_DM_PORT_EN [0].EN[0:0]	1
0xBB00A45C	ETH_DM_PORT_EN [1].EN[1:1]	1
0xBB00A45C	ETH_DM_PORT_EN [2].EN[2:2]	1
0xBB00A45C	ETH_DM_PORT_EN [3].EN[3:3]	1
0xBB00A45C	ETH_DM_PORT_EN [4].EN[4:4]	1
0xBB00A45C	ETH_DM_PORT_EN [5].EN[5:5]	1
0xBB00A45C	ETH_DM_PORT_EN [6].EN[6:6]	1
0xBB00A45C	ETH_DM_PORT_EN [7].EN[7:7]	1
0xBB00A45C	ETH_DM_PORT_EN [8].EN[8:8]	1
0xBB00A45C	ETH_DM_PORT_EN [9].EN[9:9]	1
0xBB00A45C	ETH_DM_PORT_EN [10].EN[10:10]	1
0xBB00A45C	ETH_DM_PORT_EN [11].EN[11:11]	1
0xBB00A45C	ETH_DM_PORT_EN [12].EN[12:12]	1
0xBB00A45C	ETH_DM_PORT_EN [13].EN[13:13]	1
0xBB00A45C	ETH_DM_PORT_EN [14].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A45C	ETH_DM_PORT_EN [15].EN[15:15]	1
0xBB00A45C	ETH_DM_PORT_EN [16].EN[16:16]	1
0xBB00A45C	ETH_DM_PORT_EN [17].EN[17:17]	1
0xBB00A45C	ETH_DM_PORT_EN [18].EN[18:18]	1
0xBB00A45C	ETH_DM_PORT_EN [19].EN[19:19]	1
0xBB00A45C	ETH_DM_PORT_EN [20].EN[20:20]	1
0xBB00A45C	ETH_DM_PORT_EN [21].EN[21:21]	1
0xBB00A45C	ETH_DM_PORT_EN [22].EN[22:22]	1
0xBB00A45C	ETH_DM_PORT_EN [23].EN[23:23]	1
0xBB00A45C	ETH_DM_PORT_EN [24].EN[24:24]	1
0xBB00A45C	ETH_DM_PORT_EN [25].EN[25:25]	1
0xBB00A45C	ETH_DM_PORT_EN [26].EN[26:26]	1
0xBB00A45C	ETH_DM_PORT_EN [27].EN[27:27]	1
0xBB00A45C	ETH_DM_PORT_EN [28].EN[28:28]	1
0xBB00A45C	ETH_DM_PORT_EN [29].EN[29:29]	1
0xBB00A45C	ETH_DM_PORT_EN [30].EN[30:30]	1
0xBB00A45C	ETH_DM_PORT_EN [31].EN[31:31]	1
0xBB00A460	ETH_DM_PORT_EN [32].EN[0:0]	1
0xBB00A460	ETH_DM_PORT_EN [33].EN[1:1]	1
0xBB00A460	ETH_DM_PORT_EN [34].EN[2:2]	1
0xBB00A460	ETH_DM_PORT_EN [35].EN[3:3]	1
0xBB00A460	ETH_DM_PORT_EN [36].EN[4:4]	1
0xBB00A460	ETH_DM_PORT_EN [37].EN[5:5]	1
0xBB00A460	ETH_DM_PORT_EN [38].EN[6:6]	1
0xBB00A460	ETH_DM_PORT_EN [39].EN[7:7]	1
0xBB00A460	ETH_DM_PORT_EN [40].EN[8:8]	1
0xBB00A460	ETH_DM_PORT_EN [41].EN[9:9]	1
0xBB00A460	ETH_DM_PORT_EN [42].EN[10:10]	1
0xBB00A460	ETH_DM_PORT_EN [43].EN[11:11]	1
0xBB00A460	ETH_DM_PORT_EN [44].EN[12:12]	1
0xBB00A460	ETH_DM_PORT_EN [45].EN[13:13]	1
0xBB00A460	ETH_DM_PORT_EN [46].EN[14:14]	1
0xBB00A460	ETH_DM_PORT_EN [47].EN[15:15]	1
0xBB00A460	ETH_DM_PORT_EN [48].EN[16:16]	1
0xBB00A460	ETH_DM_PORT_EN [49].EN[17:17]	1
0xBB00A460	ETH_DM_PORT_EN [50].EN[18:18]	1
0xBB00A460	ETH_DM_PORT_EN [51].EN[19:19]	1
0xBB00A460	ETH_DM_PORT_EN [52].EN[20:20]	1
0xBB00A460	ETH_DM_PORT_EN [53].EN[21:21]	1
0xBB00A460	ETH_DM_PORT_EN [54].EN[22:22]	1
0xBB00A460	ETH_DM_PORT_EN [55].EN[23:23]	1
0xBB00A464	ETH_DM_RX_CTRL.RESERVED[31:16]	16
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_7[15:14]	2
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_6[13:12]	2
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_5[11:10]	2
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_4[9:8]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_3[7:6]	2
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_2[5:4]	2
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_1[3:2]	2
0xBB00A464	ETH_DM_RX_CTRL.MD_LV_0[1:0]	2
0xBB00A468	ETH_DM_RX_TIME [0].RESERVED[31:30]	2
0xBB00A468	ETH_DM_RX_TIME [0].NSEC[29:0]	30
0xBB00A46C	ETH_DM_RX_TIME [0].SEC[31:0]	32
0xBB00A470	ETH_DM_RX_TIME [1].RESERVED[31:30]	2
0xBB00A470	ETH_DM_RX_TIME [1].NSEC[29:0]	30
0xBB00A474	ETH_DM_RX_TIME [1].SEC[31:0]	32
0xBB00A478	ETH_DM_RX_TIME [2].RESERVED[31:30]	2
0xBB00A478	ETH_DM_RX_TIME [2].NSEC[29:0]	30
0xBB00A47C	ETH_DM_RX_TIME [2].SEC[31:0]	32
0xBB00A480	ETH_DM_RX_TIME [3].RESERVED[31:30]	2
0xBB00A480	ETH_DM_RX_TIME [3].NSEC[29:0]	30
0xBB00A484	ETH_DM_RX_TIME [3].SEC[31:0]	32
0xBB00A488	ETH_DM_RX_TIME [4].RESERVED[31:30]	2
0xBB00A488	ETH_DM_RX_TIME [4].NSEC[29:0]	30
0xBB00A48C	ETH_DM_RX_TIME [4].SEC[31:0]	32
0xBB00A490	ETH_DM_RX_TIME [5].RESERVED[31:30]	2
0xBB00A490	ETH_DM_RX_TIME [5].NSEC[29:0]	30
0xBB00A494	ETH_DM_RX_TIME [5].SEC[31:0]	32
0xBB00A498	ETH_DM_RX_TIME [6].RESERVED[31:30]	2
0xBB00A498	ETH_DM_RX_TIME [6].NSEC[29:0]	30
0xBB00A49C	ETH_DM_RX_TIME [6].SEC[31:0]	32
0xBB00A4A0	ETH_DM_RX_TIME [7].RESERVED[31:30]	2
0xBB00A4A0	ETH_DM_RX_TIME [7].NSEC[29:0]	30
0xBB00A4A4	ETH_DM_RX_TIME [7].SEC[31:0]	32
0xBB00A4A8	ETH_DM_RX_TIME [8].RESERVED[31:30]	2
0xBB00A4A8	ETH_DM_RX_TIME [8].NSEC[29:0]	30
0xBB00A4AC	ETH_DM_RX_TIME [8].SEC[31:0]	32
0xBB00A4B0	ETH_DM_RX_TIME [9].RESERVED[31:30]	2
0xBB00A4B0	ETH_DM_RX_TIME [9].NSEC[29:0]	30
0xBB00A4B4	ETH_DM_RX_TIME [9].SEC[31:0]	32
0xBB00A4B8	ETH_DM_RX_TIME [10].RESERVED[31:30]	2
0xBB00A4B8	ETH_DM_RX_TIME [10].NSEC[29:0]	30
0xBB00A4BC	ETH_DM_RX_TIME [10].SEC[31:0]	32
0xBB00A4C0	ETH_DM_RX_TIME [11].RESERVED[31:30]	2
0xBB00A4C0	ETH_DM_RX_TIME [11].NSEC[29:0]	30
0xBB00A4C4	ETH_DM_RX_TIME [11].SEC[31:0]	32
0xBB00A4C8	ETH_DM_RX_TIME [12].RESERVED[31:30]	2
0xBB00A4C8	ETH_DM_RX_TIME [12].NSEC[29:0]	30
0xBB00A4CC	ETH_DM_RX_TIME [12].SEC[31:0]	32
0xBB00A4D0	ETH_DM_RX_TIME [13].RESERVED[31:30]	2
0xBB00A4D0	ETH_DM_RX_TIME [13].NSEC[29:0]	30
0xBB00A4D4	ETH_DM_RX_TIME [13].SEC[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A4D8	ETH_DM_RX_TIME [14].RESERVED[31:30]	2
0xBB00A4D8	ETH_DM_RX_TIME [14].NSEC[29:0]	30
0xBB00A4DC	ETH_DM_RX_TIME [14].SEC[31:0]	32
0xBB00A4E0	ETH_DM_RX_TIME [15].RESERVED[31:30]	2
0xBB00A4E0	ETH_DM_RX_TIME [15].NSEC[29:0]	30
0xBB00A4E4	ETH_DM_RX_TIME [15].SEC[31:0]	32
0xBB00A4E8	ETH_DM_RX_TIME [16].RESERVED[31:30]	2
0xBB00A4E8	ETH_DM_RX_TIME [16].NSEC[29:0]	30
0xBB00A4EC	ETH_DM_RX_TIME [16].SEC[31:0]	32
0xBB00A4F0	ETH_DM_RX_TIME [17].RESERVED[31:30]	2
0xBB00A4F0	ETH_DM_RX_TIME [17].NSEC[29:0]	30
0xBB00A4F4	ETH_DM_RX_TIME [17].SEC[31:0]	32
0xBB00A4F8	ETH_DM_RX_TIME [18].RESERVED[31:30]	2
0xBB00A4F8	ETH_DM_RX_TIME [18].NSEC[29:0]	30
0xBB00A4FC	ETH_DM_RX_TIME [18].SEC[31:0]	32
0xBB00A500	ETH_DM_RX_TIME [19].RESERVED[31:30]	2
0xBB00A500	ETH_DM_RX_TIME [19].NSEC[29:0]	30
0xBB00A504	ETH_DM_RX_TIME [19].SEC[31:0]	32
0xBB00A508	ETH_DM_RX_TIME [20].RESERVED[31:30]	2
0xBB00A508	ETH_DM_RX_TIME [20].NSEC[29:0]	30
0xBB00A50C	ETH_DM_RX_TIME [20].SEC[31:0]	32
0xBB00A510	ETH_DM_RX_TIME [21].RESERVED[31:30]	2
0xBB00A510	ETH_DM_RX_TIME [21].NSEC[29:0]	30
0xBB00A514	ETH_DM_RX_TIME [21].SEC[31:0]	32
0xBB00A518	ETH_DM_RX_TIME [22].RESERVED[31:30]	2
0xBB00A518	ETH_DM_RX_TIME [22].NSEC[29:0]	30
0xBB00A51C	ETH_DM_RX_TIME [22].SEC[31:0]	32
0xBB00A520	ETH_DM_RX_TIME [23].RESERVED[31:30]	2
0xBB00A520	ETH_DM_RX_TIME [23].NSEC[29:0]	30
0xBB00A524	ETH_DM_RX_TIME [23].SEC[31:0]	32
0xBB00A528	ETH_DM_RX_TIME [24].RESERVED[31:30]	2
0xBB00A528	ETH_DM_RX_TIME [24].NSEC[29:0]	30
0xBB00A52C	ETH_DM_RX_TIME [24].SEC[31:0]	32
0xBB00A530	ETH_DM_RX_TIME [25].RESERVED[31:30]	2
0xBB00A530	ETH_DM_RX_TIME [25].NSEC[29:0]	30
0xBB00A534	ETH_DM_RX_TIME [25].SEC[31:0]	32
0xBB00A538	ETH_DM_RX_TIME [26].RESERVED[31:30]	2
0xBB00A538	ETH_DM_RX_TIME [26].NSEC[29:0]	30
0xBB00A53C	ETH_DM_RX_TIME [26].SEC[31:0]	32
0xBB00A540	ETH_DM_RX_TIME [27].RESERVED[31:30]	2
0xBB00A540	ETH_DM_RX_TIME [27].NSEC[29:0]	30
0xBB00A544	ETH_DM_RX_TIME [27].SEC[31:0]	32
0xBB00A548	ETH_DM_RX_TIME [28].RESERVED[31:30]	2
0xBB00A548	ETH_DM_RX_TIME [28].NSEC[29:0]	30
0xBB00A54C	ETH_DM_RX_TIME [28].SEC[31:0]	32
0xBB00A550	ETH_DM_RX_TIME [29].RESERVED[31:30]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A550	ETH_DM_RX_TIME [29].NSEC[29:0]	30
0xBB00A554	ETH_DM_RX_TIME [29].SEC[31:0]	32
0xBB00A558	ETH_DM_RX_TIME [30].RESERVED[31:30]	2
0xBB00A558	ETH_DM_RX_TIME [30].NSEC[29:0]	30
0xBB00A55C	ETH_DM_RX_TIME [30].SEC[31:0]	32
0xBB00A560	ETH_DM_RX_TIME [31].RESERVED[31:30]	2
0xBB00A560	ETH_DM_RX_TIME [31].NSEC[29:0]	30
0xBB00A564	ETH_DM_RX_TIME [31].SEC[31:0]	32
0xBB00A568	ETH_DM_RX_TIME [32].RESERVED[31:30]	2
0xBB00A568	ETH_DM_RX_TIME [32].NSEC[29:0]	30
0xBB00A56C	ETH_DM_RX_TIME [32].SEC[31:0]	32
0xBB00A570	ETH_DM_RX_TIME [33].RESERVED[31:30]	2
0xBB00A570	ETH_DM_RX_TIME [33].NSEC[29:0]	30
0xBB00A574	ETH_DM_RX_TIME [33].SEC[31:0]	32
0xBB00A578	ETH_DM_RX_TIME [34].RESERVED[31:30]	2
0xBB00A578	ETH_DM_RX_TIME [34].NSEC[29:0]	30
0xBB00A57C	ETH_DM_RX_TIME [34].SEC[31:0]	32
0xBB00A580	ETH_DM_RX_TIME [35].RESERVED[31:30]	2
0xBB00A580	ETH_DM_RX_TIME [35].NSEC[29:0]	30
0xBB00A584	ETH_DM_RX_TIME [35].SEC[31:0]	32
0xBB00A588	ETH_DM_RX_TIME [36].RESERVED[31:30]	2
0xBB00A588	ETH_DM_RX_TIME [36].NSEC[29:0]	30
0xBB00A58C	ETH_DM_RX_TIME [36].SEC[31:0]	32
0xBB00A590	ETH_DM_RX_TIME [37].RESERVED[31:30]	2
0xBB00A590	ETH_DM_RX_TIME [37].NSEC[29:0]	30
0xBB00A594	ETH_DM_RX_TIME [37].SEC[31:0]	32
0xBB00A598	ETH_DM_RX_TIME [38].RESERVED[31:30]	2
0xBB00A598	ETH_DM_RX_TIME [38].NSEC[29:0]	30
0xBB00A59C	ETH_DM_RX_TIME [38].SEC[31:0]	32
0xBB00A5A0	ETH_DM_RX_TIME [39].RESERVED[31:30]	2
0xBB00A5A0	ETH_DM_RX_TIME [39].NSEC[29:0]	30
0xBB00A5A4	ETH_DM_RX_TIME [39].SEC[31:0]	32
0xBB00A5A8	ETH_DM_RX_TIME [40].RESERVED[31:30]	2
0xBB00A5A8	ETH_DM_RX_TIME [40].NSEC[29:0]	30
0xBB00A5AC	ETH_DM_RX_TIME [40].SEC[31:0]	32
0xBB00A5B0	ETH_DM_RX_TIME [41].RESERVED[31:30]	2
0xBB00A5B0	ETH_DM_RX_TIME [41].NSEC[29:0]	30
0xBB00A5B4	ETH_DM_RX_TIME [41].SEC[31:0]	32
0xBB00A5B8	ETH_DM_RX_TIME [42].RESERVED[31:30]	2
0xBB00A5B8	ETH_DM_RX_TIME [42].NSEC[29:0]	30
0xBB00A5BC	ETH_DM_RX_TIME [42].SEC[31:0]	32
0xBB00A5C0	ETH_DM_RX_TIME [43].RESERVED[31:30]	2
0xBB00A5C0	ETH_DM_RX_TIME [43].NSEC[29:0]	30
0xBB00A5C4	ETH_DM_RX_TIME [43].SEC[31:0]	32
0xBB00A5C8	ETH_DM_RX_TIME [44].RESERVED[31:30]	2
0xBB00A5C8	ETH_DM_RX_TIME [44].NSEC[29:0]	30

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A5CC	ETH_DM_RX_TIME [44].SEC[31:0]	32
0xBB00A5D0	ETH_DM_RX_TIME [45].RESERVED[31:30]	2
0xBB00A5D0	ETH_DM_RX_TIME [45].NSEC[29:0]	30
0xBB00A5D4	ETH_DM_RX_TIME [45].SEC[31:0]	32
0xBB00A5D8	ETH_DM_RX_TIME [46].RESERVED[31:30]	2
0xBB00A5D8	ETH_DM_RX_TIME [46].NSEC[29:0]	30
0xBB00A5DC	ETH_DM_RX_TIME [46].SEC[31:0]	32
0xBB00A5E0	ETH_DM_RX_TIME [47].RESERVED[31:30]	2
0xBB00A5E0	ETH_DM_RX_TIME [47].NSEC[29:0]	30
0xBB00A5E4	ETH_DM_RX_TIME [47].SEC[31:0]	32
0xBB00A5E8	ETH_DM_RX_TIME [48].RESERVED[31:30]	2
0xBB00A5E8	ETH_DM_RX_TIME [48].NSEC[29:0]	30
0xBB00A5EC	ETH_DM_RX_TIME [48].SEC[31:0]	32
0xBB00A5F0	ETH_DM_RX_TIME [49].RESERVED[31:30]	2
0xBB00A5F0	ETH_DM_RX_TIME [49].NSEC[29:0]	30
0xBB00A5F4	ETH_DM_RX_TIME [49].SEC[31:0]	32
0xBB00A5F8	ETH_DM_RX_TIME [50].RESERVED[31:30]	2
0xBB00A5F8	ETH_DM_RX_TIME [50].NSEC[29:0]	30
0xBB00A5FC	ETH_DM_RX_TIME [50].SEC[31:0]	32
0xBB00A600	ETH_DM_RX_TIME [51].RESERVED[31:30]	2
0xBB00A600	ETH_DM_RX_TIME [51].NSEC[29:0]	30
0xBB00A604	ETH_DM_RX_TIME [51].SEC[31:0]	32
0xBB00A608	ETH_DM_RX_TIME [52].RESERVED[31:30]	2
0xBB00A608	ETH_DM_RX_TIME [52].NSEC[29:0]	30
0xBB00A60C	ETH_DM_RX_TIME [52].SEC[31:0]	32
0xBB00A610	ETH_DM_RX_TIME [53].RESERVED[31:30]	2
0xBB00A610	ETH_DM_RX_TIME [53].NSEC[29:0]	30
0xBB00A614	ETH_DM_RX_TIME [53].SEC[31:0]	32
0xBB00A618	ETH_DM_RX_TIME [54].RESERVED[31:30]	2
0xBB00A618	ETH_DM_RX_TIME [54].NSEC[29:0]	30
0xBB00A61C	ETH_DM_RX_TIME [54].SEC[31:0]	32
0xBB00A620	ETH_DM_RX_TIME [55].RESERVED[31:30]	2
0xBB00A620	ETH_DM_RX_TIME [55].NSEC[29:0]	30
0xBB00A624	ETH_DM_RX_TIME [55].SEC[31:0]	32
0xBB00A628	ETH_DM_RX_TIME [56].RESERVED[31:30]	2
0xBB00A628	ETH_DM_RX_TIME [56].NSEC[29:0]	30
0xBB00A62C	ETH_DM_RX_TIME [56].SEC[31:0]	32
0xBB00A630	ETH_DM_RX_TIME [57].RESERVED[31:30]	2
0xBB00A630	ETH_DM_RX_TIME [57].NSEC[29:0]	30
0xBB00A634	ETH_DM_RX_TIME [57].SEC[31:0]	32
0xBB00A638	ETH_DM_RX_TIME [58].RESERVED[31:30]	2
0xBB00A638	ETH_DM_RX_TIME [58].NSEC[29:0]	30
0xBB00A63C	ETH_DM_RX_TIME [58].SEC[31:0]	32
0xBB00A640	ETH_DM_RX_TIME [59].RESERVED[31:30]	2
0xBB00A640	ETH_DM_RX_TIME [59].NSEC[29:0]	30
0xBB00A644	ETH_DM_RX_TIME [59].SEC[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A648	ETH_DM_RX_TIME [60].RESERVED[31:30]	2
0xBB00A648	ETH_DM_RX_TIME [60].NSEC[29:0]	30
0xBB00A64C	ETH_DM_RX_TIME [60].SEC[31:0]	32
0xBB00A650	ETH_DM_RX_TIME [61].RESERVED[31:30]	2
0xBB00A650	ETH_DM_RX_TIME [61].NSEC[29:0]	30
0xBB00A654	ETH_DM_RX_TIME [61].SEC[31:0]	32
0xBB00A658	ETH_DM_RX_TIME [62].RESERVED[31:30]	2
0xBB00A658	ETH_DM_RX_TIME [62].NSEC[29:0]	30
0xBB00A65C	ETH_DM_RX_TIME [62].SEC[31:0]	32
0xBB00A660	ETH_DM_RX_TIME [63].RESERVED[31:30]	2
0xBB00A660	ETH_DM_RX_TIME [63].NSEC[29:0]	30
0xBB00A664	ETH_DM_RX_TIME [63].SEC[31:0]	32
0xBB001434	ETH_DM_TIME_CTRL.RESERVED[31:2]	30
0xBB001434	ETH_DM_TIME_CTRL.EXEC[1:1]	1
0xBB001434	ETH_DM_TIME_CTRL.CMD[0:0]	1
0xBB001438	ETH_DM_TIME_CTRL_SEC.SEC[31:0]	32
0xBB00143C	ETH_DM_TIME_CTRL_SEC.RESERVED[31:30]	2
0xBB00143C	ETH_DM_TIME_CTRL_SEC.NSEC[29:0]	30
0xBB001430	ETH_DM_TIME_FREQ.RESERVED[31:28]	4
0xBB001430	ETH_DM_TIME_FREQ.TIME_FREQ[27:0]	28
0xBB004F60	ETH_DM_TX_DLY.SPD_10G[31:24]	8
0xBB004F60	ETH_DM_TX_DLY.SPD_1G[23:16]	8
0xBB004F60	ETH_DM_TX_DLY.SPD_100M[15:8]	8
0xBB004F60	ETH_DM_TX_DLY.SPD_10M[7:0]	8
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_31_DATA[31:31]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_30_DATA[30:30]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_29_DATA[29:29]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_28_DATA[28:28]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_27_DATA[27:27]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_26_DATA[26:26]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_25_DATA[25:25]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_24_DATA[24:24]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_23_DATA[23:23]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_22_DATA[22:22]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_21_DATA[21:21]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_20_DATA[20:20]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_19_DATA[19:19]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_18_DATA[18:18]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_17_DATA[17:17]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_16_DATA[16:16]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_15_DATA[15:15]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_14_DATA[14:14]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_13_DATA[13:13]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_12_DATA[12:12]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_11_DATA[11:11]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_10_DATA[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_9_DATA[9:9]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_8_DATA[8:8]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_7_DATA[7:7]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_6_DATA[6:6]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_5_DATA[5:5]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_4_DATA[4:4]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_3_DATA[3:3]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_2_DATA[2:2]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_1_DATA[1:1]	1
0xBB0007E8	EXT_GPIO_DATA_CTRL_1.EXT_GPIO_0_DATA[0:0]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_63_DATA[31:31]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_62_DATA[30:30]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_61_DATA[29:29]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_60_DATA[28:28]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_59_DATA[27:27]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_58_DATA[26:26]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_57_DATA[25:25]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_56_DATA[24:24]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_55_DATA[23:23]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_54_DATA[22:22]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_53_DATA[21:21]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_52_DATA[20:20]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_51_DATA[19:19]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_50_DATA[18:18]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_49_DATA[17:17]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_48_DATA[16:16]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_47_DATA[15:15]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_46_DATA[14:14]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_45_DATA[13:13]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_44_DATA[12:12]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_43_DATA[11:11]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_42_DATA[10:10]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_41_DATA[9:9]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_40_DATA[8:8]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_39_DATA[7:7]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_38_DATA[6:6]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_37_DATA[5:5]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_36_DATA[4:4]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_35_DATA[3:3]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_34_DATA[2:2]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_33_DATA[1:1]	1
0xBB0007EC	EXT_GPIO_DATA_CTRL_2.EXT_GPIO_32_DATA[0:0]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.RESERVED[31:10]	22
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_73_DATA[9:9]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_72_DATA[8:8]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_71_DATA[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_70_DATA[6:6]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_69_DATA[5:5]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_68_DATA[4:4]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_67_DATA[3:3]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_66_DATA[2:2]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_65_DATA[1:1]	1
0xBB0007F0	EXT_GPIO_DATA_CTRL_3.EXT_GPIO_64_DATA[0:0]	1
0xBB0007DC	EXT_GPIO_DIR_CTRL_1.EXT_GPIO_31_0_DIR[31:0]	32
0xBB0007E0	EXT_GPIO_DIR_CTRL_2.EXT_GPIO_63_32_DIR[31:0]	32
0xBB0007E4	EXT_GPIO_DIR_CTRL_3.RESERVED[31:10]	22
0xBB0007E4	EXT_GPIO_DIR_CTRL_3.EXT_GPIO_73_64_DIR[9:0]	10
0xBB0007D4	EXT_GPIO_GLB_CTRL.RESERVED[31:21]	11
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDX4_READY[20:15]	6
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDX5_READY[14:9]	6
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_EN[8:8]	1
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDIO_PREMBL[7:6]	2
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDC_PERIOD[5:4]	2
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDX4_ACCESS_MODE[3:3]	1
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDX5_ACCESS_MODE[2:2]	1
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDX4_EN[1:1]	1
0xBB0007D4	EXT_GPIO_GLB_CTRL.EXT_GPIO_MDX5_EN[0:0]	1
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.RESERVED[31:29]	3
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.GPIO_RCMD_FAIL[28:28]	1
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.GPIO_DATA[27:12]	16
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.GPIO_REG[11:7]	5
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.GPIO_PHYADDR[6:2]	5
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.GPIO_RWOP[1:1]	1
0xBB0007F4	EXT_GPIO_INDRT_ACCESS_CTRL.GPIO_CMD[0:0]	1
0xBB0012D4	EXT_GPIO_INTR_MODE [0].EXT_GPIO_INTR_MODE[1:0]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [1].EXT_GPIO_INTR_MODE[3:2]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [2].EXT_GPIO_INTR_MODE[5:4]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [3].EXT_GPIO_INTR_MODE[7:6]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [4].EXT_GPIO_INTR_MODE[9:8]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [5].EXT_GPIO_INTR_MODE[11:10]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [6].EXT_GPIO_INTR_MODE[13:12]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [7].EXT_GPIO_INTR_MODE[15:14]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [8].EXT_GPIO_INTR_MODE[17:16]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [9].EXT_GPIO_INTR_MODE[19:18]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [10].EXT_GPIO_INTR_MODE[21:20]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [11].EXT_GPIO_INTR_MODE[23:22]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [12].EXT_GPIO_INTR_MODE[25:24]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [13].EXT_GPIO_INTR_MODE[27:26]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [14].EXT_GPIO_INTR_MODE[29:28]	2
0xBB0012D4	EXT_GPIO_INTR_MODE [15].EXT_GPIO_INTR_MODE[31:30]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [16].EXT_GPIO_INTR_MODE[1:0]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [17].EXT_GPIO_INTR_MODE[3:2]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012D8	EXT_GPIO_INTR_MODE [18].EXT_GPIO_INTR_MODE[5:4]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [19].EXT_GPIO_INTR_MODE[7:6]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [20].EXT_GPIO_INTR_MODE[9:8]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [21].EXT_GPIO_INTR_MODE[11:10]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [22].EXT_GPIO_INTR_MODE[13:12]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [23].EXT_GPIO_INTR_MODE[15:14]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [24].EXT_GPIO_INTR_MODE[17:16]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [25].EXT_GPIO_INTR_MODE[19:18]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [26].EXT_GPIO_INTR_MODE[21:20]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [27].EXT_GPIO_INTR_MODE[23:22]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [28].EXT_GPIO_INTR_MODE[25:24]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [29].EXT_GPIO_INTR_MODE[27:26]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [30].EXT_GPIO_INTR_MODE[29:28]	2
0xBB0012D8	EXT_GPIO_INTR_MODE [31].EXT_GPIO_INTR_MODE[31:30]	2
0xBB0012DC	EXT_GPIO_INTR_MODE [32].EXT_GPIO_INTR_MODE[1:0]	2
0xBB0012DC	EXT_GPIO_INTR_MODE [33].EXT_GPIO_INTR_MODE[3:2]	2
0xBB0012DC	EXT_GPIO_INTR_MODE [34].EXT_GPIO_INTR_MODE[5:4]	2
0xBB0012DC	EXT_GPIO_INTR_MODE [35].EXT_GPIO_INTR_MODE[7:6]	2
0xBB0012DC	EXT_GPIO_INTR_MODE [36].EXT_GPIO_INTR_MODE[9:8]	2
0xBB0007D8	EXT_GPIO_TRIG.RESERVED[31:2]	30
0xBB0007D8	EXT_GPIO_TRIG.EXT_GPIO_MDX4_TRIG[1:1]	1
0xBB0007D8	EXT_GPIO_TRIG.EXT_GPIO_MDX5_TRIG[0:0]	1
0xBB008184	FC_ALE_NON_REPCT_Q_PAGE_CNT.RESERVED[31:29]	3
0xBB008184	FC_ALE_NON_REPCT_Q_PAGE_CNT.MAX_CNT[28:16]	13
0xBB008184	FC_ALE_NON_REPCT_Q_PAGE_CNT.RESERVED[15:13]	3
0xBB008184	FC_ALE_NON_REPCT_Q_PAGE_CNT.CNT[12:0]	13
0xBB008188	FC_ALE_REPCT_Q_PAGE_CNT.RESERVED[31:29]	3
0xBB008188	FC_ALE_REPCT_Q_PAGE_CNT.MAX_CNT[28:16]	13
0xBB008188	FC_ALE_REPCT_Q_PAGE_CNT.RESERVED[15:13]	3
0xBB008188	FC_ALE_REPCT_Q_PAGE_CNT.CNT[12:0]	13
0xBB00273C	FC_CPU_EGR_PAGE_CNT.RESERVED[31:30]	2
0xBB00273C	FC_CPU_EGR_PAGE_CNT.MAX_CNT[29:16]	14
0xBB00273C	FC_CPU_EGR_PAGE_CNT.RESERVED[15:14]	2
0xBB00273C	FC_CPU_EGR_PAGE_CNT.CNT[13:0]	14
0xBB0026A8	FC_CPU_Q_EGR_DROP_THR [0].RESERVED[31:29]	3
0xBB0026A8	FC_CPU_Q_EGR_DROP_THR [0].ON[28:16]	13
0xBB0026A8	FC_CPU_Q_EGR_DROP_THR [0].RESERVED[15:13]	3
0xBB0026A8	FC_CPU_Q_EGR_DROP_THR [0].OFF[12:0]	13
0xBB0026AC	FC_CPU_Q_EGR_DROP_THR [1].RESERVED[31:29]	3
0xBB0026AC	FC_CPU_Q_EGR_DROP_THR [1].ON[28:16]	13
0xBB0026AC	FC_CPU_Q_EGR_DROP_THR [1].RESERVED[15:13]	3
0xBB0026AC	FC_CPU_Q_EGR_DROP_THR [1].OFF[12:0]	13
0xBB0026B0	FC_CPU_Q_EGR_DROP_THR [2].RESERVED[31:29]	3
0xBB0026B0	FC_CPU_Q_EGR_DROP_THR [2].ON[28:16]	13
0xBB0026B0	FC_CPU_Q_EGR_DROP_THR [2].RESERVED[15:13]	3
0xBB0026B0	FC_CPU_Q_EGR_DROP_THR [2].OFF[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0026B4	FC_CPU_Q_EGR_DROP_THR [3].RESERVED[31:29]	3
0xBB0026B4	FC_CPU_Q_EGR_DROP_THR [3].ON[28:16]	13
0xBB0026B4	FC_CPU_Q_EGR_DROP_THR [3].RESERVED[15:13]	3
0xBB0026B4	FC_CPU_Q_EGR_DROP_THR [3].OFF[12:0]	13
0xBB0026B8	FC_CPU_Q_EGR_DROP_THR [4].RESERVED[31:29]	3
0xBB0026B8	FC_CPU_Q_EGR_DROP_THR [4].ON[28:16]	13
0xBB0026B8	FC_CPU_Q_EGR_DROP_THR [4].RESERVED[15:13]	3
0xBB0026B8	FC_CPU_Q_EGR_DROP_THR [4].OFF[12:0]	13
0xBB0026BC	FC_CPU_Q_EGR_DROP_THR [5].RESERVED[31:29]	3
0xBB0026BC	FC_CPU_Q_EGR_DROP_THR [5].ON[28:16]	13
0xBB0026BC	FC_CPU_Q_EGR_DROP_THR [5].RESERVED[15:13]	3
0xBB0026BC	FC_CPU_Q_EGR_DROP_THR [5].OFF[12:0]	13
0xBB0026C0	FC_CPU_Q_EGR_DROP_THR [6].RESERVED[31:29]	3
0xBB0026C0	FC_CPU_Q_EGR_DROP_THR [6].ON[28:16]	13
0xBB0026C0	FC_CPU_Q_EGR_DROP_THR [6].RESERVED[15:13]	3
0xBB0026C0	FC_CPU_Q_EGR_DROP_THR [6].OFF[12:0]	13
0xBB0026C4	FC_CPU_Q_EGR_DROP_THR [7].RESERVED[31:29]	3
0xBB0026C4	FC_CPU_Q_EGR_DROP_THR [7].ON[28:16]	13
0xBB0026C4	FC_CPU_Q_EGR_DROP_THR [7].RESERVED[15:13]	3
0xBB0026C4	FC_CPU_Q_EGR_DROP_THR [7].OFF[12:0]	13
0xBB0026C8	FC_CPU_Q_EGR_DROP_THR [8].RESERVED[31:29]	3
0xBB0026C8	FC_CPU_Q_EGR_DROP_THR [8].ON[28:16]	13
0xBB0026C8	FC_CPU_Q_EGR_DROP_THR [8].RESERVED[15:13]	3
0xBB0026C8	FC_CPU_Q_EGR_DROP_THR [8].OFF[12:0]	13
0xBB0026CC	FC_CPU_Q_EGR_DROP_THR [9].RESERVED[31:29]	3
0xBB0026CC	FC_CPU_Q_EGR_DROP_THR [9].ON[28:16]	13
0xBB0026CC	FC_CPU_Q_EGR_DROP_THR [9].RESERVED[15:13]	3
0xBB0026CC	FC_CPU_Q_EGR_DROP_THR [9].OFF[12:0]	13
0xBB0026D0	FC_CPU_Q_EGR_DROP_THR [10].RESERVED[31:29]	3
0xBB0026D0	FC_CPU_Q_EGR_DROP_THR [10].ON[28:16]	13
0xBB0026D0	FC_CPU_Q_EGR_DROP_THR [10].RESERVED[15:13]	3
0xBB0026D0	FC_CPU_Q_EGR_DROP_THR [10].OFF[12:0]	13
0xBB0026D4	FC_CPU_Q_EGR_DROP_THR [11].RESERVED[31:29]	3
0xBB0026D4	FC_CPU_Q_EGR_DROP_THR [11].ON[28:16]	13
0xBB0026D4	FC_CPU_Q_EGR_DROP_THR [11].RESERVED[15:13]	3
0xBB0026D4	FC_CPU_Q_EGR_DROP_THR [11].OFF[12:0]	13
0xBB0026D8	FC_CPU_Q_EGR_DROP_THR [12].RESERVED[31:29]	3
0xBB0026D8	FC_CPU_Q_EGR_DROP_THR [12].ON[28:16]	13
0xBB0026D8	FC_CPU_Q_EGR_DROP_THR [12].RESERVED[15:13]	3
0xBB0026D8	FC_CPU_Q_EGR_DROP_THR [12].OFF[12:0]	13
0xBB0026DC	FC_CPU_Q_EGR_DROP_THR [13].RESERVED[31:29]	3
0xBB0026DC	FC_CPU_Q_EGR_DROP_THR [13].ON[28:16]	13
0xBB0026DC	FC_CPU_Q_EGR_DROP_THR [13].RESERVED[15:13]	3
0xBB0026DC	FC_CPU_Q_EGR_DROP_THR [13].OFF[12:0]	13
0xBB0026E0	FC_CPU_Q_EGR_DROP_THR [14].RESERVED[31:29]	3
0xBB0026E0	FC_CPU_Q_EGR_DROP_THR [14].ON[28:16]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0026E0	FC_CPU_Q_EGR_DROP_THR [14].RESERVED[15:13]	3
0xBB0026E0	FC_CPU_Q_EGR_DROP_THR [14].OFF[12:0]	13
0xBB0026E4	FC_CPU_Q_EGR_DROP_THR [15].RESERVED[31:29]	3
0xBB0026E4	FC_CPU_Q_EGR_DROP_THR [15].ON[28:16]	13
0xBB0026E4	FC_CPU_Q_EGR_DROP_THR [15].RESERVED[15:13]	3
0xBB0026E4	FC_CPU_Q_EGR_DROP_THR [15].OFF[12:0]	13
0xBB0026E8	FC_CPU_Q_EGR_DROP_THR [16].RESERVED[31:29]	3
0xBB0026E8	FC_CPU_Q_EGR_DROP_THR [16].ON[28:16]	13
0xBB0026E8	FC_CPU_Q_EGR_DROP_THR [16].RESERVED[15:13]	3
0xBB0026E8	FC_CPU_Q_EGR_DROP_THR [16].OFF[12:0]	13
0xBB0026EC	FC_CPU_Q_EGR_DROP_THR [17].RESERVED[31:29]	3
0xBB0026EC	FC_CPU_Q_EGR_DROP_THR [17].ON[28:16]	13
0xBB0026EC	FC_CPU_Q_EGR_DROP_THR [17].RESERVED[15:13]	3
0xBB0026EC	FC_CPU_Q_EGR_DROP_THR [17].OFF[12:0]	13
0xBB0026F0	FC_CPU_Q_EGR_DROP_THR [18].RESERVED[31:29]	3
0xBB0026F0	FC_CPU_Q_EGR_DROP_THR [18].ON[28:16]	13
0xBB0026F0	FC_CPU_Q_EGR_DROP_THR [18].RESERVED[15:13]	3
0xBB0026F0	FC_CPU_Q_EGR_DROP_THR [18].OFF[12:0]	13
0xBB0026F4	FC_CPU_Q_EGR_DROP_THR [19].RESERVED[31:29]	3
0xBB0026F4	FC_CPU_Q_EGR_DROP_THR [19].ON[28:16]	13
0xBB0026F4	FC_CPU_Q_EGR_DROP_THR [19].RESERVED[15:13]	3
0xBB0026F4	FC_CPU_Q_EGR_DROP_THR [19].OFF[12:0]	13
0xBB0026F8	FC_CPU_Q_EGR_DROP_THR [20].RESERVED[31:29]	3
0xBB0026F8	FC_CPU_Q_EGR_DROP_THR [20].ON[28:16]	13
0xBB0026F8	FC_CPU_Q_EGR_DROP_THR [20].RESERVED[15:13]	3
0xBB0026F8	FC_CPU_Q_EGR_DROP_THR [20].OFF[12:0]	13
0xBB0026FC	FC_CPU_Q_EGR_DROP_THR [21].RESERVED[31:29]	3
0xBB0026FC	FC_CPU_Q_EGR_DROP_THR [21].ON[28:16]	13
0xBB0026FC	FC_CPU_Q_EGR_DROP_THR [21].RESERVED[15:13]	3
0xBB0026FC	FC_CPU_Q_EGR_DROP_THR [21].OFF[12:0]	13
0xBB002700	FC_CPU_Q_EGR_DROP_THR [22].RESERVED[31:29]	3
0xBB002700	FC_CPU_Q_EGR_DROP_THR [22].ON[28:16]	13
0xBB002700	FC_CPU_Q_EGR_DROP_THR [22].RESERVED[15:13]	3
0xBB002700	FC_CPU_Q_EGR_DROP_THR [22].OFF[12:0]	13
0xBB002704	FC_CPU_Q_EGR_DROP_THR [23].RESERVED[31:29]	3
0xBB002704	FC_CPU_Q_EGR_DROP_THR [23].ON[28:16]	13
0xBB002704	FC_CPU_Q_EGR_DROP_THR [23].RESERVED[15:13]	3
0xBB002704	FC_CPU_Q_EGR_DROP_THR [23].OFF[12:0]	13
0xBB002708	FC_CPU_Q_EGR_DROP_THR [24].RESERVED[31:29]	3
0xBB002708	FC_CPU_Q_EGR_DROP_THR [24].ON[28:16]	13
0xBB002708	FC_CPU_Q_EGR_DROP_THR [24].RESERVED[15:13]	3
0xBB002708	FC_CPU_Q_EGR_DROP_THR [24].OFF[12:0]	13
0xBB00270C	FC_CPU_Q_EGR_DROP_THR [25].RESERVED[31:29]	3
0xBB00270C	FC_CPU_Q_EGR_DROP_THR [25].ON[28:16]	13
0xBB00270C	FC_CPU_Q_EGR_DROP_THR [25].RESERVED[15:13]	3
0xBB00270C	FC_CPU_Q_EGR_DROP_THR [25].OFF[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002710	FC_CPU_Q_EGR_DROP_THR [26].RESERVED[31:29]	3
0xBB002710	FC_CPU_Q_EGR_DROP_THR [26].ON[28:16]	13
0xBB002710	FC_CPU_Q_EGR_DROP_THR [26].RESERVED[15:13]	3
0xBB002710	FC_CPU_Q_EGR_DROP_THR [26].OFF[12:0]	13
0xBB002714	FC_CPU_Q_EGR_DROP_THR [27].RESERVED[31:29]	3
0xBB002714	FC_CPU_Q_EGR_DROP_THR [27].ON[28:16]	13
0xBB002714	FC_CPU_Q_EGR_DROP_THR [27].RESERVED[15:13]	3
0xBB002714	FC_CPU_Q_EGR_DROP_THR [27].OFF[12:0]	13
0xBB002718	FC_CPU_Q_EGR_DROP_THR [28].RESERVED[31:29]	3
0xBB002718	FC_CPU_Q_EGR_DROP_THR [28].ON[28:16]	13
0xBB002718	FC_CPU_Q_EGR_DROP_THR [28].RESERVED[15:13]	3
0xBB002718	FC_CPU_Q_EGR_DROP_THR [28].OFF[12:0]	13
0xBB00271C	FC_CPU_Q_EGR_DROP_THR [29].RESERVED[31:29]	3
0xBB00271C	FC_CPU_Q_EGR_DROP_THR [29].ON[28:16]	13
0xBB00271C	FC_CPU_Q_EGR_DROP_THR [29].RESERVED[15:13]	3
0xBB00271C	FC_CPU_Q_EGR_DROP_THR [29].OFF[12:0]	13
0xBB002720	FC_CPU_Q_EGR_DROP_THR [30].RESERVED[31:29]	3
0xBB002720	FC_CPU_Q_EGR_DROP_THR [30].ON[28:16]	13
0xBB002720	FC_CPU_Q_EGR_DROP_THR [30].RESERVED[15:13]	3
0xBB002720	FC_CPU_Q_EGR_DROP_THR [30].OFF[12:0]	13
0xBB002724	FC_CPU_Q_EGR_DROP_THR [31].RESERVED[31:29]	3
0xBB002724	FC_CPU_Q_EGR_DROP_THR [31].ON[28:16]	13
0xBB002724	FC_CPU_Q_EGR_DROP_THR [31].RESERVED[15:13]	3
0xBB002724	FC_CPU_Q_EGR_DROP_THR [31].OFF[12:0]	13
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [0].EN[0:0]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [1].EN[1:1]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [2].EN[2:2]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [3].EN[3:3]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [4].EN[4:4]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [5].EN[5:5]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [6].EN[6:6]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [7].EN[7:7]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [8].EN[8:8]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [9].EN[9:9]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [10].EN[10:10]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [11].EN[11:11]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [12].EN[12:12]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [13].EN[13:13]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [14].EN[14:14]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [15].EN[15:15]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [16].EN[16:16]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [17].EN[17:17]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [18].EN[18:18]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [19].EN[19:19]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [20].EN[20:20]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [21].EN[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [22].EN[22:22]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [23].EN[23:23]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [24].EN[24:24]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [25].EN[25:25]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [26].EN[26:26]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [27].EN[27:27]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [28].EN[28:28]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [29].EN[29:29]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [30].EN[30:30]	1
0xBB00A9C8	FC_CPU_Q_EGR_FORCE_DROP_CTRL [31].EN[31:31]	1
0xBB002740	FC_CPU_Q_EGR_PAGE_CNT [0].RESERVED[31:30]	2
0xBB002740	FC_CPU_Q_EGR_PAGE_CNT [0].MAX_CNT[29:16]	14
0xBB002740	FC_CPU_Q_EGR_PAGE_CNT [0].RESERVED[15:14]	2
0xBB002740	FC_CPU_Q_EGR_PAGE_CNT [0].CNT[13:0]	14
0xBB002744	FC_CPU_Q_EGR_PAGE_CNT [1].RESERVED[31:30]	2
0xBB002744	FC_CPU_Q_EGR_PAGE_CNT [1].MAX_CNT[29:16]	14
0xBB002744	FC_CPU_Q_EGR_PAGE_CNT [1].RESERVED[15:14]	2
0xBB002744	FC_CPU_Q_EGR_PAGE_CNT [1].CNT[13:0]	14
0xBB002748	FC_CPU_Q_EGR_PAGE_CNT [2].RESERVED[31:30]	2
0xBB002748	FC_CPU_Q_EGR_PAGE_CNT [2].MAX_CNT[29:16]	14
0xBB002748	FC_CPU_Q_EGR_PAGE_CNT [2].RESERVED[15:14]	2
0xBB002748	FC_CPU_Q_EGR_PAGE_CNT [2].CNT[13:0]	14
0xBB00274C	FC_CPU_Q_EGR_PAGE_CNT [3].RESERVED[31:30]	2
0xBB00274C	FC_CPU_Q_EGR_PAGE_CNT [3].MAX_CNT[29:16]	14
0xBB00274C	FC_CPU_Q_EGR_PAGE_CNT [3].RESERVED[15:14]	2
0xBB00274C	FC_CPU_Q_EGR_PAGE_CNT [3].CNT[13:0]	14
0xBB002750	FC_CPU_Q_EGR_PAGE_CNT [4].RESERVED[31:30]	2
0xBB002750	FC_CPU_Q_EGR_PAGE_CNT [4].MAX_CNT[29:16]	14
0xBB002750	FC_CPU_Q_EGR_PAGE_CNT [4].RESERVED[15:14]	2
0xBB002750	FC_CPU_Q_EGR_PAGE_CNT [4].CNT[13:0]	14
0xBB002754	FC_CPU_Q_EGR_PAGE_CNT [5].RESERVED[31:30]	2
0xBB002754	FC_CPU_Q_EGR_PAGE_CNT [5].MAX_CNT[29:16]	14
0xBB002754	FC_CPU_Q_EGR_PAGE_CNT [5].RESERVED[15:14]	2
0xBB002754	FC_CPU_Q_EGR_PAGE_CNT [5].CNT[13:0]	14
0xBB002758	FC_CPU_Q_EGR_PAGE_CNT [6].RESERVED[31:30]	2
0xBB002758	FC_CPU_Q_EGR_PAGE_CNT [6].MAX_CNT[29:16]	14
0xBB002758	FC_CPU_Q_EGR_PAGE_CNT [6].RESERVED[15:14]	2
0xBB002758	FC_CPU_Q_EGR_PAGE_CNT [6].CNT[13:0]	14
0xBB00275C	FC_CPU_Q_EGR_PAGE_CNT [7].RESERVED[31:30]	2
0xBB00275C	FC_CPU_Q_EGR_PAGE_CNT [7].MAX_CNT[29:16]	14
0xBB00275C	FC_CPU_Q_EGR_PAGE_CNT [7].RESERVED[15:14]	2
0xBB00275C	FC_CPU_Q_EGR_PAGE_CNT [7].CNT[13:0]	14
0xBB002760	FC_CPU_Q_EGR_PAGE_CNT [8].RESERVED[31:30]	2
0xBB002760	FC_CPU_Q_EGR_PAGE_CNT [8].MAX_CNT[29:16]	14
0xBB002760	FC_CPU_Q_EGR_PAGE_CNT [8].RESERVED[15:14]	2
0xBB002760	FC_CPU_Q_EGR_PAGE_CNT [8].CNT[13:0]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002764	FC_CPU_Q_EGR_PAGE_CNT [9].RESERVED[31:30]	2
0xBB002764	FC_CPU_Q_EGR_PAGE_CNT [9].MAX_CNT[29:16]	14
0xBB002764	FC_CPU_Q_EGR_PAGE_CNT [9].RESERVED[15:14]	2
0xBB002764	FC_CPU_Q_EGR_PAGE_CNT [9].CNT[13:0]	14
0xBB002768	FC_CPU_Q_EGR_PAGE_CNT [10].RESERVED[31:30]	2
0xBB002768	FC_CPU_Q_EGR_PAGE_CNT [10].MAX_CNT[29:16]	14
0xBB002768	FC_CPU_Q_EGR_PAGE_CNT [10].RESERVED[15:14]	2
0xBB002768	FC_CPU_Q_EGR_PAGE_CNT [10].CNT[13:0]	14
0xBB00276C	FC_CPU_Q_EGR_PAGE_CNT [11].RESERVED[31:30]	2
0xBB00276C	FC_CPU_Q_EGR_PAGE_CNT [11].MAX_CNT[29:16]	14
0xBB00276C	FC_CPU_Q_EGR_PAGE_CNT [11].RESERVED[15:14]	2
0xBB00276C	FC_CPU_Q_EGR_PAGE_CNT [11].CNT[13:0]	14
0xBB002770	FC_CPU_Q_EGR_PAGE_CNT [12].RESERVED[31:30]	2
0xBB002770	FC_CPU_Q_EGR_PAGE_CNT [12].MAX_CNT[29:16]	14
0xBB002770	FC_CPU_Q_EGR_PAGE_CNT [12].RESERVED[15:14]	2
0xBB002770	FC_CPU_Q_EGR_PAGE_CNT [12].CNT[13:0]	14
0xBB002774	FC_CPU_Q_EGR_PAGE_CNT [13].RESERVED[31:30]	2
0xBB002774	FC_CPU_Q_EGR_PAGE_CNT [13].MAX_CNT[29:16]	14
0xBB002774	FC_CPU_Q_EGR_PAGE_CNT [13].RESERVED[15:14]	2
0xBB002774	FC_CPU_Q_EGR_PAGE_CNT [13].CNT[13:0]	14
0xBB002778	FC_CPU_Q_EGR_PAGE_CNT [14].RESERVED[31:30]	2
0xBB002778	FC_CPU_Q_EGR_PAGE_CNT [14].MAX_CNT[29:16]	14
0xBB002778	FC_CPU_Q_EGR_PAGE_CNT [14].RESERVED[15:14]	2
0xBB002778	FC_CPU_Q_EGR_PAGE_CNT [14].CNT[13:0]	14
0xBB00277C	FC_CPU_Q_EGR_PAGE_CNT [15].RESERVED[31:30]	2
0xBB00277C	FC_CPU_Q_EGR_PAGE_CNT [15].MAX_CNT[29:16]	14
0xBB00277C	FC_CPU_Q_EGR_PAGE_CNT [15].RESERVED[15:14]	2
0xBB00277C	FC_CPU_Q_EGR_PAGE_CNT [15].CNT[13:0]	14
0xBB002780	FC_CPU_Q_EGR_PAGE_CNT [16].RESERVED[31:30]	2
0xBB002780	FC_CPU_Q_EGR_PAGE_CNT [16].MAX_CNT[29:16]	14
0xBB002780	FC_CPU_Q_EGR_PAGE_CNT [16].RESERVED[15:14]	2
0xBB002780	FC_CPU_Q_EGR_PAGE_CNT [16].CNT[13:0]	14
0xBB002784	FC_CPU_Q_EGR_PAGE_CNT [17].RESERVED[31:30]	2
0xBB002784	FC_CPU_Q_EGR_PAGE_CNT [17].MAX_CNT[29:16]	14
0xBB002784	FC_CPU_Q_EGR_PAGE_CNT [17].RESERVED[15:14]	2
0xBB002784	FC_CPU_Q_EGR_PAGE_CNT [17].CNT[13:0]	14
0xBB002788	FC_CPU_Q_EGR_PAGE_CNT [18].RESERVED[31:30]	2
0xBB002788	FC_CPU_Q_EGR_PAGE_CNT [18].MAX_CNT[29:16]	14
0xBB002788	FC_CPU_Q_EGR_PAGE_CNT [18].RESERVED[15:14]	2
0xBB002788	FC_CPU_Q_EGR_PAGE_CNT [18].CNT[13:0]	14
0xBB00278C	FC_CPU_Q_EGR_PAGE_CNT [19].RESERVED[31:30]	2
0xBB00278C	FC_CPU_Q_EGR_PAGE_CNT [19].MAX_CNT[29:16]	14
0xBB00278C	FC_CPU_Q_EGR_PAGE_CNT [19].RESERVED[15:14]	2
0xBB00278C	FC_CPU_Q_EGR_PAGE_CNT [19].CNT[13:0]	14
0xBB002790	FC_CPU_Q_EGR_PAGE_CNT [20].RESERVED[31:30]	2
0xBB002790	FC_CPU_Q_EGR_PAGE_CNT [20].MAX_CNT[29:16]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002790	FC_CPU_Q_EGR_PAGE_CNT [20].RESERVED[15:14]	2
0xBB002790	FC_CPU_Q_EGR_PAGE_CNT [20].CNT[13:0]	14
0xBB002794	FC_CPU_Q_EGR_PAGE_CNT [21].RESERVED[31:30]	2
0xBB002794	FC_CPU_Q_EGR_PAGE_CNT [21].MAX_CNT[29:16]	14
0xBB002794	FC_CPU_Q_EGR_PAGE_CNT [21].RESERVED[15:14]	2
0xBB002794	FC_CPU_Q_EGR_PAGE_CNT [21].CNT[13:0]	14
0xBB002798	FC_CPU_Q_EGR_PAGE_CNT [22].RESERVED[31:30]	2
0xBB002798	FC_CPU_Q_EGR_PAGE_CNT [22].MAX_CNT[29:16]	14
0xBB002798	FC_CPU_Q_EGR_PAGE_CNT [22].RESERVED[15:14]	2
0xBB002798	FC_CPU_Q_EGR_PAGE_CNT [22].CNT[13:0]	14
0xBB00279C	FC_CPU_Q_EGR_PAGE_CNT [23].RESERVED[31:30]	2
0xBB00279C	FC_CPU_Q_EGR_PAGE_CNT [23].MAX_CNT[29:16]	14
0xBB00279C	FC_CPU_Q_EGR_PAGE_CNT [23].RESERVED[15:14]	2
0xBB00279C	FC_CPU_Q_EGR_PAGE_CNT [23].CNT[13:0]	14
0xBB0027A0	FC_CPU_Q_EGR_PAGE_CNT [24].RESERVED[31:30]	2
0xBB0027A0	FC_CPU_Q_EGR_PAGE_CNT [24].MAX_CNT[29:16]	14
0xBB0027A0	FC_CPU_Q_EGR_PAGE_CNT [24].RESERVED[15:14]	2
0xBB0027A0	FC_CPU_Q_EGR_PAGE_CNT [24].CNT[13:0]	14
0xBB0027A4	FC_CPU_Q_EGR_PAGE_CNT [25].RESERVED[31:30]	2
0xBB0027A4	FC_CPU_Q_EGR_PAGE_CNT [25].MAX_CNT[29:16]	14
0xBB0027A4	FC_CPU_Q_EGR_PAGE_CNT [25].RESERVED[15:14]	2
0xBB0027A4	FC_CPU_Q_EGR_PAGE_CNT [25].CNT[13:0]	14
0xBB0027A8	FC_CPU_Q_EGR_PAGE_CNT [26].RESERVED[31:30]	2
0xBB0027A8	FC_CPU_Q_EGR_PAGE_CNT [26].MAX_CNT[29:16]	14
0xBB0027A8	FC_CPU_Q_EGR_PAGE_CNT [26].RESERVED[15:14]	2
0xBB0027A8	FC_CPU_Q_EGR_PAGE_CNT [26].CNT[13:0]	14
0xBB0027AC	FC_CPU_Q_EGR_PAGE_CNT [27].RESERVED[31:30]	2
0xBB0027AC	FC_CPU_Q_EGR_PAGE_CNT [27].MAX_CNT[29:16]	14
0xBB0027AC	FC_CPU_Q_EGR_PAGE_CNT [27].RESERVED[15:14]	2
0xBB0027AC	FC_CPU_Q_EGR_PAGE_CNT [27].CNT[13:0]	14
0xBB0027B0	FC_CPU_Q_EGR_PAGE_CNT [28].RESERVED[31:30]	2
0xBB0027B0	FC_CPU_Q_EGR_PAGE_CNT [28].MAX_CNT[29:16]	14
0xBB0027B0	FC_CPU_Q_EGR_PAGE_CNT [28].RESERVED[15:14]	2
0xBB0027B0	FC_CPU_Q_EGR_PAGE_CNT [28].CNT[13:0]	14
0xBB0027B4	FC_CPU_Q_EGR_PAGE_CNT [29].RESERVED[31:30]	2
0xBB0027B4	FC_CPU_Q_EGR_PAGE_CNT [29].MAX_CNT[29:16]	14
0xBB0027B4	FC_CPU_Q_EGR_PAGE_CNT [29].RESERVED[15:14]	2
0xBB0027B4	FC_CPU_Q_EGR_PAGE_CNT [29].CNT[13:0]	14
0xBB0027B8	FC_CPU_Q_EGR_PAGE_CNT [30].RESERVED[31:30]	2
0xBB0027B8	FC_CPU_Q_EGR_PAGE_CNT [30].MAX_CNT[29:16]	14
0xBB0027B8	FC_CPU_Q_EGR_PAGE_CNT [30].RESERVED[15:14]	2
0xBB0027B8	FC_CPU_Q_EGR_PAGE_CNT [30].CNT[13:0]	14
0xBB0027BC	FC_CPU_Q_EGR_PAGE_CNT [31].RESERVED[31:30]	2
0xBB0027BC	FC_CPU_Q_EGR_PAGE_CNT [31].MAX_CNT[29:16]	14
0xBB0027BC	FC_CPU_Q_EGR_PAGE_CNT [31].RESERVED[15:14]	2
0xBB0027BC	FC_CPU_Q_EGR_PAGE_CNT [31].CNT[13:0]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005048	FC_CTRL.RESERVED[31:1]	31
0xBB005048	FC_CTRL.GUAR_PAGE_EN[0:0]	1
0xBB005134	FC_GLB_DROP_THR.RESERVED[31:13]	19
0xBB005134	FC_GLB_DROP_THR.DROP_ALL[12:0]	13
0xBB005140	FC_GLB_FCOFF_HI_THR.RESERVED[31:29]	3
0xBB005140	FC_GLB_FCOFF_HI_THR.ON[28:16]	13
0xBB005140	FC_GLB_FCOFF_HI_THR.RESERVED[15:13]	3
0xBB005140	FC_GLB_FCOFF_HI_THR.OFF[12:0]	13
0xBB005144	FC_GLB_FCOFF_LO_THR.RESERVED[31:29]	3
0xBB005144	FC_GLB_FCOFF_LO_THR.ON[28:16]	13
0xBB005144	FC_GLB_FCOFF_LO_THR.RESERVED[15:13]	3
0xBB005144	FC_GLB_FCOFF_LO_THR.OFF[12:0]	13
0xBB005138	FC_GLB_HI_THR.RESERVED[31:29]	3
0xBB005138	FC_GLB_HI_THR.ON[28:16]	13
0xBB005138	FC_GLB_HI_THR.RESERVED[15:13]	3
0xBB005138	FC_GLB_HI_THR.OFF[12:0]	13
0xBB00513C	FC_GLB_LO_THR.RESERVED[31:29]	3
0xBB00513C	FC_GLB_LO_THR.ON[28:16]	13
0xBB00513C	FC_GLB_LO_THR.RESERVED[15:13]	3
0xBB00513C	FC_GLB_LO_THR.OFF[12:0]	13
0xBB0051BC	FC_GLB_PAGE_CNT.RESERVED[31:29]	3
0xBB0051BC	FC_GLB_PAGE_CNT.MAX_CNT[28:16]	13
0xBB0051BC	FC_GLB_PAGE_CNT.RESERVED[15:13]	3
0xBB0051BC	FC_GLB_PAGE_CNT.CNT[12:0]	13
0xBB005130	FC_GLB_SYS_UTIL_THR.RESERVED[31:13]	19
0xBB005130	FC_GLB_SYS_UTIL_THR.THR[12:0]	13
0xBB00A8E4	FC_HOL_PRVNT_CTRL.RESERVED[31:4]	28
0xBB00A8E4	FC_HOL_PRVNT_CTRL.BC_EN[3:3]	1
0xBB00A8E4	FC_HOL_PRVNT_CTRL.L2_MC_EN[2:2]	1
0xBB00A8E4	FC_HOL_PRVNT_CTRL.IP_MC_EN[1:1]	1
0xBB00A8E4	FC_HOL_PRVNT_CTRL.UNKN_UC_EN[0:0]	1
0xBB0027C0	FC_HSA_PAGE_CNT.RESERVED[31:28]	4
0xBB0027C0	FC_HSA_PAGE_CNT.MAX_CNT[27:16]	12
0xBB0027C0	FC_HSA_PAGE_CNT.RESERVED[15:12]	4
0xBB0027C0	FC_HSA_PAGE_CNT.CNT[11:0]	12
0xBB005150	FC_JUMBO_FCOFF_HI_THR.RESERVED[31:29]	3
0xBB005150	FC_JUMBO_FCOFF_HI_THR.ON[28:16]	13
0xBB005150	FC_JUMBO_FCOFF_HI_THR.RESERVED[15:13]	3
0xBB005150	FC_JUMBO_FCOFF_HI_THR.OFF[12:0]	13
0xBB005154	FC_JUMBO_FCOFF_LO_THR.RESERVED[31:29]	3
0xBB005154	FC_JUMBO_FCOFF_LO_THR.ON[28:16]	13
0xBB005154	FC_JUMBO_FCOFF_LO_THR.RESERVED[15:13]	3
0xBB005154	FC_JUMBO_FCOFF_LO_THR.OFF[12:0]	13
0xBB005148	FC_JUMBO_HI_THR.RESERVED[31:29]	3
0xBB005148	FC_JUMBO_HI_THR.ON[28:16]	13
0xBB005148	FC_JUMBO_HI_THR.RESERVED[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005148	FC_JUMBO_HI_THR.OFF[12:0]	13
0xBB00514C	FC_JUMBO_LO_THR.RESERVED[31:29]	3
0xBB00514C	FC_JUMBO_LO_THR.ON[28:16]	13
0xBB00514C	FC_JUMBO_LO_THR.RESERVED[15:13]	3
0xBB00514C	FC_JUMBO_LO_THR.OFF[12:0]	13
0xBB005158	FC_JUMBO_THR_ADJUST.EN[31:31]	1
0xBB005158	FC_JUMBO_THR_ADJUST.STS[30:30]	1
0xBB005158	FC_JUMBO_THR_ADJUST.PKT_LEN[29:16]	14
0xBB005158	FC_JUMBO_THR_ADJUST.RESERVED[15:13]	3
0xBB005158	FC_JUMBO_THR_ADJUST.SYS_USED_PAGE_THR[12:0]	13
0xBB0051C0	FC_PB_PAGE_CNT.RESERVED[31:29]	3
0xBB0051C0	FC_PB_PAGE_CNT.MAX_CNT[28:16]	13
0xBB0051C0	FC_PB_PAGE_CNT.RESERVED[15:13]	3
0xBB0051C0	FC_PB_PAGE_CNT.CNT[12:0]	13
0xBB00504C	FC_PORT_ACT_CTRL [0].RESERVED[31:25]	7
0xBB00504C	FC_PORT_ACT_CTRL [0].ACT[24:24]	1
0xBB00504C	FC_PORT_ACT_CTRL [0].RESERVED[23:13]	11
0xBB00504C	FC_PORT_ACT_CTRL [0].ALLOW_PAGE_CNT[12:0]	13
0xBB005050	FC_PORT_ACT_CTRL [1].RESERVED[31:25]	7
0xBB005050	FC_PORT_ACT_CTRL [1].ACT[24:24]	1
0xBB005050	FC_PORT_ACT_CTRL [1].RESERVED[23:13]	11
0xBB005050	FC_PORT_ACT_CTRL [1].ALLOW_PAGE_CNT[12:0]	13
0xBB005054	FC_PORT_ACT_CTRL [2].RESERVED[31:25]	7
0xBB005054	FC_PORT_ACT_CTRL [2].ACT[24:24]	1
0xBB005054	FC_PORT_ACT_CTRL [2].RESERVED[23:13]	11
0xBB005054	FC_PORT_ACT_CTRL [2].ALLOW_PAGE_CNT[12:0]	13
0xBB005058	FC_PORT_ACT_CTRL [3].RESERVED[31:25]	7
0xBB005058	FC_PORT_ACT_CTRL [3].ACT[24:24]	1
0xBB005058	FC_PORT_ACT_CTRL [3].RESERVED[23:13]	11
0xBB005058	FC_PORT_ACT_CTRL [3].ALLOW_PAGE_CNT[12:0]	13
0xBB00505C	FC_PORT_ACT_CTRL [4].RESERVED[31:25]	7
0xBB00505C	FC_PORT_ACT_CTRL [4].ACT[24:24]	1
0xBB00505C	FC_PORT_ACT_CTRL [4].RESERVED[23:13]	11
0xBB00505C	FC_PORT_ACT_CTRL [4].ALLOW_PAGE_CNT[12:0]	13
0xBB005060	FC_PORT_ACT_CTRL [5].RESERVED[31:25]	7
0xBB005060	FC_PORT_ACT_CTRL [5].ACT[24:24]	1
0xBB005060	FC_PORT_ACT_CTRL [5].RESERVED[23:13]	11
0xBB005060	FC_PORT_ACT_CTRL [5].ALLOW_PAGE_CNT[12:0]	13
0xBB005064	FC_PORT_ACT_CTRL [6].RESERVED[31:25]	7
0xBB005064	FC_PORT_ACT_CTRL [6].ACT[24:24]	1
0xBB005064	FC_PORT_ACT_CTRL [6].RESERVED[23:13]	11
0xBB005064	FC_PORT_ACT_CTRL [6].ALLOW_PAGE_CNT[12:0]	13
0xBB005068	FC_PORT_ACT_CTRL [7].RESERVED[31:25]	7
0xBB005068	FC_PORT_ACT_CTRL [7].ACT[24:24]	1
0xBB005068	FC_PORT_ACT_CTRL [7].RESERVED[23:13]	11
0xBB005068	FC_PORT_ACT_CTRL [7].ALLOW_PAGE_CNT[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00506C	FC_PORT_ACT_CTRL [8].RESERVED[31:25]	7
0xBB00506C	FC_PORT_ACT_CTRL [8].ACT[24:24]	1
0xBB00506C	FC_PORT_ACT_CTRL [8].RESERVED[23:13]	11
0xBB00506C	FC_PORT_ACT_CTRL [8].ALLOW_PAGE_CNT[12:0]	13
0xBB005070	FC_PORT_ACT_CTRL [9].RESERVED[31:25]	7
0xBB005070	FC_PORT_ACT_CTRL [9].ACT[24:24]	1
0xBB005070	FC_PORT_ACT_CTRL [9].RESERVED[23:13]	11
0xBB005070	FC_PORT_ACT_CTRL [9].ALLOW_PAGE_CNT[12:0]	13
0xBB005074	FC_PORT_ACT_CTRL [10].RESERVED[31:25]	7
0xBB005074	FC_PORT_ACT_CTRL [10].ACT[24:24]	1
0xBB005074	FC_PORT_ACT_CTRL [10].RESERVED[23:13]	11
0xBB005074	FC_PORT_ACT_CTRL [10].ALLOW_PAGE_CNT[12:0]	13
0xBB005078	FC_PORT_ACT_CTRL [11].RESERVED[31:25]	7
0xBB005078	FC_PORT_ACT_CTRL [11].ACT[24:24]	1
0xBB005078	FC_PORT_ACT_CTRL [11].RESERVED[23:13]	11
0xBB005078	FC_PORT_ACT_CTRL [11].ALLOW_PAGE_CNT[12:0]	13
0xBB00507C	FC_PORT_ACT_CTRL [12].RESERVED[31:25]	7
0xBB00507C	FC_PORT_ACT_CTRL [12].ACT[24:24]	1
0xBB00507C	FC_PORT_ACT_CTRL [12].RESERVED[23:13]	11
0xBB00507C	FC_PORT_ACT_CTRL [12].ALLOW_PAGE_CNT[12:0]	13
0xBB005080	FC_PORT_ACT_CTRL [13].RESERVED[31:25]	7
0xBB005080	FC_PORT_ACT_CTRL [13].ACT[24:24]	1
0xBB005080	FC_PORT_ACT_CTRL [13].RESERVED[23:13]	11
0xBB005080	FC_PORT_ACT_CTRL [13].ALLOW_PAGE_CNT[12:0]	13
0xBB005084	FC_PORT_ACT_CTRL [14].RESERVED[31:25]	7
0xBB005084	FC_PORT_ACT_CTRL [14].ACT[24:24]	1
0xBB005084	FC_PORT_ACT_CTRL [14].RESERVED[23:13]	11
0xBB005084	FC_PORT_ACT_CTRL [14].ALLOW_PAGE_CNT[12:0]	13
0xBB005088	FC_PORT_ACT_CTRL [15].RESERVED[31:25]	7
0xBB005088	FC_PORT_ACT_CTRL [15].ACT[24:24]	1
0xBB005088	FC_PORT_ACT_CTRL [15].RESERVED[23:13]	11
0xBB005088	FC_PORT_ACT_CTRL [15].ALLOW_PAGE_CNT[12:0]	13
0xBB00508C	FC_PORT_ACT_CTRL [16].RESERVED[31:25]	7
0xBB00508C	FC_PORT_ACT_CTRL [16].ACT[24:24]	1
0xBB00508C	FC_PORT_ACT_CTRL [16].RESERVED[23:13]	11
0xBB00508C	FC_PORT_ACT_CTRL [16].ALLOW_PAGE_CNT[12:0]	13
0xBB005090	FC_PORT_ACT_CTRL [17].RESERVED[31:25]	7
0xBB005090	FC_PORT_ACT_CTRL [17].ACT[24:24]	1
0xBB005090	FC_PORT_ACT_CTRL [17].RESERVED[23:13]	11
0xBB005090	FC_PORT_ACT_CTRL [17].ALLOW_PAGE_CNT[12:0]	13
0xBB005094	FC_PORT_ACT_CTRL [18].RESERVED[31:25]	7
0xBB005094	FC_PORT_ACT_CTRL [18].ACT[24:24]	1
0xBB005094	FC_PORT_ACT_CTRL [18].RESERVED[23:13]	11
0xBB005094	FC_PORT_ACT_CTRL [18].ALLOW_PAGE_CNT[12:0]	13
0xBB005098	FC_PORT_ACT_CTRL [19].RESERVED[31:25]	7
0xBB005098	FC_PORT_ACT_CTRL [19].ACT[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005098	FC_PORT_ACT_CTRL [19].RESERVED[23:13]	11
0xBB005098	FC_PORT_ACT_CTRL [19].ALLOW_PAGE_CNT[12:0]	13
0xBB00509C	FC_PORT_ACT_CTRL [20].RESERVED[31:25]	7
0xBB00509C	FC_PORT_ACT_CTRL [20].ACT[24:24]	1
0xBB00509C	FC_PORT_ACT_CTRL [20].RESERVED[23:13]	11
0xBB00509C	FC_PORT_ACT_CTRL [20].ALLOW_PAGE_CNT[12:0]	13
0xBB0050A0	FC_PORT_ACT_CTRL [21].RESERVED[31:25]	7
0xBB0050A0	FC_PORT_ACT_CTRL [21].ACT[24:24]	1
0xBB0050A0	FC_PORT_ACT_CTRL [21].RESERVED[23:13]	11
0xBB0050A0	FC_PORT_ACT_CTRL [21].ALLOW_PAGE_CNT[12:0]	13
0xBB0050A4	FC_PORT_ACT_CTRL [22].RESERVED[31:25]	7
0xBB0050A4	FC_PORT_ACT_CTRL [22].ACT[24:24]	1
0xBB0050A4	FC_PORT_ACT_CTRL [22].RESERVED[23:13]	11
0xBB0050A4	FC_PORT_ACT_CTRL [22].ALLOW_PAGE_CNT[12:0]	13
0xBB0050A8	FC_PORT_ACT_CTRL [23].RESERVED[31:25]	7
0xBB0050A8	FC_PORT_ACT_CTRL [23].ACT[24:24]	1
0xBB0050A8	FC_PORT_ACT_CTRL [23].RESERVED[23:13]	11
0xBB0050A8	FC_PORT_ACT_CTRL [23].ALLOW_PAGE_CNT[12:0]	13
0xBB0050AC	FC_PORT_ACT_CTRL [24].RESERVED[31:25]	7
0xBB0050AC	FC_PORT_ACT_CTRL [24].ACT[24:24]	1
0xBB0050AC	FC_PORT_ACT_CTRL [24].RESERVED[23:13]	11
0xBB0050AC	FC_PORT_ACT_CTRL [24].ALLOW_PAGE_CNT[12:0]	13
0xBB0050B0	FC_PORT_ACT_CTRL [25].RESERVED[31:25]	7
0xBB0050B0	FC_PORT_ACT_CTRL [25].ACT[24:24]	1
0xBB0050B0	FC_PORT_ACT_CTRL [25].RESERVED[23:13]	11
0xBB0050B0	FC_PORT_ACT_CTRL [25].ALLOW_PAGE_CNT[12:0]	13
0xBB0050B4	FC_PORT_ACT_CTRL [26].RESERVED[31:25]	7
0xBB0050B4	FC_PORT_ACT_CTRL [26].ACT[24:24]	1
0xBB0050B4	FC_PORT_ACT_CTRL [26].RESERVED[23:13]	11
0xBB0050B4	FC_PORT_ACT_CTRL [26].ALLOW_PAGE_CNT[12:0]	13
0xBB0050B8	FC_PORT_ACT_CTRL [27].RESERVED[31:25]	7
0xBB0050B8	FC_PORT_ACT_CTRL [27].ACT[24:24]	1
0xBB0050B8	FC_PORT_ACT_CTRL [27].RESERVED[23:13]	11
0xBB0050B8	FC_PORT_ACT_CTRL [27].ALLOW_PAGE_CNT[12:0]	13
0xBB0050BC	FC_PORT_ACT_CTRL [28].RESERVED[31:25]	7
0xBB0050BC	FC_PORT_ACT_CTRL [28].ACT[24:24]	1
0xBB0050BC	FC_PORT_ACT_CTRL [28].RESERVED[23:13]	11
0xBB0050BC	FC_PORT_ACT_CTRL [28].ALLOW_PAGE_CNT[12:0]	13
0xBB0050C0	FC_PORT_ACT_CTRL [29].RESERVED[31:25]	7
0xBB0050C0	FC_PORT_ACT_CTRL [29].ACT[24:24]	1
0xBB0050C0	FC_PORT_ACT_CTRL [29].RESERVED[23:13]	11
0xBB0050C0	FC_PORT_ACT_CTRL [29].ALLOW_PAGE_CNT[12:0]	13
0xBB0050C4	FC_PORT_ACT_CTRL [30].RESERVED[31:25]	7
0xBB0050C4	FC_PORT_ACT_CTRL [30].ACT[24:24]	1
0xBB0050C4	FC_PORT_ACT_CTRL [30].RESERVED[23:13]	11
0xBB0050C4	FC_PORT_ACT_CTRL [30].ALLOW_PAGE_CNT[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0050C8	FC_PORT_ACT_CTRL [31].RESERVED[31:25]	7
0xBB0050C8	FC_PORT_ACT_CTRL [31].ACT[24:24]	1
0xBB0050C8	FC_PORT_ACT_CTRL [31].RESERVED[23:13]	11
0xBB0050C8	FC_PORT_ACT_CTRL [31].ALLOW_PAGE_CNT[12:0]	13
0xBB0050CC	FC_PORT_ACT_CTRL [32].RESERVED[31:25]	7
0xBB0050CC	FC_PORT_ACT_CTRL [32].ACT[24:24]	1
0xBB0050CC	FC_PORT_ACT_CTRL [32].RESERVED[23:13]	11
0xBB0050CC	FC_PORT_ACT_CTRL [32].ALLOW_PAGE_CNT[12:0]	13
0xBB0050D0	FC_PORT_ACT_CTRL [33].RESERVED[31:25]	7
0xBB0050D0	FC_PORT_ACT_CTRL [33].ACT[24:24]	1
0xBB0050D0	FC_PORT_ACT_CTRL [33].RESERVED[23:13]	11
0xBB0050D0	FC_PORT_ACT_CTRL [33].ALLOW_PAGE_CNT[12:0]	13
0xBB0050D4	FC_PORT_ACT_CTRL [34].RESERVED[31:25]	7
0xBB0050D4	FC_PORT_ACT_CTRL [34].ACT[24:24]	1
0xBB0050D4	FC_PORT_ACT_CTRL [34].RESERVED[23:13]	11
0xBB0050D4	FC_PORT_ACT_CTRL [34].ALLOW_PAGE_CNT[12:0]	13
0xBB0050D8	FC_PORT_ACT_CTRL [35].RESERVED[31:25]	7
0xBB0050D8	FC_PORT_ACT_CTRL [35].ACT[24:24]	1
0xBB0050D8	FC_PORT_ACT_CTRL [35].RESERVED[23:13]	11
0xBB0050D8	FC_PORT_ACT_CTRL [35].ALLOW_PAGE_CNT[12:0]	13
0xBB0050DC	FC_PORT_ACT_CTRL [36].RESERVED[31:25]	7
0xBB0050DC	FC_PORT_ACT_CTRL [36].ACT[24:24]	1
0xBB0050DC	FC_PORT_ACT_CTRL [36].RESERVED[23:13]	11
0xBB0050DC	FC_PORT_ACT_CTRL [36].ALLOW_PAGE_CNT[12:0]	13
0xBB0050E0	FC_PORT_ACT_CTRL [37].RESERVED[31:25]	7
0xBB0050E0	FC_PORT_ACT_CTRL [37].ACT[24:24]	1
0xBB0050E0	FC_PORT_ACT_CTRL [37].RESERVED[23:13]	11
0xBB0050E0	FC_PORT_ACT_CTRL [37].ALLOW_PAGE_CNT[12:0]	13
0xBB0050E4	FC_PORT_ACT_CTRL [38].RESERVED[31:25]	7
0xBB0050E4	FC_PORT_ACT_CTRL [38].ACT[24:24]	1
0xBB0050E4	FC_PORT_ACT_CTRL [38].RESERVED[23:13]	11
0xBB0050E4	FC_PORT_ACT_CTRL [38].ALLOW_PAGE_CNT[12:0]	13
0xBB0050E8	FC_PORT_ACT_CTRL [39].RESERVED[31:25]	7
0xBB0050E8	FC_PORT_ACT_CTRL [39].ACT[24:24]	1
0xBB0050E8	FC_PORT_ACT_CTRL [39].RESERVED[23:13]	11
0xBB0050E8	FC_PORT_ACT_CTRL [39].ALLOW_PAGE_CNT[12:0]	13
0xBB0050EC	FC_PORT_ACT_CTRL [40].RESERVED[31:25]	7
0xBB0050EC	FC_PORT_ACT_CTRL [40].ACT[24:24]	1
0xBB0050EC	FC_PORT_ACT_CTRL [40].RESERVED[23:13]	11
0xBB0050EC	FC_PORT_ACT_CTRL [40].ALLOW_PAGE_CNT[12:0]	13
0xBB0050F0	FC_PORT_ACT_CTRL [41].RESERVED[31:25]	7
0xBB0050F0	FC_PORT_ACT_CTRL [41].ACT[24:24]	1
0xBB0050F0	FC_PORT_ACT_CTRL [41].RESERVED[23:13]	11
0xBB0050F0	FC_PORT_ACT_CTRL [41].ALLOW_PAGE_CNT[12:0]	13
0xBB0050F4	FC_PORT_ACT_CTRL [42].RESERVED[31:25]	7
0xBB0050F4	FC_PORT_ACT_CTRL [42].ACT[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0050F4	FC_PORT_ACT_CTRL [42].RESERVED[23:13]	11
0xBB0050F4	FC_PORT_ACT_CTRL [42].ALLOW_PAGE_CNT[12:0]	13
0xBB0050F8	FC_PORT_ACT_CTRL [43].RESERVED[31:25]	7
0xBB0050F8	FC_PORT_ACT_CTRL [43].ACT[24:24]	1
0xBB0050F8	FC_PORT_ACT_CTRL [43].RESERVED[23:13]	11
0xBB0050F8	FC_PORT_ACT_CTRL [43].ALLOW_PAGE_CNT[12:0]	13
0xBB0050FC	FC_PORT_ACT_CTRL [44].RESERVED[31:25]	7
0xBB0050FC	FC_PORT_ACT_CTRL [44].ACT[24:24]	1
0xBB0050FC	FC_PORT_ACT_CTRL [44].RESERVED[23:13]	11
0xBB0050FC	FC_PORT_ACT_CTRL [44].ALLOW_PAGE_CNT[12:0]	13
0xBB005100	FC_PORT_ACT_CTRL [45].RESERVED[31:25]	7
0xBB005100	FC_PORT_ACT_CTRL [45].ACT[24:24]	1
0xBB005100	FC_PORT_ACT_CTRL [45].RESERVED[23:13]	11
0xBB005100	FC_PORT_ACT_CTRL [45].ALLOW_PAGE_CNT[12:0]	13
0xBB005104	FC_PORT_ACT_CTRL [46].RESERVED[31:25]	7
0xBB005104	FC_PORT_ACT_CTRL [46].ACT[24:24]	1
0xBB005104	FC_PORT_ACT_CTRL [46].RESERVED[23:13]	11
0xBB005104	FC_PORT_ACT_CTRL [46].ALLOW_PAGE_CNT[12:0]	13
0xBB005108	FC_PORT_ACT_CTRL [47].RESERVED[31:25]	7
0xBB005108	FC_PORT_ACT_CTRL [47].ACT[24:24]	1
0xBB005108	FC_PORT_ACT_CTRL [47].RESERVED[23:13]	11
0xBB005108	FC_PORT_ACT_CTRL [47].ALLOW_PAGE_CNT[12:0]	13
0xBB00510C	FC_PORT_ACT_CTRL [48].RESERVED[31:25]	7
0xBB00510C	FC_PORT_ACT_CTRL [48].ACT[24:24]	1
0xBB00510C	FC_PORT_ACT_CTRL [48].RESERVED[23:13]	11
0xBB00510C	FC_PORT_ACT_CTRL [48].ALLOW_PAGE_CNT[12:0]	13
0xBB005110	FC_PORT_ACT_CTRL [49].RESERVED[31:25]	7
0xBB005110	FC_PORT_ACT_CTRL [49].ACT[24:24]	1
0xBB005110	FC_PORT_ACT_CTRL [49].RESERVED[23:13]	11
0xBB005110	FC_PORT_ACT_CTRL [49].ALLOW_PAGE_CNT[12:0]	13
0xBB005114	FC_PORT_ACT_CTRL [50].RESERVED[31:25]	7
0xBB005114	FC_PORT_ACT_CTRL [50].ACT[24:24]	1
0xBB005114	FC_PORT_ACT_CTRL [50].RESERVED[23:13]	11
0xBB005114	FC_PORT_ACT_CTRL [50].ALLOW_PAGE_CNT[12:0]	13
0xBB005118	FC_PORT_ACT_CTRL [51].RESERVED[31:25]	7
0xBB005118	FC_PORT_ACT_CTRL [51].ACT[24:24]	1
0xBB005118	FC_PORT_ACT_CTRL [51].RESERVED[23:13]	11
0xBB005118	FC_PORT_ACT_CTRL [51].ALLOW_PAGE_CNT[12:0]	13
0xBB00511C	FC_PORT_ACT_CTRL [52].RESERVED[31:25]	7
0xBB00511C	FC_PORT_ACT_CTRL [52].ACT[24:24]	1
0xBB00511C	FC_PORT_ACT_CTRL [52].RESERVED[23:13]	11
0xBB00511C	FC_PORT_ACT_CTRL [52].ALLOW_PAGE_CNT[12:0]	13
0xBB005120	FC_PORT_ACT_CTRL [53].RESERVED[31:25]	7
0xBB005120	FC_PORT_ACT_CTRL [53].ACT[24:24]	1
0xBB005120	FC_PORT_ACT_CTRL [53].RESERVED[23:13]	11
0xBB005120	FC_PORT_ACT_CTRL [53].ALLOW_PAGE_CNT[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005124	FC_PORT_ACT_CTRL [54].RESERVED[31:25]	7
0xBB005124	FC_PORT_ACT_CTRL [54].ACT[24:24]	1
0xBB005124	FC_PORT_ACT_CTRL [54].RESERVED[23:13]	11
0xBB005124	FC_PORT_ACT_CTRL [54].ALLOW_PAGE_CNT[12:0]	13
0xBB005128	FC_PORT_ACT_CTRL [55].RESERVED[31:25]	7
0xBB005128	FC_PORT_ACT_CTRL [55].ACT[24:24]	1
0xBB005128	FC_PORT_ACT_CTRL [55].RESERVED[23:13]	11
0xBB005128	FC_PORT_ACT_CTRL [55].ALLOW_PAGE_CNT[12:0]	13
0xBB00512C	FC_PORT_ACT_CTRL [56].RESERVED[31:25]	7
0xBB00512C	FC_PORT_ACT_CTRL [56].ACT[24:24]	1
0xBB00512C	FC_PORT_ACT_CTRL [56].RESERVED[23:13]	11
0xBB00512C	FC_PORT_ACT_CTRL [56].ALLOW_PAGE_CNT[12:0]	13
0xBB00A800	FC_PORT_EGR_DROP_CTRL [0].RESERVED[31:3]	29
0xBB00A800	FC_PORT_EGR_DROP_CTRL [0].EGR_DROP_ALGO[2:2]	1
0xBB00A800	FC_PORT_EGR_DROP_CTRL [0].REF_RXCNGST[1:1]	1
0xBB00A800	FC_PORT_EGR_DROP_CTRL [0].HOL_PRVNT_EN[0:0]	1
0xBB00A804	FC_PORT_EGR_DROP_CTRL [1].RESERVED[31:3]	29
0xBB00A804	FC_PORT_EGR_DROP_CTRL [1].EGR_DROP_ALGO[2:2]	1
0xBB00A804	FC_PORT_EGR_DROP_CTRL [1].REF_RXCNGST[1:1]	1
0xBB00A804	FC_PORT_EGR_DROP_CTRL [1].HOL_PRVNT_EN[0:0]	1
0xBB00A808	FC_PORT_EGR_DROP_CTRL [2].RESERVED[31:3]	29
0xBB00A808	FC_PORT_EGR_DROP_CTRL [2].EGR_DROP_ALGO[2:2]	1
0xBB00A808	FC_PORT_EGR_DROP_CTRL [2].REF_RXCNGST[1:1]	1
0xBB00A808	FC_PORT_EGR_DROP_CTRL [2].HOL_PRVNT_EN[0:0]	1
0xBB00A80C	FC_PORT_EGR_DROP_CTRL [3].RESERVED[31:3]	29
0xBB00A80C	FC_PORT_EGR_DROP_CTRL [3].EGR_DROP_ALGO[2:2]	1
0xBB00A80C	FC_PORT_EGR_DROP_CTRL [3].REF_RXCNGST[1:1]	1
0xBB00A80C	FC_PORT_EGR_DROP_CTRL [3].HOL_PRVNT_EN[0:0]	1
0xBB00A810	FC_PORT_EGR_DROP_CTRL [4].RESERVED[31:3]	29
0xBB00A810	FC_PORT_EGR_DROP_CTRL [4].EGR_DROP_ALGO[2:2]	1
0xBB00A810	FC_PORT_EGR_DROP_CTRL [4].REF_RXCNGST[1:1]	1
0xBB00A810	FC_PORT_EGR_DROP_CTRL [4].HOL_PRVNT_EN[0:0]	1
0xBB00A814	FC_PORT_EGR_DROP_CTRL [5].RESERVED[31:3]	29
0xBB00A814	FC_PORT_EGR_DROP_CTRL [5].EGR_DROP_ALGO[2:2]	1
0xBB00A814	FC_PORT_EGR_DROP_CTRL [5].REF_RXCNGST[1:1]	1
0xBB00A814	FC_PORT_EGR_DROP_CTRL [5].HOL_PRVNT_EN[0:0]	1
0xBB00A818	FC_PORT_EGR_DROP_CTRL [6].RESERVED[31:3]	29
0xBB00A818	FC_PORT_EGR_DROP_CTRL [6].EGR_DROP_ALGO[2:2]	1
0xBB00A818	FC_PORT_EGR_DROP_CTRL [6].REF_RXCNGST[1:1]	1
0xBB00A818	FC_PORT_EGR_DROP_CTRL [6].HOL_PRVNT_EN[0:0]	1
0xBB00A81C	FC_PORT_EGR_DROP_CTRL [7].RESERVED[31:3]	29
0xBB00A81C	FC_PORT_EGR_DROP_CTRL [7].EGR_DROP_ALGO[2:2]	1
0xBB00A81C	FC_PORT_EGR_DROP_CTRL [7].REF_RXCNGST[1:1]	1
0xBB00A81C	FC_PORT_EGR_DROP_CTRL [7].HOL_PRVNT_EN[0:0]	1
0xBB00A820	FC_PORT_EGR_DROP_CTRL [8].RESERVED[31:3]	29
0xBB00A820	FC_PORT_EGR_DROP_CTRL [8].EGR_DROP_ALGO[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A820	FC_PORT_EGR_DROP_CTRL [8].REF_RXCNGST[1:1]	1
0xBB00A820	FC_PORT_EGR_DROP_CTRL [8].HOL_PRVNT_EN[0:0]	1
0xBB00A824	FC_PORT_EGR_DROP_CTRL [9].RESERVED[31:3]	29
0xBB00A824	FC_PORT_EGR_DROP_CTRL [9].EGR_DROP_ALGO[2:2]	1
0xBB00A824	FC_PORT_EGR_DROP_CTRL [9].REF_RXCNGST[1:1]	1
0xBB00A824	FC_PORT_EGR_DROP_CTRL [9].HOL_PRVNT_EN[0:0]	1
0xBB00A828	FC_PORT_EGR_DROP_CTRL [10].RESERVED[31:3]	29
0xBB00A828	FC_PORT_EGR_DROP_CTRL [10].EGR_DROP_ALGO[2:2]	1
0xBB00A828	FC_PORT_EGR_DROP_CTRL [10].REF_RXCNGST[1:1]	1
0xBB00A828	FC_PORT_EGR_DROP_CTRL [10].HOL_PRVNT_EN[0:0]	1
0xBB00A82C	FC_PORT_EGR_DROP_CTRL [11].RESERVED[31:3]	29
0xBB00A82C	FC_PORT_EGR_DROP_CTRL [11].EGR_DROP_ALGO[2:2]	1
0xBB00A82C	FC_PORT_EGR_DROP_CTRL [11].REF_RXCNGST[1:1]	1
0xBB00A82C	FC_PORT_EGR_DROP_CTRL [11].HOL_PRVNT_EN[0:0]	1
0xBB00A830	FC_PORT_EGR_DROP_CTRL [12].RESERVED[31:3]	29
0xBB00A830	FC_PORT_EGR_DROP_CTRL [12].EGR_DROP_ALGO[2:2]	1
0xBB00A830	FC_PORT_EGR_DROP_CTRL [12].REF_RXCNGST[1:1]	1
0xBB00A830	FC_PORT_EGR_DROP_CTRL [12].HOL_PRVNT_EN[0:0]	1
0xBB00A834	FC_PORT_EGR_DROP_CTRL [13].RESERVED[31:3]	29
0xBB00A834	FC_PORT_EGR_DROP_CTRL [13].EGR_DROP_ALGO[2:2]	1
0xBB00A834	FC_PORT_EGR_DROP_CTRL [13].REF_RXCNGST[1:1]	1
0xBB00A834	FC_PORT_EGR_DROP_CTRL [13].HOL_PRVNT_EN[0:0]	1
0xBB00A838	FC_PORT_EGR_DROP_CTRL [14].RESERVED[31:3]	29
0xBB00A838	FC_PORT_EGR_DROP_CTRL [14].EGR_DROP_ALGO[2:2]	1
0xBB00A838	FC_PORT_EGR_DROP_CTRL [14].REF_RXCNGST[1:1]	1
0xBB00A838	FC_PORT_EGR_DROP_CTRL [14].HOL_PRVNT_EN[0:0]	1
0xBB00A83C	FC_PORT_EGR_DROP_CTRL [15].RESERVED[31:3]	29
0xBB00A83C	FC_PORT_EGR_DROP_CTRL [15].EGR_DROP_ALGO[2:2]	1
0xBB00A83C	FC_PORT_EGR_DROP_CTRL [15].REF_RXCNGST[1:1]	1
0xBB00A83C	FC_PORT_EGR_DROP_CTRL [15].HOL_PRVNT_EN[0:0]	1
0xBB00A840	FC_PORT_EGR_DROP_CTRL [16].RESERVED[31:3]	29
0xBB00A840	FC_PORT_EGR_DROP_CTRL [16].EGR_DROP_ALGO[2:2]	1
0xBB00A840	FC_PORT_EGR_DROP_CTRL [16].REF_RXCNGST[1:1]	1
0xBB00A840	FC_PORT_EGR_DROP_CTRL [16].HOL_PRVNT_EN[0:0]	1
0xBB00A844	FC_PORT_EGR_DROP_CTRL [17].RESERVED[31:3]	29
0xBB00A844	FC_PORT_EGR_DROP_CTRL [17].EGR_DROP_ALGO[2:2]	1
0xBB00A844	FC_PORT_EGR_DROP_CTRL [17].REF_RXCNGST[1:1]	1
0xBB00A844	FC_PORT_EGR_DROP_CTRL [17].HOL_PRVNT_EN[0:0]	1
0xBB00A848	FC_PORT_EGR_DROP_CTRL [18].RESERVED[31:3]	29
0xBB00A848	FC_PORT_EGR_DROP_CTRL [18].EGR_DROP_ALGO[2:2]	1
0xBB00A848	FC_PORT_EGR_DROP_CTRL [18].REF_RXCNGST[1:1]	1
0xBB00A848	FC_PORT_EGR_DROP_CTRL [18].HOL_PRVNT_EN[0:0]	1
0xBB00A84C	FC_PORT_EGR_DROP_CTRL [19].RESERVED[31:3]	29
0xBB00A84C	FC_PORT_EGR_DROP_CTRL [19].EGR_DROP_ALGO[2:2]	1
0xBB00A84C	FC_PORT_EGR_DROP_CTRL [19].REF_RXCNGST[1:1]	1
0xBB00A84C	FC_PORT_EGR_DROP_CTRL [19].HOL_PRVNT_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A850	FC_PORT_EGR_DROP_CTRL [20].RESERVED[31:3]	29
0xBB00A850	FC_PORT_EGR_DROP_CTRL [20].EGR_DROP_ALGO[2:2]	1
0xBB00A850	FC_PORT_EGR_DROP_CTRL [20].REF_RXCNGST[1:1]	1
0xBB00A850	FC_PORT_EGR_DROP_CTRL [20].HOL_PRVNT_EN[0:0]	1
0xBB00A854	FC_PORT_EGR_DROP_CTRL [21].RESERVED[31:3]	29
0xBB00A854	FC_PORT_EGR_DROP_CTRL [21].EGR_DROP_ALGO[2:2]	1
0xBB00A854	FC_PORT_EGR_DROP_CTRL [21].REF_RXCNGST[1:1]	1
0xBB00A854	FC_PORT_EGR_DROP_CTRL [21].HOL_PRVNT_EN[0:0]	1
0xBB00A858	FC_PORT_EGR_DROP_CTRL [22].RESERVED[31:3]	29
0xBB00A858	FC_PORT_EGR_DROP_CTRL [22].EGR_DROP_ALGO[2:2]	1
0xBB00A858	FC_PORT_EGR_DROP_CTRL [22].REF_RXCNGST[1:1]	1
0xBB00A858	FC_PORT_EGR_DROP_CTRL [22].HOL_PRVNT_EN[0:0]	1
0xBB00A85C	FC_PORT_EGR_DROP_CTRL [23].RESERVED[31:3]	29
0xBB00A85C	FC_PORT_EGR_DROP_CTRL [23].EGR_DROP_ALGO[2:2]	1
0xBB00A85C	FC_PORT_EGR_DROP_CTRL [23].REF_RXCNGST[1:1]	1
0xBB00A85C	FC_PORT_EGR_DROP_CTRL [23].HOL_PRVNT_EN[0:0]	1
0xBB00A860	FC_PORT_EGR_DROP_CTRL [24].RESERVED[31:3]	29
0xBB00A860	FC_PORT_EGR_DROP_CTRL [24].EGR_DROP_ALGO[2:2]	1
0xBB00A860	FC_PORT_EGR_DROP_CTRL [24].REF_RXCNGST[1:1]	1
0xBB00A860	FC_PORT_EGR_DROP_CTRL [24].HOL_PRVNT_EN[0:0]	1
0xBB00A864	FC_PORT_EGR_DROP_CTRL [25].RESERVED[31:3]	29
0xBB00A864	FC_PORT_EGR_DROP_CTRL [25].EGR_DROP_ALGO[2:2]	1
0xBB00A864	FC_PORT_EGR_DROP_CTRL [25].REF_RXCNGST[1:1]	1
0xBB00A864	FC_PORT_EGR_DROP_CTRL [25].HOL_PRVNT_EN[0:0]	1
0xBB00A868	FC_PORT_EGR_DROP_CTRL [26].RESERVED[31:3]	29
0xBB00A868	FC_PORT_EGR_DROP_CTRL [26].EGR_DROP_ALGO[2:2]	1
0xBB00A868	FC_PORT_EGR_DROP_CTRL [26].REF_RXCNGST[1:1]	1
0xBB00A868	FC_PORT_EGR_DROP_CTRL [26].HOL_PRVNT_EN[0:0]	1
0xBB00A86C	FC_PORT_EGR_DROP_CTRL [27].RESERVED[31:3]	29
0xBB00A86C	FC_PORT_EGR_DROP_CTRL [27].EGR_DROP_ALGO[2:2]	1
0xBB00A86C	FC_PORT_EGR_DROP_CTRL [27].REF_RXCNGST[1:1]	1
0xBB00A86C	FC_PORT_EGR_DROP_CTRL [27].HOL_PRVNT_EN[0:0]	1
0xBB00A870	FC_PORT_EGR_DROP_CTRL [28].RESERVED[31:3]	29
0xBB00A870	FC_PORT_EGR_DROP_CTRL [28].EGR_DROP_ALGO[2:2]	1
0xBB00A870	FC_PORT_EGR_DROP_CTRL [28].REF_RXCNGST[1:1]	1
0xBB00A870	FC_PORT_EGR_DROP_CTRL [28].HOL_PRVNT_EN[0:0]	1
0xBB00A874	FC_PORT_EGR_DROP_CTRL [29].RESERVED[31:3]	29
0xBB00A874	FC_PORT_EGR_DROP_CTRL [29].EGR_DROP_ALGO[2:2]	1
0xBB00A874	FC_PORT_EGR_DROP_CTRL [29].REF_RXCNGST[1:1]	1
0xBB00A874	FC_PORT_EGR_DROP_CTRL [29].HOL_PRVNT_EN[0:0]	1
0xBB00A878	FC_PORT_EGR_DROP_CTRL [30].RESERVED[31:3]	29
0xBB00A878	FC_PORT_EGR_DROP_CTRL [30].EGR_DROP_ALGO[2:2]	1
0xBB00A878	FC_PORT_EGR_DROP_CTRL [30].REF_RXCNGST[1:1]	1
0xBB00A878	FC_PORT_EGR_DROP_CTRL [30].HOL_PRVNT_EN[0:0]	1
0xBB00A87C	FC_PORT_EGR_DROP_CTRL [31].RESERVED[31:3]	29
0xBB00A87C	FC_PORT_EGR_DROP_CTRL [31].EGR_DROP_ALGO[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A87C	FC_PORT_EGR_DROP_CTRL [31].REF_RXCNGST[1:1]	1
0xBB00A87C	FC_PORT_EGR_DROP_CTRL [31].HOL_PRVNT_EN[0:0]	1
0xBB00A880	FC_PORT_EGR_DROP_CTRL [32].RESERVED[31:3]	29
0xBB00A880	FC_PORT_EGR_DROP_CTRL [32].EGR_DROP_ALGO[2:2]	1
0xBB00A880	FC_PORT_EGR_DROP_CTRL [32].REF_RXCNGST[1:1]	1
0xBB00A880	FC_PORT_EGR_DROP_CTRL [32].HOL_PRVNT_EN[0:0]	1
0xBB00A884	FC_PORT_EGR_DROP_CTRL [33].RESERVED[31:3]	29
0xBB00A884	FC_PORT_EGR_DROP_CTRL [33].EGR_DROP_ALGO[2:2]	1
0xBB00A884	FC_PORT_EGR_DROP_CTRL [33].REF_RXCNGST[1:1]	1
0xBB00A884	FC_PORT_EGR_DROP_CTRL [33].HOL_PRVNT_EN[0:0]	1
0xBB00A888	FC_PORT_EGR_DROP_CTRL [34].RESERVED[31:3]	29
0xBB00A888	FC_PORT_EGR_DROP_CTRL [34].EGR_DROP_ALGO[2:2]	1
0xBB00A888	FC_PORT_EGR_DROP_CTRL [34].REF_RXCNGST[1:1]	1
0xBB00A888	FC_PORT_EGR_DROP_CTRL [34].HOL_PRVNT_EN[0:0]	1
0xBB00A88C	FC_PORT_EGR_DROP_CTRL [35].RESERVED[31:3]	29
0xBB00A88C	FC_PORT_EGR_DROP_CTRL [35].EGR_DROP_ALGO[2:2]	1
0xBB00A88C	FC_PORT_EGR_DROP_CTRL [35].REF_RXCNGST[1:1]	1
0xBB00A88C	FC_PORT_EGR_DROP_CTRL [35].HOL_PRVNT_EN[0:0]	1
0xBB00A890	FC_PORT_EGR_DROP_CTRL [36].RESERVED[31:3]	29
0xBB00A890	FC_PORT_EGR_DROP_CTRL [36].EGR_DROP_ALGO[2:2]	1
0xBB00A890	FC_PORT_EGR_DROP_CTRL [36].REF_RXCNGST[1:1]	1
0xBB00A890	FC_PORT_EGR_DROP_CTRL [36].HOL_PRVNT_EN[0:0]	1
0xBB00A894	FC_PORT_EGR_DROP_CTRL [37].RESERVED[31:3]	29
0xBB00A894	FC_PORT_EGR_DROP_CTRL [37].EGR_DROP_ALGO[2:2]	1
0xBB00A894	FC_PORT_EGR_DROP_CTRL [37].REF_RXCNGST[1:1]	1
0xBB00A894	FC_PORT_EGR_DROP_CTRL [37].HOL_PRVNT_EN[0:0]	1
0xBB00A898	FC_PORT_EGR_DROP_CTRL [38].RESERVED[31:3]	29
0xBB00A898	FC_PORT_EGR_DROP_CTRL [38].EGR_DROP_ALGO[2:2]	1
0xBB00A898	FC_PORT_EGR_DROP_CTRL [38].REF_RXCNGST[1:1]	1
0xBB00A898	FC_PORT_EGR_DROP_CTRL [38].HOL_PRVNT_EN[0:0]	1
0xBB00A89C	FC_PORT_EGR_DROP_CTRL [39].RESERVED[31:3]	29
0xBB00A89C	FC_PORT_EGR_DROP_CTRL [39].EGR_DROP_ALGO[2:2]	1
0xBB00A89C	FC_PORT_EGR_DROP_CTRL [39].REF_RXCNGST[1:1]	1
0xBB00A89C	FC_PORT_EGR_DROP_CTRL [39].HOL_PRVNT_EN[0:0]	1
0xBB00A8A0	FC_PORT_EGR_DROP_CTRL [40].RESERVED[31:3]	29
0xBB00A8A0	FC_PORT_EGR_DROP_CTRL [40].EGR_DROP_ALGO[2:2]	1
0xBB00A8A0	FC_PORT_EGR_DROP_CTRL [40].REF_RXCNGST[1:1]	1
0xBB00A8A0	FC_PORT_EGR_DROP_CTRL [40].HOL_PRVNT_EN[0:0]	1
0xBB00A8A4	FC_PORT_EGR_DROP_CTRL [41].RESERVED[31:3]	29
0xBB00A8A4	FC_PORT_EGR_DROP_CTRL [41].EGR_DROP_ALGO[2:2]	1
0xBB00A8A4	FC_PORT_EGR_DROP_CTRL [41].REF_RXCNGST[1:1]	1
0xBB00A8A4	FC_PORT_EGR_DROP_CTRL [41].HOL_PRVNT_EN[0:0]	1
0xBB00A8A8	FC_PORT_EGR_DROP_CTRL [42].RESERVED[31:3]	29
0xBB00A8A8	FC_PORT_EGR_DROP_CTRL [42].EGR_DROP_ALGO[2:2]	1
0xBB00A8A8	FC_PORT_EGR_DROP_CTRL [42].REF_RXCNGST[1:1]	1
0xBB00A8A8	FC_PORT_EGR_DROP_CTRL [42].HOL_PRVNT_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A8AC	FC_PORT_EGR_DROP_CTRL [43].RESERVED[31:3]	29
0xBB00A8AC	FC_PORT_EGR_DROP_CTRL [43].EGR_DROP_ALGO[2:2]	1
0xBB00A8AC	FC_PORT_EGR_DROP_CTRL [43].REF_RXCNGST[1:1]	1
0xBB00A8AC	FC_PORT_EGR_DROP_CTRL [43].HOL_PRVNT_EN[0:0]	1
0xBB00A8B0	FC_PORT_EGR_DROP_CTRL [44].RESERVED[31:3]	29
0xBB00A8B0	FC_PORT_EGR_DROP_CTRL [44].EGR_DROP_ALGO[2:2]	1
0xBB00A8B0	FC_PORT_EGR_DROP_CTRL [44].REF_RXCNGST[1:1]	1
0xBB00A8B0	FC_PORT_EGR_DROP_CTRL [44].HOL_PRVNT_EN[0:0]	1
0xBB00A8B4	FC_PORT_EGR_DROP_CTRL [45].RESERVED[31:3]	29
0xBB00A8B4	FC_PORT_EGR_DROP_CTRL [45].EGR_DROP_ALGO[2:2]	1
0xBB00A8B4	FC_PORT_EGR_DROP_CTRL [45].REF_RXCNGST[1:1]	1
0xBB00A8B4	FC_PORT_EGR_DROP_CTRL [45].HOL_PRVNT_EN[0:0]	1
0xBB00A8B8	FC_PORT_EGR_DROP_CTRL [46].RESERVED[31:3]	29
0xBB00A8B8	FC_PORT_EGR_DROP_CTRL [46].EGR_DROP_ALGO[2:2]	1
0xBB00A8B8	FC_PORT_EGR_DROP_CTRL [46].REF_RXCNGST[1:1]	1
0xBB00A8B8	FC_PORT_EGR_DROP_CTRL [46].HOL_PRVNT_EN[0:0]	1
0xBB00A8BC	FC_PORT_EGR_DROP_CTRL [47].RESERVED[31:3]	29
0xBB00A8BC	FC_PORT_EGR_DROP_CTRL [47].EGR_DROP_ALGO[2:2]	1
0xBB00A8BC	FC_PORT_EGR_DROP_CTRL [47].REF_RXCNGST[1:1]	1
0xBB00A8BC	FC_PORT_EGR_DROP_CTRL [47].HOL_PRVNT_EN[0:0]	1
0xBB00A8C0	FC_PORT_EGR_DROP_CTRL [48].RESERVED[31:3]	29
0xBB00A8C0	FC_PORT_EGR_DROP_CTRL [48].EGR_DROP_ALGO[2:2]	1
0xBB00A8C0	FC_PORT_EGR_DROP_CTRL [48].REF_RXCNGST[1:1]	1
0xBB00A8C0	FC_PORT_EGR_DROP_CTRL [48].HOL_PRVNT_EN[0:0]	1
0xBB00A8C4	FC_PORT_EGR_DROP_CTRL [49].RESERVED[31:3]	29
0xBB00A8C4	FC_PORT_EGR_DROP_CTRL [49].EGR_DROP_ALGO[2:2]	1
0xBB00A8C4	FC_PORT_EGR_DROP_CTRL [49].REF_RXCNGST[1:1]	1
0xBB00A8C4	FC_PORT_EGR_DROP_CTRL [49].HOL_PRVNT_EN[0:0]	1
0xBB00A8C8	FC_PORT_EGR_DROP_CTRL [50].RESERVED[31:3]	29
0xBB00A8C8	FC_PORT_EGR_DROP_CTRL [50].EGR_DROP_ALGO[2:2]	1
0xBB00A8C8	FC_PORT_EGR_DROP_CTRL [50].REF_RXCNGST[1:1]	1
0xBB00A8C8	FC_PORT_EGR_DROP_CTRL [50].HOL_PRVNT_EN[0:0]	1
0xBB00A8CC	FC_PORT_EGR_DROP_CTRL [51].RESERVED[31:3]	29
0xBB00A8CC	FC_PORT_EGR_DROP_CTRL [51].EGR_DROP_ALGO[2:2]	1
0xBB00A8CC	FC_PORT_EGR_DROP_CTRL [51].REF_RXCNGST[1:1]	1
0xBB00A8CC	FC_PORT_EGR_DROP_CTRL [51].HOL_PRVNT_EN[0:0]	1
0xBB00A8D0	FC_PORT_EGR_DROP_CTRL [52].RESERVED[31:3]	29
0xBB00A8D0	FC_PORT_EGR_DROP_CTRL [52].EGR_DROP_ALGO[2:2]	1
0xBB00A8D0	FC_PORT_EGR_DROP_CTRL [52].REF_RXCNGST[1:1]	1
0xBB00A8D0	FC_PORT_EGR_DROP_CTRL [52].HOL_PRVNT_EN[0:0]	1
0xBB00A8D4	FC_PORT_EGR_DROP_CTRL [53].RESERVED[31:3]	29
0xBB00A8D4	FC_PORT_EGR_DROP_CTRL [53].EGR_DROP_ALGO[2:2]	1
0xBB00A8D4	FC_PORT_EGR_DROP_CTRL [53].REF_RXCNGST[1:1]	1
0xBB00A8D4	FC_PORT_EGR_DROP_CTRL [53].HOL_PRVNT_EN[0:0]	1
0xBB00A8D8	FC_PORT_EGR_DROP_CTRL [54].RESERVED[31:3]	29
0xBB00A8D8	FC_PORT_EGR_DROP_CTRL [54].EGR_DROP_ALGO[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A8D8	FC_PORT_EGR_DROP_CTRL [54].REF_RXCNGST[1:1]	1
0xBB00A8D8	FC_PORT_EGR_DROP_CTRL [54].HOL_PRVNT_EN[0:0]	1
0xBB00A8DC	FC_PORT_EGR_DROP_CTRL [55].RESERVED[31:3]	29
0xBB00A8DC	FC_PORT_EGR_DROP_CTRL [55].EGR_DROP_ALGO[2:2]	1
0xBB00A8DC	FC_PORT_EGR_DROP_CTRL [55].REF_RXCNGST[1:1]	1
0xBB00A8DC	FC_PORT_EGR_DROP_CTRL [55].HOL_PRVNT_EN[0:0]	1
0xBB00A8E0	FC_PORT_EGR_DROP_CTRL [56].RESERVED[31:3]	29
0xBB00A8E0	FC_PORT_EGR_DROP_CTRL [56].EGR_DROP_ALGO[2:2]	1
0xBB00A8E0	FC_PORT_EGR_DROP_CTRL [56].REF_RXCNGST[1:1]	1
0xBB00A8E0	FC_PORT_EGR_DROP_CTRL [56].HOL_PRVNT_EN[0:0]	1
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [0].IDX[1:0]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [1].IDX[3:2]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [2].IDX[5:4]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [3].IDX[7:6]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [4].IDX[9:8]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [5].IDX[11:10]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [6].IDX[13:12]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [7].IDX[15:14]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [8].IDX[17:16]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [9].IDX[19:18]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [10].IDX[21:20]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [11].IDX[23:22]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [12].IDX[25:24]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [13].IDX[27:26]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [14].IDX[29:28]	2
0xBB002728	FC_PORT_EGR_DROP_THR_SET_SEL [15].IDX[31:30]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [16].IDX[1:0]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [17].IDX[3:2]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [18].IDX[5:4]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [19].IDX[7:6]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [20].IDX[9:8]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [21].IDX[11:10]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [22].IDX[13:12]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [23].IDX[15:14]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [24].IDX[17:16]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [25].IDX[19:18]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [26].IDX[21:20]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [27].IDX[23:22]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [28].IDX[25:24]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [29].IDX[27:26]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [30].IDX[29:28]	2
0xBB00272C	FC_PORT_EGR_DROP_THR_SET_SEL [31].IDX[31:30]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [32].IDX[1:0]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [33].IDX[3:2]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [34].IDX[5:4]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [35].IDX[7:6]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [36].IDX[9:8]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [37].IDX[11:10]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [38].IDX[13:12]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [39].IDX[15:14]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [40].IDX[17:16]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [41].IDX[19:18]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [42].IDX[21:20]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [43].IDX[23:22]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [44].IDX[25:24]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [45].IDX[27:26]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [46].IDX[29:28]	2
0xBB002730	FC_PORT_EGR_DROP_THR_SET_SEL [47].IDX[31:30]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [48].IDX[1:0]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [49].IDX[3:2]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [50].IDX[5:4]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [51].IDX[7:6]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [52].IDX[9:8]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [53].IDX[11:10]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [54].IDX[13:12]	2
0xBB002734	FC_PORT_EGR_DROP_THR_SET_SEL [55].IDX[15:14]	2
0xBB00517C	FC_PORT_FCOFF_HI_THR [0].RESERVED[31:29]	3
0xBB00517C	FC_PORT_FCOFF_HI_THR [0].ON[28:16]	13
0xBB00517C	FC_PORT_FCOFF_HI_THR [0].RESERVED[15:13]	3
0xBB00517C	FC_PORT_FCOFF_HI_THR [0].OFF[12:0]	13
0xBB005180	FC_PORT_FCOFF_HI_THR [1].RESERVED[31:29]	3
0xBB005180	FC_PORT_FCOFF_HI_THR [1].ON[28:16]	13
0xBB005180	FC_PORT_FCOFF_HI_THR [1].RESERVED[15:13]	3
0xBB005180	FC_PORT_FCOFF_HI_THR [1].OFF[12:0]	13
0xBB005184	FC_PORT_FCOFF_HI_THR [2].RESERVED[31:29]	3
0xBB005184	FC_PORT_FCOFF_HI_THR [2].ON[28:16]	13
0xBB005184	FC_PORT_FCOFF_HI_THR [2].RESERVED[15:13]	3
0xBB005184	FC_PORT_FCOFF_HI_THR [2].OFF[12:0]	13
0xBB005188	FC_PORT_FCOFF_HI_THR [3].RESERVED[31:29]	3
0xBB005188	FC_PORT_FCOFF_HI_THR [3].ON[28:16]	13
0xBB005188	FC_PORT_FCOFF_HI_THR [3].RESERVED[15:13]	3
0xBB005188	FC_PORT_FCOFF_HI_THR [3].OFF[12:0]	13
0xBB00518C	FC_PORT_FCOFF_LO_THR [0].RESERVED[31:29]	3
0xBB00518C	FC_PORT_FCOFF_LO_THR [0].ON[28:16]	13
0xBB00518C	FC_PORT_FCOFF_LO_THR [0].RESERVED[15:13]	3
0xBB00518C	FC_PORT_FCOFF_LO_THR [0].OFF[12:0]	13
0xBB005190	FC_PORT_FCOFF_LO_THR [1].RESERVED[31:29]	3
0xBB005190	FC_PORT_FCOFF_LO_THR [1].ON[28:16]	13
0xBB005190	FC_PORT_FCOFF_LO_THR [1].RESERVED[15:13]	3
0xBB005190	FC_PORT_FCOFF_LO_THR [1].OFF[12:0]	13
0xBB005194	FC_PORT_FCOFF_LO_THR [2].RESERVED[31:29]	3
0xBB005194	FC_PORT_FCOFF_LO_THR [2].ON[28:16]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005194	FC_PORT_FCOFF_LO_THR [2].RESERVED[15:13]	3
0xBB005194	FC_PORT_FCOFF_LO_THR [2].OFF[12:0]	13
0xBB005198	FC_PORT_FCOFF_LO_THR [3].RESERVED[31:29]	3
0xBB005198	FC_PORT_FCOFF_LO_THR [3].ON[28:16]	13
0xBB005198	FC_PORT_FCOFF_LO_THR [3].RESERVED[15:13]	3
0xBB005198	FC_PORT_FCOFF_LO_THR [3].OFF[12:0]	13
0xBB00519C	FC_PORT_GUAR_THR [0].RESERVED[31:13]	19
0xBB00519C	FC_PORT_GUAR_THR [0].THR[12:0]	13
0xBB0051A0	FC_PORT_GUAR_THR [1].RESERVED[31:13]	19
0xBB0051A0	FC_PORT_GUAR_THR [1].THR[12:0]	13
0xBB0051A4	FC_PORT_GUAR_THR [2].RESERVED[31:13]	19
0xBB0051A4	FC_PORT_GUAR_THR [2].THR[12:0]	13
0xBB0051A8	FC_PORT_GUAR_THR [3].RESERVED[31:13]	19
0xBB0051A8	FC_PORT_GUAR_THR [3].THR[12:0]	13
0xBB00515C	FC_PORT_HI_THR [0].RESERVED[31:29]	3
0xBB00515C	FC_PORT_HI_THR [0].ON[28:16]	13
0xBB00515C	FC_PORT_HI_THR [0].RESERVED[15:13]	3
0xBB00515C	FC_PORT_HI_THR [0].OFF[12:0]	13
0xBB005160	FC_PORT_HI_THR [1].RESERVED[31:29]	3
0xBB005160	FC_PORT_HI_THR [1].ON[28:16]	13
0xBB005160	FC_PORT_HI_THR [1].RESERVED[15:13]	3
0xBB005160	FC_PORT_HI_THR [1].OFF[12:0]	13
0xBB005164	FC_PORT_HI_THR [2].RESERVED[31:29]	3
0xBB005164	FC_PORT_HI_THR [2].ON[28:16]	13
0xBB005164	FC_PORT_HI_THR [2].RESERVED[15:13]	3
0xBB005164	FC_PORT_HI_THR [2].OFF[12:0]	13
0xBB005168	FC_PORT_HI_THR [3].RESERVED[31:29]	3
0xBB005168	FC_PORT_HI_THR [3].ON[28:16]	13
0xBB005168	FC_PORT_HI_THR [3].RESERVED[15:13]	3
0xBB005168	FC_PORT_HI_THR [3].OFF[12:0]	13
0xBB00516C	FC_PORT_LO_THR [0].RESERVED[31:29]	3
0xBB00516C	FC_PORT_LO_THR [0].ON[28:16]	13
0xBB00516C	FC_PORT_LO_THR [0].RESERVED[15:13]	3
0xBB00516C	FC_PORT_LO_THR [0].OFF[12:0]	13
0xBB005170	FC_PORT_LO_THR [1].RESERVED[31:29]	3
0xBB005170	FC_PORT_LO_THR [1].ON[28:16]	13
0xBB005170	FC_PORT_LO_THR [1].RESERVED[15:13]	3
0xBB005170	FC_PORT_LO_THR [1].OFF[12:0]	13
0xBB005174	FC_PORT_LO_THR [2].RESERVED[31:29]	3
0xBB005174	FC_PORT_LO_THR [2].ON[28:16]	13
0xBB005174	FC_PORT_LO_THR [2].RESERVED[15:13]	3
0xBB005174	FC_PORT_LO_THR [2].OFF[12:0]	13
0xBB005178	FC_PORT_LO_THR [3].RESERVED[31:29]	3
0xBB005178	FC_PORT_LO_THR [3].ON[28:16]	13
0xBB005178	FC_PORT_LO_THR [3].RESERVED[15:13]	3
0xBB005178	FC_PORT_LO_THR [3].OFF[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0051C4	FC_PORT_PAGE_CNT [0].RESERVED[31:29]	3
0xBB0051C4	FC_PORT_PAGE_CNT [0].MAX_CNT[28:16]	13
0xBB0051C4	FC_PORT_PAGE_CNT [0].RESERVED[15:13]	3
0xBB0051C4	FC_PORT_PAGE_CNT [0].CNT[12:0]	13
0xBB0051C8	FC_PORT_PAGE_CNT [1].RESERVED[31:29]	3
0xBB0051C8	FC_PORT_PAGE_CNT [1].MAX_CNT[28:16]	13
0xBB0051C8	FC_PORT_PAGE_CNT [1].RESERVED[15:13]	3
0xBB0051C8	FC_PORT_PAGE_CNT [1].CNT[12:0]	13
0xBB0051CC	FC_PORT_PAGE_CNT [2].RESERVED[31:29]	3
0xBB0051CC	FC_PORT_PAGE_CNT [2].MAX_CNT[28:16]	13
0xBB0051CC	FC_PORT_PAGE_CNT [2].RESERVED[15:13]	3
0xBB0051CC	FC_PORT_PAGE_CNT [2].CNT[12:0]	13
0xBB0051D0	FC_PORT_PAGE_CNT [3].RESERVED[31:29]	3
0xBB0051D0	FC_PORT_PAGE_CNT [3].MAX_CNT[28:16]	13
0xBB0051D0	FC_PORT_PAGE_CNT [3].RESERVED[15:13]	3
0xBB0051D0	FC_PORT_PAGE_CNT [3].CNT[12:0]	13
0xBB0051D4	FC_PORT_PAGE_CNT [4].RESERVED[31:29]	3
0xBB0051D4	FC_PORT_PAGE_CNT [4].MAX_CNT[28:16]	13
0xBB0051D4	FC_PORT_PAGE_CNT [4].RESERVED[15:13]	3
0xBB0051D4	FC_PORT_PAGE_CNT [4].CNT[12:0]	13
0xBB0051D8	FC_PORT_PAGE_CNT [5].RESERVED[31:29]	3
0xBB0051D8	FC_PORT_PAGE_CNT [5].MAX_CNT[28:16]	13
0xBB0051D8	FC_PORT_PAGE_CNT [5].RESERVED[15:13]	3
0xBB0051D8	FC_PORT_PAGE_CNT [5].CNT[12:0]	13
0xBB0051DC	FC_PORT_PAGE_CNT [6].RESERVED[31:29]	3
0xBB0051DC	FC_PORT_PAGE_CNT [6].MAX_CNT[28:16]	13
0xBB0051DC	FC_PORT_PAGE_CNT [6].RESERVED[15:13]	3
0xBB0051DC	FC_PORT_PAGE_CNT [6].CNT[12:0]	13
0xBB0051E0	FC_PORT_PAGE_CNT [7].RESERVED[31:29]	3
0xBB0051E0	FC_PORT_PAGE_CNT [7].MAX_CNT[28:16]	13
0xBB0051E0	FC_PORT_PAGE_CNT [7].RESERVED[15:13]	3
0xBB0051E0	FC_PORT_PAGE_CNT [7].CNT[12:0]	13
0xBB0051E4	FC_PORT_PAGE_CNT [8].RESERVED[31:29]	3
0xBB0051E4	FC_PORT_PAGE_CNT [8].MAX_CNT[28:16]	13
0xBB0051E4	FC_PORT_PAGE_CNT [8].RESERVED[15:13]	3
0xBB0051E4	FC_PORT_PAGE_CNT [8].CNT[12:0]	13
0xBB0051E8	FC_PORT_PAGE_CNT [9].RESERVED[31:29]	3
0xBB0051E8	FC_PORT_PAGE_CNT [9].MAX_CNT[28:16]	13
0xBB0051E8	FC_PORT_PAGE_CNT [9].RESERVED[15:13]	3
0xBB0051E8	FC_PORT_PAGE_CNT [9].CNT[12:0]	13
0xBB0051EC	FC_PORT_PAGE_CNT [10].RESERVED[31:29]	3
0xBB0051EC	FC_PORT_PAGE_CNT [10].MAX_CNT[28:16]	13
0xBB0051EC	FC_PORT_PAGE_CNT [10].RESERVED[15:13]	3
0xBB0051EC	FC_PORT_PAGE_CNT [10].CNT[12:0]	13
0xBB0051F0	FC_PORT_PAGE_CNT [11].RESERVED[31:29]	3
0xBB0051F0	FC_PORT_PAGE_CNT [11].MAX_CNT[28:16]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0051F0	FC_PORT_PAGE_CNT [11].RESERVED[15:13]	3
0xBB0051F0	FC_PORT_PAGE_CNT [11].CNT[12:0]	13
0xBB0051F4	FC_PORT_PAGE_CNT [12].RESERVED[31:29]	3
0xBB0051F4	FC_PORT_PAGE_CNT [12].MAX_CNT[28:16]	13
0xBB0051F4	FC_PORT_PAGE_CNT [12].RESERVED[15:13]	3
0xBB0051F4	FC_PORT_PAGE_CNT [12].CNT[12:0]	13
0xBB0051F8	FC_PORT_PAGE_CNT [13].RESERVED[31:29]	3
0xBB0051F8	FC_PORT_PAGE_CNT [13].MAX_CNT[28:16]	13
0xBB0051F8	FC_PORT_PAGE_CNT [13].RESERVED[15:13]	3
0xBB0051F8	FC_PORT_PAGE_CNT [13].CNT[12:0]	13
0xBB0051FC	FC_PORT_PAGE_CNT [14].RESERVED[31:29]	3
0xBB0051FC	FC_PORT_PAGE_CNT [14].MAX_CNT[28:16]	13
0xBB0051FC	FC_PORT_PAGE_CNT [14].RESERVED[15:13]	3
0xBB0051FC	FC_PORT_PAGE_CNT [14].CNT[12:0]	13
0xBB005200	FC_PORT_PAGE_CNT [15].RESERVED[31:29]	3
0xBB005200	FC_PORT_PAGE_CNT [15].MAX_CNT[28:16]	13
0xBB005200	FC_PORT_PAGE_CNT [15].RESERVED[15:13]	3
0xBB005200	FC_PORT_PAGE_CNT [15].CNT[12:0]	13
0xBB005204	FC_PORT_PAGE_CNT [16].RESERVED[31:29]	3
0xBB005204	FC_PORT_PAGE_CNT [16].MAX_CNT[28:16]	13
0xBB005204	FC_PORT_PAGE_CNT [16].RESERVED[15:13]	3
0xBB005204	FC_PORT_PAGE_CNT [16].CNT[12:0]	13
0xBB005208	FC_PORT_PAGE_CNT [17].RESERVED[31:29]	3
0xBB005208	FC_PORT_PAGE_CNT [17].MAX_CNT[28:16]	13
0xBB005208	FC_PORT_PAGE_CNT [17].RESERVED[15:13]	3
0xBB005208	FC_PORT_PAGE_CNT [17].CNT[12:0]	13
0xBB00520C	FC_PORT_PAGE_CNT [18].RESERVED[31:29]	3
0xBB00520C	FC_PORT_PAGE_CNT [18].MAX_CNT[28:16]	13
0xBB00520C	FC_PORT_PAGE_CNT [18].RESERVED[15:13]	3
0xBB00520C	FC_PORT_PAGE_CNT [18].CNT[12:0]	13
0xBB005210	FC_PORT_PAGE_CNT [19].RESERVED[31:29]	3
0xBB005210	FC_PORT_PAGE_CNT [19].MAX_CNT[28:16]	13
0xBB005210	FC_PORT_PAGE_CNT [19].RESERVED[15:13]	3
0xBB005210	FC_PORT_PAGE_CNT [19].CNT[12:0]	13
0xBB005214	FC_PORT_PAGE_CNT [20].RESERVED[31:29]	3
0xBB005214	FC_PORT_PAGE_CNT [20].MAX_CNT[28:16]	13
0xBB005214	FC_PORT_PAGE_CNT [20].RESERVED[15:13]	3
0xBB005214	FC_PORT_PAGE_CNT [20].CNT[12:0]	13
0xBB005218	FC_PORT_PAGE_CNT [21].RESERVED[31:29]	3
0xBB005218	FC_PORT_PAGE_CNT [21].MAX_CNT[28:16]	13
0xBB005218	FC_PORT_PAGE_CNT [21].RESERVED[15:13]	3
0xBB005218	FC_PORT_PAGE_CNT [21].CNT[12:0]	13
0xBB00521C	FC_PORT_PAGE_CNT [22].RESERVED[31:29]	3
0xBB00521C	FC_PORT_PAGE_CNT [22].MAX_CNT[28:16]	13
0xBB00521C	FC_PORT_PAGE_CNT [22].RESERVED[15:13]	3
0xBB00521C	FC_PORT_PAGE_CNT [22].CNT[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005220	FC_PORT_PAGE_CNT [23].RESERVED[31:29]	3
0xBB005220	FC_PORT_PAGE_CNT [23].MAX_CNT[28:16]	13
0xBB005220	FC_PORT_PAGE_CNT [23].RESERVED[15:13]	3
0xBB005220	FC_PORT_PAGE_CNT [23].CNT[12:0]	13
0xBB005224	FC_PORT_PAGE_CNT [24].RESERVED[31:29]	3
0xBB005224	FC_PORT_PAGE_CNT [24].MAX_CNT[28:16]	13
0xBB005224	FC_PORT_PAGE_CNT [24].RESERVED[15:13]	3
0xBB005224	FC_PORT_PAGE_CNT [24].CNT[12:0]	13
0xBB005228	FC_PORT_PAGE_CNT [25].RESERVED[31:29]	3
0xBB005228	FC_PORT_PAGE_CNT [25].MAX_CNT[28:16]	13
0xBB005228	FC_PORT_PAGE_CNT [25].RESERVED[15:13]	3
0xBB005228	FC_PORT_PAGE_CNT [25].CNT[12:0]	13
0xBB00522C	FC_PORT_PAGE_CNT [26].RESERVED[31:29]	3
0xBB00522C	FC_PORT_PAGE_CNT [26].MAX_CNT[28:16]	13
0xBB00522C	FC_PORT_PAGE_CNT [26].RESERVED[15:13]	3
0xBB00522C	FC_PORT_PAGE_CNT [26].CNT[12:0]	13
0xBB005230	FC_PORT_PAGE_CNT [27].RESERVED[31:29]	3
0xBB005230	FC_PORT_PAGE_CNT [27].MAX_CNT[28:16]	13
0xBB005230	FC_PORT_PAGE_CNT [27].RESERVED[15:13]	3
0xBB005230	FC_PORT_PAGE_CNT [27].CNT[12:0]	13
0xBB005234	FC_PORT_PAGE_CNT [28].RESERVED[31:29]	3
0xBB005234	FC_PORT_PAGE_CNT [28].MAX_CNT[28:16]	13
0xBB005234	FC_PORT_PAGE_CNT [28].RESERVED[15:13]	3
0xBB005234	FC_PORT_PAGE_CNT [28].CNT[12:0]	13
0xBB005238	FC_PORT_PAGE_CNT [29].RESERVED[31:29]	3
0xBB005238	FC_PORT_PAGE_CNT [29].MAX_CNT[28:16]	13
0xBB005238	FC_PORT_PAGE_CNT [29].RESERVED[15:13]	3
0xBB005238	FC_PORT_PAGE_CNT [29].CNT[12:0]	13
0xBB00523C	FC_PORT_PAGE_CNT [30].RESERVED[31:29]	3
0xBB00523C	FC_PORT_PAGE_CNT [30].MAX_CNT[28:16]	13
0xBB00523C	FC_PORT_PAGE_CNT [30].RESERVED[15:13]	3
0xBB00523C	FC_PORT_PAGE_CNT [30].CNT[12:0]	13
0xBB005240	FC_PORT_PAGE_CNT [31].RESERVED[31:29]	3
0xBB005240	FC_PORT_PAGE_CNT [31].MAX_CNT[28:16]	13
0xBB005240	FC_PORT_PAGE_CNT [31].RESERVED[15:13]	3
0xBB005240	FC_PORT_PAGE_CNT [31].CNT[12:0]	13
0xBB005244	FC_PORT_PAGE_CNT [32].RESERVED[31:29]	3
0xBB005244	FC_PORT_PAGE_CNT [32].MAX_CNT[28:16]	13
0xBB005244	FC_PORT_PAGE_CNT [32].RESERVED[15:13]	3
0xBB005244	FC_PORT_PAGE_CNT [32].CNT[12:0]	13
0xBB005248	FC_PORT_PAGE_CNT [33].RESERVED[31:29]	3
0xBB005248	FC_PORT_PAGE_CNT [33].MAX_CNT[28:16]	13
0xBB005248	FC_PORT_PAGE_CNT [33].RESERVED[15:13]	3
0xBB005248	FC_PORT_PAGE_CNT [33].CNT[12:0]	13
0xBB00524C	FC_PORT_PAGE_CNT [34].RESERVED[31:29]	3
0xBB00524C	FC_PORT_PAGE_CNT [34].MAX_CNT[28:16]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00524C	FC_PORT_PAGE_CNT [34].RESERVED[15:13]	3
0xBB00524C	FC_PORT_PAGE_CNT [34].CNT[12:0]	13
0xBB005250	FC_PORT_PAGE_CNT [35].RESERVED[31:29]	3
0xBB005250	FC_PORT_PAGE_CNT [35].MAX_CNT[28:16]	13
0xBB005250	FC_PORT_PAGE_CNT [35].RESERVED[15:13]	3
0xBB005250	FC_PORT_PAGE_CNT [35].CNT[12:0]	13
0xBB005254	FC_PORT_PAGE_CNT [36].RESERVED[31:29]	3
0xBB005254	FC_PORT_PAGE_CNT [36].MAX_CNT[28:16]	13
0xBB005254	FC_PORT_PAGE_CNT [36].RESERVED[15:13]	3
0xBB005254	FC_PORT_PAGE_CNT [36].CNT[12:0]	13
0xBB005258	FC_PORT_PAGE_CNT [37].RESERVED[31:29]	3
0xBB005258	FC_PORT_PAGE_CNT [37].MAX_CNT[28:16]	13
0xBB005258	FC_PORT_PAGE_CNT [37].RESERVED[15:13]	3
0xBB005258	FC_PORT_PAGE_CNT [37].CNT[12:0]	13
0xBB00525C	FC_PORT_PAGE_CNT [38].RESERVED[31:29]	3
0xBB00525C	FC_PORT_PAGE_CNT [38].MAX_CNT[28:16]	13
0xBB00525C	FC_PORT_PAGE_CNT [38].RESERVED[15:13]	3
0xBB00525C	FC_PORT_PAGE_CNT [38].CNT[12:0]	13
0xBB005260	FC_PORT_PAGE_CNT [39].RESERVED[31:29]	3
0xBB005260	FC_PORT_PAGE_CNT [39].MAX_CNT[28:16]	13
0xBB005260	FC_PORT_PAGE_CNT [39].RESERVED[15:13]	3
0xBB005260	FC_PORT_PAGE_CNT [39].CNT[12:0]	13
0xBB005264	FC_PORT_PAGE_CNT [40].RESERVED[31:29]	3
0xBB005264	FC_PORT_PAGE_CNT [40].MAX_CNT[28:16]	13
0xBB005264	FC_PORT_PAGE_CNT [40].RESERVED[15:13]	3
0xBB005264	FC_PORT_PAGE_CNT [40].CNT[12:0]	13
0xBB005268	FC_PORT_PAGE_CNT [41].RESERVED[31:29]	3
0xBB005268	FC_PORT_PAGE_CNT [41].MAX_CNT[28:16]	13
0xBB005268	FC_PORT_PAGE_CNT [41].RESERVED[15:13]	3
0xBB005268	FC_PORT_PAGE_CNT [41].CNT[12:0]	13
0xBB00526C	FC_PORT_PAGE_CNT [42].RESERVED[31:29]	3
0xBB00526C	FC_PORT_PAGE_CNT [42].MAX_CNT[28:16]	13
0xBB00526C	FC_PORT_PAGE_CNT [42].RESERVED[15:13]	3
0xBB00526C	FC_PORT_PAGE_CNT [42].CNT[12:0]	13
0xBB005270	FC_PORT_PAGE_CNT [43].RESERVED[31:29]	3
0xBB005270	FC_PORT_PAGE_CNT [43].MAX_CNT[28:16]	13
0xBB005270	FC_PORT_PAGE_CNT [43].RESERVED[15:13]	3
0xBB005270	FC_PORT_PAGE_CNT [43].CNT[12:0]	13
0xBB005274	FC_PORT_PAGE_CNT [44].RESERVED[31:29]	3
0xBB005274	FC_PORT_PAGE_CNT [44].MAX_CNT[28:16]	13
0xBB005274	FC_PORT_PAGE_CNT [44].RESERVED[15:13]	3
0xBB005274	FC_PORT_PAGE_CNT [44].CNT[12:0]	13
0xBB005278	FC_PORT_PAGE_CNT [45].RESERVED[31:29]	3
0xBB005278	FC_PORT_PAGE_CNT [45].MAX_CNT[28:16]	13
0xBB005278	FC_PORT_PAGE_CNT [45].RESERVED[15:13]	3
0xBB005278	FC_PORT_PAGE_CNT [45].CNT[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00527C	FC_PORT_PAGE_CNT [46].RESERVED[31:29]	3
0xBB00527C	FC_PORT_PAGE_CNT [46].MAX_CNT[28:16]	13
0xBB00527C	FC_PORT_PAGE_CNT [46].RESERVED[15:13]	3
0xBB00527C	FC_PORT_PAGE_CNT [46].CNT[12:0]	13
0xBB005280	FC_PORT_PAGE_CNT [47].RESERVED[31:29]	3
0xBB005280	FC_PORT_PAGE_CNT [47].MAX_CNT[28:16]	13
0xBB005280	FC_PORT_PAGE_CNT [47].RESERVED[15:13]	3
0xBB005280	FC_PORT_PAGE_CNT [47].CNT[12:0]	13
0xBB005284	FC_PORT_PAGE_CNT [48].RESERVED[31:29]	3
0xBB005284	FC_PORT_PAGE_CNT [48].MAX_CNT[28:16]	13
0xBB005284	FC_PORT_PAGE_CNT [48].RESERVED[15:13]	3
0xBB005284	FC_PORT_PAGE_CNT [48].CNT[12:0]	13
0xBB005288	FC_PORT_PAGE_CNT [49].RESERVED[31:29]	3
0xBB005288	FC_PORT_PAGE_CNT [49].MAX_CNT[28:16]	13
0xBB005288	FC_PORT_PAGE_CNT [49].RESERVED[15:13]	3
0xBB005288	FC_PORT_PAGE_CNT [49].CNT[12:0]	13
0xBB00528C	FC_PORT_PAGE_CNT [50].RESERVED[31:29]	3
0xBB00528C	FC_PORT_PAGE_CNT [50].MAX_CNT[28:16]	13
0xBB00528C	FC_PORT_PAGE_CNT [50].RESERVED[15:13]	3
0xBB00528C	FC_PORT_PAGE_CNT [50].CNT[12:0]	13
0xBB005290	FC_PORT_PAGE_CNT [51].RESERVED[31:29]	3
0xBB005290	FC_PORT_PAGE_CNT [51].MAX_CNT[28:16]	13
0xBB005290	FC_PORT_PAGE_CNT [51].RESERVED[15:13]	3
0xBB005290	FC_PORT_PAGE_CNT [51].CNT[12:0]	13
0xBB005294	FC_PORT_PAGE_CNT [52].RESERVED[31:29]	3
0xBB005294	FC_PORT_PAGE_CNT [52].MAX_CNT[28:16]	13
0xBB005294	FC_PORT_PAGE_CNT [52].RESERVED[15:13]	3
0xBB005294	FC_PORT_PAGE_CNT [52].CNT[12:0]	13
0xBB005298	FC_PORT_PAGE_CNT [53].RESERVED[31:29]	3
0xBB005298	FC_PORT_PAGE_CNT [53].MAX_CNT[28:16]	13
0xBB005298	FC_PORT_PAGE_CNT [53].RESERVED[15:13]	3
0xBB005298	FC_PORT_PAGE_CNT [53].CNT[12:0]	13
0xBB00529C	FC_PORT_PAGE_CNT [54].RESERVED[31:29]	3
0xBB00529C	FC_PORT_PAGE_CNT [54].MAX_CNT[28:16]	13
0xBB00529C	FC_PORT_PAGE_CNT [54].RESERVED[15:13]	3
0xBB00529C	FC_PORT_PAGE_CNT [54].CNT[12:0]	13
0xBB0052A0	FC_PORT_PAGE_CNT [55].RESERVED[31:29]	3
0xBB0052A0	FC_PORT_PAGE_CNT [55].MAX_CNT[28:16]	13
0xBB0052A0	FC_PORT_PAGE_CNT [55].RESERVED[15:13]	3
0xBB0052A0	FC_PORT_PAGE_CNT [55].CNT[12:0]	13
0xBB0052A4	FC_PORT_PAGE_CNT [56].RESERVED[31:29]	3
0xBB0052A4	FC_PORT_PAGE_CNT [56].MAX_CNT[28:16]	13
0xBB0052A4	FC_PORT_PAGE_CNT [56].RESERVED[15:13]	3
0xBB0052A4	FC_PORT_PAGE_CNT [56].CNT[12:0]	13
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][0].EN[0:0]	1
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][1].EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][2].EN[2:2]	1
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][3].EN[3:3]	1
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][4].EN[4:4]	1
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][5].EN[5:5]	1
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][6].EN[6:6]	1
0xBB00A8E8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [0][7].EN[7:7]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][0].EN[0:0]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][1].EN[1:1]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][2].EN[2:2]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][3].EN[3:3]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][4].EN[4:4]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][5].EN[5:5]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][6].EN[6:6]	1
0xBB00A8EC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [1][7].EN[7:7]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][0].EN[0:0]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][1].EN[1:1]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][2].EN[2:2]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][3].EN[3:3]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][4].EN[4:4]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][5].EN[5:5]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][6].EN[6:6]	1
0xBB00A8F0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [2][7].EN[7:7]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][0].EN[0:0]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][1].EN[1:1]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][2].EN[2:2]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][3].EN[3:3]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][4].EN[4:4]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][5].EN[5:5]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][6].EN[6:6]	1
0xBB00A8F4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [3][7].EN[7:7]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][0].EN[0:0]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][1].EN[1:1]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][2].EN[2:2]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][3].EN[3:3]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][4].EN[4:4]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][5].EN[5:5]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][6].EN[6:6]	1
0xBB00A8F8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [4][7].EN[7:7]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][0].EN[0:0]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][1].EN[1:1]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][2].EN[2:2]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][3].EN[3:3]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][4].EN[4:4]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][5].EN[5:5]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][6].EN[6:6]	1
0xBB00A8FC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [5][7].EN[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][0].EN[0:0]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][1].EN[1:1]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][2].EN[2:2]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][3].EN[3:3]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][4].EN[4:4]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][5].EN[5:5]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][6].EN[6:6]	1
0xBB00A900	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [6][7].EN[7:7]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][0].EN[0:0]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][1].EN[1:1]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][2].EN[2:2]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][3].EN[3:3]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][4].EN[4:4]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][5].EN[5:5]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][6].EN[6:6]	1
0xBB00A904	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [7][7].EN[7:7]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][0].EN[0:0]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][1].EN[1:1]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][2].EN[2:2]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][3].EN[3:3]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][4].EN[4:4]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][5].EN[5:5]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][6].EN[6:6]	1
0xBB00A908	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [8][7].EN[7:7]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][0].EN[0:0]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][1].EN[1:1]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][2].EN[2:2]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][3].EN[3:3]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][4].EN[4:4]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][5].EN[5:5]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][6].EN[6:6]	1
0xBB00A90C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [9][7].EN[7:7]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][0].EN[0:0]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][1].EN[1:1]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][2].EN[2:2]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][3].EN[3:3]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][4].EN[4:4]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][5].EN[5:5]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][6].EN[6:6]	1
0xBB00A910	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [10][7].EN[7:7]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][0].EN[0:0]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][1].EN[1:1]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][2].EN[2:2]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][3].EN[3:3]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][4].EN[4:4]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][5].EN[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][6].EN[6:6]	1
0xBB00A914	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [11][7].EN[7:7]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][0].EN[0:0]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][1].EN[1:1]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][2].EN[2:2]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][3].EN[3:3]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][4].EN[4:4]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][5].EN[5:5]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][6].EN[6:6]	1
0xBB00A918	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [12][7].EN[7:7]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][0].EN[0:0]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][1].EN[1:1]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][2].EN[2:2]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][3].EN[3:3]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][4].EN[4:4]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][5].EN[5:5]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][6].EN[6:6]	1
0xBB00A91C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [13][7].EN[7:7]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][0].EN[0:0]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][1].EN[1:1]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][2].EN[2:2]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][3].EN[3:3]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][4].EN[4:4]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][5].EN[5:5]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][6].EN[6:6]	1
0xBB00A920	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [14][7].EN[7:7]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][0].EN[0:0]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][1].EN[1:1]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][2].EN[2:2]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][3].EN[3:3]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][4].EN[4:4]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][5].EN[5:5]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][6].EN[6:6]	1
0xBB00A924	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [15][7].EN[7:7]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][0].EN[0:0]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][1].EN[1:1]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][2].EN[2:2]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][3].EN[3:3]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][4].EN[4:4]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][5].EN[5:5]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][6].EN[6:6]	1
0xBB00A928	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [16][7].EN[7:7]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][0].EN[0:0]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][1].EN[1:1]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][2].EN[2:2]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][3].EN[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][4].EN[4:4]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][5].EN[5:5]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][6].EN[6:6]	1
0xBB00A92C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [17][7].EN[7:7]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][0].EN[0:0]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][1].EN[1:1]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][2].EN[2:2]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][3].EN[3:3]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][4].EN[4:4]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][5].EN[5:5]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][6].EN[6:6]	1
0xBB00A930	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [18][7].EN[7:7]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][0].EN[0:0]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][1].EN[1:1]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][2].EN[2:2]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][3].EN[3:3]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][4].EN[4:4]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][5].EN[5:5]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][6].EN[6:6]	1
0xBB00A934	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [19][7].EN[7:7]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][0].EN[0:0]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][1].EN[1:1]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][2].EN[2:2]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][3].EN[3:3]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][4].EN[4:4]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][5].EN[5:5]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][6].EN[6:6]	1
0xBB00A938	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [20][7].EN[7:7]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][0].EN[0:0]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][1].EN[1:1]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][2].EN[2:2]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][3].EN[3:3]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][4].EN[4:4]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][5].EN[5:5]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][6].EN[6:6]	1
0xBB00A93C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [21][7].EN[7:7]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][0].EN[0:0]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][1].EN[1:1]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][2].EN[2:2]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][3].EN[3:3]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][4].EN[4:4]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][5].EN[5:5]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][6].EN[6:6]	1
0xBB00A940	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [22][7].EN[7:7]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][0].EN[0:0]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][1].EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][2].EN[2:2]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][3].EN[3:3]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][4].EN[4:4]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][5].EN[5:5]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][6].EN[6:6]	1
0xBB00A944	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [23][7].EN[7:7]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][0].EN[0:0]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][1].EN[1:1]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][2].EN[2:2]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][3].EN[3:3]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][4].EN[4:4]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][5].EN[5:5]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][6].EN[6:6]	1
0xBB00A948	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [24][7].EN[7:7]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][0].EN[0:0]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][1].EN[1:1]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][2].EN[2:2]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][3].EN[3:3]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][4].EN[4:4]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][5].EN[5:5]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][6].EN[6:6]	1
0xBB00A94C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [25][7].EN[7:7]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][0].EN[0:0]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][1].EN[1:1]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][2].EN[2:2]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][3].EN[3:3]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][4].EN[4:4]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][5].EN[5:5]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][6].EN[6:6]	1
0xBB00A950	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [26][7].EN[7:7]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][0].EN[0:0]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][1].EN[1:1]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][2].EN[2:2]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][3].EN[3:3]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][4].EN[4:4]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][5].EN[5:5]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][6].EN[6:6]	1
0xBB00A954	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [27][7].EN[7:7]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][0].EN[0:0]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][1].EN[1:1]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][2].EN[2:2]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][3].EN[3:3]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][4].EN[4:4]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][5].EN[5:5]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][6].EN[6:6]	1
0xBB00A958	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [28][7].EN[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][0].EN[0:0]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][1].EN[1:1]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][2].EN[2:2]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][3].EN[3:3]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][4].EN[4:4]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][5].EN[5:5]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][6].EN[6:6]	1
0xBB00A95C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [29][7].EN[7:7]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][0].EN[0:0]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][1].EN[1:1]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][2].EN[2:2]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][3].EN[3:3]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][4].EN[4:4]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][5].EN[5:5]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][6].EN[6:6]	1
0xBB00A960	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [30][7].EN[7:7]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][0].EN[0:0]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][1].EN[1:1]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][2].EN[2:2]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][3].EN[3:3]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][4].EN[4:4]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][5].EN[5:5]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][6].EN[6:6]	1
0xBB00A964	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [31][7].EN[7:7]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][0].EN[0:0]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][1].EN[1:1]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][2].EN[2:2]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][3].EN[3:3]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][4].EN[4:4]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][5].EN[5:5]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][6].EN[6:6]	1
0xBB00A968	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [32][7].EN[7:7]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][0].EN[0:0]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][1].EN[1:1]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][2].EN[2:2]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][3].EN[3:3]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][4].EN[4:4]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][5].EN[5:5]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][6].EN[6:6]	1
0xBB00A96C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [33][7].EN[7:7]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][0].EN[0:0]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][1].EN[1:1]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][2].EN[2:2]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][3].EN[3:3]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][4].EN[4:4]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][5].EN[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][6].EN[6:6]	1
0xBB00A970	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [34][7].EN[7:7]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][0].EN[0:0]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][1].EN[1:1]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][2].EN[2:2]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][3].EN[3:3]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][4].EN[4:4]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][5].EN[5:5]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][6].EN[6:6]	1
0xBB00A974	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [35][7].EN[7:7]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][0].EN[0:0]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][1].EN[1:1]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][2].EN[2:2]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][3].EN[3:3]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][4].EN[4:4]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][5].EN[5:5]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][6].EN[6:6]	1
0xBB00A978	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [36][7].EN[7:7]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][0].EN[0:0]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][1].EN[1:1]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][2].EN[2:2]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][3].EN[3:3]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][4].EN[4:4]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][5].EN[5:5]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][6].EN[6:6]	1
0xBB00A97C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [37][7].EN[7:7]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][0].EN[0:0]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][1].EN[1:1]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][2].EN[2:2]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][3].EN[3:3]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][4].EN[4:4]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][5].EN[5:5]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][6].EN[6:6]	1
0xBB00A980	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [38][7].EN[7:7]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][0].EN[0:0]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][1].EN[1:1]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][2].EN[2:2]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][3].EN[3:3]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][4].EN[4:4]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][5].EN[5:5]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][6].EN[6:6]	1
0xBB00A984	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [39][7].EN[7:7]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][0].EN[0:0]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][1].EN[1:1]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][2].EN[2:2]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][3].EN[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][4].EN[4:4]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][5].EN[5:5]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][6].EN[6:6]	1
0xBB00A988	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [40][7].EN[7:7]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][0].EN[0:0]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][1].EN[1:1]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][2].EN[2:2]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][3].EN[3:3]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][4].EN[4:4]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][5].EN[5:5]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][6].EN[6:6]	1
0xBB00A98C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [41][7].EN[7:7]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][0].EN[0:0]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][1].EN[1:1]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][2].EN[2:2]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][3].EN[3:3]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][4].EN[4:4]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][5].EN[5:5]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][6].EN[6:6]	1
0xBB00A990	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [42][7].EN[7:7]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][0].EN[0:0]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][1].EN[1:1]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][2].EN[2:2]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][3].EN[3:3]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][4].EN[4:4]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][5].EN[5:5]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][6].EN[6:6]	1
0xBB00A994	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [43][7].EN[7:7]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][0].EN[0:0]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][1].EN[1:1]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][2].EN[2:2]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][3].EN[3:3]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][4].EN[4:4]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][5].EN[5:5]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][6].EN[6:6]	1
0xBB00A998	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [44][7].EN[7:7]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][0].EN[0:0]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][1].EN[1:1]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][2].EN[2:2]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][3].EN[3:3]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][4].EN[4:4]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][5].EN[5:5]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][6].EN[6:6]	1
0xBB00A99C	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [45][7].EN[7:7]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][0].EN[0:0]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][1].EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][2].EN[2:2]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][3].EN[3:3]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][4].EN[4:4]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][5].EN[5:5]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][6].EN[6:6]	1
0xBB00A9A0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [46][7].EN[7:7]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][0].EN[0:0]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][1].EN[1:1]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][2].EN[2:2]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][3].EN[3:3]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][4].EN[4:4]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][5].EN[5:5]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][6].EN[6:6]	1
0xBB00A9A4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [47][7].EN[7:7]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][0].EN[0:0]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][1].EN[1:1]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][2].EN[2:2]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][3].EN[3:3]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][4].EN[4:4]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][5].EN[5:5]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][6].EN[6:6]	1
0xBB00A9A8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [48][7].EN[7:7]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][0].EN[0:0]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][1].EN[1:1]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][2].EN[2:2]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][3].EN[3:3]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][4].EN[4:4]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][5].EN[5:5]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][6].EN[6:6]	1
0xBB00A9AC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [49][7].EN[7:7]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][0].EN[0:0]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][1].EN[1:1]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][2].EN[2:2]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][3].EN[3:3]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][4].EN[4:4]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][5].EN[5:5]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][6].EN[6:6]	1
0xBB00A9B0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [50][7].EN[7:7]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][0].EN[0:0]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][1].EN[1:1]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][2].EN[2:2]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][3].EN[3:3]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][4].EN[4:4]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][5].EN[5:5]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][6].EN[6:6]	1
0xBB00A9B4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET0 [51][7].EN[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][0].EN[0:0]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][1].EN[1:1]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][2].EN[2:2]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][3].EN[3:3]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][4].EN[4:4]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][5].EN[5:5]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][6].EN[6:6]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][7].EN[7:7]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][8].EN[8:8]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][9].EN[9:9]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][10].EN[10:10]	1
0xBB00A9B8	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [52][11].EN[11:11]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][0].EN[0:0]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][1].EN[1:1]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][2].EN[2:2]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][3].EN[3:3]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][4].EN[4:4]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][5].EN[5:5]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][6].EN[6:6]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][7].EN[7:7]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][8].EN[8:8]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][9].EN[9:9]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][10].EN[10:10]	1
0xBB00A9BC	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [53][11].EN[11:11]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][0].EN[0:0]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][1].EN[1:1]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][2].EN[2:2]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][3].EN[3:3]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][4].EN[4:4]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][5].EN[5:5]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][6].EN[6:6]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][7].EN[7:7]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][8].EN[8:8]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][9].EN[9:9]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][10].EN[10:10]	1
0xBB00A9C0	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [54][11].EN[11:11]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][0].EN[0:0]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][1].EN[1:1]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][2].EN[2:2]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][3].EN[3:3]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][4].EN[4:4]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][5].EN[5:5]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][6].EN[6:6]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][7].EN[7:7]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][8].EN[8:8]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][9].EN[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][10].EN[10:10]	1
0xBB00A9C4	FC_PORT_Q_EGR_FORCE_DROP_CTRL_SET1 [55][11].EN[11:11]	1
0xBB0051AC	FC_PORT_THR_SET_SEL [0].IDX[1:0]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [1].IDX[3:2]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [2].IDX[5:4]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [3].IDX[7:6]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [4].IDX[9:8]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [5].IDX[11:10]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [6].IDX[13:12]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [7].IDX[15:14]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [8].IDX[17:16]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [9].IDX[19:18]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [10].IDX[21:20]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [11].IDX[23:22]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [12].IDX[25:24]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [13].IDX[27:26]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [14].IDX[29:28]	2
0xBB0051AC	FC_PORT_THR_SET_SEL [15].IDX[31:30]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [16].IDX[1:0]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [17].IDX[3:2]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [18].IDX[5:4]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [19].IDX[7:6]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [20].IDX[9:8]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [21].IDX[11:10]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [22].IDX[13:12]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [23].IDX[15:14]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [24].IDX[17:16]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [25].IDX[19:18]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [26].IDX[21:20]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [27].IDX[23:22]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [28].IDX[25:24]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [29].IDX[27:26]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [30].IDX[29:28]	2
0xBB0051B0	FC_PORT_THR_SET_SEL [31].IDX[31:30]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [32].IDX[1:0]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [33].IDX[3:2]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [34].IDX[5:4]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [35].IDX[7:6]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [36].IDX[9:8]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [37].IDX[11:10]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [38].IDX[13:12]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [39].IDX[15:14]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [40].IDX[17:16]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [41].IDX[19:18]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [42].IDX[21:20]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [43].IDX[23:22]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0051B4	FC_PORT_THR_SET_SEL [44].IDX[25:24]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [45].IDX[27:26]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [46].IDX[29:28]	2
0xBB0051B4	FC_PORT_THR_SET_SEL [47].IDX[31:30]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [48].IDX[1:0]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [49].IDX[3:2]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [50].IDX[5:4]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [51].IDX[7:6]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [52].IDX[9:8]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [53].IDX[11:10]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [54].IDX[13:12]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [55].IDX[15:14]	2
0xBB0051B8	FC_PORT_THR_SET_SEL [56].IDX[17:16]	2
0xBB002618	FC_Q_EGR_DROP THR [0][0].RESERVED[31:29]	3
0xBB002618	FC_Q_EGR_DROP THR [0][0].ON[28:16]	13
0xBB002618	FC_Q_EGR_DROP THR [0][0].RESERVED[15:13]	3
0xBB002618	FC_Q_EGR_DROP THR [0][0].OFF[12:0]	13
0xBB00261C	FC_Q_EGR_DROP THR [0][1].RESERVED[31:29]	3
0xBB00261C	FC_Q_EGR_DROP THR [0][1].ON[28:16]	13
0xBB00261C	FC_Q_EGR_DROP THR [0][1].RESERVED[15:13]	3
0xBB00261C	FC_Q_EGR_DROP THR [0][1].OFF[12:0]	13
0xBB002620	FC_Q_EGR_DROP THR [0][2].RESERVED[31:29]	3
0xBB002620	FC_Q_EGR_DROP THR [0][2].ON[28:16]	13
0xBB002620	FC_Q_EGR_DROP THR [0][2].RESERVED[15:13]	3
0xBB002620	FC_Q_EGR_DROP THR [0][2].OFF[12:0]	13
0xBB002624	FC_Q_EGR_DROP THR [1][0].RESERVED[31:29]	3
0xBB002624	FC_Q_EGR_DROP THR [1][0].ON[28:16]	13
0xBB002624	FC_Q_EGR_DROP THR [1][0].RESERVED[15:13]	3
0xBB002624	FC_Q_EGR_DROP THR [1][0].OFF[12:0]	13
0xBB002628	FC_Q_EGR_DROP THR [1][1].RESERVED[31:29]	3
0xBB002628	FC_Q_EGR_DROP THR [1][1].ON[28:16]	13
0xBB002628	FC_Q_EGR_DROP THR [1][1].RESERVED[15:13]	3
0xBB002628	FC_Q_EGR_DROP THR [1][1].OFF[12:0]	13
0xBB00262C	FC_Q_EGR_DROP THR [1][2].RESERVED[31:29]	3
0xBB00262C	FC_Q_EGR_DROP THR [1][2].ON[28:16]	13
0xBB00262C	FC_Q_EGR_DROP THR [1][2].RESERVED[15:13]	3
0xBB00262C	FC_Q_EGR_DROP THR [1][2].OFF[12:0]	13
0xBB002630	FC_Q_EGR_DROP THR [2][0].RESERVED[31:29]	3
0xBB002630	FC_Q_EGR_DROP THR [2][0].ON[28:16]	13
0xBB002630	FC_Q_EGR_DROP THR [2][0].RESERVED[15:13]	3
0xBB002630	FC_Q_EGR_DROP THR [2][0].OFF[12:0]	13
0xBB002634	FC_Q_EGR_DROP THR [2][1].RESERVED[31:29]	3
0xBB002634	FC_Q_EGR_DROP THR [2][1].ON[28:16]	13
0xBB002634	FC_Q_EGR_DROP THR [2][1].RESERVED[15:13]	3
0xBB002634	FC_Q_EGR_DROP THR [2][1].OFF[12:0]	13
0xBB002638	FC_Q_EGR_DROP THR [2][2].RESERVED[31:29]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002638	FC_Q_EGR_DROP_THR [2][2].ON[28:16]	13
0xBB002638	FC_Q_EGR_DROP_THR [2][2].RESERVED[15:13]	3
0xBB002638	FC_Q_EGR_DROP_THR [2][2].OFF[12:0]	13
0xBB00263C	FC_Q_EGR_DROP_THR [3][0].RESERVED[31:29]	3
0xBB00263C	FC_Q_EGR_DROP_THR [3][0].ON[28:16]	13
0xBB00263C	FC_Q_EGR_DROP_THR [3][0].RESERVED[15:13]	3
0xBB00263C	FC_Q_EGR_DROP_THR [3][0].OFF[12:0]	13
0xBB002640	FC_Q_EGR_DROP_THR [3][1].RESERVED[31:29]	3
0xBB002640	FC_Q_EGR_DROP_THR [3][1].ON[28:16]	13
0xBB002640	FC_Q_EGR_DROP_THR [3][1].RESERVED[15:13]	3
0xBB002640	FC_Q_EGR_DROP_THR [3][1].OFF[12:0]	13
0xBB002644	FC_Q_EGR_DROP_THR [3][2].RESERVED[31:29]	3
0xBB002644	FC_Q_EGR_DROP_THR [3][2].ON[28:16]	13
0xBB002644	FC_Q_EGR_DROP_THR [3][2].RESERVED[15:13]	3
0xBB002644	FC_Q_EGR_DROP_THR [3][2].OFF[12:0]	13
0xBB002648	FC_Q_EGR_DROP_THR [4][0].RESERVED[31:29]	3
0xBB002648	FC_Q_EGR_DROP_THR [4][0].ON[28:16]	13
0xBB002648	FC_Q_EGR_DROP_THR [4][0].RESERVED[15:13]	3
0xBB002648	FC_Q_EGR_DROP_THR [4][0].OFF[12:0]	13
0xBB00264C	FC_Q_EGR_DROP_THR [4][1].RESERVED[31:29]	3
0xBB00264C	FC_Q_EGR_DROP_THR [4][1].ON[28:16]	13
0xBB00264C	FC_Q_EGR_DROP_THR [4][1].RESERVED[15:13]	3
0xBB00264C	FC_Q_EGR_DROP_THR [4][1].OFF[12:0]	13
0xBB002650	FC_Q_EGR_DROP_THR [4][2].RESERVED[31:29]	3
0xBB002650	FC_Q_EGR_DROP_THR [4][2].ON[28:16]	13
0xBB002650	FC_Q_EGR_DROP_THR [4][2].RESERVED[15:13]	3
0xBB002650	FC_Q_EGR_DROP_THR [4][2].OFF[12:0]	13
0xBB002654	FC_Q_EGR_DROP_THR [5][0].RESERVED[31:29]	3
0xBB002654	FC_Q_EGR_DROP_THR [5][0].ON[28:16]	13
0xBB002654	FC_Q_EGR_DROP_THR [5][0].RESERVED[15:13]	3
0xBB002654	FC_Q_EGR_DROP_THR [5][0].OFF[12:0]	13
0xBB002658	FC_Q_EGR_DROP_THR [5][1].RESERVED[31:29]	3
0xBB002658	FC_Q_EGR_DROP_THR [5][1].ON[28:16]	13
0xBB002658	FC_Q_EGR_DROP_THR [5][1].RESERVED[15:13]	3
0xBB002658	FC_Q_EGR_DROP_THR [5][1].OFF[12:0]	13
0xBB00265C	FC_Q_EGR_DROP_THR [5][2].RESERVED[31:29]	3
0xBB00265C	FC_Q_EGR_DROP_THR [5][2].ON[28:16]	13
0xBB00265C	FC_Q_EGR_DROP_THR [5][2].RESERVED[15:13]	3
0xBB00265C	FC_Q_EGR_DROP_THR [5][2].OFF[12:0]	13
0xBB002660	FC_Q_EGR_DROP_THR [6][0].RESERVED[31:29]	3
0xBB002660	FC_Q_EGR_DROP_THR [6][0].ON[28:16]	13
0xBB002660	FC_Q_EGR_DROP_THR [6][0].RESERVED[15:13]	3
0xBB002660	FC_Q_EGR_DROP_THR [6][0].OFF[12:0]	13
0xBB002664	FC_Q_EGR_DROP_THR [6][1].RESERVED[31:29]	3
0xBB002664	FC_Q_EGR_DROP_THR [6][1].ON[28:16]	13
0xBB002664	FC_Q_EGR_DROP_THR [6][1].RESERVED[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002664	FC_Q_EGR_DROP_THR [6][1].OFF[12:0]	13
0xBB002668	FC_Q_EGR_DROP_THR [6][2].RESERVED[31:29]	3
0xBB002668	FC_Q_EGR_DROP_THR [6][2].ON[28:16]	13
0xBB002668	FC_Q_EGR_DROP_THR [6][2].RESERVED[15:13]	3
0xBB002668	FC_Q_EGR_DROP_THR [6][2].OFF[12:0]	13
0xBB00266C	FC_Q_EGR_DROP_THR [7][0].RESERVED[31:29]	3
0xBB00266C	FC_Q_EGR_DROP_THR [7][0].ON[28:16]	13
0xBB00266C	FC_Q_EGR_DROP_THR [7][0].RESERVED[15:13]	3
0xBB00266C	FC_Q_EGR_DROP_THR [7][0].OFF[12:0]	13
0xBB002670	FC_Q_EGR_DROP_THR [7][1].RESERVED[31:29]	3
0xBB002670	FC_Q_EGR_DROP_THR [7][1].ON[28:16]	13
0xBB002670	FC_Q_EGR_DROP_THR [7][1].RESERVED[15:13]	3
0xBB002670	FC_Q_EGR_DROP_THR [7][1].OFF[12:0]	13
0xBB002674	FC_Q_EGR_DROP_THR [7][2].RESERVED[31:29]	3
0xBB002674	FC_Q_EGR_DROP_THR [7][2].ON[28:16]	13
0xBB002674	FC_Q_EGR_DROP_THR [7][2].RESERVED[15:13]	3
0xBB002674	FC_Q_EGR_DROP_THR [7][2].OFF[12:0]	13
0xBB002678	FC_Q_EGR_DROP_THR [8][0].RESERVED[31:29]	3
0xBB002678	FC_Q_EGR_DROP_THR [8][0].ON[28:16]	13
0xBB002678	FC_Q_EGR_DROP_THR [8][0].RESERVED[15:13]	3
0xBB002678	FC_Q_EGR_DROP_THR [8][0].OFF[12:0]	13
0xBB00267C	FC_Q_EGR_DROP_THR [8][1].RESERVED[31:29]	3
0xBB00267C	FC_Q_EGR_DROP_THR [8][1].ON[28:16]	13
0xBB00267C	FC_Q_EGR_DROP_THR [8][1].RESERVED[15:13]	3
0xBB00267C	FC_Q_EGR_DROP_THR [8][1].OFF[12:0]	13
0xBB002680	FC_Q_EGR_DROP_THR [8][2].RESERVED[31:29]	3
0xBB002680	FC_Q_EGR_DROP_THR [8][2].ON[28:16]	13
0xBB002680	FC_Q_EGR_DROP_THR [8][2].RESERVED[15:13]	3
0xBB002680	FC_Q_EGR_DROP_THR [8][2].OFF[12:0]	13
0xBB002684	FC_Q_EGR_DROP_THR [9][0].RESERVED[31:29]	3
0xBB002684	FC_Q_EGR_DROP_THR [9][0].ON[28:16]	13
0xBB002684	FC_Q_EGR_DROP_THR [9][0].RESERVED[15:13]	3
0xBB002684	FC_Q_EGR_DROP_THR [9][0].OFF[12:0]	13
0xBB002688	FC_Q_EGR_DROP_THR [9][1].RESERVED[31:29]	3
0xBB002688	FC_Q_EGR_DROP_THR [9][1].ON[28:16]	13
0xBB002688	FC_Q_EGR_DROP_THR [9][1].RESERVED[15:13]	3
0xBB002688	FC_Q_EGR_DROP_THR [9][1].OFF[12:0]	13
0xBB00268C	FC_Q_EGR_DROP_THR [9][2].RESERVED[31:29]	3
0xBB00268C	FC_Q_EGR_DROP_THR [9][2].ON[28:16]	13
0xBB00268C	FC_Q_EGR_DROP_THR [9][2].RESERVED[15:13]	3
0xBB00268C	FC_Q_EGR_DROP_THR [9][2].OFF[12:0]	13
0xBB002690	FC_Q_EGR_DROP_THR [10][0].RESERVED[31:29]	3
0xBB002690	FC_Q_EGR_DROP_THR [10][0].ON[28:16]	13
0xBB002690	FC_Q_EGR_DROP_THR [10][0].RESERVED[15:13]	3
0xBB002690	FC_Q_EGR_DROP_THR [10][0].OFF[12:0]	13
0xBB002694	FC_Q_EGR_DROP_THR [10][1].RESERVED[31:29]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002694	FC_Q_EGR_DROP_THR [10][1].ON[28:16]	13
0xBB002694	FC_Q_EGR_DROP_THR [10][1].RESERVED[15:13]	3
0xBB002694	FC_Q_EGR_DROP_THR [10][1].OFF[12:0]	13
0xBB002698	FC_Q_EGR_DROP_THR [10][2].RESERVED[31:29]	3
0xBB002698	FC_Q_EGR_DROP_THR [10][2].ON[28:16]	13
0xBB002698	FC_Q_EGR_DROP_THR [10][2].RESERVED[15:13]	3
0xBB002698	FC_Q_EGR_DROP_THR [10][2].OFF[12:0]	13
0xBB00269C	FC_Q_EGR_DROP_THR [11][0].RESERVED[31:29]	3
0xBB00269C	FC_Q_EGR_DROP_THR [11][0].ON[28:16]	13
0xBB00269C	FC_Q_EGR_DROP_THR [11][0].RESERVED[15:13]	3
0xBB00269C	FC_Q_EGR_DROP_THR [11][0].OFF[12:0]	13
0xBB0026A0	FC_Q_EGR_DROP_THR [11][1].RESERVED[31:29]	3
0xBB0026A0	FC_Q_EGR_DROP_THR [11][1].ON[28:16]	13
0xBB0026A0	FC_Q_EGR_DROP_THR [11][1].RESERVED[15:13]	3
0xBB0026A0	FC_Q_EGR_DROP_THR [11][1].OFF[12:0]	13
0xBB0026A4	FC_Q_EGR_DROP_THR [11][2].RESERVED[31:29]	3
0xBB0026A4	FC_Q_EGR_DROP_THR [11][2].ON[28:16]	13
0xBB0026A4	FC_Q_EGR_DROP_THR [11][2].RESERVED[15:13]	3
0xBB0026A4	FC_Q_EGR_DROP_THR [11][2].OFF[12:0]	13
0xBB008194	FC REPCT_FCOFF_DROP_CNT.RESERVED[31:20]	12
0xBB008194	FC REPCT_FCOFF_DROP_CNT.REPCT_HSA_FREE_CNT[19:16]	4
0xBB008194	FC REPCT_FCOFF_DROP_CNT.CNT[15:0]	16
0xBB008190	FC REPCT_FCOFF_PAGE_CNT.RESERVED[31:29]	3
0xBB008190	FC REPCT_FCOFF_PAGE_CNT.MAX_CNT[28:16]	13
0xBB008190	FC REPCT_FCOFF_PAGE_CNT.RESERVED[15:13]	3
0xBB008190	FC REPCT_FCOFF_PAGE_CNT.CNT[12:0]	13
0xBB008180	FC REPCT_FCOFF_THR.RESERVED[31:29]	3
0xBB008180	FC REPCT_FCOFF_THR.DROP_ON[28:16]	13
0xBB008180	FC REPCT_FCOFF_THR.RESERVED[15:13]	3
0xBB008180	FC REPCT_FCOFF_THR.DROP_OFF[12:0]	13
0xBB00818C	FC REPCT_FCON_PAGE_CNT.RESERVED[31:29]	3
0xBB00818C	FC REPCT_FCON_PAGE_CNT.MAX_CNT[28:16]	13
0xBB00818C	FC REPCT_FCON_PAGE_CNT.RESERVED[15:13]	3
0xBB00818C	FC REPCT_FCON_PAGE_CNT.CNT[12:0]	13
0xBB000F18	FEFI_STS [0].FEFI_STS[0:0]	1
0xBB000F18	FEFI_STS [1].FEFI_STS[1:1]	1
0xBB000F18	FEFI_STS [2].FEFI_STS[2:2]	1
0xBB000F18	FEFI_STS [3].FEFI_STS[3:3]	1
0xBB000F18	FEFI_STS [4].FEFI_STS[4:4]	1
0xBB000F18	FEFI_STS [5].FEFI_STS[5:5]	1
0xBB000F18	FEFI_STS [6].FEFI_STS[6:6]	1
0xBB000F18	FEFI_STS [7].FEFI_STS[7:7]	1
0xBB000F18	FEFI_STS [8].FEFI_STS[8:8]	1
0xBB000F18	FEFI_STS [9].FEFI_STS[9:9]	1
0xBB000F18	FEFI_STS [10].FEFI_STS[10:10]	1
0xBB000F18	FEFI_STS [11].FEFI_STS[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F18	FEFI_STS [12].FEFI_STS[12:12]	1
0xBB000F18	FEFI_STS [13].FEFI_STS[13:13]	1
0xBB000F18	FEFI_STS [14].FEFI_STS[14:14]	1
0xBB000F18	FEFI_STS [15].FEFI_STS[15:15]	1
0xBB000F18	FEFI_STS [16].FEFI_STS[16:16]	1
0xBB000F18	FEFI_STS [17].FEFI_STS[17:17]	1
0xBB000F18	FEFI_STS [18].FEFI_STS[18:18]	1
0xBB000F18	FEFI_STS [19].FEFI_STS[19:19]	1
0xBB000F18	FEFI_STS [20].FEFI_STS[20:20]	1
0xBB000F18	FEFI_STS [21].FEFI_STS[21:21]	1
0xBB000F18	FEFI_STS [22].FEFI_STS[22:22]	1
0xBB000F18	FEFI_STS [23].FEFI_STS[23:23]	1
0xBB000F18	FEFI_STS [24].FEFI_STS[24:24]	1
0xBB000F18	FEFI_STS [25].FEFI_STS[25:25]	1
0xBB000F18	FEFI_STS [26].FEFI_STS[26:26]	1
0xBB000F18	FEFI_STS [27].FEFI_STS[27:27]	1
0xBB000F18	FEFI_STS [28].FEFI_STS[28:28]	1
0xBB000F18	FEFI_STS [29].FEFI_STS[29:29]	1
0xBB000F18	FEFI_STS [30].FEFI_STS[30:30]	1
0xBB000F18	FEFI_STS [31].FEFI_STS[31:31]	1
0xBB000F1C	FEFI_STS [32].FEFI_STS[0:0]	1
0xBB000F1C	FEFI_STS [33].FEFI_STS[1:1]	1
0xBB000F1C	FEFI_STS [34].FEFI_STS[2:2]	1
0xBB000F1C	FEFI_STS [35].FEFI_STS[3:3]	1
0xBB000F1C	FEFI_STS [36].FEFI_STS[4:4]	1
0xBB000F1C	FEFI_STS [37].FEFI_STS[5:5]	1
0xBB000F1C	FEFI_STS [38].FEFI_STS[6:6]	1
0xBB000F1C	FEFI_STS [39].FEFI_STS[7:7]	1
0xBB000F1C	FEFI_STS [40].FEFI_STS[8:8]	1
0xBB000F1C	FEFI_STS [41].FEFI_STS[9:9]	1
0xBB000F1C	FEFI_STS [42].FEFI_STS[10:10]	1
0xBB000F1C	FEFI_STS [43].FEFI_STS[11:11]	1
0xBB000F1C	FEFI_STS [44].FEFI_STS[12:12]	1
0xBB000F1C	FEFI_STS [45].FEFI_STS[13:13]	1
0xBB000F1C	FEFI_STS [46].FEFI_STS[14:14]	1
0xBB000F1C	FEFI_STS [47].FEFI_STS[15:15]	1
0xBB000F1C	FEFI_STS [48].FEFI_STS[16:16]	1
0xBB000F1C	FEFI_STS [49].FEFI_STS[17:17]	1
0xBB000F1C	FEFI_STS [50].FEFI_STS[18:18]	1
0xBB000F1C	FEFI_STS [51].FEFI_STS[19:19]	1
0xBB000F1C	FEFI_STS [52].FEFI_STS[20:20]	1
0xBB000F1C	FEFI_STS [53].FEFI_STS[21:21]	1
0xBB000F1C	FEFI_STS [54].FEFI_STS[22:22]	1
0xBB000F1C	FEFI_STS [55].FEFI_STS[23:23]	1
0xBB000F1C	FEFI_STS [56].FEFI_STS[24:24]	1
0xBB0013C4	FIB_UNIDIR_CTRL [0].FIB_UNIDIR_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0013C4	FIB_UNIDIR_CTRL [1].FIB_UNIDIR_EN[1:1]	1
0xBB0013C4	FIB_UNIDIR_CTRL [2].FIB_UNIDIR_EN[2:2]	1
0xBB0013C4	FIB_UNIDIR_CTRL [3].FIB_UNIDIR_EN[3:3]	1
0xBB0013C4	FIB_UNIDIR_CTRL [4].FIB_UNIDIR_EN[4:4]	1
0xBB0013C4	FIB_UNIDIR_CTRL [5].FIB_UNIDIR_EN[5:5]	1
0xBB0013C4	FIB_UNIDIR_CTRL [6].FIB_UNIDIR_EN[6:6]	1
0xBB0013C4	FIB_UNIDIR_CTRL [7].FIB_UNIDIR_EN[7:7]	1
0xBB0013C4	FIB_UNIDIR_CTRL [8].FIB_UNIDIR_EN[8:8]	1
0xBB0013C4	FIB_UNIDIR_CTRL [9].FIB_UNIDIR_EN[9:9]	1
0xBB0013C4	FIB_UNIDIR_CTRL [10].FIB_UNIDIR_EN[10:10]	1
0xBB0013C4	FIB_UNIDIR_CTRL [11].FIB_UNIDIR_EN[11:11]	1
0xBB0013C4	FIB_UNIDIR_CTRL [12].FIB_UNIDIR_EN[12:12]	1
0xBB0013C4	FIB_UNIDIR_CTRL [13].FIB_UNIDIR_EN[13:13]	1
0xBB0013C4	FIB_UNIDIR_CTRL [14].FIB_UNIDIR_EN[14:14]	1
0xBB0013C4	FIB_UNIDIR_CTRL [15].FIB_UNIDIR_EN[15:15]	1
0xBB0013C4	FIB_UNIDIR_CTRL [16].FIB_UNIDIR_EN[16:16]	1
0xBB0013C4	FIB_UNIDIR_CTRL [17].FIB_UNIDIR_EN[17:17]	1
0xBB0013C4	FIB_UNIDIR_CTRL [18].FIB_UNIDIR_EN[18:18]	1
0xBB0013C4	FIB_UNIDIR_CTRL [19].FIB_UNIDIR_EN[19:19]	1
0xBB0013C4	FIB_UNIDIR_CTRL [20].FIB_UNIDIR_EN[20:20]	1
0xBB0013C4	FIB_UNIDIR_CTRL [21].FIB_UNIDIR_EN[21:21]	1
0xBB0013C4	FIB_UNIDIR_CTRL [22].FIB_UNIDIR_EN[22:22]	1
0xBB0013C4	FIB_UNIDIR_CTRL [23].FIB_UNIDIR_EN[23:23]	1
0xBB0013C4	FIB_UNIDIR_CTRL [24].FIB_UNIDIR_EN[24:24]	1
0xBB0013C4	FIB_UNIDIR_CTRL [25].FIB_UNIDIR_EN[25:25]	1
0xBB0013C4	FIB_UNIDIR_CTRL [26].FIB_UNIDIR_EN[26:26]	1
0xBB0013C4	FIB_UNIDIR_CTRL [27].FIB_UNIDIR_EN[27:27]	1
0xBB0013C4	FIB_UNIDIR_CTRL [28].FIB_UNIDIR_EN[28:28]	1
0xBB0013C4	FIB_UNIDIR_CTRL [29].FIB_UNIDIR_EN[29:29]	1
0xBB0013C4	FIB_UNIDIR_CTRL [30].FIB_UNIDIR_EN[30:30]	1
0xBB0013C4	FIB_UNIDIR_CTRL [31].FIB_UNIDIR_EN[31:31]	1
0xBB0013C8	FIB_UNIDIR_CTRL [32].FIB_UNIDIR_EN[0:0]	1
0xBB0013C8	FIB_UNIDIR_CTRL [33].FIB_UNIDIR_EN[1:1]	1
0xBB0013C8	FIB_UNIDIR_CTRL [34].FIB_UNIDIR_EN[2:2]	1
0xBB0013C8	FIB_UNIDIR_CTRL [35].FIB_UNIDIR_EN[3:3]	1
0xBB0013C8	FIB_UNIDIR_CTRL [36].FIB_UNIDIR_EN[4:4]	1
0xBB0013C8	FIB_UNIDIR_CTRL [37].FIB_UNIDIR_EN[5:5]	1
0xBB0013C8	FIB_UNIDIR_CTRL [38].FIB_UNIDIR_EN[6:6]	1
0xBB0013C8	FIB_UNIDIR_CTRL [39].FIB_UNIDIR_EN[7:7]	1
0xBB0013C8	FIB_UNIDIR_CTRL [40].FIB_UNIDIR_EN[8:8]	1
0xBB0013C8	FIB_UNIDIR_CTRL [41].FIB_UNIDIR_EN[9:9]	1
0xBB0013C8	FIB_UNIDIR_CTRL [42].FIB_UNIDIR_EN[10:10]	1
0xBB0013C8	FIB_UNIDIR_CTRL [43].FIB_UNIDIR_EN[11:11]	1
0xBB0013C8	FIB_UNIDIR_CTRL [44].FIB_UNIDIR_EN[12:12]	1
0xBB0013C8	FIB_UNIDIR_CTRL [45].FIB_UNIDIR_EN[13:13]	1
0xBB0013C8	FIB_UNIDIR_CTRL [46].FIB_UNIDIR_EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0013C8	FIB_UNIDIR_CTRL [47].FIB_UNIDIR_EN[15:15]	1
0xBB0013C8	FIB_UNIDIR_CTRL [48].FIB_UNIDIR_EN[16:16]	1
0xBB0013C8	FIB_UNIDIR_CTRL [49].FIB_UNIDIR_EN[17:17]	1
0xBB0013C8	FIB_UNIDIR_CTRL [50].FIB_UNIDIR_EN[18:18]	1
0xBB0013C8	FIB_UNIDIR_CTRL [51].FIB_UNIDIR_EN[19:19]	1
0xBB0013C8	FIB_UNIDIR_CTRL [52].FIB_UNIDIR_EN[20:20]	1
0xBB0013C8	FIB_UNIDIR_CTRL [53].FIB_UNIDIR_EN[21:21]	1
0xBB0013C8	FIB_UNIDIR_CTRL [54].FIB_UNIDIR_EN[22:22]	1
0xBB0013C8	FIB_UNIDIR_CTRL [55].FIB_UNIDIR_EN[23:23]	1
0xBB008544	FLEX_TBL_CTRL.RESERVED[31:2]	30
0xBB008544	FLEX_TBL_CTRL.FLEX_TBL_FMT[1:0]	2
0xBB000F20	FORCE_MAC_SPD [0].FORCE_MAC_SPD[0:0]	1
0xBB000F20	FORCE_MAC_SPD [1].FORCE_MAC_SPD[1:1]	1
0xBB000F20	FORCE_MAC_SPD [2].FORCE_MAC_SPD[2:2]	1
0xBB000F20	FORCE_MAC_SPD [3].FORCE_MAC_SPD[3:3]	1
0xBB000F20	FORCE_MAC_SPD [4].FORCE_MAC_SPD[4:4]	1
0xBB000F20	FORCE_MAC_SPD [5].FORCE_MAC_SPD[5:5]	1
0xBB000F20	FORCE_MAC_SPD [6].FORCE_MAC_SPD[6:6]	1
0xBB000F20	FORCE_MAC_SPD [7].FORCE_MAC_SPD[7:7]	1
0xBB000F20	FORCE_MAC_SPD [8].FORCE_MAC_SPD[8:8]	1
0xBB000F20	FORCE_MAC_SPD [9].FORCE_MAC_SPD[9:9]	1
0xBB000F20	FORCE_MAC_SPD [10].FORCE_MAC_SPD[10:10]	1
0xBB000F20	FORCE_MAC_SPD [11].FORCE_MAC_SPD[11:11]	1
0xBB000F20	FORCE_MAC_SPD [12].FORCE_MAC_SPD[12:12]	1
0xBB000F20	FORCE_MAC_SPD [13].FORCE_MAC_SPD[13:13]	1
0xBB000F20	FORCE_MAC_SPD [14].FORCE_MAC_SPD[14:14]	1
0xBB000F20	FORCE_MAC_SPD [15].FORCE_MAC_SPD[15:15]	1
0xBB000F20	FORCE_MAC_SPD [16].FORCE_MAC_SPD[16:16]	1
0xBB000F20	FORCE_MAC_SPD [17].FORCE_MAC_SPD[17:17]	1
0xBB000F20	FORCE_MAC_SPD [18].FORCE_MAC_SPD[18:18]	1
0xBB000F20	FORCE_MAC_SPD [19].FORCE_MAC_SPD[19:19]	1
0xBB000F20	FORCE_MAC_SPD [20].FORCE_MAC_SPD[20:20]	1
0xBB000F20	FORCE_MAC_SPD [21].FORCE_MAC_SPD[21:21]	1
0xBB000F20	FORCE_MAC_SPD [22].FORCE_MAC_SPD[22:22]	1
0xBB000F20	FORCE_MAC_SPD [23].FORCE_MAC_SPD[23:23]	1
0xBB000F20	FORCE_MAC_SPD [24].FORCE_MAC_SPD[24:24]	1
0xBB000F20	FORCE_MAC_SPD [25].FORCE_MAC_SPD[25:25]	1
0xBB000F20	FORCE_MAC_SPD [26].FORCE_MAC_SPD[26:26]	1
0xBB000F20	FORCE_MAC_SPD [27].FORCE_MAC_SPD[27:27]	1
0xBB000F20	FORCE_MAC_SPD [28].FORCE_MAC_SPD[28:28]	1
0xBB000F20	FORCE_MAC_SPD [29].FORCE_MAC_SPD[29:29]	1
0xBB000F20	FORCE_MAC_SPD [30].FORCE_MAC_SPD[30:30]	1
0xBB000F20	FORCE_MAC_SPD [31].FORCE_MAC_SPD[31:31]	1
0xBB000F24	FORCE_MAC_SPD [32].FORCE_MAC_SPD[0:0]	1
0xBB000F24	FORCE_MAC_SPD [33].FORCE_MAC_SPD[1:1]	1
0xBB000F24	FORCE_MAC_SPD [34].FORCE_MAC_SPD[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F24	FORCE_MAC_SPD [35].FORCE_MAC_SPD[3:3]	1
0xBB000F24	FORCE_MAC_SPD [36].FORCE_MAC_SPD[4:4]	1
0xBB000F24	FORCE_MAC_SPD [37].FORCE_MAC_SPD[5:5]	1
0xBB000F24	FORCE_MAC_SPD [38].FORCE_MAC_SPD[6:6]	1
0xBB000F24	FORCE_MAC_SPD [39].FORCE_MAC_SPD[7:7]	1
0xBB000F24	FORCE_MAC_SPD [40].FORCE_MAC_SPD[8:8]	1
0xBB000F24	FORCE_MAC_SPD [41].FORCE_MAC_SPD[9:9]	1
0xBB000F24	FORCE_MAC_SPD [42].FORCE_MAC_SPD[10:10]	1
0xBB000F24	FORCE_MAC_SPD [43].FORCE_MAC_SPD[11:11]	1
0xBB000F24	FORCE_MAC_SPD [44].FORCE_MAC_SPD[12:12]	1
0xBB000F24	FORCE_MAC_SPD [45].FORCE_MAC_SPD[13:13]	1
0xBB000F24	FORCE_MAC_SPD [46].FORCE_MAC_SPD[14:14]	1
0xBB000F24	FORCE_MAC_SPD [47].FORCE_MAC_SPD[15:15]	1
0xBB000F24	FORCE_MAC_SPD [48].FORCE_MAC_SPD[16:16]	1
0xBB000F24	FORCE_MAC_SPD [49].FORCE_MAC_SPD[17:17]	1
0xBB000F24	FORCE_MAC_SPD [50].FORCE_MAC_SPD[18:18]	1
0xBB000F24	FORCE_MAC_SPD [51].FORCE_MAC_SPD[19:19]	1
0xBB000F24	FORCE_MAC_SPD [52].FORCE_MAC_SPD[20:20]	1
0xBB000F24	FORCE_MAC_SPD [53].FORCE_MAC_SPD[21:21]	1
0xBB000F24	FORCE_MAC_SPD [54].FORCE_MAC_SPD[22:22]	1
0xBB000F24	FORCE_MAC_SPD [55].FORCE_MAC_SPD[23:23]	1
0xBB000F24	FORCE_MAC_SPD [56].FORCE_MAC_SPD[24:24]	1
0xBB000570	GTP_PARSE_CTRL.USR_DATA_L4_PORT[31:16]	16
0xBB000570	GTP_PARSE_CTRL.CTRL_L4_PORT[15:0]	16
0xBB006008	HALF_CHG_CTRL [0].RESERVED[31:14]	18
0xBB006008	HALF_CHG_CTRL [0].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006008	HALF_CHG_CTRL [0].REF_RX_CONGEST[12:12]	1
0xBB006008	HALF_CHG_CTRL [0].CHG_DUPTHR[11:7]	5
0xBB006008	HALF_CHG_CTRL [0].FULL_DET_EN[6:6]	1
0xBB006008	HALF_CHG_CTRL [0].MAC_CHG_DUP[5:5]	1
0xBB006008	HALF_CHG_CTRL [0].COL_CUR_CNT[4:0]	5
0xBB006088	HALF_CHG_CTRL [1].RESERVED[31:14]	18
0xBB006088	HALF_CHG_CTRL [1].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006088	HALF_CHG_CTRL [1].REF_RX_CONGEST[12:12]	1
0xBB006088	HALF_CHG_CTRL [1].CHG_DUPTHR[11:7]	5
0xBB006088	HALF_CHG_CTRL [1].FULL_DET_EN[6:6]	1
0xBB006088	HALF_CHG_CTRL [1].MAC_CHG_DUP[5:5]	1
0xBB006088	HALF_CHG_CTRL [1].COL_CUR_CNT[4:0]	5
0xBB006108	HALF_CHG_CTRL [2].RESERVED[31:14]	18
0xBB006108	HALF_CHG_CTRL [2].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006108	HALF_CHG_CTRL [2].REF_RX_CONGEST[12:12]	1
0xBB006108	HALF_CHG_CTRL [2].CHG_DUPTHR[11:7]	5
0xBB006108	HALF_CHG_CTRL [2].FULL_DET_EN[6:6]	1
0xBB006108	HALF_CHG_CTRL [2].MAC_CHG_DUP[5:5]	1
0xBB006108	HALF_CHG_CTRL [2].COL_CUR_CNT[4:0]	5
0xBB006188	HALF_CHG_CTRL [3].RESERVED[31:14]	18

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006188	HALF_CHG_CTRL [3].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006188	HALF_CHG_CTRL [3].REF_RX_CONGEST[12:12]	1
0xBB006188	HALF_CHG_CTRL [3].CHG_DUP_THR[11:7]	5
0xBB006188	HALF_CHG_CTRL [3].FULL_DET_EN[6:6]	1
0xBB006188	HALF_CHG_CTRL [3].MAC_CHG_DUP[5:5]	1
0xBB006188	HALF_CHG_CTRL [3].COL_CUR_CNT[4:0]	5
0xBB006208	HALF_CHG_CTRL [4].RESERVED[31:14]	18
0xBB006208	HALF_CHG_CTRL [4].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006208	HALF_CHG_CTRL [4].REF_RX_CONGEST[12:12]	1
0xBB006208	HALF_CHG_CTRL [4].CHG_DUP_THR[11:7]	5
0xBB006208	HALF_CHG_CTRL [4].FULL_DET_EN[6:6]	1
0xBB006208	HALF_CHG_CTRL [4].MAC_CHG_DUP[5:5]	1
0xBB006208	HALF_CHG_CTRL [4].COL_CUR_CNT[4:0]	5
0xBB006288	HALF_CHG_CTRL [5].RESERVED[31:14]	18
0xBB006288	HALF_CHG_CTRL [5].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006288	HALF_CHG_CTRL [5].REF_RX_CONGEST[12:12]	1
0xBB006288	HALF_CHG_CTRL [5].CHG_DUP_THR[11:7]	5
0xBB006288	HALF_CHG_CTRL [5].FULL_DET_EN[6:6]	1
0xBB006288	HALF_CHG_CTRL [5].MAC_CHG_DUP[5:5]	1
0xBB006288	HALF_CHG_CTRL [5].COL_CUR_CNT[4:0]	5
0xBB006308	HALF_CHG_CTRL [6].RESERVED[31:14]	18
0xBB006308	HALF_CHG_CTRL [6].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006308	HALF_CHG_CTRL [6].REF_RX_CONGEST[12:12]	1
0xBB006308	HALF_CHG_CTRL [6].CHG_DUP_THR[11:7]	5
0xBB006308	HALF_CHG_CTRL [6].FULL_DET_EN[6:6]	1
0xBB006308	HALF_CHG_CTRL [6].MAC_CHG_DUP[5:5]	1
0xBB006308	HALF_CHG_CTRL [6].COL_CUR_CNT[4:0]	5
0xBB006388	HALF_CHG_CTRL [7].RESERVED[31:14]	18
0xBB006388	HALF_CHG_CTRL [7].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006388	HALF_CHG_CTRL [7].REF_RX_CONGEST[12:12]	1
0xBB006388	HALF_CHG_CTRL [7].CHG_DUP_THR[11:7]	5
0xBB006388	HALF_CHG_CTRL [7].FULL_DET_EN[6:6]	1
0xBB006388	HALF_CHG_CTRL [7].MAC_CHG_DUP[5:5]	1
0xBB006388	HALF_CHG_CTRL [7].COL_CUR_CNT[4:0]	5
0xBB006408	HALF_CHG_CTRL [8].RESERVED[31:14]	18
0xBB006408	HALF_CHG_CTRL [8].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006408	HALF_CHG_CTRL [8].REF_RX_CONGEST[12:12]	1
0xBB006408	HALF_CHG_CTRL [8].CHG_DUP_THR[11:7]	5
0xBB006408	HALF_CHG_CTRL [8].FULL_DET_EN[6:6]	1
0xBB006408	HALF_CHG_CTRL [8].MAC_CHG_DUP[5:5]	1
0xBB006408	HALF_CHG_CTRL [8].COL_CUR_CNT[4:0]	5
0xBB006488	HALF_CHG_CTRL [9].RESERVED[31:14]	18
0xBB006488	HALF_CHG_CTRL [9].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006488	HALF_CHG_CTRL [9].REF_RX_CONGEST[12:12]	1
0xBB006488	HALF_CHG_CTRL [9].CHG_DUP_THR[11:7]	5
0xBB006488	HALF_CHG_CTRL [9].FULL_DET_EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006488	HALF_CHG_CTRL [9].MAC_CHG_DUP[5:5]	1
0xBB006488	HALF_CHG_CTRL [9].COL_CUR_CNT[4:0]	5
0xBB006508	HALF_CHG_CTRL [10].RESERVED[31:14]	18
0xBB006508	HALF_CHG_CTRL [10].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006508	HALF_CHG_CTRL [10].REF_RX_CONGEST[12:12]	1
0xBB006508	HALF_CHG_CTRL [10].CHG_DUP_THR[11:7]	5
0xBB006508	HALF_CHG_CTRL [10].FULL_DET_EN[6:6]	1
0xBB006508	HALF_CHG_CTRL [10].MAC_CHG_DUP[5:5]	1
0xBB006508	HALF_CHG_CTRL [10].COL_CUR_CNT[4:0]	5
0xBB006588	HALF_CHG_CTRL [11].RESERVED[31:14]	18
0xBB006588	HALF_CHG_CTRL [11].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006588	HALF_CHG_CTRL [11].REF_RX_CONGEST[12:12]	1
0xBB006588	HALF_CHG_CTRL [11].CHG_DUP_THR[11:7]	5
0xBB006588	HALF_CHG_CTRL [11].FULL_DET_EN[6:6]	1
0xBB006588	HALF_CHG_CTRL [11].MAC_CHG_DUP[5:5]	1
0xBB006588	HALF_CHG_CTRL [11].COL_CUR_CNT[4:0]	5
0xBB006608	HALF_CHG_CTRL [12].RESERVED[31:14]	18
0xBB006608	HALF_CHG_CTRL [12].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006608	HALF_CHG_CTRL [12].REF_RX_CONGEST[12:12]	1
0xBB006608	HALF_CHG_CTRL [12].CHG_DUP_THR[11:7]	5
0xBB006608	HALF_CHG_CTRL [12].FULL_DET_EN[6:6]	1
0xBB006608	HALF_CHG_CTRL [12].MAC_CHG_DUP[5:5]	1
0xBB006608	HALF_CHG_CTRL [12].COL_CUR_CNT[4:0]	5
0xBB006688	HALF_CHG_CTRL [13].RESERVED[31:14]	18
0xBB006688	HALF_CHG_CTRL [13].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006688	HALF_CHG_CTRL [13].REF_RX_CONGEST[12:12]	1
0xBB006688	HALF_CHG_CTRL [13].CHG_DUP_THR[11:7]	5
0xBB006688	HALF_CHG_CTRL [13].FULL_DET_EN[6:6]	1
0xBB006688	HALF_CHG_CTRL [13].MAC_CHG_DUP[5:5]	1
0xBB006688	HALF_CHG_CTRL [13].COL_CUR_CNT[4:0]	5
0xBB006708	HALF_CHG_CTRL [14].RESERVED[31:14]	18
0xBB006708	HALF_CHG_CTRL [14].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006708	HALF_CHG_CTRL [14].REF_RX_CONGEST[12:12]	1
0xBB006708	HALF_CHG_CTRL [14].CHG_DUP_THR[11:7]	5
0xBB006708	HALF_CHG_CTRL [14].FULL_DET_EN[6:6]	1
0xBB006708	HALF_CHG_CTRL [14].MAC_CHG_DUP[5:5]	1
0xBB006708	HALF_CHG_CTRL [14].COL_CUR_CNT[4:0]	5
0xBB006788	HALF_CHG_CTRL [15].RESERVED[31:14]	18
0xBB006788	HALF_CHG_CTRL [15].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006788	HALF_CHG_CTRL [15].REF_RX_CONGEST[12:12]	1
0xBB006788	HALF_CHG_CTRL [15].CHG_DUP_THR[11:7]	5
0xBB006788	HALF_CHG_CTRL [15].FULL_DET_EN[6:6]	1
0xBB006788	HALF_CHG_CTRL [15].MAC_CHG_DUP[5:5]	1
0xBB006788	HALF_CHG_CTRL [15].COL_CUR_CNT[4:0]	5
0xBB006808	HALF_CHG_CTRL [16].RESERVED[31:14]	18
0xBB006808	HALF_CHG_CTRL [16].HALF_TO_FULL_PAUSE[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006808	HALF_CHG_CTRL [16].REF_RX_CONGEST[12:12]	1
0xBB006808	HALF_CHG_CTRL [16].CHG_DUP_THR[11:7]	5
0xBB006808	HALF_CHG_CTRL [16].FULL_DET_EN[6:6]	1
0xBB006808	HALF_CHG_CTRL [16].MAC_CHG_DUP[5:5]	1
0xBB006808	HALF_CHG_CTRL [16].COL_CUR_CNT[4:0]	5
0xBB006888	HALF_CHG_CTRL [17].RESERVED[31:14]	18
0xBB006888	HALF_CHG_CTRL [17].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006888	HALF_CHG_CTRL [17].REF_RX_CONGEST[12:12]	1
0xBB006888	HALF_CHG_CTRL [17].CHG_DUP_THR[11:7]	5
0xBB006888	HALF_CHG_CTRL [17].FULL_DET_EN[6:6]	1
0xBB006888	HALF_CHG_CTRL [17].MAC_CHG_DUP[5:5]	1
0xBB006888	HALF_CHG_CTRL [17].COL_CUR_CNT[4:0]	5
0xBB006908	HALF_CHG_CTRL [18].RESERVED[31:14]	18
0xBB006908	HALF_CHG_CTRL [18].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006908	HALF_CHG_CTRL [18].REF_RX_CONGEST[12:12]	1
0xBB006908	HALF_CHG_CTRL [18].CHG_DUP_THR[11:7]	5
0xBB006908	HALF_CHG_CTRL [18].FULL_DET_EN[6:6]	1
0xBB006908	HALF_CHG_CTRL [18].MAC_CHG_DUP[5:5]	1
0xBB006908	HALF_CHG_CTRL [18].COL_CUR_CNT[4:0]	5
0xBB006988	HALF_CHG_CTRL [19].RESERVED[31:14]	18
0xBB006988	HALF_CHG_CTRL [19].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006988	HALF_CHG_CTRL [19].REF_RX_CONGEST[12:12]	1
0xBB006988	HALF_CHG_CTRL [19].CHG_DUP_THR[11:7]	5
0xBB006988	HALF_CHG_CTRL [19].FULL_DET_EN[6:6]	1
0xBB006988	HALF_CHG_CTRL [19].MAC_CHG_DUP[5:5]	1
0xBB006988	HALF_CHG_CTRL [19].COL_CUR_CNT[4:0]	5
0xBB006A08	HALF_CHG_CTRL [20].RESERVED[31:14]	18
0xBB006A08	HALF_CHG_CTRL [20].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006A08	HALF_CHG_CTRL [20].REF_RX_CONGEST[12:12]	1
0xBB006A08	HALF_CHG_CTRL [20].CHG_DUP_THR[11:7]	5
0xBB006A08	HALF_CHG_CTRL [20].FULL_DET_EN[6:6]	1
0xBB006A08	HALF_CHG_CTRL [20].MAC_CHG_DUP[5:5]	1
0xBB006A08	HALF_CHG_CTRL [20].COL_CUR_CNT[4:0]	5
0xBB006A88	HALF_CHG_CTRL [21].RESERVED[31:14]	18
0xBB006A88	HALF_CHG_CTRL [21].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006A88	HALF_CHG_CTRL [21].REF_RX_CONGEST[12:12]	1
0xBB006A88	HALF_CHG_CTRL [21].CHG_DUP_THR[11:7]	5
0xBB006A88	HALF_CHG_CTRL [21].FULL_DET_EN[6:6]	1
0xBB006A88	HALF_CHG_CTRL [21].MAC_CHG_DUP[5:5]	1
0xBB006A88	HALF_CHG_CTRL [21].COL_CUR_CNT[4:0]	5
0xBB006B08	HALF_CHG_CTRL [22].RESERVED[31:14]	18
0xBB006B08	HALF_CHG_CTRL [22].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006B08	HALF_CHG_CTRL [22].REF_RX_CONGEST[12:12]	1
0xBB006B08	HALF_CHG_CTRL [22].CHG_DUP_THR[11:7]	5
0xBB006B08	HALF_CHG_CTRL [22].FULL_DET_EN[6:6]	1
0xBB006B08	HALF_CHG_CTRL [22].MAC_CHG_DUP[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006B08	HALF_CHG_CTRL [22].COL_CUR_CNT[4:0]	5
0xBB006B88	HALF_CHG_CTRL [23].RESERVED[31:14]	18
0xBB006B88	HALF_CHG_CTRL [23].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006B88	HALF_CHG_CTRL [23].REF_RX_CONGEST[12:12]	1
0xBB006B88	HALF_CHG_CTRL [23].CHG_DUP_THR[11:7]	5
0xBB006B88	HALF_CHG_CTRL [23].FULL_DET_EN[6:6]	1
0xBB006B88	HALF_CHG_CTRL [23].MAC_CHG_DUP[5:5]	1
0xBB006B88	HALF_CHG_CTRL [23].COL_CUR_CNT[4:0]	5
0xBB006C08	HALF_CHG_CTRL [24].RESERVED[31:14]	18
0xBB006C08	HALF_CHG_CTRL [24].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006C08	HALF_CHG_CTRL [24].REF_RX_CONGEST[12:12]	1
0xBB006C08	HALF_CHG_CTRL [24].CHG_DUP_THR[11:7]	5
0xBB006C08	HALF_CHG_CTRL [24].FULL_DET_EN[6:6]	1
0xBB006C08	HALF_CHG_CTRL [24].MAC_CHG_DUP[5:5]	1
0xBB006C08	HALF_CHG_CTRL [24].COL_CUR_CNT[4:0]	5
0xBB006C88	HALF_CHG_CTRL [25].RESERVED[31:14]	18
0xBB006C88	HALF_CHG_CTRL [25].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006C88	HALF_CHG_CTRL [25].REF_RX_CONGEST[12:12]	1
0xBB006C88	HALF_CHG_CTRL [25].CHG_DUP_THR[11:7]	5
0xBB006C88	HALF_CHG_CTRL [25].FULL_DET_EN[6:6]	1
0xBB006C88	HALF_CHG_CTRL [25].MAC_CHG_DUP[5:5]	1
0xBB006C88	HALF_CHG_CTRL [25].COL_CUR_CNT[4:0]	5
0xBB006D08	HALF_CHG_CTRL [26].RESERVED[31:14]	18
0xBB006D08	HALF_CHG_CTRL [26].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006D08	HALF_CHG_CTRL [26].REF_RX_CONGEST[12:12]	1
0xBB006D08	HALF_CHG_CTRL [26].CHG_DUP_THR[11:7]	5
0xBB006D08	HALF_CHG_CTRL [26].FULL_DET_EN[6:6]	1
0xBB006D08	HALF_CHG_CTRL [26].MAC_CHG_DUP[5:5]	1
0xBB006D08	HALF_CHG_CTRL [26].COL_CUR_CNT[4:0]	5
0xBB006D88	HALF_CHG_CTRL [27].RESERVED[31:14]	18
0xBB006D88	HALF_CHG_CTRL [27].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006D88	HALF_CHG_CTRL [27].REF_RX_CONGEST[12:12]	1
0xBB006D88	HALF_CHG_CTRL [27].CHG_DUP_THR[11:7]	5
0xBB006D88	HALF_CHG_CTRL [27].FULL_DET_EN[6:6]	1
0xBB006D88	HALF_CHG_CTRL [27].MAC_CHG_DUP[5:5]	1
0xBB006D88	HALF_CHG_CTRL [27].COL_CUR_CNT[4:0]	5
0xBB006E08	HALF_CHG_CTRL [28].RESERVED[31:14]	18
0xBB006E08	HALF_CHG_CTRL [28].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006E08	HALF_CHG_CTRL [28].REF_RX_CONGEST[12:12]	1
0xBB006E08	HALF_CHG_CTRL [28].CHG_DUP_THR[11:7]	5
0xBB006E08	HALF_CHG_CTRL [28].FULL_DET_EN[6:6]	1
0xBB006E08	HALF_CHG_CTRL [28].MAC_CHG_DUP[5:5]	1
0xBB006E08	HALF_CHG_CTRL [28].COL_CUR_CNT[4:0]	5
0xBB006E88	HALF_CHG_CTRL [29].RESERVED[31:14]	18
0xBB006E88	HALF_CHG_CTRL [29].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006E88	HALF_CHG_CTRL [29].REF_RX_CONGEST[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006E88	HALF_CHG_CTRL [29].CHG_DUP_THR[11:7]	5
0xBB006E88	HALF_CHG_CTRL [29].FULL_DET_EN[6:6]	1
0xBB006E88	HALF_CHG_CTRL [29].MAC_CHG_DUP[5:5]	1
0xBB006E88	HALF_CHG_CTRL [29].COL_CUR_CNT[4:0]	5
0xBB006F08	HALF_CHG_CTRL [30].RESERVED[31:14]	18
0xBB006F08	HALF_CHG_CTRL [30].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006F08	HALF_CHG_CTRL [30].REF_RX_CONGEST[12:12]	1
0xBB006F08	HALF_CHG_CTRL [30].CHG_DUP_THR[11:7]	5
0xBB006F08	HALF_CHG_CTRL [30].FULL_DET_EN[6:6]	1
0xBB006F08	HALF_CHG_CTRL [30].MAC_CHG_DUP[5:5]	1
0xBB006F08	HALF_CHG_CTRL [30].COL_CUR_CNT[4:0]	5
0xBB006F88	HALF_CHG_CTRL [31].RESERVED[31:14]	18
0xBB006F88	HALF_CHG_CTRL [31].HALF_TO_FULL_PAUSE[13:13]	1
0xBB006F88	HALF_CHG_CTRL [31].REF_RX_CONGEST[12:12]	1
0xBB006F88	HALF_CHG_CTRL [31].CHG_DUP_THR[11:7]	5
0xBB006F88	HALF_CHG_CTRL [31].FULL_DET_EN[6:6]	1
0xBB006F88	HALF_CHG_CTRL [31].MAC_CHG_DUP[5:5]	1
0xBB006F88	HALF_CHG_CTRL [31].COL_CUR_CNT[4:0]	5
0xBB007008	HALF_CHG_CTRL [32].RESERVED[31:14]	18
0xBB007008	HALF_CHG_CTRL [32].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007008	HALF_CHG_CTRL [32].REF_RX_CONGEST[12:12]	1
0xBB007008	HALF_CHG_CTRL [32].CHG_DUP_THR[11:7]	5
0xBB007008	HALF_CHG_CTRL [32].FULL_DET_EN[6:6]	1
0xBB007008	HALF_CHG_CTRL [32].MAC_CHG_DUP[5:5]	1
0xBB007008	HALF_CHG_CTRL [32].COL_CUR_CNT[4:0]	5
0xBB007088	HALF_CHG_CTRL [33].RESERVED[31:14]	18
0xBB007088	HALF_CHG_CTRL [33].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007088	HALF_CHG_CTRL [33].REF_RX_CONGEST[12:12]	1
0xBB007088	HALF_CHG_CTRL [33].CHG_DUP_THR[11:7]	5
0xBB007088	HALF_CHG_CTRL [33].FULL_DET_EN[6:6]	1
0xBB007088	HALF_CHG_CTRL [33].MAC_CHG_DUP[5:5]	1
0xBB007088	HALF_CHG_CTRL [33].COL_CUR_CNT[4:0]	5
0xBB007108	HALF_CHG_CTRL [34].RESERVED[31:14]	18
0xBB007108	HALF_CHG_CTRL [34].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007108	HALF_CHG_CTRL [34].REF_RX_CONGEST[12:12]	1
0xBB007108	HALF_CHG_CTRL [34].CHG_DUP_THR[11:7]	5
0xBB007108	HALF_CHG_CTRL [34].FULL_DET_EN[6:6]	1
0xBB007108	HALF_CHG_CTRL [34].MAC_CHG_DUP[5:5]	1
0xBB007108	HALF_CHG_CTRL [34].COL_CUR_CNT[4:0]	5
0xBB007188	HALF_CHG_CTRL [35].RESERVED[31:14]	18
0xBB007188	HALF_CHG_CTRL [35].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007188	HALF_CHG_CTRL [35].REF_RX_CONGEST[12:12]	1
0xBB007188	HALF_CHG_CTRL [35].CHG_DUP_THR[11:7]	5
0xBB007188	HALF_CHG_CTRL [35].FULL_DET_EN[6:6]	1
0xBB007188	HALF_CHG_CTRL [35].MAC_CHG_DUP[5:5]	1
0xBB007188	HALF_CHG_CTRL [35].COL_CUR_CNT[4:0]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007208	HALF_CHG_CTRL [36].RESERVED[31:14]	18
0xBB007208	HALF_CHG_CTRL [36].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007208	HALF_CHG_CTRL [36].REF_RX_CONGEST[12:12]	1
0xBB007208	HALF_CHG_CTRL [36].CHG_DUP_THR[11:7]	5
0xBB007208	HALF_CHG_CTRL [36].FULL_DET_EN[6:6]	1
0xBB007208	HALF_CHG_CTRL [36].MAC_CHG_DUP[5:5]	1
0xBB007208	HALF_CHG_CTRL [36].COL_CUR_CNT[4:0]	5
0xBB007288	HALF_CHG_CTRL [37].RESERVED[31:14]	18
0xBB007288	HALF_CHG_CTRL [37].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007288	HALF_CHG_CTRL [37].REF_RX_CONGEST[12:12]	1
0xBB007288	HALF_CHG_CTRL [37].CHG_DUP_THR[11:7]	5
0xBB007288	HALF_CHG_CTRL [37].FULL_DET_EN[6:6]	1
0xBB007288	HALF_CHG_CTRL [37].MAC_CHG_DUP[5:5]	1
0xBB007288	HALF_CHG_CTRL [37].COL_CUR_CNT[4:0]	5
0xBB007308	HALF_CHG_CTRL [38].RESERVED[31:14]	18
0xBB007308	HALF_CHG_CTRL [38].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007308	HALF_CHG_CTRL [38].REF_RX_CONGEST[12:12]	1
0xBB007308	HALF_CHG_CTRL [38].CHG_DUP_THR[11:7]	5
0xBB007308	HALF_CHG_CTRL [38].FULL_DET_EN[6:6]	1
0xBB007308	HALF_CHG_CTRL [38].MAC_CHG_DUP[5:5]	1
0xBB007308	HALF_CHG_CTRL [38].COL_CUR_CNT[4:0]	5
0xBB007388	HALF_CHG_CTRL [39].RESERVED[31:14]	18
0xBB007388	HALF_CHG_CTRL [39].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007388	HALF_CHG_CTRL [39].REF_RX_CONGEST[12:12]	1
0xBB007388	HALF_CHG_CTRL [39].CHG_DUP_THR[11:7]	5
0xBB007388	HALF_CHG_CTRL [39].FULL_DET_EN[6:6]	1
0xBB007388	HALF_CHG_CTRL [39].MAC_CHG_DUP[5:5]	1
0xBB007388	HALF_CHG_CTRL [39].COL_CUR_CNT[4:0]	5
0xBB007408	HALF_CHG_CTRL [40].RESERVED[31:14]	18
0xBB007408	HALF_CHG_CTRL [40].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007408	HALF_CHG_CTRL [40].REF_RX_CONGEST[12:12]	1
0xBB007408	HALF_CHG_CTRL [40].CHG_DUP_THR[11:7]	5
0xBB007408	HALF_CHG_CTRL [40].FULL_DET_EN[6:6]	1
0xBB007408	HALF_CHG_CTRL [40].MAC_CHG_DUP[5:5]	1
0xBB007408	HALF_CHG_CTRL [40].COL_CUR_CNT[4:0]	5
0xBB007488	HALF_CHG_CTRL [41].RESERVED[31:14]	18
0xBB007488	HALF_CHG_CTRL [41].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007488	HALF_CHG_CTRL [41].REF_RX_CONGEST[12:12]	1
0xBB007488	HALF_CHG_CTRL [41].CHG_DUP_THR[11:7]	5
0xBB007488	HALF_CHG_CTRL [41].FULL_DET_EN[6:6]	1
0xBB007488	HALF_CHG_CTRL [41].MAC_CHG_DUP[5:5]	1
0xBB007488	HALF_CHG_CTRL [41].COL_CUR_CNT[4:0]	5
0xBB007508	HALF_CHG_CTRL [42].RESERVED[31:14]	18
0xBB007508	HALF_CHG_CTRL [42].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007508	HALF_CHG_CTRL [42].REF_RX_CONGEST[12:12]	1
0xBB007508	HALF_CHG_CTRL [42].CHG_DUP_THR[11:7]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007508	HALF_CHG_CTRL [42].FULL_DET_EN[6:6]	1
0xBB007508	HALF_CHG_CTRL [42].MAC_CHG_DUP[5:5]	1
0xBB007508	HALF_CHG_CTRL [42].COL_CUR_CNT[4:0]	5
0xBB007588	HALF_CHG_CTRL [43].RESERVED[31:14]	18
0xBB007588	HALF_CHG_CTRL [43].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007588	HALF_CHG_CTRL [43].REF_RX_CONGEST[12:12]	1
0xBB007588	HALF_CHG_CTRL [43].CHG_DUP_THR[11:7]	5
0xBB007588	HALF_CHG_CTRL [43].FULL_DET_EN[6:6]	1
0xBB007588	HALF_CHG_CTRL [43].MAC_CHG_DUP[5:5]	1
0xBB007588	HALF_CHG_CTRL [43].COL_CUR_CNT[4:0]	5
0xBB007608	HALF_CHG_CTRL [44].RESERVED[31:14]	18
0xBB007608	HALF_CHG_CTRL [44].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007608	HALF_CHG_CTRL [44].REF_RX_CONGEST[12:12]	1
0xBB007608	HALF_CHG_CTRL [44].CHG_DUP_THR[11:7]	5
0xBB007608	HALF_CHG_CTRL [44].FULL_DET_EN[6:6]	1
0xBB007608	HALF_CHG_CTRL [44].MAC_CHG_DUP[5:5]	1
0xBB007608	HALF_CHG_CTRL [44].COL_CUR_CNT[4:0]	5
0xBB007688	HALF_CHG_CTRL [45].RESERVED[31:14]	18
0xBB007688	HALF_CHG_CTRL [45].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007688	HALF_CHG_CTRL [45].REF_RX_CONGEST[12:12]	1
0xBB007688	HALF_CHG_CTRL [45].CHG_DUP_THR[11:7]	5
0xBB007688	HALF_CHG_CTRL [45].FULL_DET_EN[6:6]	1
0xBB007688	HALF_CHG_CTRL [45].MAC_CHG_DUP[5:5]	1
0xBB007688	HALF_CHG_CTRL [45].COL_CUR_CNT[4:0]	5
0xBB007708	HALF_CHG_CTRL [46].RESERVED[31:14]	18
0xBB007708	HALF_CHG_CTRL [46].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007708	HALF_CHG_CTRL [46].REF_RX_CONGEST[12:12]	1
0xBB007708	HALF_CHG_CTRL [46].CHG_DUP_THR[11:7]	5
0xBB007708	HALF_CHG_CTRL [46].FULL_DET_EN[6:6]	1
0xBB007708	HALF_CHG_CTRL [46].MAC_CHG_DUP[5:5]	1
0xBB007708	HALF_CHG_CTRL [46].COL_CUR_CNT[4:0]	5
0xBB007788	HALF_CHG_CTRL [47].RESERVED[31:14]	18
0xBB007788	HALF_CHG_CTRL [47].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007788	HALF_CHG_CTRL [47].REF_RX_CONGEST[12:12]	1
0xBB007788	HALF_CHG_CTRL [47].CHG_DUP_THR[11:7]	5
0xBB007788	HALF_CHG_CTRL [47].FULL_DET_EN[6:6]	1
0xBB007788	HALF_CHG_CTRL [47].MAC_CHG_DUP[5:5]	1
0xBB007788	HALF_CHG_CTRL [47].COL_CUR_CNT[4:0]	5
0xBB007808	HALF_CHG_CTRL [48].RESERVED[31:14]	18
0xBB007808	HALF_CHG_CTRL [48].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007808	HALF_CHG_CTRL [48].REF_RX_CONGEST[12:12]	1
0xBB007808	HALF_CHG_CTRL [48].CHG_DUP_THR[11:7]	5
0xBB007808	HALF_CHG_CTRL [48].FULL_DET_EN[6:6]	1
0xBB007808	HALF_CHG_CTRL [48].MAC_CHG_DUP[5:5]	1
0xBB007808	HALF_CHG_CTRL [48].COL_CUR_CNT[4:0]	5
0xBB007888	HALF_CHG_CTRL [49].RESERVED[31:14]	18

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007888	HALF_CHG_CTRL [49].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007888	HALF_CHG_CTRL [49].REF_RX_CONGEST[12:12]	1
0xBB007888	HALF_CHG_CTRL [49].CHG_DUP_THR[11:7]	5
0xBB007888	HALF_CHG_CTRL [49].FULL_DET_EN[6:6]	1
0xBB007888	HALF_CHG_CTRL [49].MAC_CHG_DUP[5:5]	1
0xBB007888	HALF_CHG_CTRL [49].COL_CUR_CNT[4:0]	5
0xBB007908	HALF_CHG_CTRL [50].RESERVED[31:14]	18
0xBB007908	HALF_CHG_CTRL [50].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007908	HALF_CHG_CTRL [50].REF_RX_CONGEST[12:12]	1
0xBB007908	HALF_CHG_CTRL [50].CHG_DUP_THR[11:7]	5
0xBB007908	HALF_CHG_CTRL [50].FULL_DET_EN[6:6]	1
0xBB007908	HALF_CHG_CTRL [50].MAC_CHG_DUP[5:5]	1
0xBB007908	HALF_CHG_CTRL [50].COL_CUR_CNT[4:0]	5
0xBB007988	HALF_CHG_CTRL [51].RESERVED[31:14]	18
0xBB007988	HALF_CHG_CTRL [51].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007988	HALF_CHG_CTRL [51].REF_RX_CONGEST[12:12]	1
0xBB007988	HALF_CHG_CTRL [51].CHG_DUP_THR[11:7]	5
0xBB007988	HALF_CHG_CTRL [51].FULL_DET_EN[6:6]	1
0xBB007988	HALF_CHG_CTRL [51].MAC_CHG_DUP[5:5]	1
0xBB007988	HALF_CHG_CTRL [51].COL_CUR_CNT[4:0]	5
0xBB007A08	HALF_CHG_CTRL [52].RESERVED[31:14]	18
0xBB007A08	HALF_CHG_CTRL [52].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007A08	HALF_CHG_CTRL [52].REF_RX_CONGEST[12:12]	1
0xBB007A08	HALF_CHG_CTRL [52].CHG_DUP_THR[11:7]	5
0xBB007A08	HALF_CHG_CTRL [52].FULL_DET_EN[6:6]	1
0xBB007A08	HALF_CHG_CTRL [52].MAC_CHG_DUP[5:5]	1
0xBB007A08	HALF_CHG_CTRL [52].COL_CUR_CNT[4:0]	5
0xBB007A88	HALF_CHG_CTRL [53].RESERVED[31:14]	18
0xBB007A88	HALF_CHG_CTRL [53].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007A88	HALF_CHG_CTRL [53].REF_RX_CONGEST[12:12]	1
0xBB007A88	HALF_CHG_CTRL [53].CHG_DUP_THR[11:7]	5
0xBB007A88	HALF_CHG_CTRL [53].FULL_DET_EN[6:6]	1
0xBB007A88	HALF_CHG_CTRL [53].MAC_CHG_DUP[5:5]	1
0xBB007A88	HALF_CHG_CTRL [53].COL_CUR_CNT[4:0]	5
0xBB007B08	HALF_CHG_CTRL [54].RESERVED[31:14]	18
0xBB007B08	HALF_CHG_CTRL [54].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007B08	HALF_CHG_CTRL [54].REF_RX_CONGEST[12:12]	1
0xBB007B08	HALF_CHG_CTRL [54].CHG_DUP_THR[11:7]	5
0xBB007B08	HALF_CHG_CTRL [54].FULL_DET_EN[6:6]	1
0xBB007B08	HALF_CHG_CTRL [54].MAC_CHG_DUP[5:5]	1
0xBB007B08	HALF_CHG_CTRL [54].COL_CUR_CNT[4:0]	5
0xBB007B88	HALF_CHG_CTRL [55].RESERVED[31:14]	18
0xBB007B88	HALF_CHG_CTRL [55].HALF_TO_FULL_PAUSE[13:13]	1
0xBB007B88	HALF_CHG_CTRL [55].REF_RX_CONGEST[12:12]	1
0xBB007B88	HALF_CHG_CTRL [55].CHG_DUP_THR[11:7]	5
0xBB007B88	HALF_CHG_CTRL [55].FULL_DET_EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007B88	HALF_CHG_CTRL [55].MAC_CHG_DUP[5:5]	1
0xBB007B88	HALF_CHG_CTRL [55].COL_CUR_CNT[4:0]	5
0xBB00100C	I2C_MST1_CTRL.SCL_FREQ[31:30]	2
0xBB00100C	I2C_MST1_CTRL.CHK_ACK_DLY[29:26]	4
0xBB00100C	I2C_MST1_CTRL.DRV_ACK_DLY[25:22]	4
0xBB00100C	I2C_MST1_CTRL.SDA_OUT_SEL[21:18]	4
0xBB00100C	I2C_MST1_CTRL.DEV_ADDR[17:11]	7
0xBB00100C	I2C_MST1_CTRL.MEM_ADDR_WIDTH[10:9]	2
0xBB00100C	I2C_MST1_CTRL.DATA_WIDTH[8:5]	4
0xBB00100C	I2C_MST1_CTRL.READ_MODE[4:4]	1
0xBB00100C	I2C_MST1_CTRL.RST[3:3]	1
0xBB00100C	I2C_MST1_CTRL.RWOP[2:2]	1
0xBB00100C	I2C_MST1_CTRL.FAIL[1:1]	1
0xBB00100C	I2C_MST1_CTRL.TRIG[0:0]	1
0xBB001014	I2C_MST1_DATA_CTRL [0].DATA[7:0]	8
0xBB001014	I2C_MST1_DATA_CTRL [1].DATA[15:8]	8
0xBB001014	I2C_MST1_DATA_CTRL [2].DATA[23:16]	8
0xBB001014	I2C_MST1_DATA_CTRL [3].DATA[31:24]	8
0xBB001018	I2C_MST1_DATA_CTRL [4].DATA[7:0]	8
0xBB001018	I2C_MST1_DATA_CTRL [5].DATA[15:8]	8
0xBB001018	I2C_MST1_DATA_CTRL [6].DATA[23:16]	8
0xBB001018	I2C_MST1_DATA_CTRL [7].DATA[31:24]	8
0xBB00101C	I2C_MST1_DATA_CTRL [8].DATA[7:0]	8
0xBB00101C	I2C_MST1_DATA_CTRL [9].DATA[15:8]	8
0xBB00101C	I2C_MST1_DATA_CTRL [10].DATA[23:16]	8
0xBB00101C	I2C_MST1_DATA_CTRL [11].DATA[31:24]	8
0xBB001020	I2C_MST1_DATA_CTRL [12].DATA[7:0]	8
0xBB001020	I2C_MST1_DATA_CTRL [13].DATA[15:8]	8
0xBB001020	I2C_MST1_DATA_CTRL [14].DATA[23:16]	8
0xBB001020	I2C_MST1_DATA_CTRL [15].DATA[31:24]	8
0xBB001010	I2C_MST1_MEMADDR_CTRL.RESERVED[31:24]	8
0xBB001010	I2C_MST1_MEMADDR_CTRL..MEM_ADDR[23:0]	24
0xBB001024	I2C_MST2_CTRL.SCL_FREQ[31:30]	2
0xBB001024	I2C_MST2_CTRL.CHK_ACK_DLY[29:26]	4
0xBB001024	I2C_MST2_CTRL.DRV_ACK_DLY[25:22]	4
0xBB001024	I2C_MST2_CTRL.SDA_OUT_SEL[21:18]	4
0xBB001024	I2C_MST2_CTRL.DEV_ADDR[17:11]	7
0xBB001024	I2C_MST2_CTRL.MEM_ADDR_WIDTH[10:9]	2
0xBB001024	I2C_MST2_CTRL.DATA_WIDTH[8:5]	4
0xBB001024	I2C_MST2_CTRL.READ_MODE[4:4]	1
0xBB001024	I2C_MST2_CTRL.RST[3:3]	1
0xBB001024	I2C_MST2_CTRL.RWOP[2:2]	1
0xBB001024	I2C_MST2_CTRL.FAIL[1:1]	1
0xBB001024	I2C_MST2_CTRL.TRIG[0:0]	1
0xBB00102C	I2C_MST2_DATA_CTRL [0].DATA[7:0]	8
0xBB00102C	I2C_MST2_DATA_CTRL [1].DATA[15:8]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00102C	I2C_MST2_DATA_CTRL[2].DATA[23:16]	8
0xBB00102C	I2C_MST2_DATA_CTRL[3].DATA[31:24]	8
0xBB001030	I2C_MST2_DATA_CTRL[4].DATA[7:0]	8
0xBB001030	I2C_MST2_DATA_CTRL[5].DATA[15:8]	8
0xBB001030	I2C_MST2_DATA_CTRL[6].DATA[23:16]	8
0xBB001030	I2C_MST2_DATA_CTRL[7].DATA[31:24]	8
0xBB001034	I2C_MST2_DATA_CTRL[8].DATA[7:0]	8
0xBB001034	I2C_MST2_DATA_CTRL[9].DATA[15:8]	8
0xBB001034	I2C_MST2_DATA_CTRL[10].DATA[23:16]	8
0xBB001034	I2C_MST2_DATA_CTRL[11].DATA[31:24]	8
0xBB001038	I2C_MST2_DATA_CTRL[12].DATA[7:0]	8
0xBB001038	I2C_MST2_DATA_CTRL[13].DATA[15:8]	8
0xBB001038	I2C_MST2_DATA_CTRL[14].DATA[23:16]	8
0xBB001038	I2C_MST2_DATA_CTRL[15].DATA[31:24]	8
0xBB001028	I2C_MST2_MEMADDR_CTRL.RESERVED[31:24]	8
0xBB001028	I2C_MST2_MEMADDR_CTRL.MEM_ADDR[23:0]	24
0xBB001004	I2C_MST_IF_CTRL.RESERVED[31:28]	4
0xBB001004	I2C_MST_IF_CTRL.CFG_DATA_HOLD_TIME_2[27:27]	1
0xBB001004	I2C_MST_IF_CTRL.CFG_DATA_HOLD_TIME_1[26:26]	1
0xBB001004	I2C_MST_IF_CTRL.CFG_SCK_I_DLY_2[25:22]	4
0xBB001004	I2C_MST_IF_CTRL.CFG_SCK_I_DLY_1[21:18]	4
0xBB001004	I2C_MST_IF_CTRL.CFG_WAIT_SCK_MODE_2[17:16]	2
0xBB001004	I2C_MST_IF_CTRL.CFG_WAIT_SCK_MODE_1[15:14]	2
0xBB001004	I2C_MST_IF_CTRL.I2C_OPEN_DRN_SCK_2[13:13]	1
0xBB001004	I2C_MST_IF_CTRL.I2C_OPEN_DRN_SCK_1[12:12]	1
0xBB001004	I2C_MST_IF_CTRL.I2C_OPEN_DRN_SDA[11:0]	12
0xBB001008	I2C_MST_IF_SEL.RESERVED[31:14]	18
0xBB001008	I2C_MST_IF_SEL.GPIO_SCL_SEL[13:12]	2
0xBB001008	I2C_MST_IF_SEL.GPIO_SDA_SEL[11:0]	12
0xBB001000	I2C_SLV_CTRL.RESERVED[31:7]	25
0xBB001000	I2C_SLV_CTRL.CFG_SDA_DLY[6:1]	6
0xBB001000	I2C_SLV_CTRL.DATA_ENDIAN_SEL[0:0]	1
0xBB00E000	IGBW_CTRL.RESERVED[31:9]	23
0xBB00E000	IGBW_CTRL.INC_BYPASS_PKT[8:8]	1
0xBB00E000	IGBW_CTRL.INC_IFG[7:7]	1
0xBB00E000	IGBW_CTRL.ADMIT_RIP[6:6]	1
0xBB00E000	IGBW_CTRL.ADMIT_DHCP[5:5]	1
0xBB00E000	IGBW_CTRL.ADMIT_ARPREQ[4:4]	1
0xBB00E000	IGBW_CTRL.ADMIT_RMA[3:3]	1
0xBB00E000	IGBW_CTRL.ADMIT_BPDU[2:2]	1
0xBB00E000	IGBW_CTRL.ADMIT_RTKPKT[1:1]	1
0xBB00E000	IGBW_CTRL.ADMIT_IGMP[0:0]	1
0xBB00E004	IGBW_LB_CTRL.TICK[31:16]	16
0xBB00E004	IGBW_LB_CTRL.TKN[15:0]	16
0xBB00EBE0	IGBW_PAGE_CNT_CTRL.RESERVED[31:10]	22
0xBB00EBE0	IGBW_PAGE_CNT_CTRL.RST[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E8E0	IGBW_PAGE_CNT_CTRL.PORT_RST[8:8]	1
0xBB00E8E0	IGBW_PAGE_CNT_CTRL.RESERVED[7:6]	2
0xBB00E8E0	IGBW_PAGE_CNT_CTRL.PORT_IDX[5:0]	6
0xBB00E008	IGBW_PORT_CTRL [0].RESERVED[31:21]	11
0xBB00E008	IGBW_PORT_CTRL [0].EN[20:20]	1
0xBB00E008	IGBW_PORT_CTRL [0].RATE[19:0]	20
0xBB00E00C	IGBW_PORT_CTRL [0].RESERVED[31:16]	16
0xBB00E00C	IGBW_PORT_CTRL [0].BURST[15:0]	16
0xBB00E010	IGBW_PORT_CTRL [1].RESERVED[31:21]	11
0xBB00E010	IGBW_PORT_CTRL [1].EN[20:20]	1
0xBB00E010	IGBW_PORT_CTRL [1].RATE[19:0]	20
0xBB00E014	IGBW_PORT_CTRL [1].RESERVED[31:16]	16
0xBB00E014	IGBW_PORT_CTRL [1].BURST[15:0]	16
0xBB00E018	IGBW_PORT_CTRL [2].RESERVED[31:21]	11
0xBB00E018	IGBW_PORT_CTRL [2].EN[20:20]	1
0xBB00E018	IGBW_PORT_CTRL [2].RATE[19:0]	20
0xBB00E01C	IGBW_PORT_CTRL [2].RESERVED[31:16]	16
0xBB00E01C	IGBW_PORT_CTRL [2].BURST[15:0]	16
0xBB00E020	IGBW_PORT_CTRL [3].RESERVED[31:21]	11
0xBB00E020	IGBW_PORT_CTRL [3].EN[20:20]	1
0xBB00E020	IGBW_PORT_CTRL [3].RATE[19:0]	20
0xBB00E024	IGBW_PORT_CTRL [3].RESERVED[31:16]	16
0xBB00E024	IGBW_PORT_CTRL [3].BURST[15:0]	16
0xBB00E028	IGBW_PORT_CTRL [4].RESERVED[31:21]	11
0xBB00E028	IGBW_PORT_CTRL [4].EN[20:20]	1
0xBB00E028	IGBW_PORT_CTRL [4].RATE[19:0]	20
0xBB00E02C	IGBW_PORT_CTRL [4].RESERVED[31:16]	16
0xBB00E02C	IGBW_PORT_CTRL [4].BURST[15:0]	16
0xBB00E030	IGBW_PORT_CTRL [5].RESERVED[31:21]	11
0xBB00E030	IGBW_PORT_CTRL [5].EN[20:20]	1
0xBB00E030	IGBW_PORT_CTRL [5].RATE[19:0]	20
0xBB00E034	IGBW_PORT_CTRL [5].RESERVED[31:16]	16
0xBB00E034	IGBW_PORT_CTRL [5].BURST[15:0]	16
0xBB00E038	IGBW_PORT_CTRL [6].RESERVED[31:21]	11
0xBB00E038	IGBW_PORT_CTRL [6].EN[20:20]	1
0xBB00E038	IGBW_PORT_CTRL [6].RATE[19:0]	20
0xBB00E03C	IGBW_PORT_CTRL [6].RESERVED[31:16]	16
0xBB00E03C	IGBW_PORT_CTRL [6].BURST[15:0]	16
0xBB00E040	IGBW_PORT_CTRL [7].RESERVED[31:21]	11
0xBB00E040	IGBW_PORT_CTRL [7].EN[20:20]	1
0xBB00E040	IGBW_PORT_CTRL [7].RATE[19:0]	20
0xBB00E044	IGBW_PORT_CTRL [7].RESERVED[31:16]	16
0xBB00E044	IGBW_PORT_CTRL [7].BURST[15:0]	16
0xBB00E048	IGBW_PORT_CTRL [8].RESERVED[31:21]	11
0xBB00E048	IGBW_PORT_CTRL [8].EN[20:20]	1
0xBB00E048	IGBW_PORT_CTRL [8].RATE[19:0]	20

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E04C	IGBW_PORT_CTRL [8].RESERVED[31:16]	16
0xBB00E04C	IGBW_PORT_CTRL [8].BURST[15:0]	16
0xBB00E050	IGBW_PORT_CTRL [9].RESERVED[31:21]	11
0xBB00E050	IGBW_PORT_CTRL [9].EN[20:20]	1
0xBB00E050	IGBW_PORT_CTRL [9].RATE[19:0]	20
0xBB00E054	IGBW_PORT_CTRL [9].RESERVED[31:16]	16
0xBB00E054	IGBW_PORT_CTRL [9].BURST[15:0]	16
0xBB00E058	IGBW_PORT_CTRL [10].RESERVED[31:21]	11
0xBB00E058	IGBW_PORT_CTRL [10].EN[20:20]	1
0xBB00E058	IGBW_PORT_CTRL [10].RATE[19:0]	20
0xBB00E05C	IGBW_PORT_CTRL [10].RESERVED[31:16]	16
0xBB00E05C	IGBW_PORT_CTRL [10].BURST[15:0]	16
0xBB00E060	IGBW_PORT_CTRL [11].RESERVED[31:21]	11
0xBB00E060	IGBW_PORT_CTRL [11].EN[20:20]	1
0xBB00E060	IGBW_PORT_CTRL [11].RATE[19:0]	20
0xBB00E064	IGBW_PORT_CTRL [11].RESERVED[31:16]	16
0xBB00E064	IGBW_PORT_CTRL [11].BURST[15:0]	16
0xBB00E068	IGBW_PORT_CTRL [12].RESERVED[31:21]	11
0xBB00E068	IGBW_PORT_CTRL [12].EN[20:20]	1
0xBB00E068	IGBW_PORT_CTRL [12].RATE[19:0]	20
0xBB00E06C	IGBW_PORT_CTRL [12].RESERVED[31:16]	16
0xBB00E06C	IGBW_PORT_CTRL [12].BURST[15:0]	16
0xBB00E070	IGBW_PORT_CTRL [13].RESERVED[31:21]	11
0xBB00E070	IGBW_PORT_CTRL [13].EN[20:20]	1
0xBB00E070	IGBW_PORT_CTRL [13].RATE[19:0]	20
0xBB00E074	IGBW_PORT_CTRL [13].RESERVED[31:16]	16
0xBB00E074	IGBW_PORT_CTRL [13].BURST[15:0]	16
0xBB00E078	IGBW_PORT_CTRL [14].RESERVED[31:21]	11
0xBB00E078	IGBW_PORT_CTRL [14].EN[20:20]	1
0xBB00E078	IGBW_PORT_CTRL [14].RATE[19:0]	20
0xBB00E07C	IGBW_PORT_CTRL [14].RESERVED[31:16]	16
0xBB00E07C	IGBW_PORT_CTRL [14].BURST[15:0]	16
0xBB00E080	IGBW_PORT_CTRL [15].RESERVED[31:21]	11
0xBB00E080	IGBW_PORT_CTRL [15].EN[20:20]	1
0xBB00E080	IGBW_PORT_CTRL [15].RATE[19:0]	20
0xBB00E084	IGBW_PORT_CTRL [15].RESERVED[31:16]	16
0xBB00E084	IGBW_PORT_CTRL [15].BURST[15:0]	16
0xBB00E088	IGBW_PORT_CTRL [16].RESERVED[31:21]	11
0xBB00E088	IGBW_PORT_CTRL [16].EN[20:20]	1
0xBB00E088	IGBW_PORT_CTRL [16].RATE[19:0]	20
0xBB00E08C	IGBW_PORT_CTRL [16].RESERVED[31:16]	16
0xBB00E08C	IGBW_PORT_CTRL [16].BURST[15:0]	16
0xBB00E090	IGBW_PORT_CTRL [17].RESERVED[31:21]	11
0xBB00E090	IGBW_PORT_CTRL [17].EN[20:20]	1
0xBB00E090	IGBW_PORT_CTRL [17].RATE[19:0]	20
0xBB00E094	IGBW_PORT_CTRL [17].RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E094	IGBW_PORT_CTRL [17].BURST[15:0]	16
0xBB00E098	IGBW_PORT_CTRL [18].RESERVED[31:21]	11
0xBB00E098	IGBW_PORT_CTRL [18].EN[20:20]	1
0xBB00E098	IGBW_PORT_CTRL [18].RATE[19:0]	20
0xBB00E09C	IGBW_PORT_CTRL [18].RESERVED[31:16]	16
0xBB00E09C	IGBW_PORT_CTRL [18].BURST[15:0]	16
0xBB00E0A0	IGBW_PORT_CTRL [19].RESERVED[31:21]	11
0xBB00E0A0	IGBW_PORT_CTRL [19].EN[20:20]	1
0xBB00E0A0	IGBW_PORT_CTRL [19].RATE[19:0]	20
0xBB00E0A4	IGBW_PORT_CTRL [19].RESERVED[31:16]	16
0xBB00E0A4	IGBW_PORT_CTRL [19].BURST[15:0]	16
0xBB00E0A8	IGBW_PORT_CTRL [20].RESERVED[31:21]	11
0xBB00E0A8	IGBW_PORT_CTRL [20].EN[20:20]	1
0xBB00E0A8	IGBW_PORT_CTRL [20].RATE[19:0]	20
0xBB00E0AC	IGBW_PORT_CTRL [20].RESERVED[31:16]	16
0xBB00E0AC	IGBW_PORT_CTRL [20].BURST[15:0]	16
0xBB00E0B0	IGBW_PORT_CTRL [21].RESERVED[31:21]	11
0xBB00E0B0	IGBW_PORT_CTRL [21].EN[20:20]	1
0xBB00E0B0	IGBW_PORT_CTRL [21].RATE[19:0]	20
0xBB00E0B4	IGBW_PORT_CTRL [21].RESERVED[31:16]	16
0xBB00E0B4	IGBW_PORT_CTRL [21].BURST[15:0]	16
0xBB00E0B8	IGBW_PORT_CTRL [22].RESERVED[31:21]	11
0xBB00E0B8	IGBW_PORT_CTRL [22].EN[20:20]	1
0xBB00E0B8	IGBW_PORT_CTRL [22].RATE[19:0]	20
0xBB00E0BC	IGBW_PORT_CTRL [22].RESERVED[31:16]	16
0xBB00E0BC	IGBW_PORT_CTRL [22].BURST[15:0]	16
0xBB00E0C0	IGBW_PORT_CTRL [23].RESERVED[31:21]	11
0xBB00E0C0	IGBW_PORT_CTRL [23].EN[20:20]	1
0xBB00E0C0	IGBW_PORT_CTRL [23].RATE[19:0]	20
0xBB00E0C4	IGBW_PORT_CTRL [23].RESERVED[31:16]	16
0xBB00E0C4	IGBW_PORT_CTRL [23].BURST[15:0]	16
0xBB00E0C8	IGBW_PORT_CTRL [24].RESERVED[31:21]	11
0xBB00E0C8	IGBW_PORT_CTRL [24].EN[20:20]	1
0xBB00E0C8	IGBW_PORT_CTRL [24].RATE[19:0]	20
0xBB00E0CC	IGBW_PORT_CTRL [24].RESERVED[31:16]	16
0xBB00E0CC	IGBW_PORT_CTRL [24].BURST[15:0]	16
0xBB00E0D0	IGBW_PORT_CTRL [25].RESERVED[31:21]	11
0xBB00E0D0	IGBW_PORT_CTRL [25].EN[20:20]	1
0xBB00E0D0	IGBW_PORT_CTRL [25].RATE[19:0]	20
0xBB00E0D4	IGBW_PORT_CTRL [25].RESERVED[31:16]	16
0xBB00E0D4	IGBW_PORT_CTRL [25].BURST[15:0]	16
0xBB00E0D8	IGBW_PORT_CTRL [26].RESERVED[31:21]	11
0xBB00E0D8	IGBW_PORT_CTRL [26].EN[20:20]	1
0xBB00E0D8	IGBW_PORT_CTRL [26].RATE[19:0]	20
0xBB00E0DC	IGBW_PORT_CTRL [26].RESERVED[31:16]	16
0xBB00E0DC	IGBW_PORT_CTRL [26].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E0E0	IGBW_PORT_CTRL [27].RESERVED[31:21]	11
0xBB00E0E0	IGBW_PORT_CTRL [27].EN[20:20]	1
0xBB00E0E0	IGBW_PORT_CTRL [27].RATE[19:0]	20
0xBB00E0E4	IGBW_PORT_CTRL [27].RESERVED[31:16]	16
0xBB00E0E4	IGBW_PORT_CTRL [27].BURST[15:0]	16
0xBB00E0E8	IGBW_PORT_CTRL [28].RESERVED[31:21]	11
0xBB00E0E8	IGBW_PORT_CTRL [28].EN[20:20]	1
0xBB00E0E8	IGBW_PORT_CTRL [28].RATE[19:0]	20
0xBB00E0EC	IGBW_PORT_CTRL [28].RESERVED[31:16]	16
0xBB00E0EC	IGBW_PORT_CTRL [28].BURST[15:0]	16
0xBB00E0F0	IGBW_PORT_CTRL [29].RESERVED[31:21]	11
0xBB00E0F0	IGBW_PORT_CTRL [29].EN[20:20]	1
0xBB00E0F0	IGBW_PORT_CTRL [29].RATE[19:0]	20
0xBB00E0F4	IGBW_PORT_CTRL [29].RESERVED[31:16]	16
0xBB00E0F4	IGBW_PORT_CTRL [29].BURST[15:0]	16
0xBB00E0F8	IGBW_PORT_CTRL [30].RESERVED[31:21]	11
0xBB00E0F8	IGBW_PORT_CTRL [30].EN[20:20]	1
0xBB00E0F8	IGBW_PORT_CTRL [30].RATE[19:0]	20
0xBB00E0FC	IGBW_PORT_CTRL [30].RESERVED[31:16]	16
0xBB00E0FC	IGBW_PORT_CTRL [30].BURST[15:0]	16
0xBB00E100	IGBW_PORT_CTRL [31].RESERVED[31:21]	11
0xBB00E100	IGBW_PORT_CTRL [31].EN[20:20]	1
0xBB00E100	IGBW_PORT_CTRL [31].RATE[19:0]	20
0xBB00E104	IGBW_PORT_CTRL [31].RESERVED[31:16]	16
0xBB00E104	IGBW_PORT_CTRL [31].BURST[15:0]	16
0xBB00E108	IGBW_PORT_CTRL [32].RESERVED[31:21]	11
0xBB00E108	IGBW_PORT_CTRL [32].EN[20:20]	1
0xBB00E108	IGBW_PORT_CTRL [32].RATE[19:0]	20
0xBB00E10C	IGBW_PORT_CTRL [32].RESERVED[31:16]	16
0xBB00E10C	IGBW_PORT_CTRL [32].BURST[15:0]	16
0xBB00E110	IGBW_PORT_CTRL [33].RESERVED[31:21]	11
0xBB00E110	IGBW_PORT_CTRL [33].EN[20:20]	1
0xBB00E110	IGBW_PORT_CTRL [33].RATE[19:0]	20
0xBB00E114	IGBW_PORT_CTRL [33].RESERVED[31:16]	16
0xBB00E114	IGBW_PORT_CTRL [33].BURST[15:0]	16
0xBB00E118	IGBW_PORT_CTRL [34].RESERVED[31:21]	11
0xBB00E118	IGBW_PORT_CTRL [34].EN[20:20]	1
0xBB00E118	IGBW_PORT_CTRL [34].RATE[19:0]	20
0xBB00E11C	IGBW_PORT_CTRL [34].RESERVED[31:16]	16
0xBB00E11C	IGBW_PORT_CTRL [34].BURST[15:0]	16
0xBB00E120	IGBW_PORT_CTRL [35].RESERVED[31:21]	11
0xBB00E120	IGBW_PORT_CTRL [35].EN[20:20]	1
0xBB00E120	IGBW_PORT_CTRL [35].RATE[19:0]	20
0xBB00E124	IGBW_PORT_CTRL [35].RESERVED[31:16]	16
0xBB00E124	IGBW_PORT_CTRL [35].BURST[15:0]	16
0xBB00E128	IGBW_PORT_CTRL [36].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E128	IGBW_PORT_CTRL [36].EN[20:20]	1
0xBB00E128	IGBW_PORT_CTRL [36].RATE[19:0]	20
0xBB00E12C	IGBW_PORT_CTRL [36].RESERVED[31:16]	16
0xBB00E12C	IGBW_PORT_CTRL [36].BURST[15:0]	16
0xBB00E130	IGBW_PORT_CTRL [37].RESERVED[31:21]	11
0xBB00E130	IGBW_PORT_CTRL [37].EN[20:20]	1
0xBB00E130	IGBW_PORT_CTRL [37].RATE[19:0]	20
0xBB00E134	IGBW_PORT_CTRL [37].RESERVED[31:16]	16
0xBB00E134	IGBW_PORT_CTRL [37].BURST[15:0]	16
0xBB00E138	IGBW_PORT_CTRL [38].RESERVED[31:21]	11
0xBB00E138	IGBW_PORT_CTRL [38].EN[20:20]	1
0xBB00E138	IGBW_PORT_CTRL [38].RATE[19:0]	20
0xBB00E13C	IGBW_PORT_CTRL [38].RESERVED[31:16]	16
0xBB00E13C	IGBW_PORT_CTRL [38].BURST[15:0]	16
0xBB00E140	IGBW_PORT_CTRL [39].RESERVED[31:21]	11
0xBB00E140	IGBW_PORT_CTRL [39].EN[20:20]	1
0xBB00E140	IGBW_PORT_CTRL [39].RATE[19:0]	20
0xBB00E144	IGBW_PORT_CTRL [39].RESERVED[31:16]	16
0xBB00E144	IGBW_PORT_CTRL [39].BURST[15:0]	16
0xBB00E148	IGBW_PORT_CTRL [40].RESERVED[31:21]	11
0xBB00E148	IGBW_PORT_CTRL [40].EN[20:20]	1
0xBB00E148	IGBW_PORT_CTRL [40].RATE[19:0]	20
0xBB00E14C	IGBW_PORT_CTRL [40].RESERVED[31:16]	16
0xBB00E14C	IGBW_PORT_CTRL [40].BURST[15:0]	16
0xBB00E150	IGBW_PORT_CTRL [41].RESERVED[31:21]	11
0xBB00E150	IGBW_PORT_CTRL [41].EN[20:20]	1
0xBB00E150	IGBW_PORT_CTRL [41].RATE[19:0]	20
0xBB00E154	IGBW_PORT_CTRL [41].RESERVED[31:16]	16
0xBB00E154	IGBW_PORT_CTRL [41].BURST[15:0]	16
0xBB00E158	IGBW_PORT_CTRL [42].RESERVED[31:21]	11
0xBB00E158	IGBW_PORT_CTRL [42].EN[20:20]	1
0xBB00E158	IGBW_PORT_CTRL [42].RATE[19:0]	20
0xBB00E15C	IGBW_PORT_CTRL [42].RESERVED[31:16]	16
0xBB00E15C	IGBW_PORT_CTRL [42].BURST[15:0]	16
0xBB00E160	IGBW_PORT_CTRL [43].RESERVED[31:21]	11
0xBB00E160	IGBW_PORT_CTRL [43].EN[20:20]	1
0xBB00E160	IGBW_PORT_CTRL [43].RATE[19:0]	20
0xBB00E164	IGBW_PORT_CTRL [43].RESERVED[31:16]	16
0xBB00E164	IGBW_PORT_CTRL [43].BURST[15:0]	16
0xBB00E168	IGBW_PORT_CTRL [44].RESERVED[31:21]	11
0xBB00E168	IGBW_PORT_CTRL [44].EN[20:20]	1
0xBB00E168	IGBW_PORT_CTRL [44].RATE[19:0]	20
0xBB00E16C	IGBW_PORT_CTRL [44].RESERVED[31:16]	16
0xBB00E16C	IGBW_PORT_CTRL [44].BURST[15:0]	16
0xBB00E170	IGBW_PORT_CTRL [45].RESERVED[31:21]	11
0xBB00E170	IGBW_PORT_CTRL [45].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E170	IGBW_PORT_CTRL [45].RATE[19:0]	20
0xBB00E174	IGBW_PORT_CTRL [45].RESERVED[31:16]	16
0xBB00E174	IGBW_PORT_CTRL [45].BURST[15:0]	16
0xBB00E178	IGBW_PORT_CTRL [46].RESERVED[31:21]	11
0xBB00E178	IGBW_PORT_CTRL [46].EN[20:20]	1
0xBB00E178	IGBW_PORT_CTRL [46].RATE[19:0]	20
0xBB00E17C	IGBW_PORT_CTRL [46].RESERVED[31:16]	16
0xBB00E17C	IGBW_PORT_CTRL [46].BURST[15:0]	16
0xBB00E180	IGBW_PORT_CTRL [47].RESERVED[31:21]	11
0xBB00E180	IGBW_PORT_CTRL [47].EN[20:20]	1
0xBB00E180	IGBW_PORT_CTRL [47].RATE[19:0]	20
0xBB00E184	IGBW_PORT_CTRL [47].RESERVED[31:16]	16
0xBB00E184	IGBW_PORT_CTRL [47].BURST[15:0]	16
0xBB00E188	IGBW_PORT_CTRL [48].RESERVED[31:21]	11
0xBB00E188	IGBW_PORT_CTRL [48].EN[20:20]	1
0xBB00E188	IGBW_PORT_CTRL [48].RATE[19:0]	20
0xBB00E18C	IGBW_PORT_CTRL [48].RESERVED[31:16]	16
0xBB00E18C	IGBW_PORT_CTRL [48].BURST[15:0]	16
0xBB00E190	IGBW_PORT_CTRL [49].RESERVED[31:21]	11
0xBB00E190	IGBW_PORT_CTRL [49].EN[20:20]	1
0xBB00E190	IGBW_PORT_CTRL [49].RATE[19:0]	20
0xBB00E194	IGBW_PORT_CTRL [49].RESERVED[31:16]	16
0xBB00E194	IGBW_PORT_CTRL [49].BURST[15:0]	16
0xBB00E198	IGBW_PORT_CTRL [50].RESERVED[31:21]	11
0xBB00E198	IGBW_PORT_CTRL [50].EN[20:20]	1
0xBB00E198	IGBW_PORT_CTRL [50].RATE[19:0]	20
0xBB00E19C	IGBW_PORT_CTRL [50].RESERVED[31:16]	16
0xBB00E19C	IGBW_PORT_CTRL [50].BURST[15:0]	16
0xBB00E1A0	IGBW_PORT_CTRL [51].RESERVED[31:21]	11
0xBB00E1A0	IGBW_PORT_CTRL [51].EN[20:20]	1
0xBB00E1A0	IGBW_PORT_CTRL [51].RATE[19:0]	20
0xBB00E1A4	IGBW_PORT_CTRL [51].RESERVED[31:16]	16
0xBB00E1A4	IGBW_PORT_CTRL [51].BURST[15:0]	16
0xBB00E1A8	IGBW_PORT_CTRL [52].RESERVED[31:21]	11
0xBB00E1A8	IGBW_PORT_CTRL [52].EN[20:20]	1
0xBB00E1A8	IGBW_PORT_CTRL [52].RATE[19:0]	20
0xBB00E1AC	IGBW_PORT_CTRL [52].RESERVED[31:16]	16
0xBB00E1AC	IGBW_PORT_CTRL [52].BURST[15:0]	16
0xBB00E1B0	IGBW_PORT_CTRL [53].RESERVED[31:21]	11
0xBB00E1B0	IGBW_PORT_CTRL [53].EN[20:20]	1
0xBB00E1B0	IGBW_PORT_CTRL [53].RATE[19:0]	20
0xBB00E1B4	IGBW_PORT_CTRL [53].RESERVED[31:16]	16
0xBB00E1B4	IGBW_PORT_CTRL [53].BURST[15:0]	16
0xBB00E1B8	IGBW_PORT_CTRL [54].RESERVED[31:21]	11
0xBB00E1B8	IGBW_PORT_CTRL [54].EN[20:20]	1
0xBB00E1B8	IGBW_PORT_CTRL [54].RATE[19:0]	20

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E1BC	IGBW_PORT_CTRL [54].RESERVED[31:16]	16
0xBB00E1BC	IGBW_PORT_CTRL [54].BURST[15:0]	16
0xBB00E1C0	IGBW_PORT_CTRL [55].RESERVED[31:21]	11
0xBB00E1C0	IGBW_PORT_CTRL [55].EN[20:20]	1
0xBB00E1C0	IGBW_PORT_CTRL [55].RATE[19:0]	20
0xBB00E1C4	IGBW_PORT_CTRL [55].RESERVED[31:16]	16
0xBB00E1C4	IGBW_PORT_CTRL [55].BURST[15:0]	16
0xBB00E1C8	IGBW_PORT_CTRL [56].RESERVED[31:21]	11
0xBB00E1C8	IGBW_PORT_CTRL [56].EN[20:20]	1
0xBB00E1C8	IGBW_PORT_CTRL [56].RATE[19:0]	20
0xBB00E1CC	IGBW_PORT_CTRL [56].RESERVED[31:16]	16
0xBB00E1CC	IGBW_PORT_CTRL [56].BURST[15:0]	16
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00E1D8	IGBW_PORT_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [33].FLAG[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00E1DC	IGBW_PORT_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [0].EN[0:0]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [1].EN[1:1]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [2].EN[2:2]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [3].EN[3:3]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [4].EN[4:4]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [5].EN[5:5]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [6].EN[6:6]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [7].EN[7:7]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [8].EN[8:8]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [9].EN[9:9]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [10].EN[10:10]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [11].EN[11:11]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [12].EN[12:12]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [13].EN[13:13]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [14].EN[14:14]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [15].EN[15:15]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [16].EN[16:16]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [17].EN[17:17]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [18].EN[18:18]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [19].EN[19:19]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [20].EN[20:20]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [21].EN[21:21]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [22].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E1E0	IGBW_PORT_FC_CTRL [23].EN[23:23]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [24].EN[24:24]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [25].EN[25:25]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [26].EN[26:26]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [27].EN[27:27]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [28].EN[28:28]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [29].EN[29:29]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [30].EN[30:30]	1
0xBB00E1E0	IGBW_PORT_FC_CTRL [31].EN[31:31]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [32].EN[0:0]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [33].EN[1:1]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [34].EN[2:2]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [35].EN[3:3]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [36].EN[4:4]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [37].EN[5:5]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [38].EN[6:6]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [39].EN[7:7]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [40].EN[8:8]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [41].EN[9:9]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [42].EN[10:10]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [43].EN[11:11]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [44].EN[12:12]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [45].EN[13:13]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [46].EN[14:14]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [47].EN[15:15]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [48].EN[16:16]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [49].EN[17:17]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [50].EN[18:18]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [51].EN[19:19]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [52].EN[20:20]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [53].EN[21:21]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [54].EN[22:22]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [55].EN[23:23]	1
0xBB00E1E4	IGBW_PORT_FC_CTRL [56].EN[24:24]	1
0xBB00E1D0	IGBW_PORT_LB_RST [0].RST[0:0]	1
0xBB00E1D0	IGBW_PORT_LB_RST [1].RST[1:1]	1
0xBB00E1D0	IGBW_PORT_LB_RST [2].RST[2:2]	1
0xBB00E1D0	IGBW_PORT_LB_RST [3].RST[3:3]	1
0xBB00E1D0	IGBW_PORT_LB_RST [4].RST[4:4]	1
0xBB00E1D0	IGBW_PORT_LB_RST [5].RST[5:5]	1
0xBB00E1D0	IGBW_PORT_LB_RST [6].RST[6:6]	1
0xBB00E1D0	IGBW_PORT_LB_RST [7].RST[7:7]	1
0xBB00E1D0	IGBW_PORT_LB_RST [8].RST[8:8]	1
0xBB00E1D0	IGBW_PORT_LB_RST [9].RST[9:9]	1
0xBB00E1D0	IGBW_PORT_LB_RST [10].RST[10:10]	1
0xBB00E1D0	IGBW_PORT_LB_RST [11].RST[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00E1D0	IGBW_PORT_LB_RST [12].RST[12:12]	1
0xBB00E1D0	IGBW_PORT_LB_RST [13].RST[13:13]	1
0xBB00E1D0	IGBW_PORT_LB_RST [14].RST[14:14]	1
0xBB00E1D0	IGBW_PORT_LB_RST [15].RST[15:15]	1
0xBB00E1D0	IGBW_PORT_LB_RST [16].RST[16:16]	1
0xBB00E1D0	IGBW_PORT_LB_RST [17].RST[17:17]	1
0xBB00E1D0	IGBW_PORT_LB_RST [18].RST[18:18]	1
0xBB00E1D0	IGBW_PORT_LB_RST [19].RST[19:19]	1
0xBB00E1D0	IGBW_PORT_LB_RST [20].RST[20:20]	1
0xBB00E1D0	IGBW_PORT_LB_RST [21].RST[21:21]	1
0xBB00E1D0	IGBW_PORT_LB_RST [22].RST[22:22]	1
0xBB00E1D0	IGBW_PORT_LB_RST [23].RST[23:23]	1
0xBB00E1D0	IGBW_PORT_LB_RST [24].RST[24:24]	1
0xBB00E1D0	IGBW_PORT_LB_RST [25].RST[25:25]	1
0xBB00E1D0	IGBW_PORT_LB_RST [26].RST[26:26]	1
0xBB00E1D0	IGBW_PORT_LB_RST [27].RST[27:27]	1
0xBB00E1D0	IGBW_PORT_LB_RST [28].RST[28:28]	1
0xBB00E1D0	IGBW_PORT_LB_RST [29].RST[29:29]	1
0xBB00E1D0	IGBW_PORT_LB_RST [30].RST[30:30]	1
0xBB00E1D0	IGBW_PORT_LB_RST [31].RST[31:31]	1
0xBB00E1D4	IGBW_PORT_LB_RST [32].RST[0:0]	1
0xBB00E1D4	IGBW_PORT_LB_RST [33].RST[1:1]	1
0xBB00E1D4	IGBW_PORT_LB_RST [34].RST[2:2]	1
0xBB00E1D4	IGBW_PORT_LB_RST [35].RST[3:3]	1
0xBB00E1D4	IGBW_PORT_LB_RST [36].RST[4:4]	1
0xBB00E1D4	IGBW_PORT_LB_RST [37].RST[5:5]	1
0xBB00E1D4	IGBW_PORT_LB_RST [38].RST[6:6]	1
0xBB00E1D4	IGBW_PORT_LB_RST [39].RST[7:7]	1
0xBB00E1D4	IGBW_PORT_LB_RST [40].RST[8:8]	1
0xBB00E1D4	IGBW_PORT_LB_RST [41].RST[9:9]	1
0xBB00E1D4	IGBW_PORT_LB_RST [42].RST[10:10]	1
0xBB00E1D4	IGBW_PORT_LB_RST [43].RST[11:11]	1
0xBB00E1D4	IGBW_PORT_LB_RST [44].RST[12:12]	1
0xBB00E1D4	IGBW_PORT_LB_RST [45].RST[13:13]	1
0xBB00E1D4	IGBW_PORT_LB_RST [46].RST[14:14]	1
0xBB00E1D4	IGBW_PORT_LB_RST [47].RST[15:15]	1
0xBB00E1D4	IGBW_PORT_LB_RST [48].RST[16:16]	1
0xBB00E1D4	IGBW_PORT_LB_RST [49].RST[17:17]	1
0xBB00E1D4	IGBW_PORT_LB_RST [50].RST[18:18]	1
0xBB00E1D4	IGBW_PORT_LB_RST [51].RST[19:19]	1
0xBB00E1D4	IGBW_PORT_LB_RST [52].RST[20:20]	1
0xBB00E1D4	IGBW_PORT_LB_RST [53].RST[21:21]	1
0xBB00E1D4	IGBW_PORT_LB_RST [54].RST[22:22]	1
0xBB00E1D4	IGBW_PORT_LB_RST [55].RST[23:23]	1
0xBB00E1D4	IGBW_PORT_LB_RST [56].RST[24:24]	1
0xBB00128C	IMR_AUTO_RECOVERY.RESERVED[31:7]	25

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00128C	IMR_AUTO_RECOVERY.IMR_AUTO_REC[6:6]	1
0xBB00128C	IMR_AUTO_RECOVERY.IMR_TX_CRC_CNTR[5:5]	1
0xBB00128C	IMR_AUTO_RECOVERY.IMR_SMI_CHECK_REG[4:0]	5
0xBB0012A4	IMR_DBGO.RESERVED[31:1]	31
0xBB0012A4	IMR_DBGO.IMR_DBGO[0:0]	1
0xBB001280	IMR_EXT_GPIO [0].IMR_EXT_GPIO[0:0]	1
0xBB001280	IMR_EXT_GPIO [1].IMR_EXT_GPIO[1:1]	1
0xBB001280	IMR_EXT_GPIO [2].IMR_EXT_GPIO[2:2]	1
0xBB001280	IMR_EXT_GPIO [3].IMR_EXT_GPIO[3:3]	1
0xBB001280	IMR_EXT_GPIO [4].IMR_EXT_GPIO[4:4]	1
0xBB001280	IMR_EXT_GPIO [5].IMR_EXT_GPIO[5:5]	1
0xBB001280	IMR_EXT_GPIO [6].IMR_EXT_GPIO[6:6]	1
0xBB001280	IMR_EXT_GPIO [7].IMR_EXT_GPIO[7:7]	1
0xBB001280	IMR_EXT_GPIO [8].IMR_EXT_GPIO[8:8]	1
0xBB001280	IMR_EXT_GPIO [9].IMR_EXT_GPIO[9:9]	1
0xBB001280	IMR_EXT_GPIO [10].IMR_EXT_GPIO[10:10]	1
0xBB001280	IMR_EXT_GPIO [11].IMR_EXT_GPIO[11:11]	1
0xBB001280	IMR_EXT_GPIO [12].IMR_EXT_GPIO[12:12]	1
0xBB001280	IMR_EXT_GPIO [13].IMR_EXT_GPIO[13:13]	1
0xBB001280	IMR_EXT_GPIO [14].IMR_EXT_GPIO[14:14]	1
0xBB001280	IMR_EXT_GPIO [15].IMR_EXT_GPIO[15:15]	1
0xBB001280	IMR_EXT_GPIO [16].IMR_EXT_GPIO[16:16]	1
0xBB001280	IMR_EXT_GPIO [17].IMR_EXT_GPIO[17:17]	1
0xBB001280	IMR_EXT_GPIO [18].IMR_EXT_GPIO[18:18]	1
0xBB001280	IMR_EXT_GPIO [19].IMR_EXT_GPIO[19:19]	1
0xBB001280	IMR_EXT_GPIO [20].IMR_EXT_GPIO[20:20]	1
0xBB001280	IMR_EXT_GPIO [21].IMR_EXT_GPIO[21:21]	1
0xBB001280	IMR_EXT_GPIO [22].IMR_EXT_GPIO[22:22]	1
0xBB001280	IMR_EXT_GPIO [23].IMR_EXT_GPIO[23:23]	1
0xBB001280	IMR_EXT_GPIO [24].IMR_EXT_GPIO[24:24]	1
0xBB001280	IMR_EXT_GPIO [25].IMR_EXT_GPIO[25:25]	1
0xBB001280	IMR_EXT_GPIO [26].IMR_EXT_GPIO[26:26]	1
0xBB001280	IMR_EXT_GPIO [27].IMR_EXT_GPIO[27:27]	1
0xBB001280	IMR_EXT_GPIO [28].IMR_EXT_GPIO[28:28]	1
0xBB001280	IMR_EXT_GPIO [29].IMR_EXT_GPIO[29:29]	1
0xBB001280	IMR_EXT_GPIO [30].IMR_EXT_GPIO[30:30]	1
0xBB001280	IMR_EXT_GPIO [31].IMR_EXT_GPIO[31:31]	1
0xBB001284	IMR_EXT_GPIO [32].IMR_EXT_GPIO[0:0]	1
0xBB001284	IMR_EXT_GPIO [33].IMR_EXT_GPIO[1:1]	1
0xBB001284	IMR_EXT_GPIO [34].IMR_EXT_GPIO[2:2]	1
0xBB001284	IMR_EXT_GPIO [35].IMR_EXT_GPIO[3:3]	1
0xBB001284	IMR_EXT_GPIO [36].IMR_EXT_GPIO[4:4]	1
0xBB00127C	IMR_MISC.RESERVED[31:19]	13
0xBB00127C	IMR_MISC.IMR_STAT_TRIGGER[18:18]	1
0xBB00127C	IMR_MISC.IMR_ETHDM[17:17]	1
0xBB00127C	IMR_MISC.IMR_OAM_DYGASP[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00127C	IMR_MISC.IMR_CCM[15:0]	16
0xBB00126C	IMR_PORT_LINK_STS_CHG [0].IMR_PORT_LINK_STS_CHG[0:0]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [1].IMR_PORT_LINK_STS_CHG[1:1]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [2].IMR_PORT_LINK_STS_CHG[2:2]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [3].IMR_PORT_LINK_STS_CHG[3:3]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [4].IMR_PORT_LINK_STS_CHG[4:4]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [5].IMR_PORT_LINK_STS_CHG[5:5]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [6].IMR_PORT_LINK_STS_CHG[6:6]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [7].IMR_PORT_LINK_STS_CHG[7:7]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [8].IMR_PORT_LINK_STS_CHG[8:8]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [9].IMR_PORT_LINK_STS_CHG[9:9]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [10].IMR_PORT_LINK_STS_CHG[10:10]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [11].IMR_PORT_LINK_STS_CHG[11:11]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [12].IMR_PORT_LINK_STS_CHG[12:12]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [13].IMR_PORT_LINK_STS_CHG[13:13]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [14].IMR_PORT_LINK_STS_CHG[14:14]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [15].IMR_PORT_LINK_STS_CHG[15:15]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [16].IMR_PORT_LINK_STS_CHG[16:16]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [17].IMR_PORT_LINK_STS_CHG[17:17]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [18].IMR_PORT_LINK_STS_CHG[18:18]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [19].IMR_PORT_LINK_STS_CHG[19:19]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [20].IMR_PORT_LINK_STS_CHG[20:20]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [21].IMR_PORT_LINK_STS_CHG[21:21]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [22].IMR_PORT_LINK_STS_CHG[22:22]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [23].IMR_PORT_LINK_STS_CHG[23:23]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [24].IMR_PORT_LINK_STS_CHG[24:24]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [25].IMR_PORT_LINK_STS_CHG[25:25]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [26].IMR_PORT_LINK_STS_CHG[26:26]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [27].IMR_PORT_LINK_STS_CHG[27:27]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [28].IMR_PORT_LINK_STS_CHG[28:28]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [29].IMR_PORT_LINK_STS_CHG[29:29]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [30].IMR_PORT_LINK_STS_CHG[30:30]	1
0xBB00126C	IMR_PORT_LINK_STS_CHG [31].IMR_PORT_LINK_STS_CHG[31:31]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [32].IMR_PORT_LINK_STS_CHG[0:0]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [33].IMR_PORT_LINK_STS_CHG[1:1]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [34].IMR_PORT_LINK_STS_CHG[2:2]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [35].IMR_PORT_LINK_STS_CHG[3:3]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [36].IMR_PORT_LINK_STS_CHG[4:4]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [37].IMR_PORT_LINK_STS_CHG[5:5]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [38].IMR_PORT_LINK_STS_CHG[6:6]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [39].IMR_PORT_LINK_STS_CHG[7:7]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [40].IMR_PORT_LINK_STS_CHG[8:8]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [41].IMR_PORT_LINK_STS_CHG[9:9]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [42].IMR_PORT_LINK_STS_CHG[10:10]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [43].IMR_PORT_LINK_STS_CHG[11:11]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [44].IMR_PORT_LINK_STS_CHG[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001270	IMR_PORT_LINK_STS_CHG [45].IMR_PORT_LINK_STS_CHG[13:13]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [46].IMR_PORT_LINK_STS_CHG[14:14]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [47].IMR_PORT_LINK_STS_CHG[15:15]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [48].IMR_PORT_LINK_STS_CHG[16:16]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [49].IMR_PORT_LINK_STS_CHG[17:17]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [50].IMR_PORT_LINK_STS_CHG[18:18]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [51].IMR_PORT_LINK_STS_CHG[19:19]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [52].IMR_PORT_LINK_STS_CHG[20:20]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [53].IMR_PORT_LINK_STS_CHG[21:21]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [54].IMR_PORT_LINK_STS_CHG[22:22]	1
0xBB001270	IMR_PORT_LINK_STS_CHG [55].IMR_PORT_LINK_STS_CHG[23:23]	1
0xBB001278	IMR_REMOTE_INTR_STS_UPD.PRESERVED[31:16]	16
0xBB001278	IMR_REMOTE_INTR_STS_UPD.IMR_RMT_INTR_STS_UPD[15:0]	16
0xBB001298	IMR_RLFD [0].IMR_RLFD[0:0]	1
0xBB001298	IMR_RLFD [1].IMR_RLFD[1:1]	1
0xBB001298	IMR_RLFD [2].IMR_RLFD[2:2]	1
0xBB001298	IMR_RLFD [3].IMR_RLFD[3:3]	1
0xBB001298	IMR_RLFD [4].IMR_RLFD[4:4]	1
0xBB001298	IMR_RLFD [5].IMR_RLFD[5:5]	1
0xBB001298	IMR_RLFD [6].IMR_RLFD[6:6]	1
0xBB001298	IMR_RLFD [7].IMR_RLFD[7:7]	1
0xBB001298	IMR_RLFD [8].IMR_RLFD[8:8]	1
0xBB001298	IMR_RLFD [9].IMR_RLFD[9:9]	1
0xBB001298	IMR_RLFD [10].IMR_RLFD[10:10]	1
0xBB001298	IMR_RLFD [11].IMR_RLFD[11:11]	1
0xBB001298	IMR_RLFD [12].IMR_RLFD[12:12]	1
0xBB001298	IMR_RLFD [13].IMR_RLFD[13:13]	1
0xBB001298	IMR_RLFD [14].IMR_RLFD[14:14]	1
0xBB001298	IMR_RLFD [15].IMR_RLFD[15:15]	1
0xBB001298	IMR_RLFD [16].IMR_RLFD[16:16]	1
0xBB001298	IMR_RLFD [17].IMR_RLFD[17:17]	1
0xBB001298	IMR_RLFD [18].IMR_RLFD[18:18]	1
0xBB001298	IMR_RLFD [19].IMR_RLFD[19:19]	1
0xBB001298	IMR_RLFD [20].IMR_RLFD[20:20]	1
0xBB001298	IMR_RLFD [21].IMR_RLFD[21:21]	1
0xBB001298	IMR_RLFD [22].IMR_RLFD[22:22]	1
0xBB001298	IMR_RLFD [23].IMR_RLFD[23:23]	1
0xBB001298	IMR_RLFD [24].IMR_RLFD[24:24]	1
0xBB001298	IMR_RLFD [25].IMR_RLFD[25:25]	1
0xBB001298	IMR_RLFD [26].IMR_RLFD[26:26]	1
0xBB001298	IMR_RLFD [27].IMR_RLFD[27:27]	1
0xBB001298	IMR_RLFD [28].IMR_RLFD[28:28]	1
0xBB001298	IMR_RLFD [29].IMR_RLFD[29:29]	1
0xBB001298	IMR_RLFD [30].IMR_RLFD[30:30]	1
0xBB001298	IMR_RLFD [31].IMR_RLFD[31:31]	1
0xBB00129C	IMR_RLFD [32].IMR_RLFD[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00129C	IMR_RLFD [33].IMR_RLFD[1:1]	1
0xBB00129C	IMR_RLFD [34].IMR_RLFD[2:2]	1
0xBB00129C	IMR_RLFD [35].IMR_RLFD[3:3]	1
0xBB00129C	IMR_RLFD [36].IMR_RLFD[4:4]	1
0xBB00129C	IMR_RLFD [37].IMR_RLFD[5:5]	1
0xBB00129C	IMR_RLFD [38].IMR_RLFD[6:6]	1
0xBB00129C	IMR_RLFD [39].IMR_RLFD[7:7]	1
0xBB00129C	IMR_RLFD [40].IMR_RLFD[8:8]	1
0xBB00129C	IMR_RLFD [41].IMR_RLFD[9:9]	1
0xBB00129C	IMR_RLFD [42].IMR_RLFD[10:10]	1
0xBB00129C	IMR_RLFD [43].IMR_RLFD[11:11]	1
0xBB00129C	IMR_RLFD [44].IMR_RLFD[12:12]	1
0xBB00129C	IMR_RLFD [45].IMR_RLFD[13:13]	1
0xBB00129C	IMR_RLFD [46].IMR_RLFD[14:14]	1
0xBB00129C	IMR_RLFD [47].IMR_RLFD[15:15]	1
0xBB00129C	IMR_RLFD [48].IMR_RLFD[16:16]	1
0xBB00129C	IMR_RLFD [49].IMR_RLFD[17:17]	1
0xBB00129C	IMR_RLFD [50].IMR_RLFD[18:18]	1
0xBB00129C	IMR_RLFD [51].IMR_RLFD[19:19]	1
0xBB00129C	IMR_RLFD [52].IMR_RLFD[20:20]	1
0xBB00129C	IMR_RLFD [53].IMR_RLFD[21:21]	1
0xBB00129C	IMR_RLFD [54].IMR_RLFD[22:22]	1
0xBB00129C	IMR_RLFD [55].IMR_RLFD[23:23]	1
0xBB001274	IMR_SERDES_ERR [0].IMR_SERDES_ERR[0:0]	1
0xBB001274	IMR_SERDES_ERR [1].IMR_SERDES_ERR[1:1]	1
0xBB001274	IMR_SERDES_ERR [2].IMR_SERDES_ERR[2:2]	1
0xBB001274	IMR_SERDES_ERR [3].IMR_SERDES_ERR[3:3]	1
0xBB001274	IMR_SERDES_ERR [4].IMR_SERDES_ERR[4:4]	1
0xBB001274	IMR_SERDES_ERR [5].IMR_SERDES_ERR[5:5]	1
0xBB001274	IMR_SERDES_ERR [6].IMR_SERDES_ERR[6:6]	1
0xBB001274	IMR_SERDES_ERR [7].IMR_SERDES_ERR[7:7]	1
0xBB001274	IMR_SERDES_ERR [8].IMR_SERDES_ERR[8:8]	1
0xBB001274	IMR_SERDES_ERR [9].IMR_SERDES_ERR[9:9]	1
0xBB001274	IMR_SERDES_ERR [10].IMR_SERDES_ERR[10:10]	1
0xBB001274	IMR_SERDES_ERR [11].IMR_SERDES_ERR[11:11]	1
0xBB001274	IMR_SERDES_ERR [12].IMR_SERDES_ERR[12:12]	1
0xBB001274	IMR_SERDES_ERR [13].IMR_SERDES_ERR[13:13]	1
0xBB0012A0	IMR_SERDES_RXIDLE.RESERVED[31:12]	20
0xBB0012A0	IMR_SERDES_RXIDLE.IMR_SERDES_RXIDLE[11:0]	12
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [0].IMR_SDS_UPD_PHYSTS[0:0]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [1].IMR_SDS_UPD_PHYSTS[1:1]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [2].IMR_SDS_UPD_PHYSTS[2:2]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [3].IMR_SDS_UPD_PHYSTS[3:3]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [4].IMR_SDS_UPD_PHYSTS[4:4]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [5].IMR_SDS_UPD_PHYSTS[5:5]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [6].IMR_SDS_UPD_PHYSTS[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [7].IMR_SDS_UPD_PHYSTS[7:7]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [8].IMR_SDS_UPD_PHYSTS[8:8]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [9].IMR_SDS_UPD_PHYSTS[9:9]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [10].IMR_SDS_UPD_PHYSTS[10:10]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [11].IMR_SDS_UPD_PHYSTS[11:11]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [12].IMR_SDS_UPD_PHYSTS[12:12]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [13].IMR_SDS_UPD_PHYSTS[13:13]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [14].IMR_SDS_UPD_PHYSTS[14:14]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [15].IMR_SDS_UPD_PHYSTS[15:15]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [16].IMR_SDS_UPD_PHYSTS[16:16]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [17].IMR_SDS_UPD_PHYSTS[17:17]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [18].IMR_SDS_UPD_PHYSTS[18:18]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [19].IMR_SDS_UPD_PHYSTS[19:19]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [20].IMR_SDS_UPD_PHYSTS[20:20]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [21].IMR_SDS_UPD_PHYSTS[21:21]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [22].IMR_SDS_UPD_PHYSTS[22:22]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [23].IMR_SDS_UPD_PHYSTS[23:23]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [24].IMR_SDS_UPD_PHYSTS[24:24]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [25].IMR_SDS_UPD_PHYSTS[25:25]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [26].IMR_SDS_UPD_PHYSTS[26:26]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [27].IMR_SDS_UPD_PHYSTS[27:27]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [28].IMR_SDS_UPD_PHYSTS[28:28]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [29].IMR_SDS_UPD_PHYSTS[29:29]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [30].IMR_SDS_UPD_PHYSTS[30:30]	1
0xBB001290	IMR_SERDES_UPD_PHYSTS_0 [31].IMR_SDS_UPD_PHYSTS[31:31]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [32].IMR_SDS_UPD_PHYSTS[0:0]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [33].IMR_SDS_UPD_PHYSTS[1:1]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [34].IMR_SDS_UPD_PHYSTS[2:2]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [35].IMR_SDS_UPD_PHYSTS[3:3]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [36].IMR_SDS_UPD_PHYSTS[4:4]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [37].IMR_SDS_UPD_PHYSTS[5:5]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [38].IMR_SDS_UPD_PHYSTS[6:6]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [39].IMR_SDS_UPD_PHYSTS[7:7]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [40].IMR_SDS_UPD_PHYSTS[8:8]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [41].IMR_SDS_UPD_PHYSTS[9:9]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [42].IMR_SDS_UPD_PHYSTS[10:10]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [43].IMR_SDS_UPD_PHYSTS[11:11]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [44].IMR_SDS_UPD_PHYSTS[12:12]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [45].IMR_SDS_UPD_PHYSTS[13:13]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [46].IMR_SDS_UPD_PHYSTS[14:14]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [47].IMR_SDS_UPD_PHYSTS[15:15]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [48].IMR_SDS_UPD_PHYSTS[16:16]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [49].IMR_SDS_UPD_PHYSTS[17:17]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [50].IMR_SDS_UPD_PHYSTS[18:18]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [51].IMR_SDS_UPD_PHYSTS[19:19]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [52].IMR_SDS_UPD_PHYSTS[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [53].IMR_SDS_UPD_PHYSTS[21:21]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [54].IMR_SDS_UPD_PHYSTS[22:22]	1
0xBB001294	IMR_SERDES_UPD_PHYSTS_0 [55].IMR_SDS_UPD_PHYSTS[23:23]	1
0xBB0012E4	ISR_AUTO_RECOVERY.RESERVED[31:7]	25
0xBB0012E4	ISR_AUTO_RECOVERY.ISR_AUTO_REC[6:6]	1
0xBB0012E4	ISR_AUTO_RECOVERY.ISR_TX_CRC_CNTR[5:5]	1
0xBB0012E4	ISR_AUTO_RECOVERY.ISR_SMI_REG_CHECK4_0[4:0]	5
0xBB0012FC	ISR_DBGO.RESERVED[31:1]	31
0xBB0012FC	ISR_DBGO.ISR_DBGO[0:0]	1
0xBB0012CC	ISR_EXT_GPIO [0].ISR_EXT_GPIO[0:0]	1
0xBB0012CC	ISR_EXT_GPIO [1].ISR_EXT_GPIO[1:1]	1
0xBB0012CC	ISR_EXT_GPIO [2].ISR_EXT_GPIO[2:2]	1
0xBB0012CC	ISR_EXT_GPIO [3].ISR_EXT_GPIO[3:3]	1
0xBB0012CC	ISR_EXT_GPIO [4].ISR_EXT_GPIO[4:4]	1
0xBB0012CC	ISR_EXT_GPIO [5].ISR_EXT_GPIO[5:5]	1
0xBB0012CC	ISR_EXT_GPIO [6].ISR_EXT_GPIO[6:6]	1
0xBB0012CC	ISR_EXT_GPIO [7].ISR_EXT_GPIO[7:7]	1
0xBB0012CC	ISR_EXT_GPIO [8].ISR_EXT_GPIO[8:8]	1
0xBB0012CC	ISR_EXT_GPIO [9].ISR_EXT_GPIO[9:9]	1
0xBB0012CC	ISR_EXT_GPIO [10].ISR_EXT_GPIO[10:10]	1
0xBB0012CC	ISR_EXT_GPIO [11].ISR_EXT_GPIO[11:11]	1
0xBB0012CC	ISR_EXT_GPIO [12].ISR_EXT_GPIO[12:12]	1
0xBB0012CC	ISR_EXT_GPIO [13].ISR_EXT_GPIO[13:13]	1
0xBB0012CC	ISR_EXT_GPIO [14].ISR_EXT_GPIO[14:14]	1
0xBB0012CC	ISR_EXT_GPIO [15].ISR_EXT_GPIO[15:15]	1
0xBB0012CC	ISR_EXT_GPIO [16].ISR_EXT_GPIO[16:16]	1
0xBB0012CC	ISR_EXT_GPIO [17].ISR_EXT_GPIO[17:17]	1
0xBB0012CC	ISR_EXT_GPIO [18].ISR_EXT_GPIO[18:18]	1
0xBB0012CC	ISR_EXT_GPIO [19].ISR_EXT_GPIO[19:19]	1
0xBB0012CC	ISR_EXT_GPIO [20].ISR_EXT_GPIO[20:20]	1
0xBB0012CC	ISR_EXT_GPIO [21].ISR_EXT_GPIO[21:21]	1
0xBB0012CC	ISR_EXT_GPIO [22].ISR_EXT_GPIO[22:22]	1
0xBB0012CC	ISR_EXT_GPIO [23].ISR_EXT_GPIO[23:23]	1
0xBB0012CC	ISR_EXT_GPIO [24].ISR_EXT_GPIO[24:24]	1
0xBB0012CC	ISR_EXT_GPIO [25].ISR_EXT_GPIO[25:25]	1
0xBB0012CC	ISR_EXT_GPIO [26].ISR_EXT_GPIO[26:26]	1
0xBB0012CC	ISR_EXT_GPIO [27].ISR_EXT_GPIO[27:27]	1
0xBB0012CC	ISR_EXT_GPIO [28].ISR_EXT_GPIO[28:28]	1
0xBB0012CC	ISR_EXT_GPIO [29].ISR_EXT_GPIO[29:29]	1
0xBB0012CC	ISR_EXT_GPIO [30].ISR_EXT_GPIO[30:30]	1
0xBB0012CC	ISR_EXT_GPIO [31].ISR_EXT_GPIO[31:31]	1
0xBB0012D0	ISR_EXT_GPIO [32].ISR_EXT_GPIO[0:0]	1
0xBB0012D0	ISR_EXT_GPIO [33].ISR_EXT_GPIO[1:1]	1
0xBB0012D0	ISR_EXT_GPIO [34].ISR_EXT_GPIO[2:2]	1
0xBB0012D0	ISR_EXT_GPIO [35].ISR_EXT_GPIO[3:3]	1
0xBB0012D0	ISR_EXT_GPIO [36].ISR_EXT_GPIO[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012B4	ISR_GLB_SRC_STS.RESERVED[31:17]	15
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_DBGO[16:16]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_SERDES_RXIDLE[15:15]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_RLFD[14:14]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_STAT_TRIGGER[13:13]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_RMT_INTR_STS_UPD[12:12]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_AUTO_REC[11:11]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_TX_CRC_CNTR[10:10]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_SMI_CHECK[9:9]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_SERDES_UPD_PHYSTS[8:8]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_TM[7:7]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_EXT_GPIO[6:6]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_FTHDM[5:5]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_OAM_DYGASP[4:4]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_CCM[3:3]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_TIMESTAMP_LATCH[2:2]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_SERDES[1:1]	1
0xBB0012B4	ISR_GLB_SRC_STS.ISR_GLB_LINK_CHG[0:0]	1
0xBB0012C8	ISR_MISC_RESERVED[31:18]	14
0xBB0012C8	ISR_MISC_ISR_ETHDM[17:17]	1
0xBB0012C8	ISR_MISC.ISR_OAM_DYGASP[16:16]	1
0xBB0012C8	ISR_MISC.ISR_CCM[15:0]	16
0xBB0012B8	ISR_PORT_LINK_STS_CHG [0].ISR_PORT_LINK_STS_CHG[0:0]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [1].ISR_PORT_LINK_STS_CHG[1:1]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [2].ISR_PORT_LINK_STS_CHG[2:2]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [3].ISR_PORT_LINK_STS_CHG[3:3]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [4].ISR_PORT_LINK_STS_CHG[4:4]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [5].ISR_PORT_LINK_STS_CHG[5:5]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [6].ISR_PORT_LINK_STS_CHG[6:6]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [7].ISR_PORT_LINK_STS_CHG[7:7]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [8].ISR_PORT_LINK_STS_CHG[8:8]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [9].ISR_PORT_LINK_STS_CHG[9:9]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [10].ISR_PORT_LINK_STS_CHG[10:10]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [11].ISR_PORT_LINK_STS_CHG[11:11]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [12].ISR_PORT_LINK_STS_CHG[12:12]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [13].ISR_PORT_LINK_STS_CHG[13:13]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [14].ISR_PORT_LINK_STS_CHG[14:14]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [15].ISR_PORT_LINK_STS_CHG[15:15]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [16].ISR_PORT_LINK_STS_CHG[16:16]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [17].ISR_PORT_LINK_STS_CHG[17:17]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [18].ISR_PORT_LINK_STS_CHG[18:18]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [19].ISR_PORT_LINK_STS_CHG[19:19]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [20].ISR_PORT_LINK_STS_CHG[20:20]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [21].ISR_PORT_LINK_STS_CHG[21:21]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [22].ISR_PORT_LINK_STS_CHG[22:22]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [23].ISR_PORT_LINK_STS_CHG[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012B8	ISR_PORT_LINK_STS_CHG [24].ISR_PORT_LINK_STS_CHG[24:24]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [25].ISR_PORT_LINK_STS_CHG[25:25]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [26].ISR_PORT_LINK_STS_CHG[26:26]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [27].ISR_PORT_LINK_STS_CHG[27:27]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [28].ISR_PORT_LINK_STS_CHG[28:28]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [29].ISR_PORT_LINK_STS_CHG[29:29]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [30].ISR_PORT_LINK_STS_CHG[30:30]	1
0xBB0012B8	ISR_PORT_LINK_STS_CHG [31].ISR_PORT_LINK_STS_CHG[31:31]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [32].ISR_PORT_LINK_STS_CHG[0:0]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [33].ISR_PORT_LINK_STS_CHG[1:1]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [34].ISR_PORT_LINK_STS_CHG[2:2]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [35].ISR_PORT_LINK_STS_CHG[3:3]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [36].ISR_PORT_LINK_STS_CHG[4:4]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [37].ISR_PORT_LINK_STS_CHG[5:5]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [38].ISR_PORT_LINK_STS_CHG[6:6]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [39].ISR_PORT_LINK_STS_CHG[7:7]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [40].ISR_PORT_LINK_STS_CHG[8:8]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [41].ISR_PORT_LINK_STS_CHG[9:9]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [42].ISR_PORT_LINK_STS_CHG[10:10]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [43].ISR_PORT_LINK_STS_CHG[11:11]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [44].ISR_PORT_LINK_STS_CHG[12:12]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [45].ISR_PORT_LINK_STS_CHG[13:13]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [46].ISR_PORT_LINK_STS_CHG[14:14]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [47].ISR_PORT_LINK_STS_CHG[15:15]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [48].ISR_PORT_LINK_STS_CHG[16:16]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [49].ISR_PORT_LINK_STS_CHG[17:17]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [50].ISR_PORT_LINK_STS_CHG[18:18]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [51].ISR_PORT_LINK_STS_CHG[19:19]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [52].ISR_PORT_LINK_STS_CHG[20:20]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [53].ISR_PORT_LINK_STS_CHG[21:21]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [54].ISR_PORT_LINK_STS_CHG[22:22]	1
0xBB0012BC	ISR_PORT_LINK_STS_CHG [55].ISR_PORT_LINK_STS_CHG[23:23]	1
0xBB0012C4	ISR_REMOTE_INTR_STS_UPD.RESERVED[31:16]	16
0xBB0012C4	ISR_REMOTE_INTR_STS_UPD.ISR_RMT_INTR_STS_UPD[15:0]	16
0xBB0012F0	ISR_RLFD [0].ISR_RLFD[0:0]	1
0xBB0012F0	ISR_RLFD [1].ISR_RLFD[1:1]	1
0xBB0012F0	ISR_RLFD [2].ISR_RLFD[2:2]	1
0xBB0012F0	ISR_RLFD [3].ISR_RLFD[3:3]	1
0xBB0012F0	ISR_RLFD [4].ISR_RLFD[4:4]	1
0xBB0012F0	ISR_RLFD [5].ISR_RLFD[5:5]	1
0xBB0012F0	ISR_RLFD [6].ISR_RLFD[6:6]	1
0xBB0012F0	ISR_RLFD [7].ISR_RLFD[7:7]	1
0xBB0012F0	ISR_RLFD [8].ISR_RLFD[8:8]	1
0xBB0012F0	ISR_RLFD [9].ISR_RLFD[9:9]	1
0xBB0012F0	ISR_RLFD [10].ISR_RLFD[10:10]	1
0xBB0012F0	ISR_RLFD [11].ISR_RLFD[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012F0	ISR_RLFD [12].ISR_RLFD[12:12]	1
0xBB0012F0	ISR_RLFD [13].ISR_RLFD[13:13]	1
0xBB0012F0	ISR_RLFD [14].ISR_RLFD[14:14]	1
0xBB0012F0	ISR_RLFD [15].ISR_RLFD[15:15]	1
0xBB0012F0	ISR_RLFD [16].ISR_RLFD[16:16]	1
0xBB0012F0	ISR_RLFD [17].ISR_RLFD[17:17]	1
0xBB0012F0	ISR_RLFD [18].ISR_RLFD[18:18]	1
0xBB0012F0	ISR_RLFD [19].ISR_RLFD[19:19]	1
0xBB0012F0	ISR_RLFD [20].ISR_RLFD[20:20]	1
0xBB0012F0	ISR_RLFD [21].ISR_RLFD[21:21]	1
0xBB0012F0	ISR_RLFD [22].ISR_RLFD[22:22]	1
0xBB0012F0	ISR_RLFD [23].ISR_RLFD[23:23]	1
0xBB0012F0	ISR_RLFD [24].ISR_RLFD[24:24]	1
0xBB0012F0	ISR_RLFD [25].ISR_RLFD[25:25]	1
0xBB0012F0	ISR_RLFD [26].ISR_RLFD[26:26]	1
0xBB0012F0	ISR_RLFD [27].ISR_RLFD[27:27]	1
0xBB0012F0	ISR_RLFD [28].ISR_RLFD[28:28]	1
0xBB0012F0	ISR_RLFD [29].ISR_RLFD[29:29]	1
0xBB0012F0	ISR_RLFD [30].ISR_RLFD[30:30]	1
0xBB0012F0	ISR_RLFD [31].ISR_RLFD[31:31]	1
0xBB0012F4	ISR_RLFD [32].ISR_RLFD[0:0]	1
0xBB0012F4	ISR_RLFD [33].ISR_RLFD[1:1]	1
0xBB0012F4	ISR_RLFD [34].ISR_RLFD[2:2]	1
0xBB0012F4	ISR_RLFD [35].ISR_RLFD[3:3]	1
0xBB0012F4	ISR_RLFD [36].ISR_RLFD[4:4]	1
0xBB0012F4	ISR_RLFD [37].ISR_RLFD[5:5]	1
0xBB0012F4	ISR_RLFD [38].ISR_RLFD[6:6]	1
0xBB0012F4	ISR_RLFD [39].ISR_RLFD[7:7]	1
0xBB0012F4	ISR_RLFD [40].ISR_RLFD[8:8]	1
0xBB0012F4	ISR_RLFD [41].ISR_RLFD[9:9]	1
0xBB0012F4	ISR_RLFD [42].ISR_RLFD[10:10]	1
0xBB0012F4	ISR_RLFD [43].ISR_RLFD[11:11]	1
0xBB0012F4	ISR_RLFD [44].ISR_RLFD[12:12]	1
0xBB0012F4	ISR_RLFD [45].ISR_RLFD[13:13]	1
0xBB0012F4	ISR_RLFD [46].ISR_RLFD[14:14]	1
0xBB0012F4	ISR_RLFD [47].ISR_RLFD[15:15]	1
0xBB0012F4	ISR_RLFD [48].ISR_RLFD[16:16]	1
0xBB0012F4	ISR_RLFD [49].ISR_RLFD[17:17]	1
0xBB0012F4	ISR_RLFD [50].ISR_RLFD[18:18]	1
0xBB0012F4	ISR_RLFD [51].ISR_RLFD[19:19]	1
0xBB0012F4	ISR_RLFD [52].ISR_RLFD[20:20]	1
0xBB0012F4	ISR_RLFD [53].ISR_RLFD[21:21]	1
0xBB0012F4	ISR_RLFD [54].ISR_RLFD[22:22]	1
0xBB0012F4	ISR_RLFD [55].ISR_RLFD[23:23]	1
0xBB0012C0	ISR_SERDES_ERR [0].ISR_SERDES_ERR[0:0]	1
0xBB0012C0	ISR_SERDES_ERR [1].ISR_SERDES_ERR[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012C0	ISR_SERDES_ERR [2].ISR_SERDES_ERR[2:2]	1
0xBB0012C0	ISR_SERDES_ERR [3].ISR_SERDES_ERR[3:3]	1
0xBB0012C0	ISR_SERDES_ERR [4].ISR_SERDES_ERR[4:4]	1
0xBB0012C0	ISR_SERDES_ERR [5].ISR_SERDES_ERR[5:5]	1
0xBB0012C0	ISR_SERDES_ERR [6].ISR_SERDES_ERR[6:6]	1
0xBB0012C0	ISR_SERDES_ERR [7].ISR_SERDES_ERR[7:7]	1
0xBB0012C0	ISR_SERDES_ERR [8].ISR_SERDES_ERR[8:8]	1
0xBB0012C0	ISR_SERDES_ERR [9].ISR_SERDES_ERR[9:9]	1
0xBB0012C0	ISR_SERDES_ERR [10].ISR_SERDES_ERR[10:10]	1
0xBB0012C0	ISR_SERDES_ERR [11].ISR_SERDES_ERR[11:11]	1
0xBB0012C0	ISR_SERDES_ERR [12].ISR_SERDES_ERR[12:12]	1
0xBB0012C0	ISR_SERDES_ERR [13].ISR_SERDES_ERR[13:13]	1
0xBB0012F8	ISR_SERDES_RXIDLE.RESERVED[31:12]	20
0xBB0012F8	ISR_SERDES_RXIDLE.ISR_SERDES_RXIDLE[11:0]	12
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [0].ISR_SDS_UPD_PHYSTS[0:0]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [1].ISR_SDS_UPD_PHYSTS[1:1]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [2].ISR_SDS_UPD_PHYSTS[2:2]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [3].ISR_SDS_UPD_PHYSTS[3:3]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [4].ISR_SDS_UPD_PHYSTS[4:4]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [5].ISR_SDS_UPD_PHYSTS[5:5]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [6].ISR_SDS_UPD_PHYSTS[6:6]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [7].ISR_SDS_UPD_PHYSTS[7:7]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [8].ISR_SDS_UPD_PHYSTS[8:8]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [9].ISR_SDS_UPD_PHYSTS[9:9]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [10].ISR_SDS_UPD_PHYSTS[10:10]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [11].ISR_SDS_UPD_PHYSTS[11:11]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [12].ISR_SDS_UPD_PHYSTS[12:12]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [13].ISR_SDS_UPD_PHYSTS[13:13]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [14].ISR_SDS_UPD_PHYSTS[14:14]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [15].ISR_SDS_UPD_PHYSTS[15:15]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [16].ISR_SDS_UPD_PHYSTS[16:16]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [17].ISR_SDS_UPD_PHYSTS[17:17]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [18].ISR_SDS_UPD_PHYSTS[18:18]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [19].ISR_SDS_UPD_PHYSTS[19:19]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [20].ISR_SDS_UPD_PHYSTS[20:20]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [21].ISR_SDS_UPD_PHYSTS[21:21]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [22].ISR_SDS_UPD_PHYSTS[22:22]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [23].ISR_SDS_UPD_PHYSTS[23:23]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [24].ISR_SDS_UPD_PHYSTS[24:24]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [25].ISR_SDS_UPD_PHYSTS[25:25]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [26].ISR_SDS_UPD_PHYSTS[26:26]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [27].ISR_SDS_UPD_PHYSTS[27:27]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [28].ISR_SDS_UPD_PHYSTS[28:28]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [29].ISR_SDS_UPD_PHYSTS[29:29]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [30].ISR_SDS_UPD_PHYSTS[30:30]	1
0xBB0012E8	ISR_SERDES_UPD_PHYSTS [31].ISR_SDS_UPD_PHYSTS[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [32].ISR_SDS_UPD_PHYSTS[0:0]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [33].ISR_SDS_UPD_PHYSTS[1:1]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [34].ISR_SDS_UPD_PHYSTS[2:2]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [35].ISR_SDS_UPD_PHYSTS[3:3]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [36].ISR_SDS_UPD_PHYSTS[4:4]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [37].ISR_SDS_UPD_PHYSTS[5:5]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [38].ISR_SDS_UPD_PHYSTS[6:6]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [39].ISR_SDS_UPD_PHYSTS[7:7]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [40].ISR_SDS_UPD_PHYSTS[8:8]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [41].ISR_SDS_UPD_PHYSTS[9:9]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [42].ISR_SDS_UPD_PHYSTS[10:10]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [43].ISR_SDS_UPD_PHYSTS[11:11]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [44].ISR_SDS_UPD_PHYSTS[12:12]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [45].ISR_SDS_UPD_PHYSTS[13:13]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [46].ISR_SDS_UPD_PHYSTS[14:14]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [47].ISR_SDS_UPD_PHYSTS[15:15]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [48].ISR_SDS_UPD_PHYSTS[16:16]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [49].ISR_SDS_UPD_PHYSTS[17:17]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [50].ISR_SDS_UPD_PHYSTS[18:18]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [51].ISR_SDS_UPD_PHYSTS[19:19]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [52].ISR_SDS_UPD_PHYSTS[20:20]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [53].ISR_SDS_UPD_PHYSTS[21:21]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [54].ISR_SDS_UPD_PHYSTS[22:22]	1
0xBB0012EC	ISR_SERDES_UPD_PHYSTS [55].ISR_SDS_UPD_PHYSTS[23:23]	1
0xBB001800	ISR_STAT_TRIGGER [0].ISR_STAT_TRIGGER[0:0]	1
0xBB001800	ISR_STAT_TRIGGER [1].ISR_STAT_TRIGGER[1:1]	1
0xBB001800	ISR_STAT_TRIGGER [2].ISR_STAT_TRIGGER[2:2]	1
0xBB001800	ISR_STAT_TRIGGER [3].ISR_STAT_TRIGGER[3:3]	1
0xBB001800	ISR_STAT_TRIGGER [4].ISR_STAT_TRIGGER[4:4]	1
0xBB001800	ISR_STAT_TRIGGER [5].ISR_STAT_TRIGGER[5:5]	1
0xBB001800	ISR_STAT_TRIGGER [6].ISR_STAT_TRIGGER[6:6]	1
0xBB001800	ISR_STAT_TRIGGER [7].ISR_STAT_TRIGGER[7:7]	1
0xBB001800	ISR_STAT_TRIGGER [8].ISR_STAT_TRIGGER[8:8]	1
0xBB001800	ISR_STAT_TRIGGER [9].ISR_STAT_TRIGGER[9:9]	1
0xBB001800	ISR_STAT_TRIGGER [10].ISR_STAT_TRIGGER[10:10]	1
0xBB001800	ISR_STAT_TRIGGER [11].ISR_STAT_TRIGGER[11:11]	1
0xBB001800	ISR_STAT_TRIGGER [12].ISR_STAT_TRIGGER[12:12]	1
0xBB001800	ISR_STAT_TRIGGER [13].ISR_STAT_TRIGGER[13:13]	1
0xBB001800	ISR_STAT_TRIGGER [14].ISR_STAT_TRIGGER[14:14]	1
0xBB001800	ISR_STAT_TRIGGER [15].ISR_STAT_TRIGGER[15:15]	1
0xBB001800	ISR_STAT_TRIGGER [16].ISR_STAT_TRIGGER[16:16]	1
0xBB001800	ISR_STAT_TRIGGER [17].ISR_STAT_TRIGGER[17:17]	1
0xBB001800	ISR_STAT_TRIGGER [18].ISR_STAT_TRIGGER[18:18]	1
0xBB001800	ISR_STAT_TRIGGER [19].ISR_STAT_TRIGGER[19:19]	1
0xBB001800	ISR_STAT_TRIGGER [20].ISR_STAT_TRIGGER[20:20]	1
0xBB001800	ISR_STAT_TRIGGER [21].ISR_STAT_TRIGGER[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001800	ISR_STAT_TRIGGER [22].ISR_STAT_TRIGGER[22:22]	1
0xBB001800	ISR_STAT_TRIGGER [23].ISR_STAT_TRIGGER[23:23]	1
0xBB001800	ISR_STAT_TRIGGER [24].ISR_STAT_TRIGGER[24:24]	1
0xBB001800	ISR_STAT_TRIGGER [25].ISR_STAT_TRIGGER[25:25]	1
0xBB001800	ISR_STAT_TRIGGER [26].ISR_STAT_TRIGGER[26:26]	1
0xBB001800	ISR_STAT_TRIGGER [27].ISR_STAT_TRIGGER[27:27]	1
0xBB001800	ISR_STAT_TRIGGER [28].ISR_STAT_TRIGGER[28:28]	1
0xBB001800	ISR_STAT_TRIGGER [29].ISR_STAT_TRIGGER[29:29]	1
0xBB001800	ISR_STAT_TRIGGER [30].ISR_STAT_TRIGGER[30:30]	1
0xBB001800	ISR_STAT_TRIGGER [31].ISR_STAT_TRIGGER[31:31]	1
0xBB001804	ISR_STAT_TRIGGER [32].ISR_STAT_TRIGGER[0:0]	1
0xBB001804	ISR_STAT_TRIGGER [33].ISR_STAT_TRIGGER[1:1]	1
0xBB001804	ISR_STAT_TRIGGER [34].ISR_STAT_TRIGGER[2:2]	1
0xBB001804	ISR_STAT_TRIGGER [35].ISR_STAT_TRIGGER[3:3]	1
0xBB001804	ISR_STAT_TRIGGER [36].ISR_STAT_TRIGGER[4:4]	1
0xBB001804	ISR_STAT_TRIGGER [37].ISR_STAT_TRIGGER[5:5]	1
0xBB001804	ISR_STAT_TRIGGER [38].ISR_STAT_TRIGGER[6:6]	1
0xBB001804	ISR_STAT_TRIGGER [39].ISR_STAT_TRIGGER[7:7]	1
0xBB001804	ISR_STAT_TRIGGER [40].ISR_STAT_TRIGGER[8:8]	1
0xBB001804	ISR_STAT_TRIGGER [41].ISR_STAT_TRIGGER[9:9]	1
0xBB001804	ISR_STAT_TRIGGER [42].ISR_STAT_TRIGGER[10:10]	1
0xBB001804	ISR_STAT_TRIGGER [43].ISR_STAT_TRIGGER[11:11]	1
0xBB001804	ISR_STAT_TRIGGER [44].ISR_STAT_TRIGGER[12:12]	1
0xBB001804	ISR_STAT_TRIGGER [45].ISR_STAT_TRIGGER[13:13]	1
0xBB001804	ISR_STAT_TRIGGER [46].ISR_STAT_TRIGGER[14:14]	1
0xBB001804	ISR_STAT_TRIGGER [47].ISR_STAT_TRIGGER[15:15]	1
0xBB001804	ISR_STAT_TRIGGER [48].ISR_STAT_TRIGGER[16:16]	1
0xBB001804	ISR_STAT_TRIGGER [49].ISR_STAT_TRIGGER[17:17]	1
0xBB001804	ISR_STAT_TRIGGER [50].ISR_STAT_TRIGGER[18:18]	1
0xBB001804	ISR_STAT_TRIGGER [51].ISR_STAT_TRIGGER[19:19]	1
0xBB001804	ISR_STAT_TRIGGER [52].ISR_STAT_TRIGGER[20:20]	1
0xBB001804	ISR_STAT_TRIGGER [53].ISR_STAT_TRIGGER[21:21]	1
0xBB001804	ISR_STAT_TRIGGER [54].ISR_STAT_TRIGGER[22:22]	1
0xBB001804	ISR_STAT_TRIGGER [55].ISR_STAT_TRIGGER[23:23]	1
0xBB001804	ISR_STAT_TRIGGER [56].ISR_STAT_TRIGGER[24:24]	1
0xBB001804	ISR_STAT_TRIGGER [57].ISR_STAT_TRIGGER[25:25]	1
0xBB001804	ISR_STAT_TRIGGER [58].ISR_STAT_TRIGGER[26:26]	1
0xBB001804	ISR_STAT_TRIGGER [59].ISR_STAT_TRIGGER[27:27]	1
0xBB001804	ISR_STAT_TRIGGER [60].ISR_STAT_TRIGGER[28:28]	1
0xBB001804	ISR_STAT_TRIGGER [61].ISR_STAT_TRIGGER[29:29]	1
0xBB001804	ISR_STAT_TRIGGER [62].ISR_STAT_TRIGGER[30:30]	1
0xBB001804	ISR_STAT_TRIGGER [63].ISR_STAT_TRIGGER[31:31]	1
0xBB001808	ISR_STAT_TRIGGER [64].ISR_STAT_TRIGGER[0:0]	1
0xBB001808	ISR_STAT_TRIGGER [65].ISR_STAT_TRIGGER[1:1]	1
0xBB001808	ISR_STAT_TRIGGER [66].ISR_STAT_TRIGGER[2:2]	1
0xBB001808	ISR_STAT_TRIGGER [67].ISR_STAT_TRIGGER[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001808	ISR_STAT_TRIGGER [68].ISR_STAT_TRIGGER[4:4]	1
0xBB001808	ISR_STAT_TRIGGER [69].ISR_STAT_TRIGGER[5:5]	1
0xBB001808	ISR_STAT_TRIGGER [70].ISR_STAT_TRIGGER[6:6]	1
0xBB001808	ISR_STAT_TRIGGER [71].ISR_STAT_TRIGGER[7:7]	1
0xBB001808	ISR_STAT_TRIGGER [72].ISR_STAT_TRIGGER[8:8]	1
0xBB001808	ISR_STAT_TRIGGER [73].ISR_STAT_TRIGGER[9:9]	1
0xBB001808	ISR_STAT_TRIGGER [74].ISR_STAT_TRIGGER[10:10]	1
0xBB001808	ISR_STAT_TRIGGER [75].ISR_STAT_TRIGGER[11:11]	1
0xBB001808	ISR_STAT_TRIGGER [76].ISR_STAT_TRIGGER[12:12]	1
0xBB001808	ISR_STAT_TRIGGER [77].ISR_STAT_TRIGGER[13:13]	1
0xBB001808	ISR_STAT_TRIGGER [78].ISR_STAT_TRIGGER[14:14]	1
0xBB001808	ISR_STAT_TRIGGER [79].ISR_STAT_TRIGGER[15:15]	1
0xBB001808	ISR_STAT_TRIGGER [80].ISR_STAT_TRIGGER[16:16]	1
0xBB001808	ISR_STAT_TRIGGER [81].ISR_STAT_TRIGGER[17:17]	1
0xBB001808	ISR_STAT_TRIGGER [82].ISR_STAT_TRIGGER[18:18]	1
0xBB001808	ISR_STAT_TRIGGER [83].ISR_STAT_TRIGGER[19:19]	1
0xBB001808	ISR_STAT_TRIGGER [84].ISR_STAT_TRIGGER[20:20]	1
0xBB001808	ISR_STAT_TRIGGER [85].ISR_STAT_TRIGGER[21:21]	1
0xBB001808	ISR_STAT_TRIGGER [86].ISR_STAT_TRIGGER[22:22]	1
0xBB001808	ISR_STAT_TRIGGER [87].ISR_STAT_TRIGGER[23:23]	1
0xBB001808	ISR_STAT_TRIGGER [88].ISR_STAT_TRIGGER[24:24]	1
0xBB001808	ISR_STAT_TRIGGER [89].ISR_STAT_TRIGGER[25:25]	1
0xBB001808	ISR_STAT_TRIGGER [90].ISR_STAT_TRIGGER[26:26]	1
0xBB001808	ISR_STAT_TRIGGER [91].ISR_STAT_TRIGGER[27:27]	1
0xBB001808	ISR_STAT_TRIGGER [92].ISR_STAT_TRIGGER[28:28]	1
0xBB001808	ISR_STAT_TRIGGER [93].ISR_STAT_TRIGGER[29:29]	1
0xBB001808	ISR_STAT_TRIGGER [94].ISR_STAT_TRIGGER[30:30]	1
0xBB001808	ISR_STAT_TRIGGER [95].ISR_STAT_TRIGGER[31:31]	1
0xBB00180C	ISR_STAT_TRIGGER [96].ISR_STAT_TRIGGER[0:0]	1
0xBB00180C	ISR_STAT_TRIGGER [97].ISR_STAT_TRIGGER[1:1]	1
0xBB00180C	ISR_STAT_TRIGGER [98].ISR_STAT_TRIGGER[2:2]	1
0xBB00180C	ISR_STAT_TRIGGER [99].ISR_STAT_TRIGGER[3:3]	1
0xBB00180C	ISR_STAT_TRIGGER [100].ISR_STAT_TRIGGER[4:4]	1
0xBB00180C	ISR_STAT_TRIGGER [101].ISR_STAT_TRIGGER[5:5]	1
0xBB00180C	ISR_STAT_TRIGGER [102].ISR_STAT_TRIGGER[6:6]	1
0xBB00180C	ISR_STAT_TRIGGER [103].ISR_STAT_TRIGGER[7:7]	1
0xBB00180C	ISR_STAT_TRIGGER [104].ISR_STAT_TRIGGER[8:8]	1
0xBB00180C	ISR_STAT_TRIGGER [105].ISR_STAT_TRIGGER[9:9]	1
0xBB00180C	ISR_STAT_TRIGGER [106].ISR_STAT_TRIGGER[10:10]	1
0xBB00180C	ISR_STAT_TRIGGER [107].ISR_STAT_TRIGGER[11:11]	1
0xBB00180C	ISR_STAT_TRIGGER [108].ISR_STAT_TRIGGER[12:12]	1
0xBB00180C	ISR_STAT_TRIGGER [109].ISR_STAT_TRIGGER[13:13]	1
0xBB00180C	ISR_STAT_TRIGGER [110].ISR_STAT_TRIGGER[14:14]	1
0xBB00180C	ISR_STAT_TRIGGER [111].ISR_STAT_TRIGGER[15:15]	1
0xBB00180C	ISR_STAT_TRIGGER [112].ISR_STAT_TRIGGER[16:16]	1
0xBB00180C	ISR_STAT_TRIGGER [113].ISR_STAT_TRIGGER[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00180C	ISR_STAT_TRIGGER [114].ISR_STAT_TRIGGER[18:18]	1
0xBB00180C	ISR_STAT_TRIGGER [115].ISR_STAT_TRIGGER[19:19]	1
0xBB00180C	ISR_STAT_TRIGGER [116].ISR_STAT_TRIGGER[20:20]	1
0xBB00180C	ISR_STAT_TRIGGER [117].ISR_STAT_TRIGGER[21:21]	1
0xBB00180C	ISR_STAT_TRIGGER [118].ISR_STAT_TRIGGER[22:22]	1
0xBB00180C	ISR_STAT_TRIGGER [119].ISR_STAT_TRIGGER[23:23]	1
0xBB00180C	ISR_STAT_TRIGGER [120].ISR_STAT_TRIGGER[24:24]	1
0xBB00180C	ISR_STAT_TRIGGER [121].ISR_STAT_TRIGGER[25:25]	1
0xBB00180C	ISR_STAT_TRIGGER [122].ISR_STAT_TRIGGER[26:26]	1
0xBB00180C	ISR_STAT_TRIGGER [123].ISR_STAT_TRIGGER[27:27]	1
0xBB00180C	ISR_STAT_TRIGGER [124].ISR_STAT_TRIGGER[28:28]	1
0xBB00180C	ISR_STAT_TRIGGER [125].ISR_STAT_TRIGGER[29:29]	1
0xBB00180C	ISR_STAT_TRIGGER [126].ISR_STAT_TRIGGER[30:30]	1
0xBB00180C	ISR_STAT_TRIGGER [127].ISR_STAT_TRIGGER[31:31]	1
0xBB001810	ISR_STAT_TRIGGER [128].ISR_STAT_TRIGGER[0:0]	1
0xBB001810	ISR_STAT_TRIGGER [129].ISR_STAT_TRIGGER[1:1]	1
0xBB001810	ISR_STAT_TRIGGER [130].ISR_STAT_TRIGGER[2:2]	1
0xBB001810	ISR_STAT_TRIGGER [131].ISR_STAT_TRIGGER[3:3]	1
0xBB001810	ISR_STAT_TRIGGER [132].ISR_STAT_TRIGGER[4:4]	1
0xBB001810	ISR_STAT_TRIGGER [133].ISR_STAT_TRIGGER[5:5]	1
0xBB001810	ISR_STAT_TRIGGER [134].ISR_STAT_TRIGGER[6:6]	1
0xBB001810	ISR_STAT_TRIGGER [135].ISR_STAT_TRIGGER[7:7]	1
0xBB001810	ISR_STAT_TRIGGER [136].ISR_STAT_TRIGGER[8:8]	1
0xBB001810	ISR_STAT_TRIGGER [137].ISR_STAT_TRIGGER[9:9]	1
0xBB001810	ISR_STAT_TRIGGER [138].ISR_STAT_TRIGGER[10:10]	1
0xBB001810	ISR_STAT_TRIGGER [139].ISR_STAT_TRIGGER[11:11]	1
0xBB001810	ISR_STAT_TRIGGER [140].ISR_STAT_TRIGGER[12:12]	1
0xBB001810	ISR_STAT_TRIGGER [141].ISR_STAT_TRIGGER[13:13]	1
0xBB001810	ISR_STAT_TRIGGER [142].ISR_STAT_TRIGGER[14:14]	1
0xBB001810	ISR_STAT_TRIGGER [143].ISR_STAT_TRIGGER[15:15]	1
0xBB001810	ISR_STAT_TRIGGER [144].ISR_STAT_TRIGGER[16:16]	1
0xBB001810	ISR_STAT_TRIGGER [145].ISR_STAT_TRIGGER[17:17]	1
0xBB001810	ISR_STAT_TRIGGER [146].ISR_STAT_TRIGGER[18:18]	1
0xBB001810	ISR_STAT_TRIGGER [147].ISR_STAT_TRIGGER[19:19]	1
0xBB001810	ISR_STAT_TRIGGER [148].ISR_STAT_TRIGGER[20:20]	1
0xBB001810	ISR_STAT_TRIGGER [149].ISR_STAT_TRIGGER[21:21]	1
0xBB001810	ISR_STAT_TRIGGER [150].ISR_STAT_TRIGGER[22:22]	1
0xBB001810	ISR_STAT_TRIGGER [151].ISR_STAT_TRIGGER[23:23]	1
0xBB001810	ISR_STAT_TRIGGER [152].ISR_STAT_TRIGGER[24:24]	1
0xBB001810	ISR_STAT_TRIGGER [153].ISR_STAT_TRIGGER[25:25]	1
0xBB001810	ISR_STAT_TRIGGER [154].ISR_STAT_TRIGGER[26:26]	1
0xBB001810	ISR_STAT_TRIGGER [155].ISR_STAT_TRIGGER[27:27]	1
0xBB001810	ISR_STAT_TRIGGER [156].ISR_STAT_TRIGGER[28:28]	1
0xBB001810	ISR_STAT_TRIGGER [157].ISR_STAT_TRIGGER[29:29]	1
0xBB001810	ISR_STAT_TRIGGER [158].ISR_STAT_TRIGGER[30:30]	1
0xBB001810	ISR_STAT_TRIGGER [159].ISR_STAT_TRIGGER[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001814	ISR_STAT_TRIGGER [160].ISR_STAT_TRIGGER[0:0]	1
0xBB001814	ISR_STAT_TRIGGER [161].ISR_STAT_TRIGGER[1:1]	1
0xBB001814	ISR_STAT_TRIGGER [162].ISR_STAT_TRIGGER[2:2]	1
0xBB001814	ISR_STAT_TRIGGER [163].ISR_STAT_TRIGGER[3:3]	1
0xBB001814	ISR_STAT_TRIGGER [164].ISR_STAT_TRIGGER[4:4]	1
0xBB001814	ISR_STAT_TRIGGER [165].ISR_STAT_TRIGGER[5:5]	1
0xBB001814	ISR_STAT_TRIGGER [166].ISR_STAT_TRIGGER[6:6]	1
0xBB001814	ISR_STAT_TRIGGER [167].ISR_STAT_TRIGGER[7:7]	1
0xBB001814	ISR_STAT_TRIGGER [168].ISR_STAT_TRIGGER[8:8]	1
0xBB001814	ISR_STAT_TRIGGER [169].ISR_STAT_TRIGGER[9:9]	1
0xBB001814	ISR_STAT_TRIGGER [170].ISR_STAT_TRIGGER[10:10]	1
0xBB001814	ISR_STAT_TRIGGER [171].ISR_STAT_TRIGGER[11:11]	1
0xBB001814	ISR_STAT_TRIGGER [172].ISR_STAT_TRIGGER[12:12]	1
0xBB001814	ISR_STAT_TRIGGER [173].ISR_STAT_TRIGGER[13:13]	1
0xBB001814	ISR_STAT_TRIGGER [174].ISR_STAT_TRIGGER[14:14]	1
0xBB001814	ISR_STAT_TRIGGER [175].ISR_STAT_TRIGGER[15:15]	1
0xBB001814	ISR_STAT_TRIGGER [176].ISR_STAT_TRIGGER[16:16]	1
0xBB001814	ISR_STAT_TRIGGER [177].ISR_STAT_TRIGGER[17:17]	1
0xBB001814	ISR_STAT_TRIGGER [178].ISR_STAT_TRIGGER[18:18]	1
0xBB001814	ISR_STAT_TRIGGER [179].ISR_STAT_TRIGGER[19:19]	1
0xBB001814	ISR_STAT_TRIGGER [180].ISR_STAT_TRIGGER[20:20]	1
0xBB001814	ISR_STAT_TRIGGER [181].ISR_STAT_TRIGGER[21:21]	1
0xBB001814	ISR_STAT_TRIGGER [182].ISR_STAT_TRIGGER[22:22]	1
0xBB001814	ISR_STAT_TRIGGER [183].ISR_STAT_TRIGGER[23:23]	1
0xBB001814	ISR_STAT_TRIGGER [184].ISR_STAT_TRIGGER[24:24]	1
0xBB001814	ISR_STAT_TRIGGER [185].ISR_STAT_TRIGGER[25:25]	1
0xBB001814	ISR_STAT_TRIGGER [186].ISR_STAT_TRIGGER[26:26]	1
0xBB001814	ISR_STAT_TRIGGER [187].ISR_STAT_TRIGGER[27:27]	1
0xBB001814	ISR_STAT_TRIGGER [188].ISR_STAT_TRIGGER[28:28]	1
0xBB001814	ISR_STAT_TRIGGER [189].ISR_STAT_TRIGGER[29:29]	1
0xBB001814	ISR_STAT_TRIGGER [190].ISR_STAT_TRIGGER[30:30]	1
0xBB001814	ISR_STAT_TRIGGER [191].ISR_STAT_TRIGGER[31:31]	1
0xBB001818	ISR_STAT_TRIGGER [192].ISR_STAT_TRIGGER[0:0]	1
0xBB001818	ISR_STAT_TRIGGER [193].ISR_STAT_TRIGGER[1:1]	1
0xBB001818	ISR_STAT_TRIGGER [194].ISR_STAT_TRIGGER[2:2]	1
0xBB001818	ISR_STAT_TRIGGER [195].ISR_STAT_TRIGGER[3:3]	1
0xBB001818	ISR_STAT_TRIGGER [196].ISR_STAT_TRIGGER[4:4]	1
0xBB001818	ISR_STAT_TRIGGER [197].ISR_STAT_TRIGGER[5:5]	1
0xBB001818	ISR_STAT_TRIGGER [198].ISR_STAT_TRIGGER[6:6]	1
0xBB001818	ISR_STAT_TRIGGER [199].ISR_STAT_TRIGGER[7:7]	1
0xBB001818	ISR_STAT_TRIGGER [200].ISR_STAT_TRIGGER[8:8]	1
0xBB001818	ISR_STAT_TRIGGER [201].ISR_STAT_TRIGGER[9:9]	1
0xBB001818	ISR_STAT_TRIGGER [202].ISR_STAT_TRIGGER[10:10]	1
0xBB001818	ISR_STAT_TRIGGER [203].ISR_STAT_TRIGGER[11:11]	1
0xBB001818	ISR_STAT_TRIGGER [204].ISR_STAT_TRIGGER[12:12]	1
0xBB001818	ISR_STAT_TRIGGER [205].ISR_STAT_TRIGGER[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001818	ISR_STAT_TRIGGER [206].ISR_STAT_TRIGGER[14:14]	1
0xBB001818	ISR_STAT_TRIGGER [207].ISR_STAT_TRIGGER[15:15]	1
0xBB001818	ISR_STAT_TRIGGER [208].ISR_STAT_TRIGGER[16:16]	1
0xBB001818	ISR_STAT_TRIGGER [209].ISR_STAT_TRIGGER[17:17]	1
0xBB001818	ISR_STAT_TRIGGER [210].ISR_STAT_TRIGGER[18:18]	1
0xBB001818	ISR_STAT_TRIGGER [211].ISR_STAT_TRIGGER[19:19]	1
0xBB001818	ISR_STAT_TRIGGER [212].ISR_STAT_TRIGGER[20:20]	1
0xBB001818	ISR_STAT_TRIGGER [213].ISR_STAT_TRIGGER[21:21]	1
0xBB001818	ISR_STAT_TRIGGER [214].ISR_STAT_TRIGGER[22:22]	1
0xBB001818	ISR_STAT_TRIGGER [215].ISR_STAT_TRIGGER[23:23]	1
0xBB001818	ISR_STAT_TRIGGER [216].ISR_STAT_TRIGGER[24:24]	1
0xBB001818	ISR_STAT_TRIGGER [217].ISR_STAT_TRIGGER[25:25]	1
0xBB001818	ISR_STAT_TRIGGER [218].ISR_STAT_TRIGGER[26:26]	1
0xBB001818	ISR_STAT_TRIGGER [219].ISR_STAT_TRIGGER[27:27]	1
0xBB001818	ISR_STAT_TRIGGER [220].ISR_STAT_TRIGGER[28:28]	1
0xBB001818	ISR_STAT_TRIGGER [221].ISR_STAT_TRIGGER[29:29]	1
0xBB001818	ISR_STAT_TRIGGER [222].ISR_STAT_TRIGGER[30:30]	1
0xBB001818	ISR_STAT_TRIGGER [223].ISR_STAT_TRIGGER[31:31]	1
0xBB00181C	ISR_STAT_TRIGGER [224].ISR_STAT_TRIGGER[0:0]	1
0xBB00181C	ISR_STAT_TRIGGER [225].ISR_STAT_TRIGGER[1:1]	1
0xBB00181C	ISR_STAT_TRIGGER [226].ISR_STAT_TRIGGER[2:2]	1
0xBB00181C	ISR_STAT_TRIGGER [227].ISR_STAT_TRIGGER[3:3]	1
0xBB00181C	ISR_STAT_TRIGGER [228].ISR_STAT_TRIGGER[4:4]	1
0xBB00181C	ISR_STAT_TRIGGER [229].ISR_STAT_TRIGGER[5:5]	1
0xBB00181C	ISR_STAT_TRIGGER [230].ISR_STAT_TRIGGER[6:6]	1
0xBB00181C	ISR_STAT_TRIGGER [231].ISR_STAT_TRIGGER[7:7]	1
0xBB00181C	ISR_STAT_TRIGGER [232].ISR_STAT_TRIGGER[8:8]	1
0xBB00181C	ISR_STAT_TRIGGER [233].ISR_STAT_TRIGGER[9:9]	1
0xBB00181C	ISR_STAT_TRIGGER [234].ISR_STAT_TRIGGER[10:10]	1
0xBB00181C	ISR_STAT_TRIGGER [235].ISR_STAT_TRIGGER[11:11]	1
0xBB00181C	ISR_STAT_TRIGGER [236].ISR_STAT_TRIGGER[12:12]	1
0xBB00181C	ISR_STAT_TRIGGER [237].ISR_STAT_TRIGGER[13:13]	1
0xBB00181C	ISR_STAT_TRIGGER [238].ISR_STAT_TRIGGER[14:14]	1
0xBB00181C	ISR_STAT_TRIGGER [239].ISR_STAT_TRIGGER[15:15]	1
0xBB00181C	ISR_STAT_TRIGGER [240].ISR_STAT_TRIGGER[16:16]	1
0xBB00181C	ISR_STAT_TRIGGER [241].ISR_STAT_TRIGGER[17:17]	1
0xBB00181C	ISR_STAT_TRIGGER [242].ISR_STAT_TRIGGER[18:18]	1
0xBB00181C	ISR_STAT_TRIGGER [243].ISR_STAT_TRIGGER[19:19]	1
0xBB00181C	ISR_STAT_TRIGGER [244].ISR_STAT_TRIGGER[20:20]	1
0xBB00181C	ISR_STAT_TRIGGER [245].ISR_STAT_TRIGGER[21:21]	1
0xBB00181C	ISR_STAT_TRIGGER [246].ISR_STAT_TRIGGER[22:22]	1
0xBB00181C	ISR_STAT_TRIGGER [247].ISR_STAT_TRIGGER[23:23]	1
0xBB00181C	ISR_STAT_TRIGGER [248].ISR_STAT_TRIGGER[24:24]	1
0xBB00181C	ISR_STAT_TRIGGER [249].ISR_STAT_TRIGGER[25:25]	1
0xBB00181C	ISR_STAT_TRIGGER [250].ISR_STAT_TRIGGER[26:26]	1
0xBB00181C	ISR_STAT_TRIGGER [251].ISR_STAT_TRIGGER[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00181C	ISR_STAT_TRIGGER [252].ISR_STAT_TRIGGER[28:28]	1
0xBB00181C	ISR_STAT_TRIGGER [253].ISR_STAT_TRIGGER[29:29]	1
0xBB00181C	ISR_STAT_TRIGGER [254].ISR_STAT_TRIGGER[30:30]	1
0xBB00181C	ISR_STAT_TRIGGER [255].ISR_STAT_TRIGGER[31:31]	1
0xBB001820	ISR_STAT_TRIGGER [256].ISR_STAT_TRIGGER[0:0]	1
0xBB001820	ISR_STAT_TRIGGER [257].ISR_STAT_TRIGGER[1:1]	1
0xBB001820	ISR_STAT_TRIGGER [258].ISR_STAT_TRIGGER[2:2]	1
0xBB001820	ISR_STAT_TRIGGER [259].ISR_STAT_TRIGGER[3:3]	1
0xBB001820	ISR_STAT_TRIGGER [260].ISR_STAT_TRIGGER[4:4]	1
0xBB001820	ISR_STAT_TRIGGER [261].ISR_STAT_TRIGGER[5:5]	1
0xBB001820	ISR_STAT_TRIGGER [262].ISR_STAT_TRIGGER[6:6]	1
0xBB001820	ISR_STAT_TRIGGER [263].ISR_STAT_TRIGGER[7:7]	1
0xBB001820	ISR_STAT_TRIGGER [264].ISR_STAT_TRIGGER[8:8]	1
0xBB001820	ISR_STAT_TRIGGER [265].ISR_STAT_TRIGGER[9:9]	1
0xBB001820	ISR_STAT_TRIGGER [266].ISR_STAT_TRIGGER[10:10]	1
0xBB001820	ISR_STAT_TRIGGER [267].ISR_STAT_TRIGGER[11:11]	1
0xBB001820	ISR_STAT_TRIGGER [268].ISR_STAT_TRIGGER[12:12]	1
0xBB001820	ISR_STAT_TRIGGER [269].ISR_STAT_TRIGGER[13:13]	1
0xBB001820	ISR_STAT_TRIGGER [270].ISR_STAT_TRIGGER[14:14]	1
0xBB001820	ISR_STAT_TRIGGER [271].ISR_STAT_TRIGGER[15:15]	1
0xBB001820	ISR_STAT_TRIGGER [272].ISR_STAT_TRIGGER[16:16]	1
0xBB001820	ISR_STAT_TRIGGER [273].ISR_STAT_TRIGGER[17:17]	1
0xBB001820	ISR_STAT_TRIGGER [274].ISR_STAT_TRIGGER[18:18]	1
0xBB001820	ISR_STAT_TRIGGER [275].ISR_STAT_TRIGGER[19:19]	1
0xBB001820	ISR_STAT_TRIGGER [276].ISR_STAT_TRIGGER[20:20]	1
0xBB001820	ISR_STAT_TRIGGER [277].ISR_STAT_TRIGGER[21:21]	1
0xBB001820	ISR_STAT_TRIGGER [278].ISR_STAT_TRIGGER[22:22]	1
0xBB001820	ISR_STAT_TRIGGER [279].ISR_STAT_TRIGGER[23:23]	1
0xBB001820	ISR_STAT_TRIGGER [280].ISR_STAT_TRIGGER[24:24]	1
0xBB001820	ISR_STAT_TRIGGER [281].ISR_STAT_TRIGGER[25:25]	1
0xBB001820	ISR_STAT_TRIGGER [282].ISR_STAT_TRIGGER[26:26]	1
0xBB001820	ISR_STAT_TRIGGER [283].ISR_STAT_TRIGGER[27:27]	1
0xBB001820	ISR_STAT_TRIGGER [284].ISR_STAT_TRIGGER[28:28]	1
0xBB001820	ISR_STAT_TRIGGER [285].ISR_STAT_TRIGGER[29:29]	1
0xBB001820	ISR_STAT_TRIGGER [286].ISR_STAT_TRIGGER[30:30]	1
0xBB001820	ISR_STAT_TRIGGER [287].ISR_STAT_TRIGGER[31:31]	1
0xBB001824	ISR_STAT_TRIGGER [288].ISR_STAT_TRIGGER[0:0]	1
0xBB001824	ISR_STAT_TRIGGER [289].ISR_STAT_TRIGGER[1:1]	1
0xBB001824	ISR_STAT_TRIGGER [290].ISR_STAT_TRIGGER[2:2]	1
0xBB001824	ISR_STAT_TRIGGER [291].ISR_STAT_TRIGGER[3:3]	1
0xBB001824	ISR_STAT_TRIGGER [292].ISR_STAT_TRIGGER[4:4]	1
0xBB001824	ISR_STAT_TRIGGER [293].ISR_STAT_TRIGGER[5:5]	1
0xBB001824	ISR_STAT_TRIGGER [294].ISR_STAT_TRIGGER[6:6]	1
0xBB001824	ISR_STAT_TRIGGER [295].ISR_STAT_TRIGGER[7:7]	1
0xBB001824	ISR_STAT_TRIGGER [296].ISR_STAT_TRIGGER[8:8]	1
0xBB001824	ISR_STAT_TRIGGER [297].ISR_STAT_TRIGGER[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001824	ISR_STAT_TRIGGER [298].ISR_STAT_TRIGGER[10:10]	1
0xBB001824	ISR_STAT_TRIGGER [299].ISR_STAT_TRIGGER[11:11]	1
0xBB001824	ISR_STAT_TRIGGER [300].ISR_STAT_TRIGGER[12:12]	1
0xBB001824	ISR_STAT_TRIGGER [301].ISR_STAT_TRIGGER[13:13]	1
0xBB001824	ISR_STAT_TRIGGER [302].ISR_STAT_TRIGGER[14:14]	1
0xBB001824	ISR_STAT_TRIGGER [303].ISR_STAT_TRIGGER[15:15]	1
0xBB001824	ISR_STAT_TRIGGER [304].ISR_STAT_TRIGGER[16:16]	1
0xBB001824	ISR_STAT_TRIGGER [305].ISR_STAT_TRIGGER[17:17]	1
0xBB001824	ISR_STAT_TRIGGER [306].ISR_STAT_TRIGGER[18:18]	1
0xBB001824	ISR_STAT_TRIGGER [307].ISR_STAT_TRIGGER[19:19]	1
0xBB001824	ISR_STAT_TRIGGER [308].ISR_STAT_TRIGGER[20:20]	1
0xBB001824	ISR_STAT_TRIGGER [309].ISR_STAT_TRIGGER[21:21]	1
0xBB001824	ISR_STAT_TRIGGER [310].ISR_STAT_TRIGGER[22:22]	1
0xBB001824	ISR_STAT_TRIGGER [311].ISR_STAT_TRIGGER[23:23]	1
0xBB001824	ISR_STAT_TRIGGER [312].ISR_STAT_TRIGGER[24:24]	1
0xBB001824	ISR_STAT_TRIGGER [313].ISR_STAT_TRIGGER[25:25]	1
0xBB001824	ISR_STAT_TRIGGER [314].ISR_STAT_TRIGGER[26:26]	1
0xBB001824	ISR_STAT_TRIGGER [315].ISR_STAT_TRIGGER[27:27]	1
0xBB001824	ISR_STAT_TRIGGER [316].ISR_STAT_TRIGGER[28:28]	1
0xBB001824	ISR_STAT_TRIGGER [317].ISR_STAT_TRIGGER[29:29]	1
0xBB001824	ISR_STAT_TRIGGER [318].ISR_STAT_TRIGGER[30:30]	1
0xBB001824	ISR_STAT_TRIGGER [319].ISR_STAT_TRIGGER[31:31]	1
0xBB001828	ISR_STAT_TRIGGER [320].ISR_STAT_TRIGGER[0:0]	1
0xBB001828	ISR_STAT_TRIGGER [321].ISR_STAT_TRIGGER[1:1]	1
0xBB001828	ISR_STAT_TRIGGER [322].ISR_STAT_TRIGGER[2:2]	1
0xBB001828	ISR_STAT_TRIGGER [323].ISR_STAT_TRIGGER[3:3]	1
0xBB001828	ISR_STAT_TRIGGER [324].ISR_STAT_TRIGGER[4:4]	1
0xBB001828	ISR_STAT_TRIGGER [325].ISR_STAT_TRIGGER[5:5]	1
0xBB001828	ISR_STAT_TRIGGER [326].ISR_STAT_TRIGGER[6:6]	1
0xBB001828	ISR_STAT_TRIGGER [327].ISR_STAT_TRIGGER[7:7]	1
0xBB001828	ISR_STAT_TRIGGER [328].ISR_STAT_TRIGGER[8:8]	1
0xBB001828	ISR_STAT_TRIGGER [329].ISR_STAT_TRIGGER[9:9]	1
0xBB001828	ISR_STAT_TRIGGER [330].ISR_STAT_TRIGGER[10:10]	1
0xBB001828	ISR_STAT_TRIGGER [331].ISR_STAT_TRIGGER[11:11]	1
0xBB001828	ISR_STAT_TRIGGER [332].ISR_STAT_TRIGGER[12:12]	1
0xBB001828	ISR_STAT_TRIGGER [333].ISR_STAT_TRIGGER[13:13]	1
0xBB001828	ISR_STAT_TRIGGER [334].ISR_STAT_TRIGGER[14:14]	1
0xBB001828	ISR_STAT_TRIGGER [335].ISR_STAT_TRIGGER[15:15]	1
0xBB001828	ISR_STAT_TRIGGER [336].ISR_STAT_TRIGGER[16:16]	1
0xBB001828	ISR_STAT_TRIGGER [337].ISR_STAT_TRIGGER[17:17]	1
0xBB001828	ISR_STAT_TRIGGER [338].ISR_STAT_TRIGGER[18:18]	1
0xBB001828	ISR_STAT_TRIGGER [339].ISR_STAT_TRIGGER[19:19]	1
0xBB001828	ISR_STAT_TRIGGER [340].ISR_STAT_TRIGGER[20:20]	1
0xBB001828	ISR_STAT_TRIGGER [341].ISR_STAT_TRIGGER[21:21]	1
0xBB001828	ISR_STAT_TRIGGER [342].ISR_STAT_TRIGGER[22:22]	1
0xBB001828	ISR_STAT_TRIGGER [343].ISR_STAT_TRIGGER[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001828	ISR_STAT_TRIGGER [345].ISR_STAT_TRIGGER[25:25]	1
0xBB001828	ISR_STAT_TRIGGER [346].ISR_STAT_TRIGGER[26:26]	1
0xBB001828	ISR_STAT_TRIGGER [347].ISR_STAT_TRIGGER[27:27]	1
0xBB001828	ISR_STAT_TRIGGER [348].ISR_STAT_TRIGGER[28:28]	1
0xBB001828	ISR_STAT_TRIGGER [349].ISR_STAT_TRIGGER[29:29]	1
0xBB001828	ISR_STAT_TRIGGER [350].ISR_STAT_TRIGGER[30:30]	1
0xBB001828	ISR_STAT_TRIGGER [351].ISR_STAT_TRIGGER[31:31]	1
0xBB00182C	ISR_STAT_TRIGGER [352].ISR_STAT_TRIGGER[0:0]	1
0xBB00182C	ISR_STAT_TRIGGER [353].ISR_STAT_TRIGGER[1:1]	1
0xBB00182C	ISR_STAT_TRIGGER [354].ISR_STAT_TRIGGER[2:2]	1
0xBB00182C	ISR_STAT_TRIGGER [355].ISR_STAT_TRIGGER[3:3]	1
0xBB00182C	ISR_STAT_TRIGGER [356].ISR_STAT_TRIGGER[4:4]	1
0xBB00182C	ISR_STAT_TRIGGER [357].ISR_STAT_TRIGGER[5:5]	1
0xBB00182C	ISR_STAT_TRIGGER [358].ISR_STAT_TRIGGER[6:6]	1
0xBB00182C	ISR_STAT_TRIGGER [359].ISR_STAT_TRIGGER[7:7]	1
0xBB00182C	ISR_STAT_TRIGGER [360].ISR_STAT_TRIGGER[8:8]	1
0xBB00182C	ISR_STAT_TRIGGER [361].ISR_STAT_TRIGGER[9:9]	1
0xBB00182C	ISR_STAT_TRIGGER [362].ISR_STAT_TRIGGER[10:10]	1
0xBB00182C	ISR_STAT_TRIGGER [363].ISR_STAT_TRIGGER[11:11]	1
0xBB00182C	ISR_STAT_TRIGGER [364].ISR_STAT_TRIGGER[12:12]	1
0xBB00182C	ISR_STAT_TRIGGER [365].ISR_STAT_TRIGGER[13:13]	1
0xBB00182C	ISR_STAT_TRIGGER [366].ISR_STAT_TRIGGER[14:14]	1
0xBB00182C	ISR_STAT_TRIGGER [367].ISR_STAT_TRIGGER[15:15]	1
0xBB00182C	ISR_STAT_TRIGGER [368].ISR_STAT_TRIGGER[16:16]	1
0xBB00182C	ISR_STAT_TRIGGER [369].ISR_STAT_TRIGGER[17:17]	1
0xBB00182C	ISR_STAT_TRIGGER [370].ISR_STAT_TRIGGER[18:18]	1
0xBB00182C	ISR_STAT_TRIGGER [371].ISR_STAT_TRIGGER[19:19]	1
0xBB00182C	ISR_STAT_TRIGGER [372].ISR_STAT_TRIGGER[20:20]	1
0xBB00182C	ISR_STAT_TRIGGER [373].ISR_STAT_TRIGGER[21:21]	1
0xBB00182C	ISR_STAT_TRIGGER [374].ISR_STAT_TRIGGER[22:22]	1
0xBB00182C	ISR_STAT_TRIGGER [375].ISR_STAT_TRIGGER[23:23]	1
0xBB00182C	ISR_STAT_TRIGGER [376].ISR_STAT_TRIGGER[24:24]	1
0xBB00182C	ISR_STAT_TRIGGER [377].ISR_STAT_TRIGGER[25:25]	1
0xBB00182C	ISR_STAT_TRIGGER [378].ISR_STAT_TRIGGER[26:26]	1
0xBB00182C	ISR_STAT_TRIGGER [379].ISR_STAT_TRIGGER[27:27]	1
0xBB00182C	ISR_STAT_TRIGGER [380].ISR_STAT_TRIGGER[28:28]	1
0xBB00182C	ISR_STAT_TRIGGER [381].ISR_STAT_TRIGGER[29:29]	1
0xBB00182C	ISR_STAT_TRIGGER [382].ISR_STAT_TRIGGER[30:30]	1
0xBB00182C	ISR_STAT_TRIGGER [383].ISR_STAT_TRIGGER[31:31]	1
0xBB001830	ISR_STAT_TRIGGER [384].ISR_STAT_TRIGGER[0:0]	1
0xBB001830	ISR_STAT_TRIGGER [385].ISR_STAT_TRIGGER[1:1]	1
0xBB001830	ISR_STAT_TRIGGER [386].ISR_STAT_TRIGGER[2:2]	1
0xBB001830	ISR_STAT_TRIGGER [387].ISR_STAT_TRIGGER[3:3]	1
0xBB001830	ISR_STAT_TRIGGER [388].ISR_STAT_TRIGGER[4:4]	1
0xBB001830	ISR_STAT_TRIGGER [389].ISR_STAT_TRIGGER[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001830	ISR_STAT_TRIGGER [390].ISR_STAT_TRIGGER[6:6]	1
0xBB001830	ISR_STAT_TRIGGER [391].ISR_STAT_TRIGGER[7:7]	1
0xBB001830	ISR_STAT_TRIGGER [392].ISR_STAT_TRIGGER[8:8]	1
0xBB001830	ISR_STAT_TRIGGER [393].ISR_STAT_TRIGGER[9:9]	1
0xBB001830	ISR_STAT_TRIGGER [394].ISR_STAT_TRIGGER[10:10]	1
0xBB001830	ISR_STAT_TRIGGER [395].ISR_STAT_TRIGGER[11:11]	1
0xBB001830	ISR_STAT_TRIGGER [396].ISR_STAT_TRIGGER[12:12]	1
0xBB001830	ISR_STAT_TRIGGER [397].ISR_STAT_TRIGGER[13:13]	1
0xBB001830	ISR_STAT_TRIGGER [398].ISR_STAT_TRIGGER[14:14]	1
0xBB001830	ISR_STAT_TRIGGER [399].ISR_STAT_TRIGGER[15:15]	1
0xBB001830	ISR_STAT_TRIGGER [400].ISR_STAT_TRIGGER[16:16]	1
0xBB001830	ISR_STAT_TRIGGER [401].ISR_STAT_TRIGGER[17:17]	1
0xBB001830	ISR_STAT_TRIGGER [402].ISR_STAT_TRIGGER[18:18]	1
0xBB001830	ISR_STAT_TRIGGER [403].ISR_STAT_TRIGGER[19:19]	1
0xBB001830	ISR_STAT_TRIGGER [404].ISR_STAT_TRIGGER[20:20]	1
0xBB001830	ISR_STAT_TRIGGER [405].ISR_STAT_TRIGGER[21:21]	1
0xBB001830	ISR_STAT_TRIGGER [406].ISR_STAT_TRIGGER[22:22]	1
0xBB001830	ISR_STAT_TRIGGER [407].ISR_STAT_TRIGGER[23:23]	1
0xBB001830	ISR_STAT_TRIGGER [408].ISR_STAT_TRIGGER[24:24]	1
0xBB001830	ISR_STAT_TRIGGER [409].ISR_STAT_TRIGGER[25:25]	1
0xBB001830	ISR_STAT_TRIGGER [410].ISR_STAT_TRIGGER[26:26]	1
0xBB001830	ISR_STAT_TRIGGER [411].ISR_STAT_TRIGGER[27:27]	1
0xBB001830	ISR_STAT_TRIGGER [412].ISR_STAT_TRIGGER[28:28]	1
0xBB001830	ISR_STAT_TRIGGER [413].ISR_STAT_TRIGGER[29:29]	1
0xBB001830	ISR_STAT_TRIGGER [414].ISR_STAT_TRIGGER[30:30]	1
0xBB001830	ISR_STAT_TRIGGER [415].ISR_STAT_TRIGGER[31:31]	1
0xBB001834	ISR_STAT_TRIGGER [416].ISR_STAT_TRIGGER[0:0]	1
0xBB001834	ISR_STAT_TRIGGER [417].ISR_STAT_TRIGGER[1:1]	1
0xBB001834	ISR_STAT_TRIGGER [418].ISR_STAT_TRIGGER[2:2]	1
0xBB001834	ISR_STAT_TRIGGER [419].ISR_STAT_TRIGGER[3:3]	1
0xBB001834	ISR_STAT_TRIGGER [420].ISR_STAT_TRIGGER[4:4]	1
0xBB001834	ISR_STAT_TRIGGER [421].ISR_STAT_TRIGGER[5:5]	1
0xBB001834	ISR_STAT_TRIGGER [422].ISR_STAT_TRIGGER[6:6]	1
0xBB001834	ISR_STAT_TRIGGER [423].ISR_STAT_TRIGGER[7:7]	1
0xBB001834	ISR_STAT_TRIGGER [424].ISR_STAT_TRIGGER[8:8]	1
0xBB001834	ISR_STAT_TRIGGER [425].ISR_STAT_TRIGGER[9:9]	1
0xBB001834	ISR_STAT_TRIGGER [426].ISR_STAT_TRIGGER[10:10]	1
0xBB001834	ISR_STAT_TRIGGER [427].ISR_STAT_TRIGGER[11:11]	1
0xBB001834	ISR_STAT_TRIGGER [428].ISR_STAT_TRIGGER[12:12]	1
0xBB001834	ISR_STAT_TRIGGER [429].ISR_STAT_TRIGGER[13:13]	1
0xBB001834	ISR_STAT_TRIGGER [430].ISR_STAT_TRIGGER[14:14]	1
0xBB001834	ISR_STAT_TRIGGER [431].ISR_STAT_TRIGGER[15:15]	1
0xBB001834	ISR_STAT_TRIGGER [432].ISR_STAT_TRIGGER[16:16]	1
0xBB001834	ISR_STAT_TRIGGER [433].ISR_STAT_TRIGGER[17:17]	1
0xBB001834	ISR_STAT_TRIGGER [434].ISR_STAT_TRIGGER[18:18]	1
0xBB001834	ISR_STAT_TRIGGER [435].ISR_STAT_TRIGGER[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001834	ISR_STAT_TRIGGER [436].ISR_STAT_TRIGGER[20:20]	1
0xBB001834	ISR_STAT_TRIGGER [437].ISR_STAT_TRIGGER[21:21]	1
0xBB001834	ISR_STAT_TRIGGER [438].ISR_STAT_TRIGGER[22:22]	1
0xBB001834	ISR_STAT_TRIGGER [439].ISR_STAT_TRIGGER[23:23]	1
0xBB001834	ISR_STAT_TRIGGER [440].ISR_STAT_TRIGGER[24:24]	1
0xBB001834	ISR_STAT_TRIGGER [441].ISR_STAT_TRIGGER[25:25]	1
0xBB001834	ISR_STAT_TRIGGER [442].ISR_STAT_TRIGGER[26:26]	1
0xBB001834	ISR_STAT_TRIGGER [443].ISR_STAT_TRIGGER[27:27]	1
0xBB001834	ISR_STAT_TRIGGER [444].ISR_STAT_TRIGGER[28:28]	1
0xBB001834	ISR_STAT_TRIGGER [445].ISR_STAT_TRIGGER[29:29]	1
0xBB001834	ISR_STAT_TRIGGER [446].ISR_STAT_TRIGGER[30:30]	1
0xBB001834	ISR_STAT_TRIGGER [447].ISR_STAT_TRIGGER[31:31]	1
0xBB001838	ISR_STAT_TRIGGER [448].ISR_STAT_TRIGGER[0:0]	1
0xBB001838	ISR_STAT_TRIGGER [449].ISR_STAT_TRIGGER[1:1]	1
0xBB001838	ISR_STAT_TRIGGER [450].ISR_STAT_TRIGGER[2:2]	1
0xBB001838	ISR_STAT_TRIGGER [451].ISR_STAT_TRIGGER[3:3]	1
0xBB001838	ISR_STAT_TRIGGER [452].ISR_STAT_TRIGGER[4:4]	1
0xBB001838	ISR_STAT_TRIGGER [453].ISR_STAT_TRIGGER[5:5]	1
0xBB001838	ISR_STAT_TRIGGER [454].ISR_STAT_TRIGGER[6:6]	1
0xBB001838	ISR_STAT_TRIGGER [455].ISR_STAT_TRIGGER[7:7]	1
0xBB001838	ISR_STAT_TRIGGER [456].ISR_STAT_TRIGGER[8:8]	1
0xBB001838	ISR_STAT_TRIGGER [457].ISR_STAT_TRIGGER[9:9]	1
0xBB001838	ISR_STAT_TRIGGER [458].ISR_STAT_TRIGGER[10:10]	1
0xBB001838	ISR_STAT_TRIGGER [459].ISR_STAT_TRIGGER[11:11]	1
0xBB001838	ISR_STAT_TRIGGER [460].ISR_STAT_TRIGGER[12:12]	1
0xBB001838	ISR_STAT_TRIGGER [461].ISR_STAT_TRIGGER[13:13]	1
0xBB001838	ISR_STAT_TRIGGER [462].ISR_STAT_TRIGGER[14:14]	1
0xBB001838	ISR_STAT_TRIGGER [463].ISR_STAT_TRIGGER[15:15]	1
0xBB001838	ISR_STAT_TRIGGER [464].ISR_STAT_TRIGGER[16:16]	1
0xBB001838	ISR_STAT_TRIGGER [465].ISR_STAT_TRIGGER[17:17]	1
0xBB001838	ISR_STAT_TRIGGER [466].ISR_STAT_TRIGGER[18:18]	1
0xBB001838	ISR_STAT_TRIGGER [467].ISR_STAT_TRIGGER[19:19]	1
0xBB001838	ISR_STAT_TRIGGER [468].ISR_STAT_TRIGGER[20:20]	1
0xBB001838	ISR_STAT_TRIGGER [469].ISR_STAT_TRIGGER[21:21]	1
0xBB001838	ISR_STAT_TRIGGER [470].ISR_STAT_TRIGGER[22:22]	1
0xBB001838	ISR_STAT_TRIGGER [471].ISR_STAT_TRIGGER[23:23]	1
0xBB001838	ISR_STAT_TRIGGER [472].ISR_STAT_TRIGGER[24:24]	1
0xBB001838	ISR_STAT_TRIGGER [473].ISR_STAT_TRIGGER[25:25]	1
0xBB001838	ISR_STAT_TRIGGER [474].ISR_STAT_TRIGGER[26:26]	1
0xBB001838	ISR_STAT_TRIGGER [475].ISR_STAT_TRIGGER[27:27]	1
0xBB001838	ISR_STAT_TRIGGER [476].ISR_STAT_TRIGGER[28:28]	1
0xBB001838	ISR_STAT_TRIGGER [477].ISR_STAT_TRIGGER[29:29]	1
0xBB001838	ISR_STAT_TRIGGER [478].ISR_STAT_TRIGGER[30:30]	1
0xBB001838	ISR_STAT_TRIGGER [479].ISR_STAT_TRIGGER[31:31]	1
0xBB00183C	ISR_STAT_TRIGGER [480].ISR_STAT_TRIGGER[0:0]	1
0xBB00183C	ISR_STAT_TRIGGER [481].ISR_STAT_TRIGGER[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00183C	ISR_STAT_TRIGGER [483].ISR_STAT_TRIGGER[3:3]	1
0xBB00183C	ISR_STAT_TRIGGER [484].ISR_STAT_TRIGGER[4:4]	1
0xBB00183C	ISR_STAT_TRIGGER [485].ISR_STAT_TRIGGER[5:5]	1
0xBB00183C	ISR_STAT_TRIGGER [486].ISR_STAT_TRIGGER[6:6]	1
0xBB00183C	ISR_STAT_TRIGGER [487].ISR_STAT_TRIGGER[7:7]	1
0xBB00183C	ISR_STAT_TRIGGER [488].ISR_STAT_TRIGGER[8:8]	1
0xBB00183C	ISR_STAT_TRIGGER [489].ISR_STAT_TRIGGER[9:9]	1
0xBB00183C	ISR_STAT_TRIGGER [490].ISR_STAT_TRIGGER[10:10]	1
0xBB00183C	ISR_STAT_TRIGGER [491].ISR_STAT_TRIGGER[11:11]	1
0xBB00183C	ISR_STAT_TRIGGER [492].ISR_STAT_TRIGGER[12:12]	1
0xBB00183C	ISR_STAT_TRIGGER [493].ISR_STAT_TRIGGER[13:13]	1
0xBB00183C	ISR_STAT_TRIGGER [494].ISR_STAT_TRIGGER[14:14]	1
0xBB00183C	ISR_STAT_TRIGGER [495].ISR_STAT_TRIGGER[15:15]	1
0xBB00183C	ISR_STAT_TRIGGER [496].ISR_STAT_TRIGGER[16:16]	1
0xBB00183C	ISR_STAT_TRIGGER [497].ISR_STAT_TRIGGER[17:17]	1
0xBB00183C	ISR_STAT_TRIGGER [498].ISR_STAT_TRIGGER[18:18]	1
0xBB00183C	ISR_STAT_TRIGGER [499].ISR_STAT_TRIGGER[19:19]	1
0xBB00183C	ISR_STAT_TRIGGER [500].ISR_STAT_TRIGGER[20:20]	1
0xBB00183C	ISR_STAT_TRIGGER [501].ISR_STAT_TRIGGER[21:21]	1
0xBB00183C	ISR_STAT_TRIGGER [502].ISR_STAT_TRIGGER[22:22]	1
0xBB00183C	ISR_STAT_TRIGGER [503].ISR_STAT_TRIGGER[23:23]	1
0xBB00183C	ISR_STAT_TRIGGER [504].ISR_STAT_TRIGGER[24:24]	1
0xBB00183C	ISR_STAT_TRIGGER [505].ISR_STAT_TRIGGER[25:25]	1
0xBB00183C	ISR_STAT_TRIGGER [506].ISR_STAT_TRIGGER[26:26]	1
0xBB00183C	ISR_STAT_TRIGGER [507].ISR_STAT_TRIGGER[27:27]	1
0xBB00183C	ISR_STAT_TRIGGER [508].ISR_STAT_TRIGGER[28:28]	1
0xBB00183C	ISR_STAT_TRIGGER [509].ISR_STAT_TRIGGER[29:29]	1
0xBB00183C	ISR_STAT_TRIGGER [510].ISR_STAT_TRIGGER[30:30]	1
0xBB00183C	ISR_STAT_TRIGGER [511].ISR_STAT_TRIGGER[31:31]	1
0xBB001840	ISR_STAT_TRIGGER [512].ISR_STAT_TRIGGER[0:0]	1
0xBB001840	ISR_STAT_TRIGGER [513].ISR_STAT_TRIGGER[1:1]	1
0xBB001840	ISR_STAT_TRIGGER [514].ISR_STAT_TRIGGER[2:2]	1
0xBB001840	ISR_STAT_TRIGGER [515].ISR_STAT_TRIGGER[3:3]	1
0xBB001840	ISR_STAT_TRIGGER [516].ISR_STAT_TRIGGER[4:4]	1
0xBB001840	ISR_STAT_TRIGGER [517].ISR_STAT_TRIGGER[5:5]	1
0xBB001840	ISR_STAT_TRIGGER [518].ISR_STAT_TRIGGER[6:6]	1
0xBB001840	ISR_STAT_TRIGGER [519].ISR_STAT_TRIGGER[7:7]	1
0xBB001840	ISR_STAT_TRIGGER [520].ISR_STAT_TRIGGER[8:8]	1
0xBB001840	ISR_STAT_TRIGGER [521].ISR_STAT_TRIGGER[9:9]	1
0xBB001840	ISR_STAT_TRIGGER [522].ISR_STAT_TRIGGER[10:10]	1
0xBB001840	ISR_STAT_TRIGGER [523].ISR_STAT_TRIGGER[11:11]	1
0xBB001840	ISR_STAT_TRIGGER [524].ISR_STAT_TRIGGER[12:12]	1
0xBB001840	ISR_STAT_TRIGGER [525].ISR_STAT_TRIGGER[13:13]	1
0xBB001840	ISR_STAT_TRIGGER [526].ISR_STAT_TRIGGER[14:14]	1
0xBB001840	ISR_STAT_TRIGGER [527].ISR_STAT_TRIGGER[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001840	ISR_STAT_TRIGGER [529].ISR_STAT_TRIGGER[17:17]	1
0xBB001840	ISR_STAT_TRIGGER [530].ISR_STAT_TRIGGER[18:18]	1
0xBB001840	ISR_STAT_TRIGGER [531].ISR_STAT_TRIGGER[19:19]	1
0xBB001840	ISR_STAT_TRIGGER [532].ISR_STAT_TRIGGER[20:20]	1
0xBB001840	ISR_STAT_TRIGGER [533].ISR_STAT_TRIGGER[21:21]	1
0xBB001840	ISR_STAT_TRIGGER [534].ISR_STAT_TRIGGER[22:22]	1
0xBB001840	ISR_STAT_TRIGGER [535].ISR_STAT_TRIGGER[23:23]	1
0xBB001840	ISR_STAT_TRIGGER [536].ISR_STAT_TRIGGER[24:24]	1
0xBB001840	ISR_STAT_TRIGGER [537].ISR_STAT_TRIGGER[25:25]	1
0xBB001840	ISR_STAT_TRIGGER [538].ISR_STAT_TRIGGER[26:26]	1
0xBB001840	ISR_STAT_TRIGGER [539].ISR_STAT_TRIGGER[27:27]	1
0xBB001840	ISR_STAT_TRIGGER [540].ISR_STAT_TRIGGER[28:28]	1
0xBB001840	ISR_STAT_TRIGGER [541].ISR_STAT_TRIGGER[29:29]	1
0xBB001840	ISR_STAT_TRIGGER [542].ISR_STAT_TRIGGER[30:30]	1
0xBB001840	ISR_STAT_TRIGGER [543].ISR_STAT_TRIGGER[31:31]	1
0xBB001844	ISR_STAT_TRIGGER [544].ISR_STAT_TRIGGER[0:0]	1
0xBB001844	ISR_STAT_TRIGGER [545].ISR_STAT_TRIGGER[1:1]	1
0xBB001844	ISR_STAT_TRIGGER [546].ISR_STAT_TRIGGER[2:2]	1
0xBB001844	ISR_STAT_TRIGGER [547].ISR_STAT_TRIGGER[3:3]	1
0xBB001844	ISR_STAT_TRIGGER [548].ISR_STAT_TRIGGER[4:4]	1
0xBB001844	ISR_STAT_TRIGGER [549].ISR_STAT_TRIGGER[5:5]	1
0xBB001844	ISR_STAT_TRIGGER [550].ISR_STAT_TRIGGER[6:6]	1
0xBB001844	ISR_STAT_TRIGGER [551].ISR_STAT_TRIGGER[7:7]	1
0xBB001844	ISR_STAT_TRIGGER [552].ISR_STAT_TRIGGER[8:8]	1
0xBB001844	ISR_STAT_TRIGGER [553].ISR_STAT_TRIGGER[9:9]	1
0xBB001844	ISR_STAT_TRIGGER [554].ISR_STAT_TRIGGER[10:10]	1
0xBB001844	ISR_STAT_TRIGGER [555].ISR_STAT_TRIGGER[11:11]	1
0xBB001844	ISR_STAT_TRIGGER [556].ISR_STAT_TRIGGER[12:12]	1
0xBB001844	ISR_STAT_TRIGGER [557].ISR_STAT_TRIGGER[13:13]	1
0xBB001844	ISR_STAT_TRIGGER [558].ISR_STAT_TRIGGER[14:14]	1
0xBB001844	ISR_STAT_TRIGGER [559].ISR_STAT_TRIGGER[15:15]	1
0xBB001844	ISR_STAT_TRIGGER [560].ISR_STAT_TRIGGER[16:16]	1
0xBB001844	ISR_STAT_TRIGGER [561].ISR_STAT_TRIGGER[17:17]	1
0xBB001844	ISR_STAT_TRIGGER [562].ISR_STAT_TRIGGER[18:18]	1
0xBB001844	ISR_STAT_TRIGGER [563].ISR_STAT_TRIGGER[19:19]	1
0xBB001844	ISR_STAT_TRIGGER [564].ISR_STAT_TRIGGER[20:20]	1
0xBB001844	ISR_STAT_TRIGGER [565].ISR_STAT_TRIGGER[21:21]	1
0xBB001844	ISR_STAT_TRIGGER [566].ISR_STAT_TRIGGER[22:22]	1
0xBB001844	ISR_STAT_TRIGGER [567].ISR_STAT_TRIGGER[23:23]	1
0xBB001844	ISR_STAT_TRIGGER [568].ISR_STAT_TRIGGER[24:24]	1
0xBB001844	ISR_STAT_TRIGGER [569].ISR_STAT_TRIGGER[25:25]	1
0xBB001844	ISR_STAT_TRIGGER [570].ISR_STAT_TRIGGER[26:26]	1
0xBB001844	ISR_STAT_TRIGGER [571].ISR_STAT_TRIGGER[27:27]	1
0xBB001844	ISR_STAT_TRIGGER [572].ISR_STAT_TRIGGER[28:28]	1
0xBB001844	ISR_STAT_TRIGGER [573].ISR_STAT_TRIGGER[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001844	ISR_STAT_TRIGGER [574].ISR_STAT_TRIGGER[30:30]	1
0xBB001844	ISR_STAT_TRIGGER [575].ISR_STAT_TRIGGER[31:31]	1
0xBB001848	ISR_STAT_TRIGGER [576].ISR_STAT_TRIGGER[0:0]	1
0xBB001848	ISR_STAT_TRIGGER [577].ISR_STAT_TRIGGER[1:1]	1
0xBB001848	ISR_STAT_TRIGGER [578].ISR_STAT_TRIGGER[2:2]	1
0xBB001848	ISR_STAT_TRIGGER [579].ISR_STAT_TRIGGER[3:3]	1
0xBB001848	ISR_STAT_TRIGGER [580].ISR_STAT_TRIGGER[4:4]	1
0xBB001848	ISR_STAT_TRIGGER [581].ISR_STAT_TRIGGER[5:5]	1
0xBB001848	ISR_STAT_TRIGGER [582].ISR_STAT_TRIGGER[6:6]	1
0xBB001848	ISR_STAT_TRIGGER [583].ISR_STAT_TRIGGER[7:7]	1
0xBB001848	ISR_STAT_TRIGGER [584].ISR_STAT_TRIGGER[8:8]	1
0xBB001848	ISR_STAT_TRIGGER [585].ISR_STAT_TRIGGER[9:9]	1
0xBB001848	ISR_STAT_TRIGGER [586].ISR_STAT_TRIGGER[10:10]	1
0xBB001848	ISR_STAT_TRIGGER [587].ISR_STAT_TRIGGER[11:11]	1
0xBB001848	ISR_STAT_TRIGGER [588].ISR_STAT_TRIGGER[12:12]	1
0xBB001848	ISR_STAT_TRIGGER [589].ISR_STAT_TRIGGER[13:13]	1
0xBB001848	ISR_STAT_TRIGGER [590].ISR_STAT_TRIGGER[14:14]	1
0xBB001848	ISR_STAT_TRIGGER [591].ISR_STAT_TRIGGER[15:15]	1
0xBB001848	ISR_STAT_TRIGGER [592].ISR_STAT_TRIGGER[16:16]	1
0xBB001848	ISR_STAT_TRIGGER [593].ISR_STAT_TRIGGER[17:17]	1
0xBB001848	ISR_STAT_TRIGGER [594].ISR_STAT_TRIGGER[18:18]	1
0xBB001848	ISR_STAT_TRIGGER [595].ISR_STAT_TRIGGER[19:19]	1
0xBB001848	ISR_STAT_TRIGGER [596].ISR_STAT_TRIGGER[20:20]	1
0xBB001848	ISR_STAT_TRIGGER [597].ISR_STAT_TRIGGER[21:21]	1
0xBB001848	ISR_STAT_TRIGGER [598].ISR_STAT_TRIGGER[22:22]	1
0xBB001848	ISR_STAT_TRIGGER [599].ISR_STAT_TRIGGER[23:23]	1
0xBB001848	ISR_STAT_TRIGGER [600].ISR_STAT_TRIGGER[24:24]	1
0xBB001848	ISR_STAT_TRIGGER [601].ISR_STAT_TRIGGER[25:25]	1
0xBB001848	ISR_STAT_TRIGGER [602].ISR_STAT_TRIGGER[26:26]	1
0xBB001848	ISR_STAT_TRIGGER [603].ISR_STAT_TRIGGER[27:27]	1
0xBB001848	ISR_STAT_TRIGGER [604].ISR_STAT_TRIGGER[28:28]	1
0xBB001848	ISR_STAT_TRIGGER [605].ISR_STAT_TRIGGER[29:29]	1
0xBB001848	ISR_STAT_TRIGGER [606].ISR_STAT_TRIGGER[30:30]	1
0xBB001848	ISR_STAT_TRIGGER [607].ISR_STAT_TRIGGER[31:31]	1
0xBB00184C	ISR_STAT_TRIGGER [608].ISR_STAT_TRIGGER[0:0]	1
0xBB00184C	ISR_STAT_TRIGGER [609].ISR_STAT_TRIGGER[1:1]	1
0xBB00184C	ISR_STAT_TRIGGER [610].ISR_STAT_TRIGGER[2:2]	1
0xBB00184C	ISR_STAT_TRIGGER [611].ISR_STAT_TRIGGER[3:3]	1
0xBB00184C	ISR_STAT_TRIGGER [612].ISR_STAT_TRIGGER[4:4]	1
0xBB00184C	ISR_STAT_TRIGGER [613].ISR_STAT_TRIGGER[5:5]	1
0xBB00184C	ISR_STAT_TRIGGER [614].ISR_STAT_TRIGGER[6:6]	1
0xBB00184C	ISR_STAT_TRIGGER [615].ISR_STAT_TRIGGER[7:7]	1
0xBB00184C	ISR_STAT_TRIGGER [616].ISR_STAT_TRIGGER[8:8]	1
0xBB00184C	ISR_STAT_TRIGGER [617].ISR_STAT_TRIGGER[9:9]	1
0xBB00184C	ISR_STAT_TRIGGER [618].ISR_STAT_TRIGGER[10:10]	1
0xBB00184C	ISR_STAT_TRIGGER [619].ISR_STAT_TRIGGER[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00184C	ISR_STAT_TRIGGER [620].ISR_STAT_TRIGGER[12:12]	1
0xBB00184C	ISR_STAT_TRIGGER [621].ISR_STAT_TRIGGER[13:13]	1
0xBB00184C	ISR_STAT_TRIGGER [622].ISR_STAT_TRIGGER[14:14]	1
0xBB00184C	ISR_STAT_TRIGGER [623].ISR_STAT_TRIGGER[15:15]	1
0xBB00184C	ISR_STAT_TRIGGER [624].ISR_STAT_TRIGGER[16:16]	1
0xBB00184C	ISR_STAT_TRIGGER [625].ISR_STAT_TRIGGER[17:17]	1
0xBB00184C	ISR_STAT_TRIGGER [626].ISR_STAT_TRIGGER[18:18]	1
0xBB00184C	ISR_STAT_TRIGGER [627].ISR_STAT_TRIGGER[19:19]	1
0xBB00184C	ISR_STAT_TRIGGER [628].ISR_STAT_TRIGGER[20:20]	1
0xBB00184C	ISR_STAT_TRIGGER [629].ISR_STAT_TRIGGER[21:21]	1
0xBB00184C	ISR_STAT_TRIGGER [630].ISR_STAT_TRIGGER[22:22]	1
0xBB00184C	ISR_STAT_TRIGGER [631].ISR_STAT_TRIGGER[23:23]	1
0xBB00184C	ISR_STAT_TRIGGER [632].ISR_STAT_TRIGGER[24:24]	1
0xBB00184C	ISR_STAT_TRIGGER [633].ISR_STAT_TRIGGER[25:25]	1
0xBB00184C	ISR_STAT_TRIGGER [634].ISR_STAT_TRIGGER[26:26]	1
0xBB00184C	ISR_STAT_TRIGGER [635].ISR_STAT_TRIGGER[27:27]	1
0xBB00184C	ISR_STAT_TRIGGER [636].ISR_STAT_TRIGGER[28:28]	1
0xBB00184C	ISR_STAT_TRIGGER [637].ISR_STAT_TRIGGER[29:29]	1
0xBB00184C	ISR_STAT_TRIGGER [638].ISR_STAT_TRIGGER[30:30]	1
0xBB00184C	ISR_STAT_TRIGGER [639].ISR_STAT_TRIGGER[31:31]	1
0xBB001850	ISR_STAT_TRIGGER [640].ISR_STAT_TRIGGER[0:0]	1
0xBB001850	ISR_STAT_TRIGGER [641].ISR_STAT_TRIGGER[1:1]	1
0xBB001850	ISR_STAT_TRIGGER [642].ISR_STAT_TRIGGER[2:2]	1
0xBB001850	ISR_STAT_TRIGGER [643].ISR_STAT_TRIGGER[3:3]	1
0xBB001850	ISR_STAT_TRIGGER [644].ISR_STAT_TRIGGER[4:4]	1
0xBB001850	ISR_STAT_TRIGGER [645].ISR_STAT_TRIGGER[5:5]	1
0xBB001850	ISR_STAT_TRIGGER [646].ISR_STAT_TRIGGER[6:6]	1
0xBB001850	ISR_STAT_TRIGGER [647].ISR_STAT_TRIGGER[7:7]	1
0xBB001850	ISR_STAT_TRIGGER [648].ISR_STAT_TRIGGER[8:8]	1
0xBB001850	ISR_STAT_TRIGGER [649].ISR_STAT_TRIGGER[9:9]	1
0xBB001850	ISR_STAT_TRIGGER [650].ISR_STAT_TRIGGER[10:10]	1
0xBB001850	ISR_STAT_TRIGGER [651].ISR_STAT_TRIGGER[11:11]	1
0xBB001850	ISR_STAT_TRIGGER [652].ISR_STAT_TRIGGER[12:12]	1
0xBB001850	ISR_STAT_TRIGGER [653].ISR_STAT_TRIGGER[13:13]	1
0xBB001850	ISR_STAT_TRIGGER [654].ISR_STAT_TRIGGER[14:14]	1
0xBB001850	ISR_STAT_TRIGGER [655].ISR_STAT_TRIGGER[15:15]	1
0xBB001850	ISR_STAT_TRIGGER [656].ISR_STAT_TRIGGER[16:16]	1
0xBB001850	ISR_STAT_TRIGGER [657].ISR_STAT_TRIGGER[17:17]	1
0xBB001850	ISR_STAT_TRIGGER [658].ISR_STAT_TRIGGER[18:18]	1
0xBB001850	ISR_STAT_TRIGGER [659].ISR_STAT_TRIGGER[19:19]	1
0xBB001850	ISR_STAT_TRIGGER [660].ISR_STAT_TRIGGER[20:20]	1
0xBB001850	ISR_STAT_TRIGGER [661].ISR_STAT_TRIGGER[21:21]	1
0xBB001850	ISR_STAT_TRIGGER [662].ISR_STAT_TRIGGER[22:22]	1
0xBB001850	ISR_STAT_TRIGGER [663].ISR_STAT_TRIGGER[23:23]	1
0xBB001850	ISR_STAT_TRIGGER [664].ISR_STAT_TRIGGER[24:24]	1
0xBB001850	ISR_STAT_TRIGGER [665].ISR_STAT_TRIGGER[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001850	ISR_STAT_TRIGGER [666].ISR_STAT_TRIGGER[26:26]	1
0xBB001850	ISR_STAT_TRIGGER [667].ISR_STAT_TRIGGER[27:27]	1
0xBB001850	ISR_STAT_TRIGGER [668].ISR_STAT_TRIGGER[28:28]	1
0xBB001850	ISR_STAT_TRIGGER [669].ISR_STAT_TRIGGER[29:29]	1
0xBB001850	ISR_STAT_TRIGGER [670].ISR_STAT_TRIGGER[30:30]	1
0xBB001850	ISR_STAT_TRIGGER [671].ISR_STAT_TRIGGER[31:31]	1
0xBB001854	ISR_STAT_TRIGGER [672].ISR_STAT_TRIGGER[0:0]	1
0xBB001854	ISR_STAT_TRIGGER [673].ISR_STAT_TRIGGER[1:1]	1
0xBB001854	ISR_STAT_TRIGGER [674].ISR_STAT_TRIGGER[2:2]	1
0xBB001854	ISR_STAT_TRIGGER [675].ISR_STAT_TRIGGER[3:3]	1
0xBB001854	ISR_STAT_TRIGGER [676].ISR_STAT_TRIGGER[4:4]	1
0xBB001854	ISR_STAT_TRIGGER [677].ISR_STAT_TRIGGER[5:5]	1
0xBB001854	ISR_STAT_TRIGGER [678].ISR_STAT_TRIGGER[6:6]	1
0xBB001854	ISR_STAT_TRIGGER [679].ISR_STAT_TRIGGER[7:7]	1
0xBB001854	ISR_STAT_TRIGGER [680].ISR_STAT_TRIGGER[8:8]	1
0xBB001854	ISR_STAT_TRIGGER [681].ISR_STAT_TRIGGER[9:9]	1
0xBB001854	ISR_STAT_TRIGGER [682].ISR_STAT_TRIGGER[10:10]	1
0xBB001854	ISR_STAT_TRIGGER [683].ISR_STAT_TRIGGER[11:11]	1
0xBB001854	ISR_STAT_TRIGGER [684].ISR_STAT_TRIGGER[12:12]	1
0xBB001854	ISR_STAT_TRIGGER [685].ISR_STAT_TRIGGER[13:13]	1
0xBB001854	ISR_STAT_TRIGGER [686].ISR_STAT_TRIGGER[14:14]	1
0xBB001854	ISR_STAT_TRIGGER [687].ISR_STAT_TRIGGER[15:15]	1
0xBB001854	ISR_STAT_TRIGGER [688].ISR_STAT_TRIGGER[16:16]	1
0xBB001854	ISR_STAT_TRIGGER [689].ISR_STAT_TRIGGER[17:17]	1
0xBB001854	ISR_STAT_TRIGGER [690].ISR_STAT_TRIGGER[18:18]	1
0xBB001854	ISR_STAT_TRIGGER [691].ISR_STAT_TRIGGER[19:19]	1
0xBB001854	ISR_STAT_TRIGGER [692].ISR_STAT_TRIGGER[20:20]	1
0xBB001854	ISR_STAT_TRIGGER [693].ISR_STAT_TRIGGER[21:21]	1
0xBB001854	ISR_STAT_TRIGGER [694].ISR_STAT_TRIGGER[22:22]	1
0xBB001854	ISR_STAT_TRIGGER [695].ISR_STAT_TRIGGER[23:23]	1
0xBB001854	ISR_STAT_TRIGGER [696].ISR_STAT_TRIGGER[24:24]	1
0xBB001854	ISR_STAT_TRIGGER [697].ISR_STAT_TRIGGER[25:25]	1
0xBB001854	ISR_STAT_TRIGGER [698].ISR_STAT_TRIGGER[26:26]	1
0xBB001854	ISR_STAT_TRIGGER [699].ISR_STAT_TRIGGER[27:27]	1
0xBB001854	ISR_STAT_TRIGGER [700].ISR_STAT_TRIGGER[28:28]	1
0xBB001854	ISR_STAT_TRIGGER [701].ISR_STAT_TRIGGER[29:29]	1
0xBB001854	ISR_STAT_TRIGGER [702].ISR_STAT_TRIGGER[30:30]	1
0xBB001854	ISR_STAT_TRIGGER [703].ISR_STAT_TRIGGER[31:31]	1
0xBB001858	ISR_STAT_TRIGGER [704].ISR_STAT_TRIGGER[0:0]	1
0xBB001858	ISR_STAT_TRIGGER [705].ISR_STAT_TRIGGER[1:1]	1
0xBB001858	ISR_STAT_TRIGGER [706].ISR_STAT_TRIGGER[2:2]	1
0xBB001858	ISR_STAT_TRIGGER [707].ISR_STAT_TRIGGER[3:3]	1
0xBB001858	ISR_STAT_TRIGGER [708].ISR_STAT_TRIGGER[4:4]	1
0xBB001858	ISR_STAT_TRIGGER [709].ISR_STAT_TRIGGER[5:5]	1
0xBB001858	ISR_STAT_TRIGGER [710].ISR_STAT_TRIGGER[6:6]	1
0xBB001858	ISR_STAT_TRIGGER [711].ISR_STAT_TRIGGER[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001858	ISR_STAT_TRIGGER [713].ISR_STAT_TRIGGER[9:9]	1
0xBB001858	ISR_STAT_TRIGGER [714].ISR_STAT_TRIGGER[10:10]	1
0xBB001858	ISR_STAT_TRIGGER [715].ISR_STAT_TRIGGER[11:11]	1
0xBB001858	ISR_STAT_TRIGGER [716].ISR_STAT_TRIGGER[12:12]	1
0xBB001858	ISR_STAT_TRIGGER [717].ISR_STAT_TRIGGER[13:13]	1
0xBB001858	ISR_STAT_TRIGGER [718].ISR_STAT_TRIGGER[14:14]	1
0xBB001858	ISR_STAT_TRIGGER [719].ISR_STAT_TRIGGER[15:15]	1
0xBB001858	ISR_STAT_TRIGGER [720].ISR_STAT_TRIGGER[16:16]	1
0xBB001858	ISR_STAT_TRIGGER [721].ISR_STAT_TRIGGER[17:17]	1
0xBB001858	ISR_STAT_TRIGGER [722].ISR_STAT_TRIGGER[18:18]	1
0xBB001858	ISR_STAT_TRIGGER [723].ISR_STAT_TRIGGER[19:19]	1
0xBB001858	ISR_STAT_TRIGGER [724].ISR_STAT_TRIGGER[20:20]	1
0xBB001858	ISR_STAT_TRIGGER [725].ISR_STAT_TRIGGER[21:21]	1
0xBB001858	ISR_STAT_TRIGGER [726].ISR_STAT_TRIGGER[22:22]	1
0xBB001858	ISR_STAT_TRIGGER [727].ISR_STAT_TRIGGER[23:23]	1
0xBB001858	ISR_STAT_TRIGGER [728].ISR_STAT_TRIGGER[24:24]	1
0xBB001858	ISR_STAT_TRIGGER [729].ISR_STAT_TRIGGER[25:25]	1
0xBB001858	ISR_STAT_TRIGGER [730].ISR_STAT_TRIGGER[26:26]	1
0xBB001858	ISR_STAT_TRIGGER [731].ISR_STAT_TRIGGER[27:27]	1
0xBB001858	ISR_STAT_TRIGGER [732].ISR_STAT_TRIGGER[28:28]	1
0xBB001858	ISR_STAT_TRIGGER [733].ISR_STAT_TRIGGER[29:29]	1
0xBB001858	ISR_STAT_TRIGGER [734].ISR_STAT_TRIGGER[30:30]	1
0xBB001858	ISR_STAT_TRIGGER [735].ISR_STAT_TRIGGER[31:31]	1
0xBB00185C	ISR_STAT_TRIGGER [736].ISR_STAT_TRIGGER[0:0]	1
0xBB00185C	ISR_STAT_TRIGGER [737].ISR_STAT_TRIGGER[1:1]	1
0xBB00185C	ISR_STAT_TRIGGER [738].ISR_STAT_TRIGGER[2:2]	1
0xBB00185C	ISR_STAT_TRIGGER [739].ISR_STAT_TRIGGER[3:3]	1
0xBB00185C	ISR_STAT_TRIGGER [740].ISR_STAT_TRIGGER[4:4]	1
0xBB00185C	ISR_STAT_TRIGGER [741].ISR_STAT_TRIGGER[5:5]	1
0xBB00185C	ISR_STAT_TRIGGER [742].ISR_STAT_TRIGGER[6:6]	1
0xBB00185C	ISR_STAT_TRIGGER [743].ISR_STAT_TRIGGER[7:7]	1
0xBB00185C	ISR_STAT_TRIGGER [744].ISR_STAT_TRIGGER[8:8]	1
0xBB00185C	ISR_STAT_TRIGGER [745].ISR_STAT_TRIGGER[9:9]	1
0xBB00185C	ISR_STAT_TRIGGER [746].ISR_STAT_TRIGGER[10:10]	1
0xBB00185C	ISR_STAT_TRIGGER [747].ISR_STAT_TRIGGER[11:11]	1
0xBB00185C	ISR_STAT_TRIGGER [748].ISR_STAT_TRIGGER[12:12]	1
0xBB00185C	ISR_STAT_TRIGGER [749].ISR_STAT_TRIGGER[13:13]	1
0xBB00185C	ISR_STAT_TRIGGER [750].ISR_STAT_TRIGGER[14:14]	1
0xBB00185C	ISR_STAT_TRIGGER [751].ISR_STAT_TRIGGER[15:15]	1
0xBB00185C	ISR_STAT_TRIGGER [752].ISR_STAT_TRIGGER[16:16]	1
0xBB00185C	ISR_STAT_TRIGGER [753].ISR_STAT_TRIGGER[17:17]	1
0xBB00185C	ISR_STAT_TRIGGER [754].ISR_STAT_TRIGGER[18:18]	1
0xBB00185C	ISR_STAT_TRIGGER [755].ISR_STAT_TRIGGER[19:19]	1
0xBB00185C	ISR_STAT_TRIGGER [756].ISR_STAT_TRIGGER[20:20]	1
0xBB00185C	ISR_STAT_TRIGGER [757].ISR_STAT_TRIGGER[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00185C	ISR_STAT_TRIGGER [758].ISR_STAT_TRIGGER[22:22]	1
0xBB00185C	ISR_STAT_TRIGGER [759].ISR_STAT_TRIGGER[23:23]	1
0xBB00185C	ISR_STAT_TRIGGER [760].ISR_STAT_TRIGGER[24:24]	1
0xBB00185C	ISR_STAT_TRIGGER [761].ISR_STAT_TRIGGER[25:25]	1
0xBB00185C	ISR_STAT_TRIGGER [762].ISR_STAT_TRIGGER[26:26]	1
0xBB00185C	ISR_STAT_TRIGGER [763].ISR_STAT_TRIGGER[27:27]	1
0xBB00185C	ISR_STAT_TRIGGER [764].ISR_STAT_TRIGGER[28:28]	1
0xBB00185C	ISR_STAT_TRIGGER [765].ISR_STAT_TRIGGER[29:29]	1
0xBB00185C	ISR_STAT_TRIGGER [766].ISR_STAT_TRIGGER[30:30]	1
0xBB00185C	ISR_STAT_TRIGGER [767].ISR_STAT_TRIGGER[31:31]	1
0xBB001860	ISR_STAT_TRIGGER [768].ISR_STAT_TRIGGER[0:0]	1
0xBB001860	ISR_STAT_TRIGGER [769].ISR_STAT_TRIGGER[1:1]	1
0xBB001860	ISR_STAT_TRIGGER [770].ISR_STAT_TRIGGER[2:2]	1
0xBB001860	ISR_STAT_TRIGGER [771].ISR_STAT_TRIGGER[3:3]	1
0xBB001860	ISR_STAT_TRIGGER [772].ISR_STAT_TRIGGER[4:4]	1
0xBB001860	ISR_STAT_TRIGGER [773].ISR_STAT_TRIGGER[5:5]	1
0xBB001860	ISR_STAT_TRIGGER [774].ISR_STAT_TRIGGER[6:6]	1
0xBB001860	ISR_STAT_TRIGGER [775].ISR_STAT_TRIGGER[7:7]	1
0xBB001860	ISR_STAT_TRIGGER [776].ISR_STAT_TRIGGER[8:8]	1
0xBB001860	ISR_STAT_TRIGGER [777].ISR_STAT_TRIGGER[9:9]	1
0xBB001860	ISR_STAT_TRIGGER [778].ISR_STAT_TRIGGER[10:10]	1
0xBB001860	ISR_STAT_TRIGGER [779].ISR_STAT_TRIGGER[11:11]	1
0xBB001860	ISR_STAT_TRIGGER [780].ISR_STAT_TRIGGER[12:12]	1
0xBB001860	ISR_STAT_TRIGGER [781].ISR_STAT_TRIGGER[13:13]	1
0xBB001860	ISR_STAT_TRIGGER [782].ISR_STAT_TRIGGER[14:14]	1
0xBB001860	ISR_STAT_TRIGGER [783].ISR_STAT_TRIGGER[15:15]	1
0xBB001860	ISR_STAT_TRIGGER [784].ISR_STAT_TRIGGER[16:16]	1
0xBB001860	ISR_STAT_TRIGGER [785].ISR_STAT_TRIGGER[17:17]	1
0xBB001860	ISR_STAT_TRIGGER [786].ISR_STAT_TRIGGER[18:18]	1
0xBB001860	ISR_STAT_TRIGGER [787].ISR_STAT_TRIGGER[19:19]	1
0xBB001860	ISR_STAT_TRIGGER [788].ISR_STAT_TRIGGER[20:20]	1
0xBB001860	ISR_STAT_TRIGGER [789].ISR_STAT_TRIGGER[21:21]	1
0xBB001860	ISR_STAT_TRIGGER [790].ISR_STAT_TRIGGER[22:22]	1
0xBB001860	ISR_STAT_TRIGGER [791].ISR_STAT_TRIGGER[23:23]	1
0xBB001860	ISR_STAT_TRIGGER [792].ISR_STAT_TRIGGER[24:24]	1
0xBB001860	ISR_STAT_TRIGGER [793].ISR_STAT_TRIGGER[25:25]	1
0xBB001860	ISR_STAT_TRIGGER [794].ISR_STAT_TRIGGER[26:26]	1
0xBB001860	ISR_STAT_TRIGGER [795].ISR_STAT_TRIGGER[27:27]	1
0xBB001860	ISR_STAT_TRIGGER [796].ISR_STAT_TRIGGER[28:28]	1
0xBB001860	ISR_STAT_TRIGGER [797].ISR_STAT_TRIGGER[29:29]	1
0xBB001860	ISR_STAT_TRIGGER [798].ISR_STAT_TRIGGER[30:30]	1
0xBB001860	ISR_STAT_TRIGGER [799].ISR_STAT_TRIGGER[31:31]	1
0xBB001864	ISR_STAT_TRIGGER [800].ISR_STAT_TRIGGER[0:0]	1
0xBB001864	ISR_STAT_TRIGGER [801].ISR_STAT_TRIGGER[1:1]	1
0xBB001864	ISR_STAT_TRIGGER [802].ISR_STAT_TRIGGER[2:2]	1
0xBB001864	ISR_STAT_TRIGGER [803].ISR_STAT_TRIGGER[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001864	ISR_STAT_TRIGGER [804].ISR_STAT_TRIGGER[4:4]	1
0xBB001864	ISR_STAT_TRIGGER [805].ISR_STAT_TRIGGER[5:5]	1
0xBB001864	ISR_STAT_TRIGGER [806].ISR_STAT_TRIGGER[6:6]	1
0xBB001864	ISR_STAT_TRIGGER [807].ISR_STAT_TRIGGER[7:7]	1
0xBB001864	ISR_STAT_TRIGGER [808].ISR_STAT_TRIGGER[8:8]	1
0xBB001864	ISR_STAT_TRIGGER [809].ISR_STAT_TRIGGER[9:9]	1
0xBB001864	ISR_STAT_TRIGGER [810].ISR_STAT_TRIGGER[10:10]	1
0xBB001864	ISR_STAT_TRIGGER [811].ISR_STAT_TRIGGER[11:11]	1
0xBB001864	ISR_STAT_TRIGGER [812].ISR_STAT_TRIGGER[12:12]	1
0xBB001864	ISR_STAT_TRIGGER [813].ISR_STAT_TRIGGER[13:13]	1
0xBB001864	ISR_STAT_TRIGGER [814].ISR_STAT_TRIGGER[14:14]	1
0xBB001864	ISR_STAT_TRIGGER [815].ISR_STAT_TRIGGER[15:15]	1
0xBB001864	ISR_STAT_TRIGGER [816].ISR_STAT_TRIGGER[16:16]	1
0xBB001864	ISR_STAT_TRIGGER [817].ISR_STAT_TRIGGER[17:17]	1
0xBB001864	ISR_STAT_TRIGGER [818].ISR_STAT_TRIGGER[18:18]	1
0xBB001864	ISR_STAT_TRIGGER [819].ISR_STAT_TRIGGER[19:19]	1
0xBB001864	ISR_STAT_TRIGGER [820].ISR_STAT_TRIGGER[20:20]	1
0xBB001864	ISR_STAT_TRIGGER [821].ISR_STAT_TRIGGER[21:21]	1
0xBB001864	ISR_STAT_TRIGGER [822].ISR_STAT_TRIGGER[22:22]	1
0xBB001864	ISR_STAT_TRIGGER [823].ISR_STAT_TRIGGER[23:23]	1
0xBB001864	ISR_STAT_TRIGGER [824].ISR_STAT_TRIGGER[24:24]	1
0xBB001864	ISR_STAT_TRIGGER [825].ISR_STAT_TRIGGER[25:25]	1
0xBB001864	ISR_STAT_TRIGGER [826].ISR_STAT_TRIGGER[26:26]	1
0xBB001864	ISR_STAT_TRIGGER [827].ISR_STAT_TRIGGER[27:27]	1
0xBB001864	ISR_STAT_TRIGGER [828].ISR_STAT_TRIGGER[28:28]	1
0xBB001864	ISR_STAT_TRIGGER [829].ISR_STAT_TRIGGER[29:29]	1
0xBB001864	ISR_STAT_TRIGGER [830].ISR_STAT_TRIGGER[30:30]	1
0xBB001864	ISR_STAT_TRIGGER [831].ISR_STAT_TRIGGER[31:31]	1
0xBB001868	ISR_STAT_TRIGGER [832].ISR_STAT_TRIGGER[0:0]	1
0xBB001868	ISR_STAT_TRIGGER [833].ISR_STAT_TRIGGER[1:1]	1
0xBB001868	ISR_STAT_TRIGGER [834].ISR_STAT_TRIGGER[2:2]	1
0xBB001868	ISR_STAT_TRIGGER [835].ISR_STAT_TRIGGER[3:3]	1
0xBB001868	ISR_STAT_TRIGGER [836].ISR_STAT_TRIGGER[4:4]	1
0xBB001868	ISR_STAT_TRIGGER [837].ISR_STAT_TRIGGER[5:5]	1
0xBB001868	ISR_STAT_TRIGGER [838].ISR_STAT_TRIGGER[6:6]	1
0xBB001868	ISR_STAT_TRIGGER [839].ISR_STAT_TRIGGER[7:7]	1
0xBB001868	ISR_STAT_TRIGGER [840].ISR_STAT_TRIGGER[8:8]	1
0xBB001868	ISR_STAT_TRIGGER [841].ISR_STAT_TRIGGER[9:9]	1
0xBB001868	ISR_STAT_TRIGGER [842].ISR_STAT_TRIGGER[10:10]	1
0xBB001868	ISR_STAT_TRIGGER [843].ISR_STAT_TRIGGER[11:11]	1
0xBB001868	ISR_STAT_TRIGGER [844].ISR_STAT_TRIGGER[12:12]	1
0xBB001868	ISR_STAT_TRIGGER [845].ISR_STAT_TRIGGER[13:13]	1
0xBB001868	ISR_STAT_TRIGGER [846].ISR_STAT_TRIGGER[14:14]	1
0xBB001868	ISR_STAT_TRIGGER [847].ISR_STAT_TRIGGER[15:15]	1
0xBB001868	ISR_STAT_TRIGGER [848].ISR_STAT_TRIGGER[16:16]	1
0xBB001868	ISR_STAT_TRIGGER [849].ISR_STAT_TRIGGER[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001868	ISR_STAT_TRIGGER [850].ISR_STAT_TRIGGER[18:18]	1
0xBB001868	ISR_STAT_TRIGGER [851].ISR_STAT_TRIGGER[19:19]	1
0xBB001868	ISR_STAT_TRIGGER [852].ISR_STAT_TRIGGER[20:20]	1
0xBB001868	ISR_STAT_TRIGGER [853].ISR_STAT_TRIGGER[21:21]	1
0xBB001868	ISR_STAT_TRIGGER [854].ISR_STAT_TRIGGER[22:22]	1
0xBB001868	ISR_STAT_TRIGGER [855].ISR_STAT_TRIGGER[23:23]	1
0xBB001868	ISR_STAT_TRIGGER [856].ISR_STAT_TRIGGER[24:24]	1
0xBB001868	ISR_STAT_TRIGGER [857].ISR_STAT_TRIGGER[25:25]	1
0xBB001868	ISR_STAT_TRIGGER [858].ISR_STAT_TRIGGER[26:26]	1
0xBB001868	ISR_STAT_TRIGGER [859].ISR_STAT_TRIGGER[27:27]	1
0xBB001868	ISR_STAT_TRIGGER [860].ISR_STAT_TRIGGER[28:28]	1
0xBB001868	ISR_STAT_TRIGGER [861].ISR_STAT_TRIGGER[29:29]	1
0xBB001868	ISR_STAT_TRIGGER [862].ISR_STAT_TRIGGER[30:30]	1
0xBB001868	ISR_STAT_TRIGGER [863].ISR_STAT_TRIGGER[31:31]	1
0xBB00186C	ISR_STAT_TRIGGER [864].ISR_STAT_TRIGGER[0:0]	1
0xBB00186C	ISR_STAT_TRIGGER [865].ISR_STAT_TRIGGER[1:1]	1
0xBB00186C	ISR_STAT_TRIGGER [866].ISR_STAT_TRIGGER[2:2]	1
0xBB00186C	ISR_STAT_TRIGGER [867].ISR_STAT_TRIGGER[3:3]	1
0xBB00186C	ISR_STAT_TRIGGER [868].ISR_STAT_TRIGGER[4:4]	1
0xBB00186C	ISR_STAT_TRIGGER [869].ISR_STAT_TRIGGER[5:5]	1
0xBB00186C	ISR_STAT_TRIGGER [870].ISR_STAT_TRIGGER[6:6]	1
0xBB00186C	ISR_STAT_TRIGGER [871].ISR_STAT_TRIGGER[7:7]	1
0xBB00186C	ISR_STAT_TRIGGER [872].ISR_STAT_TRIGGER[8:8]	1
0xBB00186C	ISR_STAT_TRIGGER [873].ISR_STAT_TRIGGER[9:9]	1
0xBB00186C	ISR_STAT_TRIGGER [874].ISR_STAT_TRIGGER[10:10]	1
0xBB00186C	ISR_STAT_TRIGGER [875].ISR_STAT_TRIGGER[11:11]	1
0xBB00186C	ISR_STAT_TRIGGER [876].ISR_STAT_TRIGGER[12:12]	1
0xBB00186C	ISR_STAT_TRIGGER [877].ISR_STAT_TRIGGER[13:13]	1
0xBB00186C	ISR_STAT_TRIGGER [878].ISR_STAT_TRIGGER[14:14]	1
0xBB00186C	ISR_STAT_TRIGGER [879].ISR_STAT_TRIGGER[15:15]	1
0xBB00186C	ISR_STAT_TRIGGER [880].ISR_STAT_TRIGGER[16:16]	1
0xBB00186C	ISR_STAT_TRIGGER [881].ISR_STAT_TRIGGER[17:17]	1
0xBB00186C	ISR_STAT_TRIGGER [882].ISR_STAT_TRIGGER[18:18]	1
0xBB00186C	ISR_STAT_TRIGGER [883].ISR_STAT_TRIGGER[19:19]	1
0xBB00186C	ISR_STAT_TRIGGER [884].ISR_STAT_TRIGGER[20:20]	1
0xBB00186C	ISR_STAT_TRIGGER [885].ISR_STAT_TRIGGER[21:21]	1
0xBB00186C	ISR_STAT_TRIGGER [886].ISR_STAT_TRIGGER[22:22]	1
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0xBB00186C	ISR_STAT_TRIGGER [888].ISR_STAT_TRIGGER[24:24]	1
0xBB00186C	ISR_STAT_TRIGGER [889].ISR_STAT_TRIGGER[25:25]	1
0xBB00186C	ISR_STAT_TRIGGER [890].ISR_STAT_TRIGGER[26:26]	1
0xBB00186C	ISR_STAT_TRIGGER [891].ISR_STAT_TRIGGER[27:27]	1
0xBB00186C	ISR_STAT_TRIGGER [892].ISR_STAT_TRIGGER[28:28]	1
0xBB00186C	ISR_STAT_TRIGGER [893].ISR_STAT_TRIGGER[29:29]	1
0xBB00186C	ISR_STAT_TRIGGER [894].ISR_STAT_TRIGGER[30:30]	1
0xBB00186C	ISR_STAT_TRIGGER [895].ISR_STAT_TRIGGER[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001870	ISR_STAT_TRIGGER [898].ISR_STAT_TRIGGER[2:2]	1
0xBB001870	ISR_STAT_TRIGGER [899].ISR_STAT_TRIGGER[3:3]	1
0xBB001870	ISR_STAT_TRIGGER [900].ISR_STAT_TRIGGER[4:4]	1
0xBB001870	ISR_STAT_TRIGGER [901].ISR_STAT_TRIGGER[5:5]	1
0xBB001870	ISR_STAT_TRIGGER [902].ISR_STAT_TRIGGER[6:6]	1
0xBB001870	ISR_STAT_TRIGGER [903].ISR_STAT_TRIGGER[7:7]	1
0xBB001870	ISR_STAT_TRIGGER [904].ISR_STAT_TRIGGER[8:8]	1
0xBB001870	ISR_STAT_TRIGGER [905].ISR_STAT_TRIGGER[9:9]	1
0xBB001870	ISR_STAT_TRIGGER [906].ISR_STAT_TRIGGER[10:10]	1
0xBB001870	ISR_STAT_TRIGGER [907].ISR_STAT_TRIGGER[11:11]	1
0xBB001870	ISR_STAT_TRIGGER [908].ISR_STAT_TRIGGER[12:12]	1
0xBB001870	ISR_STAT_TRIGGER [909].ISR_STAT_TRIGGER[13:13]	1
0xBB001870	ISR_STAT_TRIGGER [910].ISR_STAT_TRIGGER[14:14]	1
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0xBB001870	ISR_STAT_TRIGGER [912].ISR_STAT_TRIGGER[16:16]	1
0xBB001870	ISR_STAT_TRIGGER [913].ISR_STAT_TRIGGER[17:17]	1
0xBB001870	ISR_STAT_TRIGGER [914].ISR_STAT_TRIGGER[18:18]	1
0xBB001870	ISR_STAT_TRIGGER [915].ISR_STAT_TRIGGER[19:19]	1
0xBB001870	ISR_STAT_TRIGGER [916].ISR_STAT_TRIGGER[20:20]	1
0xBB001870	ISR_STAT_TRIGGER [917].ISR_STAT_TRIGGER[21:21]	1
0xBB001870	ISR_STAT_TRIGGER [918].ISR_STAT_TRIGGER[22:22]	1
0xBB001870	ISR_STAT_TRIGGER [919].ISR_STAT_TRIGGER[23:23]	1
0xBB001870	ISR_STAT_TRIGGER [920].ISR_STAT_TRIGGER[24:24]	1
0xBB001870	ISR_STAT_TRIGGER [921].ISR_STAT_TRIGGER[25:25]	1
0xBB001870	ISR_STAT_TRIGGER [922].ISR_STAT_TRIGGER[26:26]	1
0xBB001870	ISR_STAT_TRIGGER [923].ISR_STAT_TRIGGER[27:27]	1
0xBB001870	ISR_STAT_TRIGGER [924].ISR_STAT_TRIGGER[28:28]	1
0xBB001870	ISR_STAT_TRIGGER [925].ISR_STAT_TRIGGER[29:29]	1
0xBB001870	ISR_STAT_TRIGGER [926].ISR_STAT_TRIGGER[30:30]	1
0xBB001870	ISR_STAT_TRIGGER [927].ISR_STAT_TRIGGER[31:31]	1
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0xBB001874	ISR_STAT_TRIGGER [930].ISR_STAT_TRIGGER[2:2]	1
0xBB001874	ISR_STAT_TRIGGER [931].ISR_STAT_TRIGGER[3:3]	1
0xBB001874	ISR_STAT_TRIGGER [932].ISR_STAT_TRIGGER[4:4]	1
0xBB001874	ISR_STAT_TRIGGER [933].ISR_STAT_TRIGGER[5:5]	1
0xBB001874	ISR_STAT_TRIGGER [934].ISR_STAT_TRIGGER[6:6]	1
0xBB001874	ISR_STAT_TRIGGER [935].ISR_STAT_TRIGGER[7:7]	1
0xBB001874	ISR_STAT_TRIGGER [936].ISR_STAT_TRIGGER[8:8]	1
0xBB001874	ISR_STAT_TRIGGER [937].ISR_STAT_TRIGGER[9:9]	1
0xBB001874	ISR_STAT_TRIGGER [938].ISR_STAT_TRIGGER[10:10]	1
0xBB001874	ISR_STAT_TRIGGER [939].ISR_STAT_TRIGGER[11:11]	1
0xBB001874	ISR_STAT_TRIGGER [940].ISR_STAT_TRIGGER[12:12]	1
0xBB001874	ISR_STAT_TRIGGER [941].ISR_STAT_TRIGGER[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001874	ISR_STAT_TRIGGER [943].ISR_STAT_TRIGGER[15:15]	1
0xBB001874	ISR_STAT_TRIGGER [944].ISR_STAT_TRIGGER[16:16]	1
0xBB001874	ISR_STAT_TRIGGER [945].ISR_STAT_TRIGGER[17:17]	1
0xBB001874	ISR_STAT_TRIGGER [946].ISR_STAT_TRIGGER[18:18]	1
0xBB001874	ISR_STAT_TRIGGER [947].ISR_STAT_TRIGGER[19:19]	1
0xBB001874	ISR_STAT_TRIGGER [948].ISR_STAT_TRIGGER[20:20]	1
0xBB001874	ISR_STAT_TRIGGER [949].ISR_STAT_TRIGGER[21:21]	1
0xBB001874	ISR_STAT_TRIGGER [950].ISR_STAT_TRIGGER[22:22]	1
0xBB001874	ISR_STAT_TRIGGER [951].ISR_STAT_TRIGGER[23:23]	1
0xBB001874	ISR_STAT_TRIGGER [952].ISR_STAT_TRIGGER[24:24]	1
0xBB001874	ISR_STAT_TRIGGER [953].ISR_STAT_TRIGGER[25:25]	1
0xBB001874	ISR_STAT_TRIGGER [954].ISR_STAT_TRIGGER[26:26]	1
0xBB001874	ISR_STAT_TRIGGER [955].ISR_STAT_TRIGGER[27:27]	1
0xBB001874	ISR_STAT_TRIGGER [956].ISR_STAT_TRIGGER[28:28]	1
0xBB001874	ISR_STAT_TRIGGER [957].ISR_STAT_TRIGGER[29:29]	1
0xBB001874	ISR_STAT_TRIGGER [958].ISR_STAT_TRIGGER[30:30]	1
0xBB001874	ISR_STAT_TRIGGER [959].ISR_STAT_TRIGGER[31:31]	1
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0xBB001878	ISR_STAT_TRIGGER [961].ISR_STAT_TRIGGER[1:1]	1
0xBB001878	ISR_STAT_TRIGGER [962].ISR_STAT_TRIGGER[2:2]	1
0xBB001878	ISR_STAT_TRIGGER [963].ISR_STAT_TRIGGER[3:3]	1
0xBB001878	ISR_STAT_TRIGGER [964].ISR_STAT_TRIGGER[4:4]	1
0xBB001878	ISR_STAT_TRIGGER [965].ISR_STAT_TRIGGER[5:5]	1
0xBB001878	ISR_STAT_TRIGGER [966].ISR_STAT_TRIGGER[6:6]	1
0xBB001878	ISR_STAT_TRIGGER [967].ISR_STAT_TRIGGER[7:7]	1
0xBB001878	ISR_STAT_TRIGGER [968].ISR_STAT_TRIGGER[8:8]	1
0xBB001878	ISR_STAT_TRIGGER [969].ISR_STAT_TRIGGER[9:9]	1
0xBB001878	ISR_STAT_TRIGGER [970].ISR_STAT_TRIGGER[10:10]	1
0xBB001878	ISR_STAT_TRIGGER [971].ISR_STAT_TRIGGER[11:11]	1
0xBB001878	ISR_STAT_TRIGGER [972].ISR_STAT_TRIGGER[12:12]	1
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0xBB001878	ISR_STAT_TRIGGER [974].ISR_STAT_TRIGGER[14:14]	1
0xBB001878	ISR_STAT_TRIGGER [975].ISR_STAT_TRIGGER[15:15]	1
0xBB001878	ISR_STAT_TRIGGER [976].ISR_STAT_TRIGGER[16:16]	1
0xBB001878	ISR_STAT_TRIGGER [977].ISR_STAT_TRIGGER[17:17]	1
0xBB001878	ISR_STAT_TRIGGER [978].ISR_STAT_TRIGGER[18:18]	1
0xBB001878	ISR_STAT_TRIGGER [979].ISR_STAT_TRIGGER[19:19]	1
0xBB001878	ISR_STAT_TRIGGER [980].ISR_STAT_TRIGGER[20:20]	1
0xBB001878	ISR_STAT_TRIGGER [981].ISR_STAT_TRIGGER[21:21]	1
0xBB001878	ISR_STAT_TRIGGER [982].ISR_STAT_TRIGGER[22:22]	1
0xBB001878	ISR_STAT_TRIGGER [983].ISR_STAT_TRIGGER[23:23]	1
0xBB001878	ISR_STAT_TRIGGER [984].ISR_STAT_TRIGGER[24:24]	1
0xBB001878	ISR_STAT_TRIGGER [985].ISR_STAT_TRIGGER[25:25]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001878	ISR_STAT_TRIGGER [989].ISR_STAT_TRIGGER[29:29]	1
0xBB001878	ISR_STAT_TRIGGER [990].ISR_STAT_TRIGGER[30:30]	1
0xBB001878	ISR_STAT_TRIGGER [991].ISR_STAT_TRIGGER[31:31]	1
0xBB00187C	ISR_STAT_TRIGGER [992].ISR_STAT_TRIGGER[0:0]	1
0xBB00187C	ISR_STAT_TRIGGER [993].ISR_STAT_TRIGGER[1:1]	1
0xBB00187C	ISR_STAT_TRIGGER [994].ISR_STAT_TRIGGER[2:2]	1
0xBB00187C	ISR_STAT_TRIGGER [995].ISR_STAT_TRIGGER[3:3]	1
0xBB00187C	ISR_STAT_TRIGGER [996].ISR_STAT_TRIGGER[4:4]	1
0xBB00187C	ISR_STAT_TRIGGER [997].ISR_STAT_TRIGGER[5:5]	1
0xBB00187C	ISR_STAT_TRIGGER [998].ISR_STAT_TRIGGER[6:6]	1
0xBB00187C	ISR_STAT_TRIGGER [999].ISR_STAT_TRIGGER[7:7]	1
0xBB00187C	ISR_STAT_TRIGGER [1000].ISR_STAT_TRIGGER[8:8]	1
0xBB00187C	ISR_STAT_TRIGGER [1001].ISR_STAT_TRIGGER[9:9]	1
0xBB00187C	ISR_STAT_TRIGGER [1002].ISR_STAT_TRIGGER[10:10]	1
0xBB00187C	ISR_STAT_TRIGGER [1003].ISR_STAT_TRIGGER[11:11]	1
0xBB00187C	ISR_STAT_TRIGGER [1004].ISR_STAT_TRIGGER[12:12]	1
0xBB00187C	ISR_STAT_TRIGGER [1005].ISR_STAT_TRIGGER[13:13]	1
0xBB00187C	ISR_STAT_TRIGGER [1006].ISR_STAT_TRIGGER[14:14]	1
0xBB00187C	ISR_STAT_TRIGGER [1007].ISR_STAT_TRIGGER[15:15]	1
0xBB00187C	ISR_STAT_TRIGGER [1008].ISR_STAT_TRIGGER[16:16]	1
0xBB00187C	ISR_STAT_TRIGGER [1009].ISR_STAT_TRIGGER[17:17]	1
0xBB00187C	ISR_STAT_TRIGGER [1010].ISR_STAT_TRIGGER[18:18]	1
0xBB00187C	ISR_STAT_TRIGGER [1011].ISR_STAT_TRIGGER[19:19]	1
0xBB00187C	ISR_STAT_TRIGGER [1012].ISR_STAT_TRIGGER[20:20]	1
0xBB00187C	ISR_STAT_TRIGGER [1013].ISR_STAT_TRIGGER[21:21]	1
0xBB00187C	ISR_STAT_TRIGGER [1014].ISR_STAT_TRIGGER[22:22]	1
0xBB00187C	ISR_STAT_TRIGGER [1015].ISR_STAT_TRIGGER[23:23]	1
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0xBB00187C	ISR_STAT_TRIGGER [1017].ISR_STAT_TRIGGER[25:25]	1
0xBB00187C	ISR_STAT_TRIGGER [1018].ISR_STAT_TRIGGER[26:26]	1
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0xBB001880	ISR_STAT_TRIGGER [1026].ISR_STAT_TRIGGER[2:2]	1
0xBB001880	ISR_STAT_TRIGGER [1027].ISR_STAT_TRIGGER[3:3]	1
0xBB001880	ISR_STAT_TRIGGER [1028].ISR_STAT_TRIGGER[4:4]	1
0xBB001880	ISR_STAT_TRIGGER [1029].ISR_STAT_TRIGGER[5:5]	1
0xBB001880	ISR_STAT_TRIGGER [1030].ISR_STAT_TRIGGER[6:6]	1
0xBB001880	ISR_STAT_TRIGGER [1031].ISR_STAT_TRIGGER[7:7]	1
0xBB001880	ISR_STAT_TRIGGER [1032].ISR_STAT_TRIGGER[8:8]	1
0xBB001880	ISR_STAT_TRIGGER [1033].ISR_STAT_TRIGGER[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001880	ISR_STAT_TRIGGER [1036].ISR_STAT_TRIGGER[12:12]	1
0xBB001880	ISR_STAT_TRIGGER [1037].ISR_STAT_TRIGGER[13:13]	1
0xBB001880	ISR_STAT_TRIGGER [1038].ISR_STAT_TRIGGER[14:14]	1
0xBB001880	ISR_STAT_TRIGGER [1039].ISR_STAT_TRIGGER[15:15]	1
0xBB001880	ISR_STAT_TRIGGER [1040].ISR_STAT_TRIGGER[16:16]	1
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0xBB001880	ISR_STAT_TRIGGER [1042].ISR_STAT_TRIGGER[18:18]	1
0xBB001880	ISR_STAT_TRIGGER [1043].ISR_STAT_TRIGGER[19:19]	1
0xBB001880	ISR_STAT_TRIGGER [1044].ISR_STAT_TRIGGER[20:20]	1
0xBB001880	ISR_STAT_TRIGGER [1045].ISR_STAT_TRIGGER[21:21]	1
0xBB001880	ISR_STAT_TRIGGER [1046].ISR_STAT_TRIGGER[22:22]	1
0xBB001880	ISR_STAT_TRIGGER [1047].ISR_STAT_TRIGGER[23:23]	1
0xBB001880	ISR_STAT_TRIGGER [1048].ISR_STAT_TRIGGER[24:24]	1
0xBB001880	ISR_STAT_TRIGGER [1049].ISR_STAT_TRIGGER[25:25]	1
0xBB001880	ISR_STAT_TRIGGER [1050].ISR_STAT_TRIGGER[26:26]	1
0xBB001880	ISR_STAT_TRIGGER [1051].ISR_STAT_TRIGGER[27:27]	1
0xBB001880	ISR_STAT_TRIGGER [1052].ISR_STAT_TRIGGER[28:28]	1
0xBB001880	ISR_STAT_TRIGGER [1053].ISR_STAT_TRIGGER[29:29]	1
0xBB001880	ISR_STAT_TRIGGER [1054].ISR_STAT_TRIGGER[30:30]	1
0xBB001880	ISR_STAT_TRIGGER [1055].ISR_STAT_TRIGGER[31:31]	1
0xBB001884	ISR_STAT_TRIGGER [1056].ISR_STAT_TRIGGER[0:0]	1
0xBB001884	ISR_STAT_TRIGGER [1057].ISR_STAT_TRIGGER[1:1]	1
0xBB001884	ISR_STAT_TRIGGER [1058].ISR_STAT_TRIGGER[2:2]	1
0xBB001884	ISR_STAT_TRIGGER [1059].ISR_STAT_TRIGGER[3:3]	1
0xBB001884	ISR_STAT_TRIGGER [1060].ISR_STAT_TRIGGER[4:4]	1
0xBB001884	ISR_STAT_TRIGGER [1061].ISR_STAT_TRIGGER[5:5]	1
0xBB001884	ISR_STAT_TRIGGER [1062].ISR_STAT_TRIGGER[6:6]	1
0xBB001884	ISR_STAT_TRIGGER [1063].ISR_STAT_TRIGGER[7:7]	1
0xBB001884	ISR_STAT_TRIGGER [1064].ISR_STAT_TRIGGER[8:8]	1
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0xBB001884	ISR_STAT_TRIGGER [1066].ISR_STAT_TRIGGER[10:10]	1
0xBB001884	ISR_STAT_TRIGGER [1067].ISR_STAT_TRIGGER[11:11]	1
0xBB001884	ISR_STAT_TRIGGER [1068].ISR_STAT_TRIGGER[12:12]	1
0xBB001884	ISR_STAT_TRIGGER [1069].ISR_STAT_TRIGGER[13:13]	1
0xBB001884	ISR_STAT_TRIGGER [1070].ISR_STAT_TRIGGER[14:14]	1
0xBB001884	ISR_STAT_TRIGGER [1071].ISR_STAT_TRIGGER[15:15]	1
0xBB001884	ISR_STAT_TRIGGER [1072].ISR_STAT_TRIGGER[16:16]	1
0xBB001884	ISR_STAT_TRIGGER [1073].ISR_STAT_TRIGGER[17:17]	1
0xBB001884	ISR_STAT_TRIGGER [1074].ISR_STAT_TRIGGER[18:18]	1
0xBB001884	ISR_STAT_TRIGGER [1075].ISR_STAT_TRIGGER[19:19]	1
0xBB001884	ISR_STAT_TRIGGER [1076].ISR_STAT_TRIGGER[20:20]	1
0xBB001884	ISR_STAT_TRIGGER [1077].ISR_STAT_TRIGGER[21:21]	1
0xBB001884	ISR_STAT_TRIGGER [1078].ISR_STAT_TRIGGER[22:22]	1
0xBB001884	ISR_STAT_TRIGGER [1079].ISR_STAT_TRIGGER[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001884	ISR_STAT_TRIGGER [1083].ISR_STAT_TRIGGER[27:27]	1
0xBB001884	ISR_STAT_TRIGGER [1084].ISR_STAT_TRIGGER[28:28]	1
0xBB001884	ISR_STAT_TRIGGER [1085].ISR_STAT_TRIGGER[29:29]	1
0xBB001884	ISR_STAT_TRIGGER [1086].ISR_STAT_TRIGGER[30:30]	1
0xBB001884	ISR_STAT_TRIGGER [1087].ISR_STAT_TRIGGER[31:31]	1
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0xBB001888	ISR_STAT_TRIGGER [1089].ISR_STAT_TRIGGER[1:1]	1
0xBB001888	ISR_STAT_TRIGGER [1090].ISR_STAT_TRIGGER[2:2]	1
0xBB001888	ISR_STAT_TRIGGER [1091].ISR_STAT_TRIGGER[3:3]	1
0xBB001888	ISR_STAT_TRIGGER [1092].ISR_STAT_TRIGGER[4:4]	1
0xBB001888	ISR_STAT_TRIGGER [1093].ISR_STAT_TRIGGER[5:5]	1
0xBB001888	ISR_STAT_TRIGGER [1094].ISR_STAT_TRIGGER[6:6]	1
0xBB001888	ISR_STAT_TRIGGER [1095].ISR_STAT_TRIGGER[7:7]	1
0xBB001888	ISR_STAT_TRIGGER [1096].ISR_STAT_TRIGGER[8:8]	1
0xBB001888	ISR_STAT_TRIGGER [1097].ISR_STAT_TRIGGER[9:9]	1
0xBB001888	ISR_STAT_TRIGGER [1098].ISR_STAT_TRIGGER[10:10]	1
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0xBB001888	ISR_STAT_TRIGGER [1100].ISR_STAT_TRIGGER[12:12]	1
0xBB001888	ISR_STAT_TRIGGER [1101].ISR_STAT_TRIGGER[13:13]	1
0xBB001888	ISR_STAT_TRIGGER [1102].ISR_STAT_TRIGGER[14:14]	1
0xBB001888	ISR_STAT_TRIGGER [1103].ISR_STAT_TRIGGER[15:15]	1
0xBB001888	ISR_STAT_TRIGGER [1104].ISR_STAT_TRIGGER[16:16]	1
0xBB001888	ISR_STAT_TRIGGER [1105].ISR_STAT_TRIGGER[17:17]	1
0xBB001888	ISR_STAT_TRIGGER [1106].ISR_STAT_TRIGGER[18:18]	1
0xBB001888	ISR_STAT_TRIGGER [1107].ISR_STAT_TRIGGER[19:19]	1
0xBB001888	ISR_STAT_TRIGGER [1108].ISR_STAT_TRIGGER[20:20]	1
0xBB001888	ISR_STAT_TRIGGER [1109].ISR_STAT_TRIGGER[21:21]	1
0xBB001888	ISR_STAT_TRIGGER [1110].ISR_STAT_TRIGGER[22:22]	1
0xBB001888	ISR_STAT_TRIGGER [1111].ISR_STAT_TRIGGER[23:23]	1
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0xBB001888	ISR_STAT_TRIGGER [1113].ISR_STAT_TRIGGER[25:25]	1
0xBB001888	ISR_STAT_TRIGGER [1114].ISR_STAT_TRIGGER[26:26]	1
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0xBB001888	ISR_STAT_TRIGGER [1118].ISR_STAT_TRIGGER[30:30]	1
0xBB001888	ISR_STAT_TRIGGER [1119].ISR_STAT_TRIGGER[31:31]	1
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0xBB00188C	ISR_STAT_TRIGGER [1121].ISR_STAT_TRIGGER[1:1]	1
0xBB00188C	ISR_STAT_TRIGGER [1122].ISR_STAT_TRIGGER[2:2]	1
0xBB00188C	ISR_STAT_TRIGGER [1123].ISR_STAT_TRIGGER[3:3]	1
0xBB00188C	ISR_STAT_TRIGGER [1124].ISR_STAT_TRIGGER[4:4]	1
0xBB00188C	ISR_STAT_TRIGGER [1125].ISR_STAT_TRIGGER[5:5]	1

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0xBB00188C	ISR_STAT_TRIGGER [1127].ISR_STAT_TRIGGER[7:7]	1
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0xBB00188C	ISR_STAT_TRIGGER [1129].ISR_STAT_TRIGGER[9:9]	1
0xBB00188C	ISR_STAT_TRIGGER [1130].ISR_STAT_TRIGGER[10:10]	1
0xBB00188C	ISR_STAT_TRIGGER [1131].ISR_STAT_TRIGGER[11:11]	1
0xBB00188C	ISR_STAT_TRIGGER [1132].ISR_STAT_TRIGGER[12:12]	1
0xBB00188C	ISR_STAT_TRIGGER [1133].ISR_STAT_TRIGGER[13:13]	1
0xBB00188C	ISR_STAT_TRIGGER [1134].ISR_STAT_TRIGGER[14:14]	1
0xBB00188C	ISR_STAT_TRIGGER [1135].ISR_STAT_TRIGGER[15:15]	1
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0xBB00188C	ISR_STAT_TRIGGER [1137].ISR_STAT_TRIGGER[17:17]	1
0xBB00188C	ISR_STAT_TRIGGER [1138].ISR_STAT_TRIGGER[18:18]	1
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0xBB00188C	ISR_STAT_TRIGGER [1141].ISR_STAT_TRIGGER[21:21]	1
0xBB00188C	ISR_STAT_TRIGGER [1142].ISR_STAT_TRIGGER[22:22]	1
0xBB00188C	ISR_STAT_TRIGGER [1143].ISR_STAT_TRIGGER[23:23]	1
0xBB00188C	ISR_STAT_TRIGGER [1144].ISR_STAT_TRIGGER[24:24]	1
0xBB00188C	ISR_STAT_TRIGGER [1145].ISR_STAT_TRIGGER[25:25]	1
0xBB00188C	ISR_STAT_TRIGGER [1146].ISR_STAT_TRIGGER[26:26]	1
0xBB00188C	ISR_STAT_TRIGGER [1147].ISR_STAT_TRIGGER[27:27]	1
0xBB00188C	ISR_STAT_TRIGGER [1148].ISR_STAT_TRIGGER[28:28]	1
0xBB00188C	ISR_STAT_TRIGGER [1149].ISR_STAT_TRIGGER[29:29]	1
0xBB00188C	ISR_STAT_TRIGGER [1150].ISR_STAT_TRIGGER[30:30]	1
0xBB00188C	ISR_STAT_TRIGGER [1151].ISR_STAT_TRIGGER[31:31]	1
0xBB001890	ISR_STAT_TRIGGER [1152].ISR_STAT_TRIGGER[0:0]	1
0xBB001890	ISR_STAT_TRIGGER [1153].ISR_STAT_TRIGGER[1:1]	1
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0xBB001890	ISR_STAT_TRIGGER [1157].ISR_STAT_TRIGGER[5:5]	1
0xBB001890	ISR_STAT_TRIGGER [1158].ISR_STAT_TRIGGER[6:6]	1
0xBB001890	ISR_STAT_TRIGGER [1159].ISR_STAT_TRIGGER[7:7]	1
0xBB001890	ISR_STAT_TRIGGER [1160].ISR_STAT_TRIGGER[8:8]	1
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0xBB001890	ISR_STAT_TRIGGER [1166].ISR_STAT_TRIGGER[14:14]	1
0xBB001890	ISR_STAT_TRIGGER [1167].ISR_STAT_TRIGGER[15:15]	1
0xBB001890	ISR_STAT_TRIGGER [1168].ISR_STAT_TRIGGER[16:16]	1
0xBB001890	ISR_STAT_TRIGGER [1169].ISR_STAT_TRIGGER[17:17]	1
0xBB001890	ISR_STAT_TRIGGER [1170].ISR_STAT_TRIGGER[18:18]	1
0xBB001890	ISR_STAT_TRIGGER [1171].ISR_STAT_TRIGGER[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001890	ISR_STAT_TRIGGER [1174].ISR_STAT_TRIGGER[22:22]	1
0xBB001890	ISR_STAT_TRIGGER [1175].ISR_STAT_TRIGGER[23:23]	1
0xBB001890	ISR_STAT_TRIGGER [1176].ISR_STAT_TRIGGER[24:24]	1
0xBB001890	ISR_STAT_TRIGGER [1177].ISR_STAT_TRIGGER[25:25]	1
0xBB001890	ISR_STAT_TRIGGER [1178].ISR_STAT_TRIGGER[26:26]	1
0xBB001890	ISR_STAT_TRIGGER [1179].ISR_STAT_TRIGGER[27:27]	1
0xBB001890	ISR_STAT_TRIGGER [1180].ISR_STAT_TRIGGER[28:28]	1
0xBB001890	ISR_STAT_TRIGGER [1181].ISR_STAT_TRIGGER[29:29]	1
0xBB001890	ISR_STAT_TRIGGER [1182].ISR_STAT_TRIGGER[30:30]	1
0xBB001890	ISR_STAT_TRIGGER [1183].ISR_STAT_TRIGGER[31:31]	1
0xBB001894	ISR_STAT_TRIGGER [1184].ISR_STAT_TRIGGER[0:0]	1
0xBB001894	ISR_STAT_TRIGGER [1185].ISR_STAT_TRIGGER[1:1]	1
0xBB001894	ISR_STAT_TRIGGER [1186].ISR_STAT_TRIGGER[2:2]	1
0xBB001894	ISR_STAT_TRIGGER [1187].ISR_STAT_TRIGGER[3:3]	1
0xBB001894	ISR_STAT_TRIGGER [1188].ISR_STAT_TRIGGER[4:4]	1
0xBB001894	ISR_STAT_TRIGGER [1189].ISR_STAT_TRIGGER[5:5]	1
0xBB001894	ISR_STAT_TRIGGER [1190].ISR_STAT_TRIGGER[6:6]	1
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0xBB001894	ISR_STAT_TRIGGER [1200].ISR_STAT_TRIGGER[16:16]	1
0xBB001894	ISR_STAT_TRIGGER [1201].ISR_STAT_TRIGGER[17:17]	1
0xBB001894	ISR_STAT_TRIGGER [1202].ISR_STAT_TRIGGER[18:18]	1
0xBB001894	ISR_STAT_TRIGGER [1203].ISR_STAT_TRIGGER[19:19]	1
0xBB001894	ISR_STAT_TRIGGER [1204].ISR_STAT_TRIGGER[20:20]	1
0xBB001894	ISR_STAT_TRIGGER [1205].ISR_STAT_TRIGGER[21:21]	1
0xBB001894	ISR_STAT_TRIGGER [1206].ISR_STAT_TRIGGER[22:22]	1
0xBB001894	ISR_STAT_TRIGGER [1207].ISR_STAT_TRIGGER[23:23]	1
0xBB001894	ISR_STAT_TRIGGER [1208].ISR_STAT_TRIGGER[24:24]	1
0xBB001894	ISR_STAT_TRIGGER [1209].ISR_STAT_TRIGGER[25:25]	1
0xBB001894	ISR_STAT_TRIGGER [1210].ISR_STAT_TRIGGER[26:26]	1
0xBB001894	ISR_STAT_TRIGGER [1211].ISR_STAT_TRIGGER[27:27]	1
0xBB001894	ISR_STAT_TRIGGER [1212].ISR_STAT_TRIGGER[28:28]	1
0xBB001894	ISR_STAT_TRIGGER [1213].ISR_STAT_TRIGGER[29:29]	1
0xBB001894	ISR_STAT_TRIGGER [1214].ISR_STAT_TRIGGER[30:30]	1
0xBB001894	ISR_STAT_TRIGGER [1215].ISR_STAT_TRIGGER[31:31]	1
0xBB001898	ISR_STAT_TRIGGER [1216].ISR_STAT_TRIGGER[0:0]	1
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0xBB001898	ISR_STAT_TRIGGER [1224].ISR_STAT_TRIGGER[8:8]	1
0xBB001898	ISR_STAT_TRIGGER [1225].ISR_STAT_TRIGGER[9:9]	1
0xBB001898	ISR_STAT_TRIGGER [1226].ISR_STAT_TRIGGER[10:10]	1
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0xBB001898	ISR_STAT_TRIGGER [1232].ISR_STAT_TRIGGER[16:16]	1
0xBB001898	ISR_STAT_TRIGGER [1233].ISR_STAT_TRIGGER[17:17]	1
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0xBB001898	ISR_STAT_TRIGGER [1236].ISR_STAT_TRIGGER[20:20]	1
0xBB001898	ISR_STAT_TRIGGER [1237].ISR_STAT_TRIGGER[21:21]	1
0xBB001898	ISR_STAT_TRIGGER [1238].ISR_STAT_TRIGGER[22:22]	1
0xBB001898	ISR_STAT_TRIGGER [1239].ISR_STAT_TRIGGER[23:23]	1
0xBB001898	ISR_STAT_TRIGGER [1240].ISR_STAT_TRIGGER[24:24]	1
0xBB001898	ISR_STAT_TRIGGER [1241].ISR_STAT_TRIGGER[25:25]	1
0xBB001898	ISR_STAT_TRIGGER [1242].ISR_STAT_TRIGGER[26:26]	1
0xBB001898	ISR_STAT_TRIGGER [1243].ISR_STAT_TRIGGER[27:27]	1
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0xBB00189C	ISR_STAT_TRIGGER [1257].ISR_STAT_TRIGGER[9:9]	1
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0xBB00189C	ISR_STAT_TRIGGER [1259].ISR_STAT_TRIGGER[11:11]	1
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0xBB00189C	ISR_STAT_TRIGGER [1262].ISR_STAT_TRIGGER[14:14]	1
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0xBB00189C	ISR_STAT_TRIGGER [1268].ISR_STAT_TRIGGER[20:20]	1
0xBB00189C	ISR_STAT_TRIGGER [1269].ISR_STAT_TRIGGER[21:21]	1
0xBB00189C	ISR_STAT_TRIGGER [1270].ISR_STAT_TRIGGER[22:22]	1
0xBB00189C	ISR_STAT_TRIGGER [1271].ISR_STAT_TRIGGER[23:23]	1
0xBB00189C	ISR_STAT_TRIGGER [1272].ISR_STAT_TRIGGER[24:24]	1
0xBB00189C	ISR_STAT_TRIGGER [1273].ISR_STAT_TRIGGER[25:25]	1
0xBB00189C	ISR_STAT_TRIGGER [1274].ISR_STAT_TRIGGER[26:26]	1
0xBB00189C	ISR_STAT_TRIGGER [1275].ISR_STAT_TRIGGER[27:27]	1
0xBB00189C	ISR_STAT_TRIGGER [1276].ISR_STAT_TRIGGER[28:28]	1
0xBB00189C	ISR_STAT_TRIGGER [1277].ISR_STAT_TRIGGER[29:29]	1
0xBB00189C	ISR_STAT_TRIGGER [1278].ISR_STAT_TRIGGER[30:30]	1
0xBB00189C	ISR_STAT_TRIGGER [1279].ISR_STAT_TRIGGER[31:31]	1
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0xBB0018A0	ISR_STAT_TRIGGER [1281].ISR_STAT_TRIGGER[1:1]	1
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0xBB0018A0	ISR_STAT_TRIGGER [1283].ISR_STAT_TRIGGER[3:3]	1
0xBB0018A0	ISR_STAT_TRIGGER [1284].ISR_STAT_TRIGGER[4:4]	1
0xBB0018A0	ISR_STAT_TRIGGER [1285].ISR_STAT_TRIGGER[5:5]	1
0xBB0018A0	ISR_STAT_TRIGGER [1286].ISR_STAT_TRIGGER[6:6]	1
0xBB0018A0	ISR_STAT_TRIGGER [1287].ISR_STAT_TRIGGER[7:7]	1
0xBB0018A0	ISR_STAT_TRIGGER [1288].ISR_STAT_TRIGGER[8:8]	1
0xBB0018A0	ISR_STAT_TRIGGER [1289].ISR_STAT_TRIGGER[9:9]	1
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0xBB0018A0	ISR_STAT_TRIGGER [1293].ISR_STAT_TRIGGER[13:13]	1
0xBB0018A0	ISR_STAT_TRIGGER [1294].ISR_STAT_TRIGGER[14:14]	1
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0xBB0018A0	ISR_STAT_TRIGGER [1305].ISR_STAT_TRIGGER[25:25]	1
0xBB0018A0	ISR_STAT_TRIGGER [1306].ISR_STAT_TRIGGER[26:26]	1
0xBB0018A0	ISR_STAT_TRIGGER [1307].ISR_STAT_TRIGGER[27:27]	1
0xBB0018A0	ISR_STAT_TRIGGER [1308].ISR_STAT_TRIGGER[28:28]	1
0xBB0018A0	ISR_STAT_TRIGGER [1309].ISR_STAT_TRIGGER[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018A4	ISR_STAT_TRIGGER [1313].ISR_STAT_TRIGGER[1:1]	1
0xBB0018A4	ISR_STAT_TRIGGER [1314].ISR_STAT_TRIGGER[2:2]	1
0xBB0018A4	ISR_STAT_TRIGGER [1315].ISR_STAT_TRIGGER[3:3]	1
0xBB0018A4	ISR_STAT_TRIGGER [1316].ISR_STAT_TRIGGER[4:4]	1
0xBB0018A4	ISR_STAT_TRIGGER [1317].ISR_STAT_TRIGGER[5:5]	1
0xBB0018A4	ISR_STAT_TRIGGER [1318].ISR_STAT_TRIGGER[6:6]	1
0xBB0018A4	ISR_STAT_TRIGGER [1319].ISR_STAT_TRIGGER[7:7]	1
0xBB0018A4	ISR_STAT_TRIGGER [1320].ISR_STAT_TRIGGER[8:8]	1
0xBB0018A4	ISR_STAT_TRIGGER [1321].ISR_STAT_TRIGGER[9:9]	1
0xBB0018A4	ISR_STAT_TRIGGER [1322].ISR_STAT_TRIGGER[10:10]	1
0xBB0018A4	ISR_STAT_TRIGGER [1323].ISR_STAT_TRIGGER[11:11]	1
0xBB0018A4	ISR_STAT_TRIGGER [1324].ISR_STAT_TRIGGER[12:12]	1
0xBB0018A4	ISR_STAT_TRIGGER [1325].ISR_STAT_TRIGGER[13:13]	1
0xBB0018A4	ISR_STAT_TRIGGER [1326].ISR_STAT_TRIGGER[14:14]	1
0xBB0018A4	ISR_STAT_TRIGGER [1327].ISR_STAT_TRIGGER[15:15]	1
0xBB0018A4	ISR_STAT_TRIGGER [1328].ISR_STAT_TRIGGER[16:16]	1
0xBB0018A4	ISR_STAT_TRIGGER [1329].ISR_STAT_TRIGGER[17:17]	1
0xBB0018A4	ISR_STAT_TRIGGER [1330].ISR_STAT_TRIGGER[18:18]	1
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0xBB0018A4	ISR_STAT_TRIGGER [1339].ISR_STAT_TRIGGER[27:27]	1
0xBB0018A4	ISR_STAT_TRIGGER [1340].ISR_STAT_TRIGGER[28:28]	1
0xBB0018A4	ISR_STAT_TRIGGER [1341].ISR_STAT_TRIGGER[29:29]	1
0xBB0018A4	ISR_STAT_TRIGGER [1342].ISR_STAT_TRIGGER[30:30]	1
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0xBB0018A8	ISR_STAT_TRIGGER [1345].ISR_STAT_TRIGGER[1:1]	1
0xBB0018A8	ISR_STAT_TRIGGER [1346].ISR_STAT_TRIGGER[2:2]	1
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0xBB0018A8	ISR_STAT_TRIGGER [1350].ISR_STAT_TRIGGER[6:6]	1
0xBB0018A8	ISR_STAT_TRIGGER [1351].ISR_STAT_TRIGGER[7:7]	1
0xBB0018A8	ISR_STAT_TRIGGER [1352].ISR_STAT_TRIGGER[8:8]	1
0xBB0018A8	ISR_STAT_TRIGGER [1353].ISR_STAT_TRIGGER[9:9]	1
0xBB0018A8	ISR_STAT_TRIGGER [1354].ISR_STAT_TRIGGER[10:10]	1
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0xBB0018A8	ISR_STAT_TRIGGER [1359].ISR_STAT_TRIGGER[15:15]	1
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0xBB0018A8	ISR_STAT_TRIGGER [1361].ISR_STAT_TRIGGER[17:17]	1
0xBB0018A8	ISR_STAT_TRIGGER [1362].ISR_STAT_TRIGGER[18:18]	1
0xBB0018A8	ISR_STAT_TRIGGER [1363].ISR_STAT_TRIGGER[19:19]	1
0xBB0018A8	ISR_STAT_TRIGGER [1364].ISR_STAT_TRIGGER[20:20]	1
0xBB0018A8	ISR_STAT_TRIGGER [1365].ISR_STAT_TRIGGER[21:21]	1
0xBB0018A8	ISR_STAT_TRIGGER [1366].ISR_STAT_TRIGGER[22:22]	1
0xBB0018A8	ISR_STAT_TRIGGER [1367].ISR_STAT_TRIGGER[23:23]	1
0xBB0018A8	ISR_STAT_TRIGGER [1368].ISR_STAT_TRIGGER[24:24]	1
0xBB0018A8	ISR_STAT_TRIGGER [1369].ISR_STAT_TRIGGER[25:25]	1
0xBB0018A8	ISR_STAT_TRIGGER [1370].ISR_STAT_TRIGGER[26:26]	1
0xBB0018A8	ISR_STAT_TRIGGER [1371].ISR_STAT_TRIGGER[27:27]	1
0xBB0018A8	ISR_STAT_TRIGGER [1372].ISR_STAT_TRIGGER[28:28]	1
0xBB0018A8	ISR_STAT_TRIGGER [1373].ISR_STAT_TRIGGER[29:29]	1
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0xBB0018A8	ISR_STAT_TRIGGER [1375].ISR_STAT_TRIGGER[31:31]	1
0xBB0018AC	ISR_STAT_TRIGGER [1376].ISR_STAT_TRIGGER[0:0]	1
0xBB0018AC	ISR_STAT_TRIGGER [1377].ISR_STAT_TRIGGER[1:1]	1
0xBB0018AC	ISR_STAT_TRIGGER [1378].ISR_STAT_TRIGGER[2:2]	1
0xBB0018AC	ISR_STAT_TRIGGER [1379].ISR_STAT_TRIGGER[3:3]	1
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0xBB0018AC	ISR_STAT_TRIGGER [1390].ISR_STAT_TRIGGER[14:14]	1
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0xBB0018AC	ISR_STAT_TRIGGER [1395].ISR_STAT_TRIGGER[19:19]	1
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0xBB0018AC	ISR_STAT_TRIGGER [1397].ISR_STAT_TRIGGER[21:21]	1
0xBB0018AC	ISR_STAT_TRIGGER [1398].ISR_STAT_TRIGGER[22:22]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018AC	ISR_STAT_TRIGGER [1403].ISR_STAT_TRIGGER[27:27]	1
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0xBB0018AC	ISR_STAT_TRIGGER [1405].ISR_STAT_TRIGGER[29:29]	1
0xBB0018AC	ISR_STAT_TRIGGER [1406].ISR_STAT_TRIGGER[30:30]	1
0xBB0018AC	ISR_STAT_TRIGGER [1407].ISR_STAT_TRIGGER[31:31]	1
0xBB0018B0	ISR_STAT_TRIGGER [1408].ISR_STAT_TRIGGER[0:0]	1
0xBB0018B0	ISR_STAT_TRIGGER [1409].ISR_STAT_TRIGGER[1:1]	1
0xBB0018B0	ISR_STAT_TRIGGER [1410].ISR_STAT_TRIGGER[2:2]	1
0xBB0018B0	ISR_STAT_TRIGGER [1411].ISR_STAT_TRIGGER[3:3]	1
0xBB0018B0	ISR_STAT_TRIGGER [1412].ISR_STAT_TRIGGER[4:4]	1
0xBB0018B0	ISR_STAT_TRIGGER [1413].ISR_STAT_TRIGGER[5:5]	1
0xBB0018B0	ISR_STAT_TRIGGER [1414].ISR_STAT_TRIGGER[6:6]	1
0xBB0018B0	ISR_STAT_TRIGGER [1415].ISR_STAT_TRIGGER[7:7]	1
0xBB0018B0	ISR_STAT_TRIGGER [1416].ISR_STAT_TRIGGER[8:8]	1
0xBB0018B0	ISR_STAT_TRIGGER [1417].ISR_STAT_TRIGGER[9:9]	1
0xBB0018B0	ISR_STAT_TRIGGER [1418].ISR_STAT_TRIGGER[10:10]	1
0xBB0018B0	ISR_STAT_TRIGGER [1419].ISR_STAT_TRIGGER[11:11]	1
0xBB0018B0	ISR_STAT_TRIGGER [1420].ISR_STAT_TRIGGER[12:12]	1
0xBB0018B0	ISR_STAT_TRIGGER [1421].ISR_STAT_TRIGGER[13:13]	1
0xBB0018B0	ISR_STAT_TRIGGER [1422].ISR_STAT_TRIGGER[14:14]	1
0xBB0018B0	ISR_STAT_TRIGGER [1423].ISR_STAT_TRIGGER[15:15]	1
0xBB0018B0	ISR_STAT_TRIGGER [1424].ISR_STAT_TRIGGER[16:16]	1
0xBB0018B0	ISR_STAT_TRIGGER [1425].ISR_STAT_TRIGGER[17:17]	1
0xBB0018B0	ISR_STAT_TRIGGER [1426].ISR_STAT_TRIGGER[18:18]	1
0xBB0018B0	ISR_STAT_TRIGGER [1427].ISR_STAT_TRIGGER[19:19]	1
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0xBB0018B0	ISR_STAT_TRIGGER [1436].ISR_STAT_TRIGGER[28:28]	1
0xBB0018B0	ISR_STAT_TRIGGER [1437].ISR_STAT_TRIGGER[29:29]	1
0xBB0018B0	ISR_STAT_TRIGGER [1438].ISR_STAT_TRIGGER[30:30]	1
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0xBB0018B4	ISR_STAT_TRIGGER [1440].ISR_STAT_TRIGGER[0:0]	1
0xBB0018B4	ISR_STAT_TRIGGER [1441].ISR_STAT_TRIGGER[1:1]	1
0xBB0018B4	ISR_STAT_TRIGGER [1442].ISR_STAT_TRIGGER[2:2]	1
0xBB0018B4	ISR_STAT_TRIGGER [1443].ISR_STAT_TRIGGER[3:3]	1
0xBB0018B4	ISR_STAT_TRIGGER [1444].ISR_STAT_TRIGGER[4:4]	1
0xBB0018B4	ISR_STAT_TRIGGER [1445].ISR_STAT_TRIGGER[5:5]	1
0xBB0018B4	ISR_STAT_TRIGGER [1446].ISR_STAT_TRIGGER[6:6]	1
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0xBB0018B4	ISR_STAT_TRIGGER [1452].ISR_STAT_TRIGGER[12:12]	1
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0xBB0018B4	ISR_STAT_TRIGGER [1454].ISR_STAT_TRIGGER[14:14]	1
0xBB0018B4	ISR_STAT_TRIGGER [1455].ISR_STAT_TRIGGER[15:15]	1
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0xBB0018B4	ISR_STAT_TRIGGER [1457].ISR_STAT_TRIGGER[17:17]	1
0xBB0018B4	ISR_STAT_TRIGGER [1458].ISR_STAT_TRIGGER[18:18]	1
0xBB0018B4	ISR_STAT_TRIGGER [1459].ISR_STAT_TRIGGER[19:19]	1
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0xBB0018B4	ISR_STAT_TRIGGER [1462].ISR_STAT_TRIGGER[22:22]	1
0xBB0018B4	ISR_STAT_TRIGGER [1463].ISR_STAT_TRIGGER[23:23]	1
0xBB0018B4	ISR_STAT_TRIGGER [1464].ISR_STAT_TRIGGER[24:24]	1
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0xBB0018B8	ISR_STAT_TRIGGER [1475].ISR_STAT_TRIGGER[3:3]	1
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0xBB0018B8	ISR_STAT_TRIGGER [1483].ISR_STAT_TRIGGER[11:11]	1
0xBB0018B8	ISR_STAT_TRIGGER [1484].ISR_STAT_TRIGGER[12:12]	1
0xBB0018B8	ISR_STAT_TRIGGER [1485].ISR_STAT_TRIGGER[13:13]	1
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0xBB0018B8	ISR_STAT_TRIGGER [1490].ISR_STAT_TRIGGER[18:18]	1
0xBB0018B8	ISR_STAT_TRIGGER [1491].ISR_STAT_TRIGGER[19:19]	1
0xBB0018B8	ISR_STAT_TRIGGER [1492].ISR_STAT_TRIGGER[20:20]	1
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0xBB0018B8	ISR_STAT_TRIGGER [1496].ISR_STAT_TRIGGER[24:24]	1
0xBB0018B8	ISR_STAT_TRIGGER [1497].ISR_STAT_TRIGGER[25:25]	1
0xBB0018B8	ISR_STAT_TRIGGER [1498].ISR_STAT_TRIGGER[26:26]	1
0xBB0018B8	ISR_STAT_TRIGGER [1499].ISR_STAT_TRIGGER[27:27]	1
0xBB0018B8	ISR_STAT_TRIGGER [1500].ISR_STAT_TRIGGER[28:28]	1
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0xBB0018B8	ISR_STAT_TRIGGER [1503].ISR_STAT_TRIGGER[31:31]	1
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0xBB0018BC	ISR_STAT_TRIGGER [1507].ISR_STAT_TRIGGER[3:3]	1
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0xBB0018BC	ISR_STAT_TRIGGER [1511].ISR_STAT_TRIGGER[7:7]	1
0xBB0018BC	ISR_STAT_TRIGGER [1512].ISR_STAT_TRIGGER[8:8]	1
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0xBB0018BC	ISR_STAT_TRIGGER [1528].ISR_STAT_TRIGGER[24:24]	1
0xBB0018BC	ISR_STAT_TRIGGER [1529].ISR_STAT_TRIGGER[25:25]	1
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0xBB0018BC	ISR_STAT_TRIGGER [1531].ISR_STAT_TRIGGER[27:27]	1
0xBB0018BC	ISR_STAT_TRIGGER [1532].ISR_STAT_TRIGGER[28:28]	1
0xBB0018BC	ISR_STAT_TRIGGER [1533].ISR_STAT_TRIGGER[29:29]	1
0xBB0018BC	ISR_STAT_TRIGGER [1534].ISR_STAT_TRIGGER[30:30]	1
0xBB0018BC	ISR_STAT_TRIGGER [1535].ISR_STAT_TRIGGER[31:31]	1
0xBB0018C0	ISR_STAT_TRIGGER [1536].ISR_STAT_TRIGGER[0:0]	1
0xBB0018C0	ISR_STAT_TRIGGER [1537].ISR_STAT_TRIGGER[1:1]	1
0xBB0018C0	ISR_STAT_TRIGGER [1538].ISR_STAT_TRIGGER[2:2]	1
0xBB0018C0	ISR_STAT_TRIGGER [1539].ISR_STAT_TRIGGER[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0018C0	ISR_STAT_TRIGGER [1540].ISR_STAT_TRIGGER[4:4]	1
0xBB0018C0	ISR_STAT_TRIGGER [1541].ISR_STAT_TRIGGER[5:5]	1
0xBB0018C0	ISR_STAT_TRIGGER [1542].ISR_STAT_TRIGGER[6:6]	1
0xBB0018C0	ISR_STAT_TRIGGER [1543].ISR_STAT_TRIGGER[7:7]	1
0xBB0018C0	ISR_STAT_TRIGGER [1544].ISR_STAT_TRIGGER[8:8]	1
0xBB0018C0	ISR_STAT_TRIGGER [1545].ISR_STAT_TRIGGER[9:9]	1
0xBB0018C0	ISR_STAT_TRIGGER [1546].ISR_STAT_TRIGGER[10:10]	1
0xBB0018C0	ISR_STAT_TRIGGER [1547].ISR_STAT_TRIGGER[11:11]	1
0xBB0018C0	ISR_STAT_TRIGGER [1548].ISR_STAT_TRIGGER[12:12]	1
0xBB0018C0	ISR_STAT_TRIGGER [1549].ISR_STAT_TRIGGER[13:13]	1
0xBB0018C0	ISR_STAT_TRIGGER [1550].ISR_STAT_TRIGGER[14:14]	1
0xBB0018C0	ISR_STAT_TRIGGER [1551].ISR_STAT_TRIGGER[15:15]	1
0xBB0018C0	ISR_STAT_TRIGGER [1552].ISR_STAT_TRIGGER[16:16]	1
0xBB0018C0	ISR_STAT_TRIGGER [1553].ISR_STAT_TRIGGER[17:17]	1
0xBB0018C0	ISR_STAT_TRIGGER [1554].ISR_STAT_TRIGGER[18:18]	1
0xBB0018C0	ISR_STAT_TRIGGER [1555].ISR_STAT_TRIGGER[19:19]	1
0xBB0018C0	ISR_STAT_TRIGGER [1556].ISR_STAT_TRIGGER[20:20]	1
0xBB0018C0	ISR_STAT_TRIGGER [1557].ISR_STAT_TRIGGER[21:21]	1
0xBB0018C0	ISR_STAT_TRIGGER [1558].ISR_STAT_TRIGGER[22:22]	1
0xBB0018C0	ISR_STAT_TRIGGER [1559].ISR_STAT_TRIGGER[23:23]	1
0xBB0018C0	ISR_STAT_TRIGGER [1560].ISR_STAT_TRIGGER[24:24]	1
0xBB0018C0	ISR_STAT_TRIGGER [1561].ISR_STAT_TRIGGER[25:25]	1
0xBB0018C0	ISR_STAT_TRIGGER [1562].ISR_STAT_TRIGGER[26:26]	1
0xBB0018C0	ISR_STAT_TRIGGER [1563].ISR_STAT_TRIGGER[27:27]	1
0xBB0018C0	ISR_STAT_TRIGGER [1564].ISR_STAT_TRIGGER[28:28]	1
0xBB0018C0	ISR_STAT_TRIGGER [1565].ISR_STAT_TRIGGER[29:29]	1
0xBB0018C0	ISR_STAT_TRIGGER [1566].ISR_STAT_TRIGGER[30:30]	1
0xBB0018C0	ISR_STAT_TRIGGER [1567].ISR_STAT_TRIGGER[31:31]	1
0xBB0018C4	ISR_STAT_TRIGGER [1568].ISR_STAT_TRIGGER[0:0]	1
0xBB0018C4	ISR_STAT_TRIGGER [1569].ISR_STAT_TRIGGER[1:1]	1
0xBB0018C4	ISR_STAT_TRIGGER [1570].ISR_STAT_TRIGGER[2:2]	1
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0xBB0018C4	ISR_STAT_TRIGGER [1572].ISR_STAT_TRIGGER[4:4]	1
0xBB0018C4	ISR_STAT_TRIGGER [1573].ISR_STAT_TRIGGER[5:5]	1
0xBB0018C4	ISR_STAT_TRIGGER [1574].ISR_STAT_TRIGGER[6:6]	1
0xBB0018C4	ISR_STAT_TRIGGER [1575].ISR_STAT_TRIGGER[7:7]	1
0xBB0018C4	ISR_STAT_TRIGGER [1576].ISR_STAT_TRIGGER[8:8]	1
0xBB0018C4	ISR_STAT_TRIGGER [1577].ISR_STAT_TRIGGER[9:9]	1
0xBB0018C4	ISR_STAT_TRIGGER [1578].ISR_STAT_TRIGGER[10:10]	1
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0xBB0018C4	ISR_STAT_TRIGGER [1580].ISR_STAT_TRIGGER[12:12]	1
0xBB0018C4	ISR_STAT_TRIGGER [1581].ISR_STAT_TRIGGER[13:13]	1
0xBB0018C4	ISR_STAT_TRIGGER [1582].ISR_STAT_TRIGGER[14:14]	1
0xBB0018C4	ISR_STAT_TRIGGER [1583].ISR_STAT_TRIGGER[15:15]	1
0xBB0018C4	ISR_STAT_TRIGGER [1584].ISR_STAT_TRIGGER[16:16]	1
0xBB0018C4	ISR_STAT_TRIGGER [1585].ISR_STAT_TRIGGER[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018C4	ISR_STAT_TRIGGER [1587].ISR_STAT_TRIGGER[19:19]	1
0xBB0018C4	ISR_STAT_TRIGGER [1588].ISR_STAT_TRIGGER[20:20]	1
0xBB0018C4	ISR_STAT_TRIGGER [1589].ISR_STAT_TRIGGER[21:21]	1
0xBB0018C4	ISR_STAT_TRIGGER [1590].ISR_STAT_TRIGGER[22:22]	1
0xBB0018C4	ISR_STAT_TRIGGER [1591].ISR_STAT_TRIGGER[23:23]	1
0xBB0018C4	ISR_STAT_TRIGGER [1592].ISR_STAT_TRIGGER[24:24]	1
0xBB0018C4	ISR_STAT_TRIGGER [1593].ISR_STAT_TRIGGER[25:25]	1
0xBB0018C4	ISR_STAT_TRIGGER [1594].ISR_STAT_TRIGGER[26:26]	1
0xBB0018C4	ISR_STAT_TRIGGER [1595].ISR_STAT_TRIGGER[27:27]	1
0xBB0018C4	ISR_STAT_TRIGGER [1596].ISR_STAT_TRIGGER[28:28]	1
0xBB0018C4	ISR_STAT_TRIGGER [1597].ISR_STAT_TRIGGER[29:29]	1
0xBB0018C4	ISR_STAT_TRIGGER [1598].ISR_STAT_TRIGGER[30:30]	1
0xBB0018C4	ISR_STAT_TRIGGER [1599].ISR_STAT_TRIGGER[31:31]	1
0xBB0018C8	ISR_STAT_TRIGGER [1600].ISR_STAT_TRIGGER[0:0]	1
0xBB0018C8	ISR_STAT_TRIGGER [1601].ISR_STAT_TRIGGER[1:1]	1
0xBB0018C8	ISR_STAT_TRIGGER [1602].ISR_STAT_TRIGGER[2:2]	1
0xBB0018C8	ISR_STAT_TRIGGER [1603].ISR_STAT_TRIGGER[3:3]	1
0xBB0018C8	ISR_STAT_TRIGGER [1604].ISR_STAT_TRIGGER[4:4]	1
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0xBB0018C8	ISR_STAT_TRIGGER [1608].ISR_STAT_TRIGGER[8:8]	1
0xBB0018C8	ISR_STAT_TRIGGER [1609].ISR_STAT_TRIGGER[9:9]	1
0xBB0018C8	ISR_STAT_TRIGGER [1610].ISR_STAT_TRIGGER[10:10]	1
0xBB0018C8	ISR_STAT_TRIGGER [1611].ISR_STAT_TRIGGER[11:11]	1
0xBB0018C8	ISR_STAT_TRIGGER [1612].ISR_STAT_TRIGGER[12:12]	1
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0xBB0018C8	ISR_STAT_TRIGGER [1614].ISR_STAT_TRIGGER[14:14]	1
0xBB0018C8	ISR_STAT_TRIGGER [1615].ISR_STAT_TRIGGER[15:15]	1
0xBB0018C8	ISR_STAT_TRIGGER [1616].ISR_STAT_TRIGGER[16:16]	1
0xBB0018C8	ISR_STAT_TRIGGER [1617].ISR_STAT_TRIGGER[17:17]	1
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0xBB0018C8	ISR_STAT_TRIGGER [1619].ISR_STAT_TRIGGER[19:19]	1
0xBB0018C8	ISR_STAT_TRIGGER [1620].ISR_STAT_TRIGGER[20:20]	1
0xBB0018C8	ISR_STAT_TRIGGER [1621].ISR_STAT_TRIGGER[21:21]	1
0xBB0018C8	ISR_STAT_TRIGGER [1622].ISR_STAT_TRIGGER[22:22]	1
0xBB0018C8	ISR_STAT_TRIGGER [1623].ISR_STAT_TRIGGER[23:23]	1
0xBB0018C8	ISR_STAT_TRIGGER [1624].ISR_STAT_TRIGGER[24:24]	1
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0xBB0018C8	ISR_STAT_TRIGGER [1626].ISR_STAT_TRIGGER[26:26]	1
0xBB0018C8	ISR_STAT_TRIGGER [1627].ISR_STAT_TRIGGER[27:27]	1
0xBB0018C8	ISR_STAT_TRIGGER [1628].ISR_STAT_TRIGGER[28:28]	1
0xBB0018C8	ISR_STAT_TRIGGER [1629].ISR_STAT_TRIGGER[29:29]	1
0xBB0018C8	ISR_STAT_TRIGGER [1630].ISR_STAT_TRIGGER[30:30]	1
0xBB0018C8	ISR_STAT_TRIGGER [1631].ISR_STAT_TRIGGER[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018CC	ISR_STAT_TRIGGER [1633].ISR_STAT_TRIGGER[1:1]	1
0xBB0018CC	ISR_STAT_TRIGGER [1634].ISR_STAT_TRIGGER[2:2]	1
0xBB0018CC	ISR_STAT_TRIGGER [1635].ISR_STAT_TRIGGER[3:3]	1
0xBB0018CC	ISR_STAT_TRIGGER [1636].ISR_STAT_TRIGGER[4:4]	1
0xBB0018CC	ISR_STAT_TRIGGER [1637].ISR_STAT_TRIGGER[5:5]	1
0xBB0018CC	ISR_STAT_TRIGGER [1638].ISR_STAT_TRIGGER[6:6]	1
0xBB0018CC	ISR_STAT_TRIGGER [1639].ISR_STAT_TRIGGER[7:7]	1
0xBB0018CC	ISR_STAT_TRIGGER [1640].ISR_STAT_TRIGGER[8:8]	1
0xBB0018CC	ISR_STAT_TRIGGER [1641].ISR_STAT_TRIGGER[9:9]	1
0xBB0018CC	ISR_STAT_TRIGGER [1642].ISR_STAT_TRIGGER[10:10]	1
0xBB0018CC	ISR_STAT_TRIGGER [1643].ISR_STAT_TRIGGER[11:11]	1
0xBB0018CC	ISR_STAT_TRIGGER [1644].ISR_STAT_TRIGGER[12:12]	1
0xBB0018CC	ISR_STAT_TRIGGER [1645].ISR_STAT_TRIGGER[13:13]	1
0xBB0018CC	ISR_STAT_TRIGGER [1646].ISR_STAT_TRIGGER[14:14]	1
0xBB0018CC	ISR_STAT_TRIGGER [1647].ISR_STAT_TRIGGER[15:15]	1
0xBB0018CC	ISR_STAT_TRIGGER [1648].ISR_STAT_TRIGGER[16:16]	1
0xBB0018CC	ISR_STAT_TRIGGER [1649].ISR_STAT_TRIGGER[17:17]	1
0xBB0018CC	ISR_STAT_TRIGGER [1650].ISR_STAT_TRIGGER[18:18]	1
0xBB0018CC	ISR_STAT_TRIGGER [1651].ISR_STAT_TRIGGER[19:19]	1
0xBB0018CC	ISR_STAT_TRIGGER [1652].ISR_STAT_TRIGGER[20:20]	1
0xBB0018CC	ISR_STAT_TRIGGER [1653].ISR_STAT_TRIGGER[21:21]	1
0xBB0018CC	ISR_STAT_TRIGGER [1654].ISR_STAT_TRIGGER[22:22]	1
0xBB0018CC	ISR_STAT_TRIGGER [1655].ISR_STAT_TRIGGER[23:23]	1
0xBB0018CC	ISR_STAT_TRIGGER [1656].ISR_STAT_TRIGGER[24:24]	1
0xBB0018CC	ISR_STAT_TRIGGER [1657].ISR_STAT_TRIGGER[25:25]	1
0xBB0018CC	ISR_STAT_TRIGGER [1658].ISR_STAT_TRIGGER[26:26]	1
0xBB0018CC	ISR_STAT_TRIGGER [1659].ISR_STAT_TRIGGER[27:27]	1
0xBB0018CC	ISR_STAT_TRIGGER [1660].ISR_STAT_TRIGGER[28:28]	1
0xBB0018CC	ISR_STAT_TRIGGER [1661].ISR_STAT_TRIGGER[29:29]	1
0xBB0018CC	ISR_STAT_TRIGGER [1662].ISR_STAT_TRIGGER[30:30]	1
0xBB0018CC	ISR_STAT_TRIGGER [1663].ISR_STAT_TRIGGER[31:31]	1
0xBB0018D0	ISR_STAT_TRIGGER [1664].ISR_STAT_TRIGGER[0:0]	1
0xBB0018D0	ISR_STAT_TRIGGER [1665].ISR_STAT_TRIGGER[1:1]	1
0xBB0018D0	ISR_STAT_TRIGGER [1666].ISR_STAT_TRIGGER[2:2]	1
0xBB0018D0	ISR_STAT_TRIGGER [1667].ISR_STAT_TRIGGER[3:3]	1
0xBB0018D0	ISR_STAT_TRIGGER [1668].ISR_STAT_TRIGGER[4:4]	1
0xBB0018D0	ISR_STAT_TRIGGER [1669].ISR_STAT_TRIGGER[5:5]	1
0xBB0018D0	ISR_STAT_TRIGGER [1670].ISR_STAT_TRIGGER[6:6]	1
0xBB0018D0	ISR_STAT_TRIGGER [1671].ISR_STAT_TRIGGER[7:7]	1
0xBB0018D0	ISR_STAT_TRIGGER [1672].ISR_STAT_TRIGGER[8:8]	1
0xBB0018D0	ISR_STAT_TRIGGER [1673].ISR_STAT_TRIGGER[9:9]	1
0xBB0018D0	ISR_STAT_TRIGGER [1674].ISR_STAT_TRIGGER[10:10]	1
0xBB0018D0	ISR_STAT_TRIGGER [1675].ISR_STAT_TRIGGER[11:11]	1
0xBB0018D0	ISR_STAT_TRIGGER [1676].ISR_STAT_TRIGGER[12:12]	1
0xBB0018D0	ISR_STAT_TRIGGER [1677].ISR_STAT_TRIGGER[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018D0	ISR_STAT_TRIGGER [1680].ISR_STAT_TRIGGER[16:16]	1
0xBB0018D0	ISR_STAT_TRIGGER [1681].ISR_STAT_TRIGGER[17:17]	1
0xBB0018D0	ISR_STAT_TRIGGER [1682].ISR_STAT_TRIGGER[18:18]	1
0xBB0018D0	ISR_STAT_TRIGGER [1683].ISR_STAT_TRIGGER[19:19]	1
0xBB0018D0	ISR_STAT_TRIGGER [1684].ISR_STAT_TRIGGER[20:20]	1
0xBB0018D0	ISR_STAT_TRIGGER [1685].ISR_STAT_TRIGGER[21:21]	1
0xBB0018D0	ISR_STAT_TRIGGER [1686].ISR_STAT_TRIGGER[22:22]	1
0xBB0018D0	ISR_STAT_TRIGGER [1687].ISR_STAT_TRIGGER[23:23]	1
0xBB0018D0	ISR_STAT_TRIGGER [1688].ISR_STAT_TRIGGER[24:24]	1
0xBB0018D0	ISR_STAT_TRIGGER [1689].ISR_STAT_TRIGGER[25:25]	1
0xBB0018D0	ISR_STAT_TRIGGER [1690].ISR_STAT_TRIGGER[26:26]	1
0xBB0018D0	ISR_STAT_TRIGGER [1691].ISR_STAT_TRIGGER[27:27]	1
0xBB0018D0	ISR_STAT_TRIGGER [1692].ISR_STAT_TRIGGER[28:28]	1
0xBB0018D0	ISR_STAT_TRIGGER [1693].ISR_STAT_TRIGGER[29:29]	1
0xBB0018D0	ISR_STAT_TRIGGER [1694].ISR_STAT_TRIGGER[30:30]	1
0xBB0018D0	ISR_STAT_TRIGGER [1695].ISR_STAT_TRIGGER[31:31]	1
0xBB0018D4	ISR_STAT_TRIGGER [1696].ISR_STAT_TRIGGER[0:0]	1
0xBB0018D4	ISR_STAT_TRIGGER [1697].ISR_STAT_TRIGGER[1:1]	1
0xBB0018D4	ISR_STAT_TRIGGER [1698].ISR_STAT_TRIGGER[2:2]	1
0xBB0018D4	ISR_STAT_TRIGGER [1699].ISR_STAT_TRIGGER[3:3]	1
0xBB0018D4	ISR_STAT_TRIGGER [1700].ISR_STAT_TRIGGER[4:4]	1
0xBB0018D4	ISR_STAT_TRIGGER [1701].ISR_STAT_TRIGGER[5:5]	1
0xBB0018D4	ISR_STAT_TRIGGER [1702].ISR_STAT_TRIGGER[6:6]	1
0xBB0018D4	ISR_STAT_TRIGGER [1703].ISR_STAT_TRIGGER[7:7]	1
0xBB0018D4	ISR_STAT_TRIGGER [1704].ISR_STAT_TRIGGER[8:8]	1
0xBB0018D4	ISR_STAT_TRIGGER [1705].ISR_STAT_TRIGGER[9:9]	1
0xBB0018D4	ISR_STAT_TRIGGER [1706].ISR_STAT_TRIGGER[10:10]	1
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0xBB0018D4	ISR_STAT_TRIGGER [1708].ISR_STAT_TRIGGER[12:12]	1
0xBB0018D4	ISR_STAT_TRIGGER [1709].ISR_STAT_TRIGGER[13:13]	1
0xBB0018D4	ISR_STAT_TRIGGER [1710].ISR_STAT_TRIGGER[14:14]	1
0xBB0018D4	ISR_STAT_TRIGGER [1711].ISR_STAT_TRIGGER[15:15]	1
0xBB0018D4	ISR_STAT_TRIGGER [1712].ISR_STAT_TRIGGER[16:16]	1
0xBB0018D4	ISR_STAT_TRIGGER [1713].ISR_STAT_TRIGGER[17:17]	1
0xBB0018D4	ISR_STAT_TRIGGER [1714].ISR_STAT_TRIGGER[18:18]	1
0xBB0018D4	ISR_STAT_TRIGGER [1715].ISR_STAT_TRIGGER[19:19]	1
0xBB0018D4	ISR_STAT_TRIGGER [1716].ISR_STAT_TRIGGER[20:20]	1
0xBB0018D4	ISR_STAT_TRIGGER [1717].ISR_STAT_TRIGGER[21:21]	1
0xBB0018D4	ISR_STAT_TRIGGER [1718].ISR_STAT_TRIGGER[22:22]	1
0xBB0018D4	ISR_STAT_TRIGGER [1719].ISR_STAT_TRIGGER[23:23]	1
0xBB0018D4	ISR_STAT_TRIGGER [1720].ISR_STAT_TRIGGER[24:24]	1
0xBB0018D4	ISR_STAT_TRIGGER [1721].ISR_STAT_TRIGGER[25:25]	1
0xBB0018D4	ISR_STAT_TRIGGER [1722].ISR_STAT_TRIGGER[26:26]	1
0xBB0018D4	ISR_STAT_TRIGGER [1723].ISR_STAT_TRIGGER[27:27]	1

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0xBB0018D4	ISR_STAT_TRIGGER [1726].ISR_STAT_TRIGGER[30:30]	1
0xBB0018D4	ISR_STAT_TRIGGER [1727].ISR_STAT_TRIGGER[31:31]	1
0xBB0018D8	ISR_STAT_TRIGGER [1728].ISR_STAT_TRIGGER[0:0]	1
0xBB0018D8	ISR_STAT_TRIGGER [1729].ISR_STAT_TRIGGER[1:1]	1
0xBB0018D8	ISR_STAT_TRIGGER [1730].ISR_STAT_TRIGGER[2:2]	1
0xBB0018D8	ISR_STAT_TRIGGER [1731].ISR_STAT_TRIGGER[3:3]	1
0xBB0018D8	ISR_STAT_TRIGGER [1732].ISR_STAT_TRIGGER[4:4]	1
0xBB0018D8	ISR_STAT_TRIGGER [1733].ISR_STAT_TRIGGER[5:5]	1
0xBB0018D8	ISR_STAT_TRIGGER [1734].ISR_STAT_TRIGGER[6:6]	1
0xBB0018D8	ISR_STAT_TRIGGER [1735].ISR_STAT_TRIGGER[7:7]	1
0xBB0018D8	ISR_STAT_TRIGGER [1736].ISR_STAT_TRIGGER[8:8]	1
0xBB0018D8	ISR_STAT_TRIGGER [1737].ISR_STAT_TRIGGER[9:9]	1
0xBB0018D8	ISR_STAT_TRIGGER [1738].ISR_STAT_TRIGGER[10:10]	1
0xBB0018D8	ISR_STAT_TRIGGER [1739].ISR_STAT_TRIGGER[11:11]	1
0xBB0018D8	ISR_STAT_TRIGGER [1740].ISR_STAT_TRIGGER[12:12]	1
0xBB0018D8	ISR_STAT_TRIGGER [1741].ISR_STAT_TRIGGER[13:13]	1
0xBB0018D8	ISR_STAT_TRIGGER [1742].ISR_STAT_TRIGGER[14:14]	1
0xBB0018D8	ISR_STAT_TRIGGER [1743].ISR_STAT_TRIGGER[15:15]	1
0xBB0018D8	ISR_STAT_TRIGGER [1744].ISR_STAT_TRIGGER[16:16]	1
0xBB0018D8	ISR_STAT_TRIGGER [1745].ISR_STAT_TRIGGER[17:17]	1
0xBB0018D8	ISR_STAT_TRIGGER [1746].ISR_STAT_TRIGGER[18:18]	1
0xBB0018D8	ISR_STAT_TRIGGER [1747].ISR_STAT_TRIGGER[19:19]	1
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0xBB0018D8	ISR_STAT_TRIGGER [1749].ISR_STAT_TRIGGER[21:21]	1
0xBB0018D8	ISR_STAT_TRIGGER [1750].ISR_STAT_TRIGGER[22:22]	1
0xBB0018D8	ISR_STAT_TRIGGER [1751].ISR_STAT_TRIGGER[23:23]	1
0xBB0018D8	ISR_STAT_TRIGGER [1752].ISR_STAT_TRIGGER[24:24]	1
0xBB0018D8	ISR_STAT_TRIGGER [1753].ISR_STAT_TRIGGER[25:25]	1
0xBB0018D8	ISR_STAT_TRIGGER [1754].ISR_STAT_TRIGGER[26:26]	1
0xBB0018D8	ISR_STAT_TRIGGER [1755].ISR_STAT_TRIGGER[27:27]	1
0xBB0018D8	ISR_STAT_TRIGGER [1756].ISR_STAT_TRIGGER[28:28]	1
0xBB0018D8	ISR_STAT_TRIGGER [1757].ISR_STAT_TRIGGER[29:29]	1
0xBB0018D8	ISR_STAT_TRIGGER [1758].ISR_STAT_TRIGGER[30:30]	1
0xBB0018D8	ISR_STAT_TRIGGER [1759].ISR_STAT_TRIGGER[31:31]	1
0xBB0018DC	ISR_STAT_TRIGGER [1760].ISR_STAT_TRIGGER[0:0]	1
0xBB0018DC	ISR_STAT_TRIGGER [1761].ISR_STAT_TRIGGER[1:1]	1
0xBB0018DC	ISR_STAT_TRIGGER [1762].ISR_STAT_TRIGGER[2:2]	1
0xBB0018DC	ISR_STAT_TRIGGER [1763].ISR_STAT_TRIGGER[3:3]	1
0xBB0018DC	ISR_STAT_TRIGGER [1764].ISR_STAT_TRIGGER[4:4]	1
0xBB0018DC	ISR_STAT_TRIGGER [1765].ISR_STAT_TRIGGER[5:5]	1
0xBB0018DC	ISR_STAT_TRIGGER [1766].ISR_STAT_TRIGGER[6:6]	1
0xBB0018DC	ISR_STAT_TRIGGER [1767].ISR_STAT_TRIGGER[7:7]	1
0xBB0018DC	ISR_STAT_TRIGGER [1768].ISR_STAT_TRIGGER[8:8]	1
0xBB0018DC	ISR_STAT_TRIGGER [1769].ISR_STAT_TRIGGER[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0018DC	ISR_STAT_TRIGGER [1770].ISR_STAT_TRIGGER[10:10]	1
0xBB0018DC	ISR_STAT_TRIGGER [1771].ISR_STAT_TRIGGER[11:11]	1
0xBB0018DC	ISR_STAT_TRIGGER [1772].ISR_STAT_TRIGGER[12:12]	1
0xBB0018DC	ISR_STAT_TRIGGER [1773].ISR_STAT_TRIGGER[13:13]	1
0xBB0018DC	ISR_STAT_TRIGGER [1774].ISR_STAT_TRIGGER[14:14]	1
0xBB0018DC	ISR_STAT_TRIGGER [1775].ISR_STAT_TRIGGER[15:15]	1
0xBB0018DC	ISR_STAT_TRIGGER [1776].ISR_STAT_TRIGGER[16:16]	1
0xBB0018DC	ISR_STAT_TRIGGER [1777].ISR_STAT_TRIGGER[17:17]	1
0xBB0018DC	ISR_STAT_TRIGGER [1778].ISR_STAT_TRIGGER[18:18]	1
0xBB0018DC	ISR_STAT_TRIGGER [1779].ISR_STAT_TRIGGER[19:19]	1
0xBB0018DC	ISR_STAT_TRIGGER [1780].ISR_STAT_TRIGGER[20:20]	1
0xBB0018DC	ISR_STAT_TRIGGER [1781].ISR_STAT_TRIGGER[21:21]	1
0xBB0018DC	ISR_STAT_TRIGGER [1782].ISR_STAT_TRIGGER[22:22]	1
0xBB0018DC	ISR_STAT_TRIGGER [1783].ISR_STAT_TRIGGER[23:23]	1
0xBB0018DC	ISR_STAT_TRIGGER [1784].ISR_STAT_TRIGGER[24:24]	1
0xBB0018DC	ISR_STAT_TRIGGER [1785].ISR_STAT_TRIGGER[25:25]	1
0xBB0018DC	ISR_STAT_TRIGGER [1786].ISR_STAT_TRIGGER[26:26]	1
0xBB0018DC	ISR_STAT_TRIGGER [1787].ISR_STAT_TRIGGER[27:27]	1
0xBB0018DC	ISR_STAT_TRIGGER [1788].ISR_STAT_TRIGGER[28:28]	1
0xBB0018DC	ISR_STAT_TRIGGER [1789].ISR_STAT_TRIGGER[29:29]	1
0xBB0018DC	ISR_STAT_TRIGGER [1790].ISR_STAT_TRIGGER[30:30]	1
0xBB0018DC	ISR_STAT_TRIGGER [1791].ISR_STAT_TRIGGER[31:31]	1
0xBB0018E0	ISR_STAT_TRIGGER [1792].ISR_STAT_TRIGGER[0:0]	1
0xBB0018E0	ISR_STAT_TRIGGER [1793].ISR_STAT_TRIGGER[1:1]	1
0xBB0018E0	ISR_STAT_TRIGGER [1794].ISR_STAT_TRIGGER[2:2]	1
0xBB0018E0	ISR_STAT_TRIGGER [1795].ISR_STAT_TRIGGER[3:3]	1
0xBB0018E0	ISR_STAT_TRIGGER [1796].ISR_STAT_TRIGGER[4:4]	1
0xBB0018E0	ISR_STAT_TRIGGER [1797].ISR_STAT_TRIGGER[5:5]	1
0xBB0018E0	ISR_STAT_TRIGGER [1798].ISR_STAT_TRIGGER[6:6]	1
0xBB0018E0	ISR_STAT_TRIGGER [1799].ISR_STAT_TRIGGER[7:7]	1
0xBB0018E0	ISR_STAT_TRIGGER [1800].ISR_STAT_TRIGGER[8:8]	1
0xBB0018E0	ISR_STAT_TRIGGER [1801].ISR_STAT_TRIGGER[9:9]	1
0xBB0018E0	ISR_STAT_TRIGGER [1802].ISR_STAT_TRIGGER[10:10]	1
0xBB0018E0	ISR_STAT_TRIGGER [1803].ISR_STAT_TRIGGER[11:11]	1
0xBB0018E0	ISR_STAT_TRIGGER [1804].ISR_STAT_TRIGGER[12:12]	1
0xBB0018E0	ISR_STAT_TRIGGER [1805].ISR_STAT_TRIGGER[13:13]	1
0xBB0018E0	ISR_STAT_TRIGGER [1806].ISR_STAT_TRIGGER[14:14]	1
0xBB0018E0	ISR_STAT_TRIGGER [1807].ISR_STAT_TRIGGER[15:15]	1
0xBB0018E0	ISR_STAT_TRIGGER [1808].ISR_STAT_TRIGGER[16:16]	1
0xBB0018E0	ISR_STAT_TRIGGER [1809].ISR_STAT_TRIGGER[17:17]	1
0xBB0018E0	ISR_STAT_TRIGGER [1810].ISR_STAT_TRIGGER[18:18]	1
0xBB0018E0	ISR_STAT_TRIGGER [1811].ISR_STAT_TRIGGER[19:19]	1
0xBB0018E0	ISR_STAT_TRIGGER [1812].ISR_STAT_TRIGGER[20:20]	1
0xBB0018E0	ISR_STAT_TRIGGER [1813].ISR_STAT_TRIGGER[21:21]	1
0xBB0018E0	ISR_STAT_TRIGGER [1814].ISR_STAT_TRIGGER[22:22]	1
0xBB0018E0	ISR_STAT_TRIGGER [1815].ISR_STAT_TRIGGER[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018E0	ISR_STAT_TRIGGER [1817].ISR_STAT_TRIGGER[25:25]	1
0xBB0018E0	ISR_STAT_TRIGGER [1818].ISR_STAT_TRIGGER[26:26]	1
0xBB0018E0	ISR_STAT_TRIGGER [1819].ISR_STAT_TRIGGER[27:27]	1
0xBB0018E0	ISR_STAT_TRIGGER [1820].ISR_STAT_TRIGGER[28:28]	1
0xBB0018E0	ISR_STAT_TRIGGER [1821].ISR_STAT_TRIGGER[29:29]	1
0xBB0018E0	ISR_STAT_TRIGGER [1822].ISR_STAT_TRIGGER[30:30]	1
0xBB0018E0	ISR_STAT_TRIGGER [1823].ISR_STAT_TRIGGER[31:31]	1
0xBB0018E4	ISR_STAT_TRIGGER [1824].ISR_STAT_TRIGGER[0:0]	1
0xBB0018E4	ISR_STAT_TRIGGER [1825].ISR_STAT_TRIGGER[1:1]	1
0xBB0018E4	ISR_STAT_TRIGGER [1826].ISR_STAT_TRIGGER[2:2]	1
0xBB0018E4	ISR_STAT_TRIGGER [1827].ISR_STAT_TRIGGER[3:3]	1
0xBB0018E4	ISR_STAT_TRIGGER [1828].ISR_STAT_TRIGGER[4:4]	1
0xBB0018E4	ISR_STAT_TRIGGER [1829].ISR_STAT_TRIGGER[5:5]	1
0xBB0018E4	ISR_STAT_TRIGGER [1830].ISR_STAT_TRIGGER[6:6]	1
0xBB0018E4	ISR_STAT_TRIGGER [1831].ISR_STAT_TRIGGER[7:7]	1
0xBB0018E4	ISR_STAT_TRIGGER [1832].ISR_STAT_TRIGGER[8:8]	1
0xBB0018E4	ISR_STAT_TRIGGER [1833].ISR_STAT_TRIGGER[9:9]	1
0xBB0018E4	ISR_STAT_TRIGGER [1834].ISR_STAT_TRIGGER[10:10]	1
0xBB0018E4	ISR_STAT_TRIGGER [1835].ISR_STAT_TRIGGER[11:11]	1
0xBB0018E4	ISR_STAT_TRIGGER [1836].ISR_STAT_TRIGGER[12:12]	1
0xBB0018E4	ISR_STAT_TRIGGER [1837].ISR_STAT_TRIGGER[13:13]	1
0xBB0018E4	ISR_STAT_TRIGGER [1838].ISR_STAT_TRIGGER[14:14]	1
0xBB0018E4	ISR_STAT_TRIGGER [1839].ISR_STAT_TRIGGER[15:15]	1
0xBB0018E4	ISR_STAT_TRIGGER [1840].ISR_STAT_TRIGGER[16:16]	1
0xBB0018E4	ISR_STAT_TRIGGER [1841].ISR_STAT_TRIGGER[17:17]	1
0xBB0018E4	ISR_STAT_TRIGGER [1842].ISR_STAT_TRIGGER[18:18]	1
0xBB0018E4	ISR_STAT_TRIGGER [1843].ISR_STAT_TRIGGER[19:19]	1
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0xBB0018E4	ISR_STAT_TRIGGER [1845].ISR_STAT_TRIGGER[21:21]	1
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0xBB0018E4	ISR_STAT_TRIGGER [1847].ISR_STAT_TRIGGER[23:23]	1
0xBB0018E4	ISR_STAT_TRIGGER [1848].ISR_STAT_TRIGGER[24:24]	1
0xBB0018E4	ISR_STAT_TRIGGER [1849].ISR_STAT_TRIGGER[25:25]	1
0xBB0018E4	ISR_STAT_TRIGGER [1850].ISR_STAT_TRIGGER[26:26]	1
0xBB0018E4	ISR_STAT_TRIGGER [1851].ISR_STAT_TRIGGER[27:27]	1
0xBB0018E4	ISR_STAT_TRIGGER [1852].ISR_STAT_TRIGGER[28:28]	1
0xBB0018E4	ISR_STAT_TRIGGER [1853].ISR_STAT_TRIGGER[29:29]	1
0xBB0018E4	ISR_STAT_TRIGGER [1854].ISR_STAT_TRIGGER[30:30]	1
0xBB0018E4	ISR_STAT_TRIGGER [1855].ISR_STAT_TRIGGER[31:31]	1
0xBB0018E8	ISR_STAT_TRIGGER [1856].ISR_STAT_TRIGGER[0:0]	1
0xBB0018E8	ISR_STAT_TRIGGER [1857].ISR_STAT_TRIGGER[1:1]	1
0xBB0018E8	ISR_STAT_TRIGGER [1858].ISR_STAT_TRIGGER[2:2]	1
0xBB0018E8	ISR_STAT_TRIGGER [1859].ISR_STAT_TRIGGER[3:3]	1
0xBB0018E8	ISR_STAT_TRIGGER [1860].ISR_STAT_TRIGGER[4:4]	1
0xBB0018E8	ISR_STAT_TRIGGER [1861].ISR_STAT_TRIGGER[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018E8	ISR_STAT_TRIGGER [1864].ISR_STAT_TRIGGER[8:8]	1
0xBB0018E8	ISR_STAT_TRIGGER [1865].ISR_STAT_TRIGGER[9:9]	1
0xBB0018E8	ISR_STAT_TRIGGER [1866].ISR_STAT_TRIGGER[10:10]	1
0xBB0018E8	ISR_STAT_TRIGGER [1867].ISR_STAT_TRIGGER[11:11]	1
0xBB0018E8	ISR_STAT_TRIGGER [1868].ISR_STAT_TRIGGER[12:12]	1
0xBB0018E8	ISR_STAT_TRIGGER [1869].ISR_STAT_TRIGGER[13:13]	1
0xBB0018E8	ISR_STAT_TRIGGER [1870].ISR_STAT_TRIGGER[14:14]	1
0xBB0018E8	ISR_STAT_TRIGGER [1871].ISR_STAT_TRIGGER[15:15]	1
0xBB0018E8	ISR_STAT_TRIGGER [1872].ISR_STAT_TRIGGER[16:16]	1
0xBB0018E8	ISR_STAT_TRIGGER [1873].ISR_STAT_TRIGGER[17:17]	1
0xBB0018E8	ISR_STAT_TRIGGER [1874].ISR_STAT_TRIGGER[18:18]	1
0xBB0018E8	ISR_STAT_TRIGGER [1875].ISR_STAT_TRIGGER[19:19]	1
0xBB0018E8	ISR_STAT_TRIGGER [1876].ISR_STAT_TRIGGER[20:20]	1
0xBB0018E8	ISR_STAT_TRIGGER [1877].ISR_STAT_TRIGGER[21:21]	1
0xBB0018E8	ISR_STAT_TRIGGER [1878].ISR_STAT_TRIGGER[22:22]	1
0xBB0018E8	ISR_STAT_TRIGGER [1879].ISR_STAT_TRIGGER[23:23]	1
0xBB0018E8	ISR_STAT_TRIGGER [1880].ISR_STAT_TRIGGER[24:24]	1
0xBB0018E8	ISR_STAT_TRIGGER [1881].ISR_STAT_TRIGGER[25:25]	1
0xBB0018E8	ISR_STAT_TRIGGER [1882].ISR_STAT_TRIGGER[26:26]	1
0xBB0018E8	ISR_STAT_TRIGGER [1883].ISR_STAT_TRIGGER[27:27]	1
0xBB0018E8	ISR_STAT_TRIGGER [1884].ISR_STAT_TRIGGER[28:28]	1
0xBB0018E8	ISR_STAT_TRIGGER [1885].ISR_STAT_TRIGGER[29:29]	1
0xBB0018E8	ISR_STAT_TRIGGER [1886].ISR_STAT_TRIGGER[30:30]	1
0xBB0018E8	ISR_STAT_TRIGGER [1887].ISR_STAT_TRIGGER[31:31]	1
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0xBB0018EC	ISR_STAT_TRIGGER [1891].ISR_STAT_TRIGGER[3:3]	1
0xBB0018EC	ISR_STAT_TRIGGER [1892].ISR_STAT_TRIGGER[4:4]	1
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0xBB0018EC	ISR_STAT_TRIGGER [1894].ISR_STAT_TRIGGER[6:6]	1
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0xBB0018EC	ISR_STAT_TRIGGER [1903].ISR_STAT_TRIGGER[15:15]	1
0xBB0018EC	ISR_STAT_TRIGGER [1904].ISR_STAT_TRIGGER[16:16]	1
0xBB0018EC	ISR_STAT_TRIGGER [1905].ISR_STAT_TRIGGER[17:17]	1
0xBB0018EC	ISR_STAT_TRIGGER [1906].ISR_STAT_TRIGGER[18:18]	1
0xBB0018EC	ISR_STAT_TRIGGER [1907].ISR_STAT_TRIGGER[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018EC	ISR_STAT_TRIGGER [1910].ISR_STAT_TRIGGER[22:22]	1
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0xBB0018EC	ISR_STAT_TRIGGER [1912].ISR_STAT_TRIGGER[24:24]	1
0xBB0018EC	ISR_STAT_TRIGGER [1913].ISR_STAT_TRIGGER[25:25]	1
0xBB0018EC	ISR_STAT_TRIGGER [1914].ISR_STAT_TRIGGER[26:26]	1
0xBB0018EC	ISR_STAT_TRIGGER [1915].ISR_STAT_TRIGGER[27:27]	1
0xBB0018EC	ISR_STAT_TRIGGER [1916].ISR_STAT_TRIGGER[28:28]	1
0xBB0018EC	ISR_STAT_TRIGGER [1917].ISR_STAT_TRIGGER[29:29]	1
0xBB0018EC	ISR_STAT_TRIGGER [1918].ISR_STAT_TRIGGER[30:30]	1
0xBB0018EC	ISR_STAT_TRIGGER [1919].ISR_STAT_TRIGGER[31:31]	1
0xBB0018F0	ISR_STAT_TRIGGER [1920].ISR_STAT_TRIGGER[0:0]	1
0xBB0018F0	ISR_STAT_TRIGGER [1921].ISR_STAT_TRIGGER[1:1]	1
0xBB0018F0	ISR_STAT_TRIGGER [1922].ISR_STAT_TRIGGER[2:2]	1
0xBB0018F0	ISR_STAT_TRIGGER [1923].ISR_STAT_TRIGGER[3:3]	1
0xBB0018F0	ISR_STAT_TRIGGER [1924].ISR_STAT_TRIGGER[4:4]	1
0xBB0018F0	ISR_STAT_TRIGGER [1925].ISR_STAT_TRIGGER[5:5]	1
0xBB0018F0	ISR_STAT_TRIGGER [1926].ISR_STAT_TRIGGER[6:6]	1
0xBB0018F0	ISR_STAT_TRIGGER [1927].ISR_STAT_TRIGGER[7:7]	1
0xBB0018F0	ISR_STAT_TRIGGER [1928].ISR_STAT_TRIGGER[8:8]	1
0xBB0018F0	ISR_STAT_TRIGGER [1929].ISR_STAT_TRIGGER[9:9]	1
0xBB0018F0	ISR_STAT_TRIGGER [1930].ISR_STAT_TRIGGER[10:10]	1
0xBB0018F0	ISR_STAT_TRIGGER [1931].ISR_STAT_TRIGGER[11:11]	1
0xBB0018F0	ISR_STAT_TRIGGER [1932].ISR_STAT_TRIGGER[12:12]	1
0xBB0018F0	ISR_STAT_TRIGGER [1933].ISR_STAT_TRIGGER[13:13]	1
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0xBB0018F0	ISR_STAT_TRIGGER [1944].ISR_STAT_TRIGGER[24:24]	1
0xBB0018F0	ISR_STAT_TRIGGER [1945].ISR_STAT_TRIGGER[25:25]	1
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0xBB0018F0	ISR_STAT_TRIGGER [1947].ISR_STAT_TRIGGER[27:27]	1
0xBB0018F0	ISR_STAT_TRIGGER [1948].ISR_STAT_TRIGGER[28:28]	1
0xBB0018F0	ISR_STAT_TRIGGER [1949].ISR_STAT_TRIGGER[29:29]	1
0xBB0018F0	ISR_STAT_TRIGGER [1950].ISR_STAT_TRIGGER[30:30]	1
0xBB0018F0	ISR_STAT_TRIGGER [1951].ISR_STAT_TRIGGER[31:31]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0018F4	ISR_STAT_TRIGGER [1956].ISR_STAT_TRIGGER[4:4]	1
0xBB0018F4	ISR_STAT_TRIGGER [1957].ISR_STAT_TRIGGER[5:5]	1
0xBB0018F4	ISR_STAT_TRIGGER [1958].ISR_STAT_TRIGGER[6:6]	1
0xBB0018F4	ISR_STAT_TRIGGER [1959].ISR_STAT_TRIGGER[7:7]	1
0xBB0018F4	ISR_STAT_TRIGGER [1960].ISR_STAT_TRIGGER[8:8]	1
0xBB0018F4	ISR_STAT_TRIGGER [1961].ISR_STAT_TRIGGER[9:9]	1
0xBB0018F4	ISR_STAT_TRIGGER [1962].ISR_STAT_TRIGGER[10:10]	1
0xBB0018F4	ISR_STAT_TRIGGER [1963].ISR_STAT_TRIGGER[11:11]	1
0xBB0018F4	ISR_STAT_TRIGGER [1964].ISR_STAT_TRIGGER[12:12]	1
0xBB0018F4	ISR_STAT_TRIGGER [1965].ISR_STAT_TRIGGER[13:13]	1
0xBB0018F4	ISR_STAT_TRIGGER [1966].ISR_STAT_TRIGGER[14:14]	1
0xBB0018F4	ISR_STAT_TRIGGER [1967].ISR_STAT_TRIGGER[15:15]	1
0xBB0018F4	ISR_STAT_TRIGGER [1968].ISR_STAT_TRIGGER[16:16]	1
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0xBB0018F4	ISR_STAT_TRIGGER [1970].ISR_STAT_TRIGGER[18:18]	1
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0xBB0018F4	ISR_STAT_TRIGGER [1972].ISR_STAT_TRIGGER[20:20]	1
0xBB0018F4	ISR_STAT_TRIGGER [1973].ISR_STAT_TRIGGER[21:21]	1
0xBB0018F4	ISR_STAT_TRIGGER [1974].ISR_STAT_TRIGGER[22:22]	1
0xBB0018F4	ISR_STAT_TRIGGER [1975].ISR_STAT_TRIGGER[23:23]	1
0xBB0018F4	ISR_STAT_TRIGGER [1976].ISR_STAT_TRIGGER[24:24]	1
0xBB0018F4	ISR_STAT_TRIGGER [1977].ISR_STAT_TRIGGER[25:25]	1
0xBB0018F4	ISR_STAT_TRIGGER [1978].ISR_STAT_TRIGGER[26:26]	1
0xBB0018F4	ISR_STAT_TRIGGER [1979].ISR_STAT_TRIGGER[27:27]	1
0xBB0018F4	ISR_STAT_TRIGGER [1980].ISR_STAT_TRIGGER[28:28]	1
0xBB0018F4	ISR_STAT_TRIGGER [1981].ISR_STAT_TRIGGER[29:29]	1
0xBB0018F4	ISR_STAT_TRIGGER [1982].ISR_STAT_TRIGGER[30:30]	1
0xBB0018F4	ISR_STAT_TRIGGER [1983].ISR_STAT_TRIGGER[31:31]	1
0xBB0018F8	ISR_STAT_TRIGGER [1984].ISR_STAT_TRIGGER[0:0]	1
0xBB0018F8	ISR_STAT_TRIGGER [1985].ISR_STAT_TRIGGER[1:1]	1
0xBB0018F8	ISR_STAT_TRIGGER [1986].ISR_STAT_TRIGGER[2:2]	1
0xBB0018F8	ISR_STAT_TRIGGER [1987].ISR_STAT_TRIGGER[3:3]	1
0xBB0018F8	ISR_STAT_TRIGGER [1988].ISR_STAT_TRIGGER[4:4]	1
0xBB0018F8	ISR_STAT_TRIGGER [1989].ISR_STAT_TRIGGER[5:5]	1
0xBB0018F8	ISR_STAT_TRIGGER [1990].ISR_STAT_TRIGGER[6:6]	1
0xBB0018F8	ISR_STAT_TRIGGER [1991].ISR_STAT_TRIGGER[7:7]	1
0xBB0018F8	ISR_STAT_TRIGGER [1992].ISR_STAT_TRIGGER[8:8]	1
0xBB0018F8	ISR_STAT_TRIGGER [1993].ISR_STAT_TRIGGER[9:9]	1
0xBB0018F8	ISR_STAT_TRIGGER [1994].ISR_STAT_TRIGGER[10:10]	1
0xBB0018F8	ISR_STAT_TRIGGER [1995].ISR_STAT_TRIGGER[11:11]	1
0xBB0018F8	ISR_STAT_TRIGGER [1996].ISR_STAT_TRIGGER[12:12]	1
0xBB0018F8	ISR_STAT_TRIGGER [1997].ISR_STAT_TRIGGER[13:13]	1
0xBB0018F8	ISR_STAT_TRIGGER [1998].ISR_STAT_TRIGGER[14:14]	1
0xBB0018F8	ISR_STAT_TRIGGER [1999].ISR_STAT_TRIGGER[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0018F8	ISR_STAT_TRIGGER [2000].ISR_STAT_TRIGGER[16:16]	1
0xBB0018F8	ISR_STAT_TRIGGER [2001].ISR_STAT_TRIGGER[17:17]	1
0xBB0018F8	ISR_STAT_TRIGGER [2002].ISR_STAT_TRIGGER[18:18]	1
0xBB0018F8	ISR_STAT_TRIGGER [2003].ISR_STAT_TRIGGER[19:19]	1
0xBB0018F8	ISR_STAT_TRIGGER [2004].ISR_STAT_TRIGGER[20:20]	1
0xBB0018F8	ISR_STAT_TRIGGER [2005].ISR_STAT_TRIGGER[21:21]	1
0xBB0018F8	ISR_STAT_TRIGGER [2006].ISR_STAT_TRIGGER[22:22]	1
0xBB0018F8	ISR_STAT_TRIGGER [2007].ISR_STAT_TRIGGER[23:23]	1
0xBB0018F8	ISR_STAT_TRIGGER [2008].ISR_STAT_TRIGGER[24:24]	1
0xBB0018F8	ISR_STAT_TRIGGER [2009].ISR_STAT_TRIGGER[25:25]	1
0xBB0018F8	ISR_STAT_TRIGGER [2010].ISR_STAT_TRIGGER[26:26]	1
0xBB0018F8	ISR_STAT_TRIGGER [2011].ISR_STAT_TRIGGER[27:27]	1
0xBB0018F8	ISR_STAT_TRIGGER [2012].ISR_STAT_TRIGGER[28:28]	1
0xBB0018F8	ISR_STAT_TRIGGER [2013].ISR_STAT_TRIGGER[29:29]	1
0xBB0018F8	ISR_STAT_TRIGGER [2014].ISR_STAT_TRIGGER[30:30]	1
0xBB0018F8	ISR_STAT_TRIGGER [2015].ISR_STAT_TRIGGER[31:31]	1
0xBB0018FC	ISR_STAT_TRIGGER [2016].ISR_STAT_TRIGGER[0:0]	1
0xBB0018FC	ISR_STAT_TRIGGER [2017].ISR_STAT_TRIGGER[1:1]	1
0xBB0018FC	ISR_STAT_TRIGGER [2018].ISR_STAT_TRIGGER[2:2]	1
0xBB0018FC	ISR_STAT_TRIGGER [2019].ISR_STAT_TRIGGER[3:3]	1
0xBB0018FC	ISR_STAT_TRIGGER [2020].ISR_STAT_TRIGGER[4:4]	1
0xBB0018FC	ISR_STAT_TRIGGER [2021].ISR_STAT_TRIGGER[5:5]	1
0xBB0018FC	ISR_STAT_TRIGGER [2022].ISR_STAT_TRIGGER[6:6]	1
0xBB0018FC	ISR_STAT_TRIGGER [2023].ISR_STAT_TRIGGER[7:7]	1
0xBB0018FC	ISR_STAT_TRIGGER [2024].ISR_STAT_TRIGGER[8:8]	1
0xBB0018FC	ISR_STAT_TRIGGER [2025].ISR_STAT_TRIGGER[9:9]	1
0xBB0018FC	ISR_STAT_TRIGGER [2026].ISR_STAT_TRIGGER[10:10]	1
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0xBB0018FC	ISR_STAT_TRIGGER [2029].ISR_STAT_TRIGGER[13:13]	1
0xBB0018FC	ISR_STAT_TRIGGER [2030].ISR_STAT_TRIGGER[14:14]	1
0xBB0018FC	ISR_STAT_TRIGGER [2031].ISR_STAT_TRIGGER[15:15]	1
0xBB0018FC	ISR_STAT_TRIGGER [2032].ISR_STAT_TRIGGER[16:16]	1
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0xBB0018FC	ISR_STAT_TRIGGER [2035].ISR_STAT_TRIGGER[19:19]	1
0xBB0018FC	ISR_STAT_TRIGGER [2036].ISR_STAT_TRIGGER[20:20]	1
0xBB0018FC	ISR_STAT_TRIGGER [2037].ISR_STAT_TRIGGER[21:21]	1
0xBB0018FC	ISR_STAT_TRIGGER [2038].ISR_STAT_TRIGGER[22:22]	1
0xBB0018FC	ISR_STAT_TRIGGER [2039].ISR_STAT_TRIGGER[23:23]	1
0xBB0018FC	ISR_STAT_TRIGGER [2040].ISR_STAT_TRIGGER[24:24]	1
0xBB0018FC	ISR_STAT_TRIGGER [2041].ISR_STAT_TRIGGER[25:25]	1
0xBB0018FC	ISR_STAT_TRIGGER [2042].ISR_STAT_TRIGGER[26:26]	1
0xBB0018FC	ISR_STAT_TRIGGER [2043].ISR_STAT_TRIGGER[27:27]	1
0xBB0018FC	ISR_STAT_TRIGGER [2044].ISR_STAT_TRIGGER[28:28]	1
0xBB0018FC	ISR_STAT_TRIGGER [2045].ISR_STAT_TRIGGER[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001900	ISR_STAT_TRIGGER [2049].ISR_STAT_TRIGGER[1:1]	1
0xBB001900	ISR_STAT_TRIGGER [2050].ISR_STAT_TRIGGER[2:2]	1
0xBB001900	ISR_STAT_TRIGGER [2051].ISR_STAT_TRIGGER[3:3]	1
0xBB001900	ISR_STAT_TRIGGER [2052].ISR_STAT_TRIGGER[4:4]	1
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0xBB001900	ISR_STAT_TRIGGER [2064].ISR_STAT_TRIGGER[16:16]	1
0xBB001900	ISR_STAT_TRIGGER [2065].ISR_STAT_TRIGGER[17:17]	1
0xBB001900	ISR_STAT_TRIGGER [2066].ISR_STAT_TRIGGER[18:18]	1
0xBB001900	ISR_STAT_TRIGGER [2067].ISR_STAT_TRIGGER[19:19]	1
0xBB001900	ISR_STAT_TRIGGER [2068].ISR_STAT_TRIGGER[20:20]	1
0xBB001900	ISR_STAT_TRIGGER [2069].ISR_STAT_TRIGGER[21:21]	1
0xBB001900	ISR_STAT_TRIGGER [2070].ISR_STAT_TRIGGER[22:22]	1
0xBB001900	ISR_STAT_TRIGGER [2071].ISR_STAT_TRIGGER[23:23]	1
0xBB001900	ISR_STAT_TRIGGER [2072].ISR_STAT_TRIGGER[24:24]	1
0xBB001900	ISR_STAT_TRIGGER [2073].ISR_STAT_TRIGGER[25:25]	1
0xBB001900	ISR_STAT_TRIGGER [2074].ISR_STAT_TRIGGER[26:26]	1
0xBB001900	ISR_STAT_TRIGGER [2075].ISR_STAT_TRIGGER[27:27]	1
0xBB001900	ISR_STAT_TRIGGER [2076].ISR_STAT_TRIGGER[28:28]	1
0xBB001900	ISR_STAT_TRIGGER [2077].ISR_STAT_TRIGGER[29:29]	1
0xBB001900	ISR_STAT_TRIGGER [2078].ISR_STAT_TRIGGER[30:30]	1
0xBB001900	ISR_STAT_TRIGGER [2079].ISR_STAT_TRIGGER[31:31]	1
0xBB001904	ISR_STAT_TRIGGER [2080].ISR_STAT_TRIGGER[0:0]	1
0xBB001904	ISR_STAT_TRIGGER [2081].ISR_STAT_TRIGGER[1:1]	1
0xBB001904	ISR_STAT_TRIGGER [2082].ISR_STAT_TRIGGER[2:2]	1
0xBB001904	ISR_STAT_TRIGGER [2083].ISR_STAT_TRIGGER[3:3]	1
0xBB001904	ISR_STAT_TRIGGER [2084].ISR_STAT_TRIGGER[4:4]	1
0xBB001904	ISR_STAT_TRIGGER [2085].ISR_STAT_TRIGGER[5:5]	1
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0xBB001904	ISR_STAT_TRIGGER [2087].ISR_STAT_TRIGGER[7:7]	1
0xBB001904	ISR_STAT_TRIGGER [2088].ISR_STAT_TRIGGER[8:8]	1
0xBB001904	ISR_STAT_TRIGGER [2089].ISR_STAT_TRIGGER[9:9]	1
0xBB001904	ISR_STAT_TRIGGER [2090].ISR_STAT_TRIGGER[10:10]	1
0xBB001904	ISR_STAT_TRIGGER [2091].ISR_STAT_TRIGGER[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001904	ISR_STAT_TRIGGER [2094].ISR_STAT_TRIGGER[14:14]	1
0xBB001904	ISR_STAT_TRIGGER [2095].ISR_STAT_TRIGGER[15:15]	1
0xBB001904	ISR_STAT_TRIGGER [2096].ISR_STAT_TRIGGER[16:16]	1
0xBB001904	ISR_STAT_TRIGGER [2097].ISR_STAT_TRIGGER[17:17]	1
0xBB001904	ISR_STAT_TRIGGER [2098].ISR_STAT_TRIGGER[18:18]	1
0xBB001904	ISR_STAT_TRIGGER [2099].ISR_STAT_TRIGGER[19:19]	1
0xBB001904	ISR_STAT_TRIGGER [2100].ISR_STAT_TRIGGER[20:20]	1
0xBB001904	ISR_STAT_TRIGGER [2101].ISR_STAT_TRIGGER[21:21]	1
0xBB001904	ISR_STAT_TRIGGER [2102].ISR_STAT_TRIGGER[22:22]	1
0xBB001904	ISR_STAT_TRIGGER [2103].ISR_STAT_TRIGGER[23:23]	1
0xBB001904	ISR_STAT_TRIGGER [2104].ISR_STAT_TRIGGER[24:24]	1
0xBB001904	ISR_STAT_TRIGGER [2105].ISR_STAT_TRIGGER[25:25]	1
0xBB001904	ISR_STAT_TRIGGER [2106].ISR_STAT_TRIGGER[26:26]	1
0xBB001904	ISR_STAT_TRIGGER [2107].ISR_STAT_TRIGGER[27:27]	1
0xBB001904	ISR_STAT_TRIGGER [2108].ISR_STAT_TRIGGER[28:28]	1
0xBB001904	ISR_STAT_TRIGGER [2109].ISR_STAT_TRIGGER[29:29]	1
0xBB001904	ISR_STAT_TRIGGER [2110].ISR_STAT_TRIGGER[30:30]	1
0xBB001904	ISR_STAT_TRIGGER [2111].ISR_STAT_TRIGGER[31:31]	1
0xBB001908	ISR_STAT_TRIGGER [2112].ISR_STAT_TRIGGER[0:0]	1
0xBB001908	ISR_STAT_TRIGGER [2113].ISR_STAT_TRIGGER[1:1]	1
0xBB001908	ISR_STAT_TRIGGER [2114].ISR_STAT_TRIGGER[2:2]	1
0xBB001908	ISR_STAT_TRIGGER [2115].ISR_STAT_TRIGGER[3:3]	1
0xBB001908	ISR_STAT_TRIGGER [2116].ISR_STAT_TRIGGER[4:4]	1
0xBB001908	ISR_STAT_TRIGGER [2117].ISR_STAT_TRIGGER[5:5]	1
0xBB001908	ISR_STAT_TRIGGER [2118].ISR_STAT_TRIGGER[6:6]	1
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0xBB001908	ISR_STAT_TRIGGER [2124].ISR_STAT_TRIGGER[12:12]	1
0xBB001908	ISR_STAT_TRIGGER [2125].ISR_STAT_TRIGGER[13:13]	1
0xBB001908	ISR_STAT_TRIGGER [2126].ISR_STAT_TRIGGER[14:14]	1
0xBB001908	ISR_STAT_TRIGGER [2127].ISR_STAT_TRIGGER[15:15]	1
0xBB001908	ISR_STAT_TRIGGER [2128].ISR_STAT_TRIGGER[16:16]	1
0xBB001908	ISR_STAT_TRIGGER [2129].ISR_STAT_TRIGGER[17:17]	1
0xBB001908	ISR_STAT_TRIGGER [2130].ISR_STAT_TRIGGER[18:18]	1
0xBB001908	ISR_STAT_TRIGGER [2131].ISR_STAT_TRIGGER[19:19]	1
0xBB001908	ISR_STAT_TRIGGER [2132].ISR_STAT_TRIGGER[20:20]	1
0xBB001908	ISR_STAT_TRIGGER [2133].ISR_STAT_TRIGGER[21:21]	1
0xBB001908	ISR_STAT_TRIGGER [2134].ISR_STAT_TRIGGER[22:22]	1
0xBB001908	ISR_STAT_TRIGGER [2135].ISR_STAT_TRIGGER[23:23]	1
0xBB001908	ISR_STAT_TRIGGER [2136].ISR_STAT_TRIGGER[24:24]	1
0xBB001908	ISR_STAT_TRIGGER [2137].ISR_STAT_TRIGGER[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001908	ISR_STAT_TRIGGER [2139].ISR_STAT_TRIGGER[27:27]	1
0xBB001908	ISR_STAT_TRIGGER [2140].ISR_STAT_TRIGGER[28:28]	1
0xBB001908	ISR_STAT_TRIGGER [2141].ISR_STAT_TRIGGER[29:29]	1
0xBB001908	ISR_STAT_TRIGGER [2142].ISR_STAT_TRIGGER[30:30]	1
0xBB001908	ISR_STAT_TRIGGER [2143].ISR_STAT_TRIGGER[31:31]	1
0xBB00190C	ISR_STAT_TRIGGER [2144].ISR_STAT_TRIGGER[0:0]	1
0xBB00190C	ISR_STAT_TRIGGER [2145].ISR_STAT_TRIGGER[1:1]	1
0xBB00190C	ISR_STAT_TRIGGER [2146].ISR_STAT_TRIGGER[2:2]	1
0xBB00190C	ISR_STAT_TRIGGER [2147].ISR_STAT_TRIGGER[3:3]	1
0xBB00190C	ISR_STAT_TRIGGER [2148].ISR_STAT_TRIGGER[4:4]	1
0xBB00190C	ISR_STAT_TRIGGER [2149].ISR_STAT_TRIGGER[5:5]	1
0xBB00190C	ISR_STAT_TRIGGER [2150].ISR_STAT_TRIGGER[6:6]	1
0xBB00190C	ISR_STAT_TRIGGER [2151].ISR_STAT_TRIGGER[7:7]	1
0xBB00190C	ISR_STAT_TRIGGER [2152].ISR_STAT_TRIGGER[8:8]	1
0xBB00190C	ISR_STAT_TRIGGER [2153].ISR_STAT_TRIGGER[9:9]	1
0xBB00190C	ISR_STAT_TRIGGER [2154].ISR_STAT_TRIGGER[10:10]	1
0xBB00190C	ISR_STAT_TRIGGER [2155].ISR_STAT_TRIGGER[11:11]	1
0xBB00190C	ISR_STAT_TRIGGER [2156].ISR_STAT_TRIGGER[12:12]	1
0xBB00190C	ISR_STAT_TRIGGER [2157].ISR_STAT_TRIGGER[13:13]	1
0xBB00190C	ISR_STAT_TRIGGER [2158].ISR_STAT_TRIGGER[14:14]	1
0xBB00190C	ISR_STAT_TRIGGER [2159].ISR_STAT_TRIGGER[15:15]	1
0xBB00190C	ISR_STAT_TRIGGER [2160].ISR_STAT_TRIGGER[16:16]	1
0xBB00190C	ISR_STAT_TRIGGER [2161].ISR_STAT_TRIGGER[17:17]	1
0xBB00190C	ISR_STAT_TRIGGER [2162].ISR_STAT_TRIGGER[18:18]	1
0xBB00190C	ISR_STAT_TRIGGER [2163].ISR_STAT_TRIGGER[19:19]	1
0xBB00190C	ISR_STAT_TRIGGER [2164].ISR_STAT_TRIGGER[20:20]	1
0xBB00190C	ISR_STAT_TRIGGER [2165].ISR_STAT_TRIGGER[21:21]	1
0xBB00190C	ISR_STAT_TRIGGER [2166].ISR_STAT_TRIGGER[22:22]	1
0xBB00190C	ISR_STAT_TRIGGER [2167].ISR_STAT_TRIGGER[23:23]	1
0xBB00190C	ISR_STAT_TRIGGER [2168].ISR_STAT_TRIGGER[24:24]	1
0xBB00190C	ISR_STAT_TRIGGER [2169].ISR_STAT_TRIGGER[25:25]	1
0xBB00190C	ISR_STAT_TRIGGER [2170].ISR_STAT_TRIGGER[26:26]	1
0xBB00190C	ISR_STAT_TRIGGER [2171].ISR_STAT_TRIGGER[27:27]	1
0xBB00190C	ISR_STAT_TRIGGER [2172].ISR_STAT_TRIGGER[28:28]	1
0xBB00190C	ISR_STAT_TRIGGER [2173].ISR_STAT_TRIGGER[29:29]	1
0xBB00190C	ISR_STAT_TRIGGER [2174].ISR_STAT_TRIGGER[30:30]	1
0xBB00190C	ISR_STAT_TRIGGER [2175].ISR_STAT_TRIGGER[31:31]	1
0xBB001910	ISR_STAT_TRIGGER [2176].ISR_STAT_TRIGGER[0:0]	1
0xBB001910	ISR_STAT_TRIGGER [2177].ISR_STAT_TRIGGER[1:1]	1
0xBB001910	ISR_STAT_TRIGGER [2178].ISR_STAT_TRIGGER[2:2]	1
0xBB001910	ISR_STAT_TRIGGER [2179].ISR_STAT_TRIGGER[3:3]	1
0xBB001910	ISR_STAT_TRIGGER [2180].ISR_STAT_TRIGGER[4:4]	1
0xBB001910	ISR_STAT_TRIGGER [2181].ISR_STAT_TRIGGER[5:5]	1
0xBB001910	ISR_STAT_TRIGGER [2182].ISR_STAT_TRIGGER[6:6]	1
0xBB001910	ISR_STAT_TRIGGER [2183].ISR_STAT_TRIGGER[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001910	ISR_STAT_TRIGGER [2186].ISR_STAT_TRIGGER[10:10]	1
0xBB001910	ISR_STAT_TRIGGER [2187].ISR_STAT_TRIGGER[11:11]	1
0xBB001910	ISR_STAT_TRIGGER [2188].ISR_STAT_TRIGGER[12:12]	1
0xBB001910	ISR_STAT_TRIGGER [2189].ISR_STAT_TRIGGER[13:13]	1
0xBB001910	ISR_STAT_TRIGGER [2190].ISR_STAT_TRIGGER[14:14]	1
0xBB001910	ISR_STAT_TRIGGER [2191].ISR_STAT_TRIGGER[15:15]	1
0xBB001910	ISR_STAT_TRIGGER [2192].ISR_STAT_TRIGGER[16:16]	1
0xBB001910	ISR_STAT_TRIGGER [2193].ISR_STAT_TRIGGER[17:17]	1
0xBB001910	ISR_STAT_TRIGGER [2194].ISR_STAT_TRIGGER[18:18]	1
0xBB001910	ISR_STAT_TRIGGER [2195].ISR_STAT_TRIGGER[19:19]	1
0xBB001910	ISR_STAT_TRIGGER [2196].ISR_STAT_TRIGGER[20:20]	1
0xBB001910	ISR_STAT_TRIGGER [2197].ISR_STAT_TRIGGER[21:21]	1
0xBB001910	ISR_STAT_TRIGGER [2198].ISR_STAT_TRIGGER[22:22]	1
0xBB001910	ISR_STAT_TRIGGER [2199].ISR_STAT_TRIGGER[23:23]	1
0xBB001910	ISR_STAT_TRIGGER [2200].ISR_STAT_TRIGGER[24:24]	1
0xBB001910	ISR_STAT_TRIGGER [2201].ISR_STAT_TRIGGER[25:25]	1
0xBB001910	ISR_STAT_TRIGGER [2202].ISR_STAT_TRIGGER[26:26]	1
0xBB001910	ISR_STAT_TRIGGER [2203].ISR_STAT_TRIGGER[27:27]	1
0xBB001910	ISR_STAT_TRIGGER [2204].ISR_STAT_TRIGGER[28:28]	1
0xBB001910	ISR_STAT_TRIGGER [2205].ISR_STAT_TRIGGER[29:29]	1
0xBB001910	ISR_STAT_TRIGGER [2206].ISR_STAT_TRIGGER[30:30]	1
0xBB001910	ISR_STAT_TRIGGER [2207].ISR_STAT_TRIGGER[31:31]	1
0xBB001914	ISR_STAT_TRIGGER [2208].ISR_STAT_TRIGGER[0:0]	1
0xBB001914	ISR_STAT_TRIGGER [2209].ISR_STAT_TRIGGER[1:1]	1
0xBB001914	ISR_STAT_TRIGGER [2210].ISR_STAT_TRIGGER[2:2]	1
0xBB001914	ISR_STAT_TRIGGER [2211].ISR_STAT_TRIGGER[3:3]	1
0xBB001914	ISR_STAT_TRIGGER [2212].ISR_STAT_TRIGGER[4:4]	1
0xBB001914	ISR_STAT_TRIGGER [2213].ISR_STAT_TRIGGER[5:5]	1
0xBB001914	ISR_STAT_TRIGGER [2214].ISR_STAT_TRIGGER[6:6]	1
0xBB001914	ISR_STAT_TRIGGER [2215].ISR_STAT_TRIGGER[7:7]	1
0xBB001914	ISR_STAT_TRIGGER [2216].ISR_STAT_TRIGGER[8:8]	1
0xBB001914	ISR_STAT_TRIGGER [2217].ISR_STAT_TRIGGER[9:9]	1
0xBB001914	ISR_STAT_TRIGGER [2218].ISR_STAT_TRIGGER[10:10]	1
0xBB001914	ISR_STAT_TRIGGER [2219].ISR_STAT_TRIGGER[11:11]	1
0xBB001914	ISR_STAT_TRIGGER [2220].ISR_STAT_TRIGGER[12:12]	1
0xBB001914	ISR_STAT_TRIGGER [2221].ISR_STAT_TRIGGER[13:13]	1
0xBB001914	ISR_STAT_TRIGGER [2222].ISR_STAT_TRIGGER[14:14]	1
0xBB001914	ISR_STAT_TRIGGER [2223].ISR_STAT_TRIGGER[15:15]	1
0xBB001914	ISR_STAT_TRIGGER [2224].ISR_STAT_TRIGGER[16:16]	1
0xBB001914	ISR_STAT_TRIGGER [2225].ISR_STAT_TRIGGER[17:17]	1
0xBB001914	ISR_STAT_TRIGGER [2226].ISR_STAT_TRIGGER[18:18]	1
0xBB001914	ISR_STAT_TRIGGER [2227].ISR_STAT_TRIGGER[19:19]	1
0xBB001914	ISR_STAT_TRIGGER [2228].ISR_STAT_TRIGGER[20:20]	1
0xBB001914	ISR_STAT_TRIGGER [2229].ISR_STAT_TRIGGER[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001914	ISR_STAT_TRIGGER [2230].ISR_STAT_TRIGGER[22:22]	1
0xBB001914	ISR_STAT_TRIGGER [2231].ISR_STAT_TRIGGER[23:23]	1
0xBB001914	ISR_STAT_TRIGGER [2232].ISR_STAT_TRIGGER[24:24]	1
0xBB001914	ISR_STAT_TRIGGER [2233].ISR_STAT_TRIGGER[25:25]	1
0xBB001914	ISR_STAT_TRIGGER [2234].ISR_STAT_TRIGGER[26:26]	1
0xBB001914	ISR_STAT_TRIGGER [2235].ISR_STAT_TRIGGER[27:27]	1
0xBB001914	ISR_STAT_TRIGGER [2236].ISR_STAT_TRIGGER[28:28]	1
0xBB001914	ISR_STAT_TRIGGER [2237].ISR_STAT_TRIGGER[29:29]	1
0xBB001914	ISR_STAT_TRIGGER [2238].ISR_STAT_TRIGGER[30:30]	1
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0xBB001918	ISR_STAT_TRIGGER [2241].ISR_STAT_TRIGGER[1:1]	1
0xBB001918	ISR_STAT_TRIGGER [2242].ISR_STAT_TRIGGER[2:2]	1
0xBB001918	ISR_STAT_TRIGGER [2243].ISR_STAT_TRIGGER[3:3]	1
0xBB001918	ISR_STAT_TRIGGER [2244].ISR_STAT_TRIGGER[4:4]	1
0xBB001918	ISR_STAT_TRIGGER [2245].ISR_STAT_TRIGGER[5:5]	1
0xBB001918	ISR_STAT_TRIGGER [2246].ISR_STAT_TRIGGER[6:6]	1
0xBB001918	ISR_STAT_TRIGGER [2247].ISR_STAT_TRIGGER[7:7]	1
0xBB001918	ISR_STAT_TRIGGER [2248].ISR_STAT_TRIGGER[8:8]	1
0xBB001918	ISR_STAT_TRIGGER [2249].ISR_STAT_TRIGGER[9:9]	1
0xBB001918	ISR_STAT_TRIGGER [2250].ISR_STAT_TRIGGER[10:10]	1
0xBB001918	ISR_STAT_TRIGGER [2251].ISR_STAT_TRIGGER[11:11]	1
0xBB001918	ISR_STAT_TRIGGER [2252].ISR_STAT_TRIGGER[12:12]	1
0xBB001918	ISR_STAT_TRIGGER [2253].ISR_STAT_TRIGGER[13:13]	1
0xBB001918	ISR_STAT_TRIGGER [2254].ISR_STAT_TRIGGER[14:14]	1
0xBB001918	ISR_STAT_TRIGGER [2255].ISR_STAT_TRIGGER[15:15]	1
0xBB001918	ISR_STAT_TRIGGER [2256].ISR_STAT_TRIGGER[16:16]	1
0xBB001918	ISR_STAT_TRIGGER [2257].ISR_STAT_TRIGGER[17:17]	1
0xBB001918	ISR_STAT_TRIGGER [2258].ISR_STAT_TRIGGER[18:18]	1
0xBB001918	ISR_STAT_TRIGGER [2259].ISR_STAT_TRIGGER[19:19]	1
0xBB001918	ISR_STAT_TRIGGER [2260].ISR_STAT_TRIGGER[20:20]	1
0xBB001918	ISR_STAT_TRIGGER [2261].ISR_STAT_TRIGGER[21:21]	1
0xBB001918	ISR_STAT_TRIGGER [2262].ISR_STAT_TRIGGER[22:22]	1
0xBB001918	ISR_STAT_TRIGGER [2263].ISR_STAT_TRIGGER[23:23]	1
0xBB001918	ISR_STAT_TRIGGER [2264].ISR_STAT_TRIGGER[24:24]	1
0xBB001918	ISR_STAT_TRIGGER [2265].ISR_STAT_TRIGGER[25:25]	1
0xBB001918	ISR_STAT_TRIGGER [2266].ISR_STAT_TRIGGER[26:26]	1
0xBB001918	ISR_STAT_TRIGGER [2267].ISR_STAT_TRIGGER[27:27]	1
0xBB001918	ISR_STAT_TRIGGER [2268].ISR_STAT_TRIGGER[28:28]	1
0xBB001918	ISR_STAT_TRIGGER [2269].ISR_STAT_TRIGGER[29:29]	1
0xBB001918	ISR_STAT_TRIGGER [2270].ISR_STAT_TRIGGER[30:30]	1
0xBB001918	ISR_STAT_TRIGGER [2271].ISR_STAT_TRIGGER[31:31]	1
0xBB00191C	ISR_STAT_TRIGGER [2272].ISR_STAT_TRIGGER[0:0]	1
0xBB00191C	ISR_STAT_TRIGGER [2273].ISR_STAT_TRIGGER[1:1]	1
0xBB00191C	ISR_STAT_TRIGGER [2274].ISR_STAT_TRIGGER[2:2]	1
0xBB00191C	ISR_STAT_TRIGGER [2275].ISR_STAT_TRIGGER[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00191C	ISR_STAT_TRIGGER [2277].ISR_STAT_TRIGGER[5:5]	1
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0xBB00191C	ISR_STAT_TRIGGER [2279].ISR_STAT_TRIGGER[7:7]	1
0xBB00191C	ISR_STAT_TRIGGER [2280].ISR_STAT_TRIGGER[8:8]	1
0xBB00191C	ISR_STAT_TRIGGER [2281].ISR_STAT_TRIGGER[9:9]	1
0xBB00191C	ISR_STAT_TRIGGER [2282].ISR_STAT_TRIGGER[10:10]	1
0xBB00191C	ISR_STAT_TRIGGER [2283].ISR_STAT_TRIGGER[11:11]	1
0xBB00191C	ISR_STAT_TRIGGER [2284].ISR_STAT_TRIGGER[12:12]	1
0xBB00191C	ISR_STAT_TRIGGER [2285].ISR_STAT_TRIGGER[13:13]	1
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0xBB00191C	ISR_STAT_TRIGGER [2291].ISR_STAT_TRIGGER[19:19]	1
0xBB00191C	ISR_STAT_TRIGGER [2292].ISR_STAT_TRIGGER[20:20]	1
0xBB00191C	ISR_STAT_TRIGGER [2293].ISR_STAT_TRIGGER[21:21]	1
0xBB00191C	ISR_STAT_TRIGGER [2294].ISR_STAT_TRIGGER[22:22]	1
0xBB00191C	ISR_STAT_TRIGGER [2295].ISR_STAT_TRIGGER[23:23]	1
0xBB00191C	ISR_STAT_TRIGGER [2296].ISR_STAT_TRIGGER[24:24]	1
0xBB00191C	ISR_STAT_TRIGGER [2297].ISR_STAT_TRIGGER[25:25]	1
0xBB00191C	ISR_STAT_TRIGGER [2298].ISR_STAT_TRIGGER[26:26]	1
0xBB00191C	ISR_STAT_TRIGGER [2299].ISR_STAT_TRIGGER[27:27]	1
0xBB00191C	ISR_STAT_TRIGGER [2300].ISR_STAT_TRIGGER[28:28]	1
0xBB00191C	ISR_STAT_TRIGGER [2301].ISR_STAT_TRIGGER[29:29]	1
0xBB00191C	ISR_STAT_TRIGGER [2302].ISR_STAT_TRIGGER[30:30]	1
0xBB00191C	ISR_STAT_TRIGGER [2303].ISR_STAT_TRIGGER[31:31]	1
0xBB001920	ISR_STAT_TRIGGER [2304].ISR_STAT_TRIGGER[0:0]	1
0xBB001920	ISR_STAT_TRIGGER [2305].ISR_STAT_TRIGGER[1:1]	1
0xBB001920	ISR_STAT_TRIGGER [2306].ISR_STAT_TRIGGER[2:2]	1
0xBB001920	ISR_STAT_TRIGGER [2307].ISR_STAT_TRIGGER[3:3]	1
0xBB001920	ISR_STAT_TRIGGER [2308].ISR_STAT_TRIGGER[4:4]	1
0xBB001920	ISR_STAT_TRIGGER [2309].ISR_STAT_TRIGGER[5:5]	1
0xBB001920	ISR_STAT_TRIGGER [2310].ISR_STAT_TRIGGER[6:6]	1
0xBB001920	ISR_STAT_TRIGGER [2311].ISR_STAT_TRIGGER[7:7]	1
0xBB001920	ISR_STAT_TRIGGER [2312].ISR_STAT_TRIGGER[8:8]	1
0xBB001920	ISR_STAT_TRIGGER [2313].ISR_STAT_TRIGGER[9:9]	1
0xBB001920	ISR_STAT_TRIGGER [2314].ISR_STAT_TRIGGER[10:10]	1
0xBB001920	ISR_STAT_TRIGGER [2315].ISR_STAT_TRIGGER[11:11]	1
0xBB001920	ISR_STAT_TRIGGER [2316].ISR_STAT_TRIGGER[12:12]	1
0xBB001920	ISR_STAT_TRIGGER [2317].ISR_STAT_TRIGGER[13:13]	1
0xBB001920	ISR_STAT_TRIGGER [2318].ISR_STAT_TRIGGER[14:14]	1
0xBB001920	ISR_STAT_TRIGGER [2319].ISR_STAT_TRIGGER[15:15]	1
0xBB001920	ISR_STAT_TRIGGER [2320].ISR_STAT_TRIGGER[16:16]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001920	ISR_STAT_TRIGGER [2324].ISR_STAT_TRIGGER[20:20]	1
0xBB001920	ISR_STAT_TRIGGER [2325].ISR_STAT_TRIGGER[21:21]	1
0xBB001920	ISR_STAT_TRIGGER [2326].ISR_STAT_TRIGGER[22:22]	1
0xBB001920	ISR_STAT_TRIGGER [2327].ISR_STAT_TRIGGER[23:23]	1
0xBB001920	ISR_STAT_TRIGGER [2328].ISR_STAT_TRIGGER[24:24]	1
0xBB001920	ISR_STAT_TRIGGER [2329].ISR_STAT_TRIGGER[25:25]	1
0xBB001920	ISR_STAT_TRIGGER [2330].ISR_STAT_TRIGGER[26:26]	1
0xBB001920	ISR_STAT_TRIGGER [2331].ISR_STAT_TRIGGER[27:27]	1
0xBB001920	ISR_STAT_TRIGGER [2332].ISR_STAT_TRIGGER[28:28]	1
0xBB001920	ISR_STAT_TRIGGER [2333].ISR_STAT_TRIGGER[29:29]	1
0xBB001920	ISR_STAT_TRIGGER [2334].ISR_STAT_TRIGGER[30:30]	1
0xBB001920	ISR_STAT_TRIGGER [2335].ISR_STAT_TRIGGER[31:31]	1
0xBB001924	ISR_STAT_TRIGGER [2336].ISR_STAT_TRIGGER[0:0]	1
0xBB001924	ISR_STAT_TRIGGER [2337].ISR_STAT_TRIGGER[1:1]	1
0xBB001924	ISR_STAT_TRIGGER [2338].ISR_STAT_TRIGGER[2:2]	1
0xBB001924	ISR_STAT_TRIGGER [2339].ISR_STAT_TRIGGER[3:3]	1
0xBB001924	ISR_STAT_TRIGGER [2340].ISR_STAT_TRIGGER[4:4]	1
0xBB001924	ISR_STAT_TRIGGER [2341].ISR_STAT_TRIGGER[5:5]	1
0xBB001924	ISR_STAT_TRIGGER [2342].ISR_STAT_TRIGGER[6:6]	1
0xBB001924	ISR_STAT_TRIGGER [2343].ISR_STAT_TRIGGER[7:7]	1
0xBB001924	ISR_STAT_TRIGGER [2344].ISR_STAT_TRIGGER[8:8]	1
0xBB001924	ISR_STAT_TRIGGER [2345].ISR_STAT_TRIGGER[9:9]	1
0xBB001924	ISR_STAT_TRIGGER [2346].ISR_STAT_TRIGGER[10:10]	1
0xBB001924	ISR_STAT_TRIGGER [2347].ISR_STAT_TRIGGER[11:11]	1
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0xBB001924	ISR_STAT_TRIGGER [2352].ISR_STAT_TRIGGER[16:16]	1
0xBB001924	ISR_STAT_TRIGGER [2353].ISR_STAT_TRIGGER[17:17]	1
0xBB001924	ISR_STAT_TRIGGER [2354].ISR_STAT_TRIGGER[18:18]	1
0xBB001924	ISR_STAT_TRIGGER [2355].ISR_STAT_TRIGGER[19:19]	1
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0xBB001924	ISR_STAT_TRIGGER [2358].ISR_STAT_TRIGGER[22:22]	1
0xBB001924	ISR_STAT_TRIGGER [2359].ISR_STAT_TRIGGER[23:23]	1
0xBB001924	ISR_STAT_TRIGGER [2360].ISR_STAT_TRIGGER[24:24]	1
0xBB001924	ISR_STAT_TRIGGER [2361].ISR_STAT_TRIGGER[25:25]	1
0xBB001924	ISR_STAT_TRIGGER [2362].ISR_STAT_TRIGGER[26:26]	1
0xBB001924	ISR_STAT_TRIGGER [2363].ISR_STAT_TRIGGER[27:27]	1
0xBB001924	ISR_STAT_TRIGGER [2364].ISR_STAT_TRIGGER[28:28]	1
0xBB001924	ISR_STAT_TRIGGER [2365].ISR_STAT_TRIGGER[29:29]	1
0xBB001924	ISR_STAT_TRIGGER [2366].ISR_STAT_TRIGGER[30:30]	1
0xBB001924	ISR_STAT_TRIGGER [2367].ISR_STAT_TRIGGER[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001928	ISR_STAT_TRIGGER [2370].ISR_STAT_TRIGGER[2:2]	1
0xBB001928	ISR_STAT_TRIGGER [2371].ISR_STAT_TRIGGER[3:3]	1
0xBB001928	ISR_STAT_TRIGGER [2372].ISR_STAT_TRIGGER[4:4]	1
0xBB001928	ISR_STAT_TRIGGER [2373].ISR_STAT_TRIGGER[5:5]	1
0xBB001928	ISR_STAT_TRIGGER [2374].ISR_STAT_TRIGGER[6:6]	1
0xBB001928	ISR_STAT_TRIGGER [2375].ISR_STAT_TRIGGER[7:7]	1
0xBB001928	ISR_STAT_TRIGGER [2376].ISR_STAT_TRIGGER[8:8]	1
0xBB001928	ISR_STAT_TRIGGER [2377].ISR_STAT_TRIGGER[9:9]	1
0xBB001928	ISR_STAT_TRIGGER [2378].ISR_STAT_TRIGGER[10:10]	1
0xBB001928	ISR_STAT_TRIGGER [2379].ISR_STAT_TRIGGER[11:11]	1
0xBB001928	ISR_STAT_TRIGGER [2380].ISR_STAT_TRIGGER[12:12]	1
0xBB001928	ISR_STAT_TRIGGER [2381].ISR_STAT_TRIGGER[13:13]	1
0xBB001928	ISR_STAT_TRIGGER [2382].ISR_STAT_TRIGGER[14:14]	1
0xBB001928	ISR_STAT_TRIGGER [2383].ISR_STAT_TRIGGER[15:15]	1
0xBB001928	ISR_STAT_TRIGGER [2384].ISR_STAT_TRIGGER[16:16]	1
0xBB001928	ISR_STAT_TRIGGER [2385].ISR_STAT_TRIGGER[17:17]	1
0xBB001928	ISR_STAT_TRIGGER [2386].ISR_STAT_TRIGGER[18:18]	1
0xBB001928	ISR_STAT_TRIGGER [2387].ISR_STAT_TRIGGER[19:19]	1
0xBB001928	ISR_STAT_TRIGGER [2388].ISR_STAT_TRIGGER[20:20]	1
0xBB001928	ISR_STAT_TRIGGER [2389].ISR_STAT_TRIGGER[21:21]	1
0xBB001928	ISR_STAT_TRIGGER [2390].ISR_STAT_TRIGGER[22:22]	1
0xBB001928	ISR_STAT_TRIGGER [2391].ISR_STAT_TRIGGER[23:23]	1
0xBB001928	ISR_STAT_TRIGGER [2392].ISR_STAT_TRIGGER[24:24]	1
0xBB001928	ISR_STAT_TRIGGER [2393].ISR_STAT_TRIGGER[25:25]	1
0xBB001928	ISR_STAT_TRIGGER [2394].ISR_STAT_TRIGGER[26:26]	1
0xBB001928	ISR_STAT_TRIGGER [2395].ISR_STAT_TRIGGER[27:27]	1
0xBB001928	ISR_STAT_TRIGGER [2396].ISR_STAT_TRIGGER[28:28]	1
0xBB001928	ISR_STAT_TRIGGER [2397].ISR_STAT_TRIGGER[29:29]	1
0xBB001928	ISR_STAT_TRIGGER [2398].ISR_STAT_TRIGGER[30:30]	1
0xBB001928	ISR_STAT_TRIGGER [2399].ISR_STAT_TRIGGER[31:31]	1
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0xBB00192C	ISR_STAT_TRIGGER [2402].ISR_STAT_TRIGGER[2:2]	1
0xBB00192C	ISR_STAT_TRIGGER [2403].ISR_STAT_TRIGGER[3:3]	1
0xBB00192C	ISR_STAT_TRIGGER [2404].ISR_STAT_TRIGGER[4:4]	1
0xBB00192C	ISR_STAT_TRIGGER [2405].ISR_STAT_TRIGGER[5:5]	1
0xBB00192C	ISR_STAT_TRIGGER [2406].ISR_STAT_TRIGGER[6:6]	1
0xBB00192C	ISR_STAT_TRIGGER [2407].ISR_STAT_TRIGGER[7:7]	1
0xBB00192C	ISR_STAT_TRIGGER [2408].ISR_STAT_TRIGGER[8:8]	1
0xBB00192C	ISR_STAT_TRIGGER [2409].ISR_STAT_TRIGGER[9:9]	1
0xBB00192C	ISR_STAT_TRIGGER [2410].ISR_STAT_TRIGGER[10:10]	1
0xBB00192C	ISR_STAT_TRIGGER [2411].ISR_STAT_TRIGGER[11:11]	1
0xBB00192C	ISR_STAT_TRIGGER [2412].ISR_STAT_TRIGGER[12:12]	1
0xBB00192C	ISR_STAT_TRIGGER [2413].ISR_STAT_TRIGGER[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00192C	ISR_STAT_TRIGGER [2417].ISR_STAT_TRIGGER[17:17]	1
0xBB00192C	ISR_STAT_TRIGGER [2418].ISR_STAT_TRIGGER[18:18]	1
0xBB00192C	ISR_STAT_TRIGGER [2419].ISR_STAT_TRIGGER[19:19]	1
0xBB00192C	ISR_STAT_TRIGGER [2420].ISR_STAT_TRIGGER[20:20]	1
0xBB00192C	ISR_STAT_TRIGGER [2421].ISR_STAT_TRIGGER[21:21]	1
0xBB00192C	ISR_STAT_TRIGGER [2422].ISR_STAT_TRIGGER[22:22]	1
0xBB00192C	ISR_STAT_TRIGGER [2423].ISR_STAT_TRIGGER[23:23]	1
0xBB00192C	ISR_STAT_TRIGGER [2424].ISR_STAT_TRIGGER[24:24]	1
0xBB00192C	ISR_STAT_TRIGGER [2425].ISR_STAT_TRIGGER[25:25]	1
0xBB00192C	ISR_STAT_TRIGGER [2426].ISR_STAT_TRIGGER[26:26]	1
0xBB00192C	ISR_STAT_TRIGGER [2427].ISR_STAT_TRIGGER[27:27]	1
0xBB00192C	ISR_STAT_TRIGGER [2428].ISR_STAT_TRIGGER[28:28]	1
0xBB00192C	ISR_STAT_TRIGGER [2429].ISR_STAT_TRIGGER[29:29]	1
0xBB00192C	ISR_STAT_TRIGGER [2430].ISR_STAT_TRIGGER[30:30]	1
0xBB00192C	ISR_STAT_TRIGGER [2431].ISR_STAT_TRIGGER[31:31]	1
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0xBB001930	ISR_STAT_TRIGGER [2433].ISR_STAT_TRIGGER[1:1]	1
0xBB001930	ISR_STAT_TRIGGER [2434].ISR_STAT_TRIGGER[2:2]	1
0xBB001930	ISR_STAT_TRIGGER [2435].ISR_STAT_TRIGGER[3:3]	1
0xBB001930	ISR_STAT_TRIGGER [2436].ISR_STAT_TRIGGER[4:4]	1
0xBB001930	ISR_STAT_TRIGGER [2437].ISR_STAT_TRIGGER[5:5]	1
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0xBB001930	ISR_STAT_TRIGGER [2439].ISR_STAT_TRIGGER[7:7]	1
0xBB001930	ISR_STAT_TRIGGER [2440].ISR_STAT_TRIGGER[8:8]	1
0xBB001930	ISR_STAT_TRIGGER [2441].ISR_STAT_TRIGGER[9:9]	1
0xBB001930	ISR_STAT_TRIGGER [2442].ISR_STAT_TRIGGER[10:10]	1
0xBB001930	ISR_STAT_TRIGGER [2443].ISR_STAT_TRIGGER[11:11]	1
0xBB001930	ISR_STAT_TRIGGER [2444].ISR_STAT_TRIGGER[12:12]	1
0xBB001930	ISR_STAT_TRIGGER [2445].ISR_STAT_TRIGGER[13:13]	1
0xBB001930	ISR_STAT_TRIGGER [2446].ISR_STAT_TRIGGER[14:14]	1
0xBB001930	ISR_STAT_TRIGGER [2447].ISR_STAT_TRIGGER[15:15]	1
0xBB001930	ISR_STAT_TRIGGER [2448].ISR_STAT_TRIGGER[16:16]	1
0xBB001930	ISR_STAT_TRIGGER [2449].ISR_STAT_TRIGGER[17:17]	1
0xBB001930	ISR_STAT_TRIGGER [2450].ISR_STAT_TRIGGER[18:18]	1
0xBB001930	ISR_STAT_TRIGGER [2451].ISR_STAT_TRIGGER[19:19]	1
0xBB001930	ISR_STAT_TRIGGER [2452].ISR_STAT_TRIGGER[20:20]	1
0xBB001930	ISR_STAT_TRIGGER [2453].ISR_STAT_TRIGGER[21:21]	1
0xBB001930	ISR_STAT_TRIGGER [2454].ISR_STAT_TRIGGER[22:22]	1
0xBB001930	ISR_STAT_TRIGGER [2455].ISR_STAT_TRIGGER[23:23]	1
0xBB001930	ISR_STAT_TRIGGER [2456].ISR_STAT_TRIGGER[24:24]	1
0xBB001930	ISR_STAT_TRIGGER [2457].ISR_STAT_TRIGGER[25:25]	1
0xBB001930	ISR_STAT_TRIGGER [2458].ISR_STAT_TRIGGER[26:26]	1
0xBB001930	ISR_STAT_TRIGGER [2459].ISR_STAT_TRIGGER[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001930	ISR_STAT_TRIGGER [2460].ISR_STAT_TRIGGER[28:28]	1
0xBB001930	ISR_STAT_TRIGGER [2461].ISR_STAT_TRIGGER[29:29]	1
0xBB001930	ISR_STAT_TRIGGER [2462].ISR_STAT_TRIGGER[30:30]	1
0xBB001930	ISR_STAT_TRIGGER [2463].ISR_STAT_TRIGGER[31:31]	1
0xBB001934	ISR_STAT_TRIGGER [2464].ISR_STAT_TRIGGER[0:0]	1
0xBB001934	ISR_STAT_TRIGGER [2465].ISR_STAT_TRIGGER[1:1]	1
0xBB001934	ISR_STAT_TRIGGER [2466].ISR_STAT_TRIGGER[2:2]	1
0xBB001934	ISR_STAT_TRIGGER [2467].ISR_STAT_TRIGGER[3:3]	1
0xBB001934	ISR_STAT_TRIGGER [2468].ISR_STAT_TRIGGER[4:4]	1
0xBB001934	ISR_STAT_TRIGGER [2469].ISR_STAT_TRIGGER[5:5]	1
0xBB001934	ISR_STAT_TRIGGER [2470].ISR_STAT_TRIGGER[6:6]	1
0xBB001934	ISR_STAT_TRIGGER [2471].ISR_STAT_TRIGGER[7:7]	1
0xBB001934	ISR_STAT_TRIGGER [2472].ISR_STAT_TRIGGER[8:8]	1
0xBB001934	ISR_STAT_TRIGGER [2473].ISR_STAT_TRIGGER[9:9]	1
0xBB001934	ISR_STAT_TRIGGER [2474].ISR_STAT_TRIGGER[10:10]	1
0xBB001934	ISR_STAT_TRIGGER [2475].ISR_STAT_TRIGGER[11:11]	1
0xBB001934	ISR_STAT_TRIGGER [2476].ISR_STAT_TRIGGER[12:12]	1
0xBB001934	ISR_STAT_TRIGGER [2477].ISR_STAT_TRIGGER[13:13]	1
0xBB001934	ISR_STAT_TRIGGER [2478].ISR_STAT_TRIGGER[14:14]	1
0xBB001934	ISR_STAT_TRIGGER [2479].ISR_STAT_TRIGGER[15:15]	1
0xBB001934	ISR_STAT_TRIGGER [2480].ISR_STAT_TRIGGER[16:16]	1
0xBB001934	ISR_STAT_TRIGGER [2481].ISR_STAT_TRIGGER[17:17]	1
0xBB001934	ISR_STAT_TRIGGER [2482].ISR_STAT_TRIGGER[18:18]	1
0xBB001934	ISR_STAT_TRIGGER [2483].ISR_STAT_TRIGGER[19:19]	1
0xBB001934	ISR_STAT_TRIGGER [2484].ISR_STAT_TRIGGER[20:20]	1
0xBB001934	ISR_STAT_TRIGGER [2485].ISR_STAT_TRIGGER[21:21]	1
0xBB001934	ISR_STAT_TRIGGER [2486].ISR_STAT_TRIGGER[22:22]	1
0xBB001934	ISR_STAT_TRIGGER [2487].ISR_STAT_TRIGGER[23:23]	1
0xBB001934	ISR_STAT_TRIGGER [2488].ISR_STAT_TRIGGER[24:24]	1
0xBB001934	ISR_STAT_TRIGGER [2489].ISR_STAT_TRIGGER[25:25]	1
0xBB001934	ISR_STAT_TRIGGER [2490].ISR_STAT_TRIGGER[26:26]	1
0xBB001934	ISR_STAT_TRIGGER [2491].ISR_STAT_TRIGGER[27:27]	1
0xBB001934	ISR_STAT_TRIGGER [2492].ISR_STAT_TRIGGER[28:28]	1
0xBB001934	ISR_STAT_TRIGGER [2493].ISR_STAT_TRIGGER[29:29]	1
0xBB001934	ISR_STAT_TRIGGER [2494].ISR_STAT_TRIGGER[30:30]	1
0xBB001934	ISR_STAT_TRIGGER [2495].ISR_STAT_TRIGGER[31:31]	1
0xBB001938	ISR_STAT_TRIGGER [2496].ISR_STAT_TRIGGER[0:0]	1
0xBB001938	ISR_STAT_TRIGGER [2497].ISR_STAT_TRIGGER[1:1]	1
0xBB001938	ISR_STAT_TRIGGER [2498].ISR_STAT_TRIGGER[2:2]	1
0xBB001938	ISR_STAT_TRIGGER [2499].ISR_STAT_TRIGGER[3:3]	1
0xBB001938	ISR_STAT_TRIGGER [2500].ISR_STAT_TRIGGER[4:4]	1
0xBB001938	ISR_STAT_TRIGGER [2501].ISR_STAT_TRIGGER[5:5]	1
0xBB001938	ISR_STAT_TRIGGER [2502].ISR_STAT_TRIGGER[6:6]	1
0xBB001938	ISR_STAT_TRIGGER [2503].ISR_STAT_TRIGGER[7:7]	1
0xBB001938	ISR_STAT_TRIGGER [2504].ISR_STAT_TRIGGER[8:8]	1
0xBB001938	ISR_STAT_TRIGGER [2505].ISR_STAT_TRIGGER[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001938	ISR_STAT_TRIGGER [2507].ISR_STAT_TRIGGER[11:11]	1
0xBB001938	ISR_STAT_TRIGGER [2508].ISR_STAT_TRIGGER[12:12]	1
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0xBB001938	ISR_STAT_TRIGGER [2512].ISR_STAT_TRIGGER[16:16]	1
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0xBB001938	ISR_STAT_TRIGGER [2515].ISR_STAT_TRIGGER[19:19]	1
0xBB001938	ISR_STAT_TRIGGER [2516].ISR_STAT_TRIGGER[20:20]	1
0xBB001938	ISR_STAT_TRIGGER [2517].ISR_STAT_TRIGGER[21:21]	1
0xBB001938	ISR_STAT_TRIGGER [2518].ISR_STAT_TRIGGER[22:22]	1
0xBB001938	ISR_STAT_TRIGGER [2519].ISR_STAT_TRIGGER[23:23]	1
0xBB001938	ISR_STAT_TRIGGER [2520].ISR_STAT_TRIGGER[24:24]	1
0xBB001938	ISR_STAT_TRIGGER [2521].ISR_STAT_TRIGGER[25:25]	1
0xBB001938	ISR_STAT_TRIGGER [2522].ISR_STAT_TRIGGER[26:26]	1
0xBB001938	ISR_STAT_TRIGGER [2523].ISR_STAT_TRIGGER[27:27]	1
0xBB001938	ISR_STAT_TRIGGER [2524].ISR_STAT_TRIGGER[28:28]	1
0xBB001938	ISR_STAT_TRIGGER [2525].ISR_STAT_TRIGGER[29:29]	1
0xBB001938	ISR_STAT_TRIGGER [2526].ISR_STAT_TRIGGER[30:30]	1
0xBB001938	ISR_STAT_TRIGGER [2527].ISR_STAT_TRIGGER[31:31]	1
0xBB00193C	ISR_STAT_TRIGGER [2528].ISR_STAT_TRIGGER[0:0]	1
0xBB00193C	ISR_STAT_TRIGGER [2529].ISR_STAT_TRIGGER[1:1]	1
0xBB00193C	ISR_STAT_TRIGGER [2530].ISR_STAT_TRIGGER[2:2]	1
0xBB00193C	ISR_STAT_TRIGGER [2531].ISR_STAT_TRIGGER[3:3]	1
0xBB00193C	ISR_STAT_TRIGGER [2532].ISR_STAT_TRIGGER[4:4]	1
0xBB00193C	ISR_STAT_TRIGGER [2533].ISR_STAT_TRIGGER[5:5]	1
0xBB00193C	ISR_STAT_TRIGGER [2534].ISR_STAT_TRIGGER[6:6]	1
0xBB00193C	ISR_STAT_TRIGGER [2535].ISR_STAT_TRIGGER[7:7]	1
0xBB00193C	ISR_STAT_TRIGGER [2536].ISR_STAT_TRIGGER[8:8]	1
0xBB00193C	ISR_STAT_TRIGGER [2537].ISR_STAT_TRIGGER[9:9]	1
0xBB00193C	ISR_STAT_TRIGGER [2538].ISR_STAT_TRIGGER[10:10]	1
0xBB00193C	ISR_STAT_TRIGGER [2539].ISR_STAT_TRIGGER[11:11]	1
0xBB00193C	ISR_STAT_TRIGGER [2540].ISR_STAT_TRIGGER[12:12]	1
0xBB00193C	ISR_STAT_TRIGGER [2541].ISR_STAT_TRIGGER[13:13]	1
0xBB00193C	ISR_STAT_TRIGGER [2542].ISR_STAT_TRIGGER[14:14]	1
0xBB00193C	ISR_STAT_TRIGGER [2543].ISR_STAT_TRIGGER[15:15]	1
0xBB00193C	ISR_STAT_TRIGGER [2544].ISR_STAT_TRIGGER[16:16]	1
0xBB00193C	ISR_STAT_TRIGGER [2545].ISR_STAT_TRIGGER[17:17]	1
0xBB00193C	ISR_STAT_TRIGGER [2546].ISR_STAT_TRIGGER[18:18]	1
0xBB00193C	ISR_STAT_TRIGGER [2547].ISR_STAT_TRIGGER[19:19]	1
0xBB00193C	ISR_STAT_TRIGGER [2548].ISR_STAT_TRIGGER[20:20]	1
0xBB00193C	ISR_STAT_TRIGGER [2549].ISR_STAT_TRIGGER[21:21]	1
0xBB00193C	ISR_STAT_TRIGGER [2550].ISR_STAT_TRIGGER[22:22]	1
0xBB00193C	ISR_STAT_TRIGGER [2551].ISR_STAT_TRIGGER[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00193C	ISR_STAT_TRIGGER [2552].ISR_STAT_TRIGGER[24:24]	1
0xBB00193C	ISR_STAT_TRIGGER [2553].ISR_STAT_TRIGGER[25:25]	1
0xBB00193C	ISR_STAT_TRIGGER [2554].ISR_STAT_TRIGGER[26:26]	1
0xBB00193C	ISR_STAT_TRIGGER [2555].ISR_STAT_TRIGGER[27:27]	1
0xBB00193C	ISR_STAT_TRIGGER [2556].ISR_STAT_TRIGGER[28:28]	1
0xBB00193C	ISR_STAT_TRIGGER [2557].ISR_STAT_TRIGGER[29:29]	1
0xBB00193C	ISR_STAT_TRIGGER [2558].ISR_STAT_TRIGGER[30:30]	1
0xBB00193C	ISR_STAT_TRIGGER [2559].ISR_STAT_TRIGGER[31:31]	1
0xBB001940	ISR_STAT_TRIGGER [2560].ISR_STAT_TRIGGER[0:0]	1
0xBB001940	ISR_STAT_TRIGGER [2561].ISR_STAT_TRIGGER[1:1]	1
0xBB001940	ISR_STAT_TRIGGER [2562].ISR_STAT_TRIGGER[2:2]	1
0xBB001940	ISR_STAT_TRIGGER [2563].ISR_STAT_TRIGGER[3:3]	1
0xBB001940	ISR_STAT_TRIGGER [2564].ISR_STAT_TRIGGER[4:4]	1
0xBB001940	ISR_STAT_TRIGGER [2565].ISR_STAT_TRIGGER[5:5]	1
0xBB001940	ISR_STAT_TRIGGER [2566].ISR_STAT_TRIGGER[6:6]	1
0xBB001940	ISR_STAT_TRIGGER [2567].ISR_STAT_TRIGGER[7:7]	1
0xBB001940	ISR_STAT_TRIGGER [2568].ISR_STAT_TRIGGER[8:8]	1
0xBB001940	ISR_STAT_TRIGGER [2569].ISR_STAT_TRIGGER[9:9]	1
0xBB001940	ISR_STAT_TRIGGER [2570].ISR_STAT_TRIGGER[10:10]	1
0xBB001940	ISR_STAT_TRIGGER [2571].ISR_STAT_TRIGGER[11:11]	1
0xBB001940	ISR_STAT_TRIGGER [2572].ISR_STAT_TRIGGER[12:12]	1
0xBB001940	ISR_STAT_TRIGGER [2573].ISR_STAT_TRIGGER[13:13]	1
0xBB001940	ISR_STAT_TRIGGER [2574].ISR_STAT_TRIGGER[14:14]	1
0xBB001940	ISR_STAT_TRIGGER [2575].ISR_STAT_TRIGGER[15:15]	1
0xBB001940	ISR_STAT_TRIGGER [2576].ISR_STAT_TRIGGER[16:16]	1
0xBB001940	ISR_STAT_TRIGGER [2577].ISR_STAT_TRIGGER[17:17]	1
0xBB001940	ISR_STAT_TRIGGER [2578].ISR_STAT_TRIGGER[18:18]	1
0xBB001940	ISR_STAT_TRIGGER [2579].ISR_STAT_TRIGGER[19:19]	1
0xBB001940	ISR_STAT_TRIGGER [2580].ISR_STAT_TRIGGER[20:20]	1
0xBB001940	ISR_STAT_TRIGGER [2581].ISR_STAT_TRIGGER[21:21]	1
0xBB001940	ISR_STAT_TRIGGER [2582].ISR_STAT_TRIGGER[22:22]	1
0xBB001940	ISR_STAT_TRIGGER [2583].ISR_STAT_TRIGGER[23:23]	1
0xBB001940	ISR_STAT_TRIGGER [2584].ISR_STAT_TRIGGER[24:24]	1
0xBB001940	ISR_STAT_TRIGGER [2585].ISR_STAT_TRIGGER[25:25]	1
0xBB001940	ISR_STAT_TRIGGER [2586].ISR_STAT_TRIGGER[26:26]	1
0xBB001940	ISR_STAT_TRIGGER [2587].ISR_STAT_TRIGGER[27:27]	1
0xBB001940	ISR_STAT_TRIGGER [2588].ISR_STAT_TRIGGER[28:28]	1
0xBB001940	ISR_STAT_TRIGGER [2589].ISR_STAT_TRIGGER[29:29]	1
0xBB001940	ISR_STAT_TRIGGER [2590].ISR_STAT_TRIGGER[30:30]	1
0xBB001940	ISR_STAT_TRIGGER [2591].ISR_STAT_TRIGGER[31:31]	1
0xBB001944	ISR_STAT_TRIGGER [2592].ISR_STAT_TRIGGER[0:0]	1
0xBB001944	ISR_STAT_TRIGGER [2593].ISR_STAT_TRIGGER[1:1]	1
0xBB001944	ISR_STAT_TRIGGER [2594].ISR_STAT_TRIGGER[2:2]	1
0xBB001944	ISR_STAT_TRIGGER [2595].ISR_STAT_TRIGGER[3:3]	1
0xBB001944	ISR_STAT_TRIGGER [2596].ISR_STAT_TRIGGER[4:4]	1
0xBB001944	ISR_STAT_TRIGGER [2597].ISR_STAT_TRIGGER[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001944	ISR_STAT_TRIGGER [2603].ISR_STAT_TRIGGER[11:11]	1
0xBB001944	ISR_STAT_TRIGGER [2604].ISR_STAT_TRIGGER[12:12]	1
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0xBB001944	ISR_STAT_TRIGGER [2606].ISR_STAT_TRIGGER[14:14]	1
0xBB001944	ISR_STAT_TRIGGER [2607].ISR_STAT_TRIGGER[15:15]	1
0xBB001944	ISR_STAT_TRIGGER [2608].ISR_STAT_TRIGGER[16:16]	1
0xBB001944	ISR_STAT_TRIGGER [2609].ISR_STAT_TRIGGER[17:17]	1
0xBB001944	ISR_STAT_TRIGGER [2610].ISR_STAT_TRIGGER[18:18]	1
0xBB001944	ISR_STAT_TRIGGER [2611].ISR_STAT_TRIGGER[19:19]	1
0xBB001944	ISR_STAT_TRIGGER [2612].ISR_STAT_TRIGGER[20:20]	1
0xBB001944	ISR_STAT_TRIGGER [2613].ISR_STAT_TRIGGER[21:21]	1
0xBB001944	ISR_STAT_TRIGGER [2614].ISR_STAT_TRIGGER[22:22]	1
0xBB001944	ISR_STAT_TRIGGER [2615].ISR_STAT_TRIGGER[23:23]	1
0xBB001944	ISR_STAT_TRIGGER [2616].ISR_STAT_TRIGGER[24:24]	1
0xBB001944	ISR_STAT_TRIGGER [2617].ISR_STAT_TRIGGER[25:25]	1
0xBB001944	ISR_STAT_TRIGGER [2618].ISR_STAT_TRIGGER[26:26]	1
0xBB001944	ISR_STAT_TRIGGER [2619].ISR_STAT_TRIGGER[27:27]	1
0xBB001944	ISR_STAT_TRIGGER [2620].ISR_STAT_TRIGGER[28:28]	1
0xBB001944	ISR_STAT_TRIGGER [2621].ISR_STAT_TRIGGER[29:29]	1
0xBB001944	ISR_STAT_TRIGGER [2622].ISR_STAT_TRIGGER[30:30]	1
0xBB001944	ISR_STAT_TRIGGER [2623].ISR_STAT_TRIGGER[31:31]	1
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0xBB001948	ISR_STAT_TRIGGER [2629].ISR_STAT_TRIGGER[5:5]	1
0xBB001948	ISR_STAT_TRIGGER [2630].ISR_STAT_TRIGGER[6:6]	1
0xBB001948	ISR_STAT_TRIGGER [2631].ISR_STAT_TRIGGER[7:7]	1
0xBB001948	ISR_STAT_TRIGGER [2632].ISR_STAT_TRIGGER[8:8]	1
0xBB001948	ISR_STAT_TRIGGER [2633].ISR_STAT_TRIGGER[9:9]	1
0xBB001948	ISR_STAT_TRIGGER [2634].ISR_STAT_TRIGGER[10:10]	1
0xBB001948	ISR_STAT_TRIGGER [2635].ISR_STAT_TRIGGER[11:11]	1
0xBB001948	ISR_STAT_TRIGGER [2636].ISR_STAT_TRIGGER[12:12]	1
0xBB001948	ISR_STAT_TRIGGER [2637].ISR_STAT_TRIGGER[13:13]	1
0xBB001948	ISR_STAT_TRIGGER [2638].ISR_STAT_TRIGGER[14:14]	1
0xBB001948	ISR_STAT_TRIGGER [2639].ISR_STAT_TRIGGER[15:15]	1
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0xBB001948	ISR_STAT_TRIGGER [2641].ISR_STAT_TRIGGER[17:17]	1
0xBB001948	ISR_STAT_TRIGGER [2642].ISR_STAT_TRIGGER[18:18]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001948	ISR_STAT_TRIGGER [2646].ISR_STAT_TRIGGER[22:22]	1
0xBB001948	ISR_STAT_TRIGGER [2647].ISR_STAT_TRIGGER[23:23]	1
0xBB001948	ISR_STAT_TRIGGER [2648].ISR_STAT_TRIGGER[24:24]	1
0xBB001948	ISR_STAT_TRIGGER [2649].ISR_STAT_TRIGGER[25:25]	1
0xBB001948	ISR_STAT_TRIGGER [2650].ISR_STAT_TRIGGER[26:26]	1
0xBB001948	ISR_STAT_TRIGGER [2651].ISR_STAT_TRIGGER[27:27]	1
0xBB001948	ISR_STAT_TRIGGER [2652].ISR_STAT_TRIGGER[28:28]	1
0xBB001948	ISR_STAT_TRIGGER [2653].ISR_STAT_TRIGGER[29:29]	1
0xBB001948	ISR_STAT_TRIGGER [2654].ISR_STAT_TRIGGER[30:30]	1
0xBB001948	ISR_STAT_TRIGGER [2655].ISR_STAT_TRIGGER[31:31]	1
0xBB00194C	ISR_STAT_TRIGGER [2656].ISR_STAT_TRIGGER[0:0]	1
0xBB00194C	ISR_STAT_TRIGGER [2657].ISR_STAT_TRIGGER[1:1]	1
0xBB00194C	ISR_STAT_TRIGGER [2658].ISR_STAT_TRIGGER[2:2]	1
0xBB00194C	ISR_STAT_TRIGGER [2659].ISR_STAT_TRIGGER[3:3]	1
0xBB00194C	ISR_STAT_TRIGGER [2660].ISR_STAT_TRIGGER[4:4]	1
0xBB00194C	ISR_STAT_TRIGGER [2661].ISR_STAT_TRIGGER[5:5]	1
0xBB00194C	ISR_STAT_TRIGGER [2662].ISR_STAT_TRIGGER[6:6]	1
0xBB00194C	ISR_STAT_TRIGGER [2663].ISR_STAT_TRIGGER[7:7]	1
0xBB00194C	ISR_STAT_TRIGGER [2664].ISR_STAT_TRIGGER[8:8]	1
0xBB00194C	ISR_STAT_TRIGGER [2665].ISR_STAT_TRIGGER[9:9]	1
0xBB00194C	ISR_STAT_TRIGGER [2666].ISR_STAT_TRIGGER[10:10]	1
0xBB00194C	ISR_STAT_TRIGGER [2667].ISR_STAT_TRIGGER[11:11]	1
0xBB00194C	ISR_STAT_TRIGGER [2668].ISR_STAT_TRIGGER[12:12]	1
0xBB00194C	ISR_STAT_TRIGGER [2669].ISR_STAT_TRIGGER[13:13]	1
0xBB00194C	ISR_STAT_TRIGGER [2670].ISR_STAT_TRIGGER[14:14]	1
0xBB00194C	ISR_STAT_TRIGGER [2671].ISR_STAT_TRIGGER[15:15]	1
0xBB00194C	ISR_STAT_TRIGGER [2672].ISR_STAT_TRIGGER[16:16]	1
0xBB00194C	ISR_STAT_TRIGGER [2673].ISR_STAT_TRIGGER[17:17]	1
0xBB00194C	ISR_STAT_TRIGGER [2674].ISR_STAT_TRIGGER[18:18]	1
0xBB00194C	ISR_STAT_TRIGGER [2675].ISR_STAT_TRIGGER[19:19]	1
0xBB00194C	ISR_STAT_TRIGGER [2676].ISR_STAT_TRIGGER[20:20]	1
0xBB00194C	ISR_STAT_TRIGGER [2677].ISR_STAT_TRIGGER[21:21]	1
0xBB00194C	ISR_STAT_TRIGGER [2678].ISR_STAT_TRIGGER[22:22]	1
0xBB00194C	ISR_STAT_TRIGGER [2679].ISR_STAT_TRIGGER[23:23]	1
0xBB00194C	ISR_STAT_TRIGGER [2680].ISR_STAT_TRIGGER[24:24]	1
0xBB00194C	ISR_STAT_TRIGGER [2681].ISR_STAT_TRIGGER[25:25]	1
0xBB00194C	ISR_STAT_TRIGGER [2682].ISR_STAT_TRIGGER[26:26]	1
0xBB00194C	ISR_STAT_TRIGGER [2683].ISR_STAT_TRIGGER[27:27]	1
0xBB00194C	ISR_STAT_TRIGGER [2684].ISR_STAT_TRIGGER[28:28]	1
0xBB00194C	ISR_STAT_TRIGGER [2685].ISR_STAT_TRIGGER[29:29]	1
0xBB00194C	ISR_STAT_TRIGGER [2686].ISR_STAT_TRIGGER[30:30]	1
0xBB00194C	ISR_STAT_TRIGGER [2687].ISR_STAT_TRIGGER[31:31]	1
0xBB001950	ISR_STAT_TRIGGER [2688].ISR_STAT_TRIGGER[0:0]	1
0xBB001950	ISR_STAT_TRIGGER [2689].ISR_STAT_TRIGGER[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001950	ISR_STAT_TRIGGER [2691].ISR_STAT_TRIGGER[3:3]	1
0xBB001950	ISR_STAT_TRIGGER [2692].ISR_STAT_TRIGGER[4:4]	1
0xBB001950	ISR_STAT_TRIGGER [2693].ISR_STAT_TRIGGER[5:5]	1
0xBB001950	ISR_STAT_TRIGGER [2694].ISR_STAT_TRIGGER[6:6]	1
0xBB001950	ISR_STAT_TRIGGER [2695].ISR_STAT_TRIGGER[7:7]	1
0xBB001950	ISR_STAT_TRIGGER [2696].ISR_STAT_TRIGGER[8:8]	1
0xBB001950	ISR_STAT_TRIGGER [2697].ISR_STAT_TRIGGER[9:9]	1
0xBB001950	ISR_STAT_TRIGGER [2698].ISR_STAT_TRIGGER[10:10]	1
0xBB001950	ISR_STAT_TRIGGER [2699].ISR_STAT_TRIGGER[11:11]	1
0xBB001950	ISR_STAT_TRIGGER [2700].ISR_STAT_TRIGGER[12:12]	1
0xBB001950	ISR_STAT_TRIGGER [2701].ISR_STAT_TRIGGER[13:13]	1
0xBB001950	ISR_STAT_TRIGGER [2702].ISR_STAT_TRIGGER[14:14]	1
0xBB001950	ISR_STAT_TRIGGER [2703].ISR_STAT_TRIGGER[15:15]	1
0xBB001950	ISR_STAT_TRIGGER [2704].ISR_STAT_TRIGGER[16:16]	1
0xBB001950	ISR_STAT_TRIGGER [2705].ISR_STAT_TRIGGER[17:17]	1
0xBB001950	ISR_STAT_TRIGGER [2706].ISR_STAT_TRIGGER[18:18]	1
0xBB001950	ISR_STAT_TRIGGER [2707].ISR_STAT_TRIGGER[19:19]	1
0xBB001950	ISR_STAT_TRIGGER [2708].ISR_STAT_TRIGGER[20:20]	1
0xBB001950	ISR_STAT_TRIGGER [2709].ISR_STAT_TRIGGER[21:21]	1
0xBB001950	ISR_STAT_TRIGGER [2710].ISR_STAT_TRIGGER[22:22]	1
0xBB001950	ISR_STAT_TRIGGER [2711].ISR_STAT_TRIGGER[23:23]	1
0xBB001950	ISR_STAT_TRIGGER [2712].ISR_STAT_TRIGGER[24:24]	1
0xBB001950	ISR_STAT_TRIGGER [2713].ISR_STAT_TRIGGER[25:25]	1
0xBB001950	ISR_STAT_TRIGGER [2714].ISR_STAT_TRIGGER[26:26]	1
0xBB001950	ISR_STAT_TRIGGER [2715].ISR_STAT_TRIGGER[27:27]	1
0xBB001950	ISR_STAT_TRIGGER [2716].ISR_STAT_TRIGGER[28:28]	1
0xBB001950	ISR_STAT_TRIGGER [2717].ISR_STAT_TRIGGER[29:29]	1
0xBB001950	ISR_STAT_TRIGGER [2718].ISR_STAT_TRIGGER[30:30]	1
0xBB001950	ISR_STAT_TRIGGER [2719].ISR_STAT_TRIGGER[31:31]	1
0xBB001954	ISR_STAT_TRIGGER [2720].ISR_STAT_TRIGGER[0:0]	1
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0xBB001954	ISR_STAT_TRIGGER [2722].ISR_STAT_TRIGGER[2:2]	1
0xBB001954	ISR_STAT_TRIGGER [2723].ISR_STAT_TRIGGER[3:3]	1
0xBB001954	ISR_STAT_TRIGGER [2724].ISR_STAT_TRIGGER[4:4]	1
0xBB001954	ISR_STAT_TRIGGER [2725].ISR_STAT_TRIGGER[5:5]	1
0xBB001954	ISR_STAT_TRIGGER [2726].ISR_STAT_TRIGGER[6:6]	1
0xBB001954	ISR_STAT_TRIGGER [2727].ISR_STAT_TRIGGER[7:7]	1
0xBB001954	ISR_STAT_TRIGGER [2728].ISR_STAT_TRIGGER[8:8]	1
0xBB001954	ISR_STAT_TRIGGER [2729].ISR_STAT_TRIGGER[9:9]	1
0xBB001954	ISR_STAT_TRIGGER [2730].ISR_STAT_TRIGGER[10:10]	1
0xBB001954	ISR_STAT_TRIGGER [2731].ISR_STAT_TRIGGER[11:11]	1
0xBB001954	ISR_STAT_TRIGGER [2732].ISR_STAT_TRIGGER[12:12]	1
0xBB001954	ISR_STAT_TRIGGER [2733].ISR_STAT_TRIGGER[13:13]	1
0xBB001954	ISR_STAT_TRIGGER [2734].ISR_STAT_TRIGGER[14:14]	1
0xBB001954	ISR_STAT_TRIGGER [2735].ISR_STAT_TRIGGER[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001954	ISR_STAT_TRIGGER [2739].ISR_STAT_TRIGGER[19:19]	1
0xBB001954	ISR_STAT_TRIGGER [2740].ISR_STAT_TRIGGER[20:20]	1
0xBB001954	ISR_STAT_TRIGGER [2741].ISR_STAT_TRIGGER[21:21]	1
0xBB001954	ISR_STAT_TRIGGER [2742].ISR_STAT_TRIGGER[22:22]	1
0xBB001954	ISR_STAT_TRIGGER [2743].ISR_STAT_TRIGGER[23:23]	1
0xBB001954	ISR_STAT_TRIGGER [2744].ISR_STAT_TRIGGER[24:24]	1
0xBB001954	ISR_STAT_TRIGGER [2745].ISR_STAT_TRIGGER[25:25]	1
0xBB001954	ISR_STAT_TRIGGER [2746].ISR_STAT_TRIGGER[26:26]	1
0xBB001954	ISR_STAT_TRIGGER [2747].ISR_STAT_TRIGGER[27:27]	1
0xBB001954	ISR_STAT_TRIGGER [2748].ISR_STAT_TRIGGER[28:28]	1
0xBB001954	ISR_STAT_TRIGGER [2749].ISR_STAT_TRIGGER[29:29]	1
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0xBB001954	ISR_STAT_TRIGGER [2751].ISR_STAT_TRIGGER[31:31]	1
0xBB001958	ISR_STAT_TRIGGER [2752].ISR_STAT_TRIGGER[0:0]	1
0xBB001958	ISR_STAT_TRIGGER [2753].ISR_STAT_TRIGGER[1:1]	1
0xBB001958	ISR_STAT_TRIGGER [2754].ISR_STAT_TRIGGER[2:2]	1
0xBB001958	ISR_STAT_TRIGGER [2755].ISR_STAT_TRIGGER[3:3]	1
0xBB001958	ISR_STAT_TRIGGER [2756].ISR_STAT_TRIGGER[4:4]	1
0xBB001958	ISR_STAT_TRIGGER [2757].ISR_STAT_TRIGGER[5:5]	1
0xBB001958	ISR_STAT_TRIGGER [2758].ISR_STAT_TRIGGER[6:6]	1
0xBB001958	ISR_STAT_TRIGGER [2759].ISR_STAT_TRIGGER[7:7]	1
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0xBB001958	ISR_STAT_TRIGGER [2765].ISR_STAT_TRIGGER[13:13]	1
0xBB001958	ISR_STAT_TRIGGER [2766].ISR_STAT_TRIGGER[14:14]	1
0xBB001958	ISR_STAT_TRIGGER [2767].ISR_STAT_TRIGGER[15:15]	1
0xBB001958	ISR_STAT_TRIGGER [2768].ISR_STAT_TRIGGER[16:16]	1
0xBB001958	ISR_STAT_TRIGGER [2769].ISR_STAT_TRIGGER[17:17]	1
0xBB001958	ISR_STAT_TRIGGER [2770].ISR_STAT_TRIGGER[18:18]	1
0xBB001958	ISR_STAT_TRIGGER [2771].ISR_STAT_TRIGGER[19:19]	1
0xBB001958	ISR_STAT_TRIGGER [2772].ISR_STAT_TRIGGER[20:20]	1
0xBB001958	ISR_STAT_TRIGGER [2773].ISR_STAT_TRIGGER[21:21]	1
0xBB001958	ISR_STAT_TRIGGER [2774].ISR_STAT_TRIGGER[22:22]	1
0xBB001958	ISR_STAT_TRIGGER [2775].ISR_STAT_TRIGGER[23:23]	1
0xBB001958	ISR_STAT_TRIGGER [2776].ISR_STAT_TRIGGER[24:24]	1
0xBB001958	ISR_STAT_TRIGGER [2777].ISR_STAT_TRIGGER[25:25]	1
0xBB001958	ISR_STAT_TRIGGER [2778].ISR_STAT_TRIGGER[26:26]	1
0xBB001958	ISR_STAT_TRIGGER [2779].ISR_STAT_TRIGGER[27:27]	1
0xBB001958	ISR_STAT_TRIGGER [2780].ISR_STAT_TRIGGER[28:28]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001958	ISR_STAT_TRIGGER [2783].ISR_STAT_TRIGGER[31:31]	1
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0xBB00195C	ISR_STAT_TRIGGER [2785].ISR_STAT_TRIGGER[1:1]	1
0xBB00195C	ISR_STAT_TRIGGER [2786].ISR_STAT_TRIGGER[2:2]	1
0xBB00195C	ISR_STAT_TRIGGER [2787].ISR_STAT_TRIGGER[3:3]	1
0xBB00195C	ISR_STAT_TRIGGER [2788].ISR_STAT_TRIGGER[4:4]	1
0xBB00195C	ISR_STAT_TRIGGER [2789].ISR_STAT_TRIGGER[5:5]	1
0xBB00195C	ISR_STAT_TRIGGER [2790].ISR_STAT_TRIGGER[6:6]	1
0xBB00195C	ISR_STAT_TRIGGER [2791].ISR_STAT_TRIGGER[7:7]	1
0xBB00195C	ISR_STAT_TRIGGER [2792].ISR_STAT_TRIGGER[8:8]	1
0xBB00195C	ISR_STAT_TRIGGER [2793].ISR_STAT_TRIGGER[9:9]	1
0xBB00195C	ISR_STAT_TRIGGER [2794].ISR_STAT_TRIGGER[10:10]	1
0xBB00195C	ISR_STAT_TRIGGER [2795].ISR_STAT_TRIGGER[11:11]	1
0xBB00195C	ISR_STAT_TRIGGER [2796].ISR_STAT_TRIGGER[12:12]	1
0xBB00195C	ISR_STAT_TRIGGER [2797].ISR_STAT_TRIGGER[13:13]	1
0xBB00195C	ISR_STAT_TRIGGER [2798].ISR_STAT_TRIGGER[14:14]	1
0xBB00195C	ISR_STAT_TRIGGER [2799].ISR_STAT_TRIGGER[15:15]	1
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0xBB00195C	ISR_STAT_TRIGGER [2805].ISR_STAT_TRIGGER[21:21]	1
0xBB00195C	ISR_STAT_TRIGGER [2806].ISR_STAT_TRIGGER[22:22]	1
0xBB00195C	ISR_STAT_TRIGGER [2807].ISR_STAT_TRIGGER[23:23]	1
0xBB00195C	ISR_STAT_TRIGGER [2808].ISR_STAT_TRIGGER[24:24]	1
0xBB00195C	ISR_STAT_TRIGGER [2809].ISR_STAT_TRIGGER[25:25]	1
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0xBB00195C	ISR_STAT_TRIGGER [2811].ISR_STAT_TRIGGER[27:27]	1
0xBB00195C	ISR_STAT_TRIGGER [2812].ISR_STAT_TRIGGER[28:28]	1
0xBB00195C	ISR_STAT_TRIGGER [2813].ISR_STAT_TRIGGER[29:29]	1
0xBB00195C	ISR_STAT_TRIGGER [2814].ISR_STAT_TRIGGER[30:30]	1
0xBB00195C	ISR_STAT_TRIGGER [2815].ISR_STAT_TRIGGER[31:31]	1
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0xBB001960	ISR_STAT_TRIGGER [2817].ISR_STAT_TRIGGER[1:1]	1
0xBB001960	ISR_STAT_TRIGGER [2818].ISR_STAT_TRIGGER[2:2]	1
0xBB001960	ISR_STAT_TRIGGER [2819].ISR_STAT_TRIGGER[3:3]	1
0xBB001960	ISR_STAT_TRIGGER [2820].ISR_STAT_TRIGGER[4:4]	1
0xBB001960	ISR_STAT_TRIGGER [2821].ISR_STAT_TRIGGER[5:5]	1
0xBB001960	ISR_STAT_TRIGGER [2822].ISR_STAT_TRIGGER[6:6]	1
0xBB001960	ISR_STAT_TRIGGER [2823].ISR_STAT_TRIGGER[7:7]	1
0xBB001960	ISR_STAT_TRIGGER [2824].ISR_STAT_TRIGGER[8:8]	1
0xBB001960	ISR_STAT_TRIGGER [2825].ISR_STAT_TRIGGER[9:9]	1
0xBB001960	ISR_STAT_TRIGGER [2826].ISR_STAT_TRIGGER[10:10]	1
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0xBB001960	ISR_STAT_TRIGGER [2832].ISR_STAT_TRIGGER[16:16]	1
0xBB001960	ISR_STAT_TRIGGER [2833].ISR_STAT_TRIGGER[17:17]	1
0xBB001960	ISR_STAT_TRIGGER [2834].ISR_STAT_TRIGGER[18:18]	1
0xBB001960	ISR_STAT_TRIGGER [2835].ISR_STAT_TRIGGER[19:19]	1
0xBB001960	ISR_STAT_TRIGGER [2836].ISR_STAT_TRIGGER[20:20]	1
0xBB001960	ISR_STAT_TRIGGER [2837].ISR_STAT_TRIGGER[21:21]	1
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0xBB001960	ISR_STAT_TRIGGER [2839].ISR_STAT_TRIGGER[23:23]	1
0xBB001960	ISR_STAT_TRIGGER [2840].ISR_STAT_TRIGGER[24:24]	1
0xBB001960	ISR_STAT_TRIGGER [2841].ISR_STAT_TRIGGER[25:25]	1
0xBB001960	ISR_STAT_TRIGGER [2842].ISR_STAT_TRIGGER[26:26]	1
0xBB001960	ISR_STAT_TRIGGER [2843].ISR_STAT_TRIGGER[27:27]	1
0xBB001960	ISR_STAT_TRIGGER [2844].ISR_STAT_TRIGGER[28:28]	1
0xBB001960	ISR_STAT_TRIGGER [2845].ISR_STAT_TRIGGER[29:29]	1
0xBB001960	ISR_STAT_TRIGGER [2846].ISR_STAT_TRIGGER[30:30]	1
0xBB001960	ISR_STAT_TRIGGER [2847].ISR_STAT_TRIGGER[31:31]	1
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0xBB001964	ISR_STAT_TRIGGER [2870].ISR_STAT_TRIGGER[22:22]	1
0xBB001964	ISR_STAT_TRIGGER [2871].ISR_STAT_TRIGGER[23:23]	1
0xBB001964	ISR_STAT_TRIGGER [2872].ISR_STAT_TRIGGER[24:24]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001964	ISR_STAT_TRIGGER [2876].ISR_STAT_TRIGGER[28:28]	1
0xBB001964	ISR_STAT_TRIGGER [2877].ISR_STAT_TRIGGER[29:29]	1
0xBB001964	ISR_STAT_TRIGGER [2878].ISR_STAT_TRIGGER[30:30]	1
0xBB001964	ISR_STAT_TRIGGER [2879].ISR_STAT_TRIGGER[31:31]	1
0xBB001968	ISR_STAT_TRIGGER [2880].ISR_STAT_TRIGGER[0:0]	1
0xBB001968	ISR_STAT_TRIGGER [2881].ISR_STAT_TRIGGER[1:1]	1
0xBB001968	ISR_STAT_TRIGGER [2882].ISR_STAT_TRIGGER[2:2]	1
0xBB001968	ISR_STAT_TRIGGER [2883].ISR_STAT_TRIGGER[3:3]	1
0xBB001968	ISR_STAT_TRIGGER [2884].ISR_STAT_TRIGGER[4:4]	1
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0xBB001968	ISR_STAT_TRIGGER [2886].ISR_STAT_TRIGGER[6:6]	1
0xBB001968	ISR_STAT_TRIGGER [2887].ISR_STAT_TRIGGER[7:7]	1
0xBB001968	ISR_STAT_TRIGGER [2888].ISR_STAT_TRIGGER[8:8]	1
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0xBB001968	ISR_STAT_TRIGGER [2892].ISR_STAT_TRIGGER[12:12]	1
0xBB001968	ISR_STAT_TRIGGER [2893].ISR_STAT_TRIGGER[13:13]	1
0xBB001968	ISR_STAT_TRIGGER [2894].ISR_STAT_TRIGGER[14:14]	1
0xBB001968	ISR_STAT_TRIGGER [2895].ISR_STAT_TRIGGER[15:15]	1
0xBB001968	ISR_STAT_TRIGGER [2896].ISR_STAT_TRIGGER[16:16]	1
0xBB001968	ISR_STAT_TRIGGER [2897].ISR_STAT_TRIGGER[17:17]	1
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0xBB001968	ISR_STAT_TRIGGER [2903].ISR_STAT_TRIGGER[23:23]	1
0xBB001968	ISR_STAT_TRIGGER [2904].ISR_STAT_TRIGGER[24:24]	1
0xBB001968	ISR_STAT_TRIGGER [2905].ISR_STAT_TRIGGER[25:25]	1
0xBB001968	ISR_STAT_TRIGGER [2906].ISR_STAT_TRIGGER[26:26]	1
0xBB001968	ISR_STAT_TRIGGER [2907].ISR_STAT_TRIGGER[27:27]	1
0xBB001968	ISR_STAT_TRIGGER [2908].ISR_STAT_TRIGGER[28:28]	1
0xBB001968	ISR_STAT_TRIGGER [2909].ISR_STAT_TRIGGER[29:29]	1
0xBB001968	ISR_STAT_TRIGGER [2910].ISR_STAT_TRIGGER[30:30]	1
0xBB001968	ISR_STAT_TRIGGER [2911].ISR_STAT_TRIGGER[31:31]	1
0xBB00196C	ISR_STAT_TRIGGER [2912].ISR_STAT_TRIGGER[0:0]	1
0xBB00196C	ISR_STAT_TRIGGER [2913].ISR_STAT_TRIGGER[1:1]	1
0xBB00196C	ISR_STAT_TRIGGER [2914].ISR_STAT_TRIGGER[2:2]	1
0xBB00196C	ISR_STAT_TRIGGER [2915].ISR_STAT_TRIGGER[3:3]	1
0xBB00196C	ISR_STAT_TRIGGER [2916].ISR_STAT_TRIGGER[4:4]	1
0xBB00196C	ISR_STAT_TRIGGER [2917].ISR_STAT_TRIGGER[5:5]	1
0xBB00196C	ISR_STAT_TRIGGER [2918].ISR_STAT_TRIGGER[6:6]	1
0xBB00196C	ISR_STAT_TRIGGER [2919].ISR_STAT_TRIGGER[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00196C	ISR_STAT_TRIGGER [2922].ISR_STAT_TRIGGER[10:10]	1
0xBB00196C	ISR_STAT_TRIGGER [2923].ISR_STAT_TRIGGER[11:11]	1
0xBB00196C	ISR_STAT_TRIGGER [2924].ISR_STAT_TRIGGER[12:12]	1
0xBB00196C	ISR_STAT_TRIGGER [2925].ISR_STAT_TRIGGER[13:13]	1
0xBB00196C	ISR_STAT_TRIGGER [2926].ISR_STAT_TRIGGER[14:14]	1
0xBB00196C	ISR_STAT_TRIGGER [2927].ISR_STAT_TRIGGER[15:15]	1
0xBB00196C	ISR_STAT_TRIGGER [2928].ISR_STAT_TRIGGER[16:16]	1
0xBB00196C	ISR_STAT_TRIGGER [2929].ISR_STAT_TRIGGER[17:17]	1
0xBB00196C	ISR_STAT_TRIGGER [2930].ISR_STAT_TRIGGER[18:18]	1
0xBB00196C	ISR_STAT_TRIGGER [2931].ISR_STAT_TRIGGER[19:19]	1
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0xBB00196C	ISR_STAT_TRIGGER [2940].ISR_STAT_TRIGGER[28:28]	1
0xBB00196C	ISR_STAT_TRIGGER [2941].ISR_STAT_TRIGGER[29:29]	1
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0xBB001970	ISR_STAT_TRIGGER [2945].ISR_STAT_TRIGGER[1:1]	1
0xBB001970	ISR_STAT_TRIGGER [2946].ISR_STAT_TRIGGER[2:2]	1
0xBB001970	ISR_STAT_TRIGGER [2947].ISR_STAT_TRIGGER[3:3]	1
0xBB001970	ISR_STAT_TRIGGER [2948].ISR_STAT_TRIGGER[4:4]	1
0xBB001970	ISR_STAT_TRIGGER [2949].ISR_STAT_TRIGGER[5:5]	1
0xBB001970	ISR_STAT_TRIGGER [2950].ISR_STAT_TRIGGER[6:6]	1
0xBB001970	ISR_STAT_TRIGGER [2951].ISR_STAT_TRIGGER[7:7]	1
0xBB001970	ISR_STAT_TRIGGER [2952].ISR_STAT_TRIGGER[8:8]	1
0xBB001970	ISR_STAT_TRIGGER [2953].ISR_STAT_TRIGGER[9:9]	1
0xBB001970	ISR_STAT_TRIGGER [2954].ISR_STAT_TRIGGER[10:10]	1
0xBB001970	ISR_STAT_TRIGGER [2955].ISR_STAT_TRIGGER[11:11]	1
0xBB001970	ISR_STAT_TRIGGER [2956].ISR_STAT_TRIGGER[12:12]	1
0xBB001970	ISR_STAT_TRIGGER [2957].ISR_STAT_TRIGGER[13:13]	1
0xBB001970	ISR_STAT_TRIGGER [2958].ISR_STAT_TRIGGER[14:14]	1
0xBB001970	ISR_STAT_TRIGGER [2959].ISR_STAT_TRIGGER[15:15]	1
0xBB001970	ISR_STAT_TRIGGER [2960].ISR_STAT_TRIGGER[16:16]	1
0xBB001970	ISR_STAT_TRIGGER [2961].ISR_STAT_TRIGGER[17:17]	1
0xBB001970	ISR_STAT_TRIGGER [2962].ISR_STAT_TRIGGER[18:18]	1
0xBB001970	ISR_STAT_TRIGGER [2963].ISR_STAT_TRIGGER[19:19]	1
0xBB001970	ISR_STAT_TRIGGER [2964].ISR_STAT_TRIGGER[20:20]	1
0xBB001970	ISR_STAT_TRIGGER [2965].ISR_STAT_TRIGGER[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001970	ISR_STAT_TRIGGER [2967].ISR_STAT_TRIGGER[23:23]	1
0xBB001970	ISR_STAT_TRIGGER [2968].ISR_STAT_TRIGGER[24:24]	1
0xBB001970	ISR_STAT_TRIGGER [2969].ISR_STAT_TRIGGER[25:25]	1
0xBB001970	ISR_STAT_TRIGGER [2970].ISR_STAT_TRIGGER[26:26]	1
0xBB001970	ISR_STAT_TRIGGER [2971].ISR_STAT_TRIGGER[27:27]	1
0xBB001970	ISR_STAT_TRIGGER [2972].ISR_STAT_TRIGGER[28:28]	1
0xBB001970	ISR_STAT_TRIGGER [2973].ISR_STAT_TRIGGER[29:29]	1
0xBB001970	ISR_STAT_TRIGGER [2974].ISR_STAT_TRIGGER[30:30]	1
0xBB001970	ISR_STAT_TRIGGER [2975].ISR_STAT_TRIGGER[31:31]	1
0xBB001974	ISR_STAT_TRIGGER [2976].ISR_STAT_TRIGGER[0:0]	1
0xBB001974	ISR_STAT_TRIGGER [2977].ISR_STAT_TRIGGER[1:1]	1
0xBB001974	ISR_STAT_TRIGGER [2978].ISR_STAT_TRIGGER[2:2]	1
0xBB001974	ISR_STAT_TRIGGER [2979].ISR_STAT_TRIGGER[3:3]	1
0xBB001974	ISR_STAT_TRIGGER [2980].ISR_STAT_TRIGGER[4:4]	1
0xBB001974	ISR_STAT_TRIGGER [2981].ISR_STAT_TRIGGER[5:5]	1
0xBB001974	ISR_STAT_TRIGGER [2982].ISR_STAT_TRIGGER[6:6]	1
0xBB001974	ISR_STAT_TRIGGER [2983].ISR_STAT_TRIGGER[7:7]	1
0xBB001974	ISR_STAT_TRIGGER [2984].ISR_STAT_TRIGGER[8:8]	1
0xBB001974	ISR_STAT_TRIGGER [2985].ISR_STAT_TRIGGER[9:9]	1
0xBB001974	ISR_STAT_TRIGGER [2986].ISR_STAT_TRIGGER[10:10]	1
0xBB001974	ISR_STAT_TRIGGER [2987].ISR_STAT_TRIGGER[11:11]	1
0xBB001974	ISR_STAT_TRIGGER [2988].ISR_STAT_TRIGGER[12:12]	1
0xBB001974	ISR_STAT_TRIGGER [2989].ISR_STAT_TRIGGER[13:13]	1
0xBB001974	ISR_STAT_TRIGGER [2990].ISR_STAT_TRIGGER[14:14]	1
0xBB001974	ISR_STAT_TRIGGER [2991].ISR_STAT_TRIGGER[15:15]	1
0xBB001974	ISR_STAT_TRIGGER [2992].ISR_STAT_TRIGGER[16:16]	1
0xBB001974	ISR_STAT_TRIGGER [2993].ISR_STAT_TRIGGER[17:17]	1
0xBB001974	ISR_STAT_TRIGGER [2994].ISR_STAT_TRIGGER[18:18]	1
0xBB001974	ISR_STAT_TRIGGER [2995].ISR_STAT_TRIGGER[19:19]	1
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0xBB001974	ISR_STAT_TRIGGER [2999].ISR_STAT_TRIGGER[23:23]	1
0xBB001974	ISR_STAT_TRIGGER [3000].ISR_STAT_TRIGGER[24:24]	1
0xBB001974	ISR_STAT_TRIGGER [3001].ISR_STAT_TRIGGER[25:25]	1
0xBB001974	ISR_STAT_TRIGGER [3002].ISR_STAT_TRIGGER[26:26]	1
0xBB001974	ISR_STAT_TRIGGER [3003].ISR_STAT_TRIGGER[27:27]	1
0xBB001974	ISR_STAT_TRIGGER [3004].ISR_STAT_TRIGGER[28:28]	1
0xBB001974	ISR_STAT_TRIGGER [3005].ISR_STAT_TRIGGER[29:29]	1
0xBB001974	ISR_STAT_TRIGGER [3006].ISR_STAT_TRIGGER[30:30]	1
0xBB001974	ISR_STAT_TRIGGER [3007].ISR_STAT_TRIGGER[31:31]	1
0xBB001978	ISR_STAT_TRIGGER [3008].ISR_STAT_TRIGGER[0:0]	1
0xBB001978	ISR_STAT_TRIGGER [3009].ISR_STAT_TRIGGER[1:1]	1
0xBB001978	ISR_STAT_TRIGGER [3010].ISR_STAT_TRIGGER[2:2]	1
0xBB001978	ISR_STAT_TRIGGER [3011].ISR_STAT_TRIGGER[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001978	ISR_STAT_TRIGGER [3013].ISR_STAT_TRIGGER[5:5]	1
0xBB001978	ISR_STAT_TRIGGER [3014].ISR_STAT_TRIGGER[6:6]	1
0xBB001978	ISR_STAT_TRIGGER [3015].ISR_STAT_TRIGGER[7:7]	1
0xBB001978	ISR_STAT_TRIGGER [3016].ISR_STAT_TRIGGER[8:8]	1
0xBB001978	ISR_STAT_TRIGGER [3017].ISR_STAT_TRIGGER[9:9]	1
0xBB001978	ISR_STAT_TRIGGER [3018].ISR_STAT_TRIGGER[10:10]	1
0xBB001978	ISR_STAT_TRIGGER [3019].ISR_STAT_TRIGGER[11:11]	1
0xBB001978	ISR_STAT_TRIGGER [3020].ISR_STAT_TRIGGER[12:12]	1
0xBB001978	ISR_STAT_TRIGGER [3021].ISR_STAT_TRIGGER[13:13]	1
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0xBB001978	ISR_STAT_TRIGGER [3025].ISR_STAT_TRIGGER[17:17]	1
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0xBB001978	ISR_STAT_TRIGGER [3028].ISR_STAT_TRIGGER[20:20]	1
0xBB001978	ISR_STAT_TRIGGER [3029].ISR_STAT_TRIGGER[21:21]	1
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0xBB001978	ISR_STAT_TRIGGER [3031].ISR_STAT_TRIGGER[23:23]	1
0xBB001978	ISR_STAT_TRIGGER [3032].ISR_STAT_TRIGGER[24:24]	1
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0xBB001978	ISR_STAT_TRIGGER [3035].ISR_STAT_TRIGGER[27:27]	1
0xBB001978	ISR_STAT_TRIGGER [3036].ISR_STAT_TRIGGER[28:28]	1
0xBB001978	ISR_STAT_TRIGGER [3037].ISR_STAT_TRIGGER[29:29]	1
0xBB001978	ISR_STAT_TRIGGER [3038].ISR_STAT_TRIGGER[30:30]	1
0xBB001978	ISR_STAT_TRIGGER [3039].ISR_STAT_TRIGGER[31:31]	1
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0xBB00197C	ISR_STAT_TRIGGER [3041].ISR_STAT_TRIGGER[1:1]	1
0xBB00197C	ISR_STAT_TRIGGER [3042].ISR_STAT_TRIGGER[2:2]	1
0xBB00197C	ISR_STAT_TRIGGER [3043].ISR_STAT_TRIGGER[3:3]	1
0xBB00197C	ISR_STAT_TRIGGER [3044].ISR_STAT_TRIGGER[4:4]	1
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0xBB00197C	ISR_STAT_TRIGGER [3046].ISR_STAT_TRIGGER[6:6]	1
0xBB00197C	ISR_STAT_TRIGGER [3047].ISR_STAT_TRIGGER[7:7]	1
0xBB00197C	ISR_STAT_TRIGGER [3048].ISR_STAT_TRIGGER[8:8]	1
0xBB00197C	ISR_STAT_TRIGGER [3049].ISR_STAT_TRIGGER[9:9]	1
0xBB00197C	ISR_STAT_TRIGGER [3050].ISR_STAT_TRIGGER[10:10]	1
0xBB00197C	ISR_STAT_TRIGGER [3051].ISR_STAT_TRIGGER[11:11]	1
0xBB00197C	ISR_STAT_TRIGGER [3052].ISR_STAT_TRIGGER[12:12]	1
0xBB00197C	ISR_STAT_TRIGGER [3053].ISR_STAT_TRIGGER[13:13]	1
0xBB00197C	ISR_STAT_TRIGGER [3054].ISR_STAT_TRIGGER[14:14]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00197C	ISR_STAT_TRIGGER [3062].ISR_STAT_TRIGGER[22:22]	1
0xBB00197C	ISR_STAT_TRIGGER [3063].ISR_STAT_TRIGGER[23:23]	1
0xBB00197C	ISR_STAT_TRIGGER [3064].ISR_STAT_TRIGGER[24:24]	1
0xBB00197C	ISR_STAT_TRIGGER [3065].ISR_STAT_TRIGGER[25:25]	1
0xBB00197C	ISR_STAT_TRIGGER [3066].ISR_STAT_TRIGGER[26:26]	1
0xBB00197C	ISR_STAT_TRIGGER [3067].ISR_STAT_TRIGGER[27:27]	1
0xBB00197C	ISR_STAT_TRIGGER [3068].ISR_STAT_TRIGGER[28:28]	1
0xBB00197C	ISR_STAT_TRIGGER [3069].ISR_STAT_TRIGGER[29:29]	1
0xBB00197C	ISR_STAT_TRIGGER [3070].ISR_STAT_TRIGGER[30:30]	1
0xBB00197C	ISR_STAT_TRIGGER [3071].ISR_STAT_TRIGGER[31:31]	1
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0xBB001980	ISR_STAT_TRIGGER [3073].ISR_STAT_TRIGGER[1:1]	1
0xBB001980	ISR_STAT_TRIGGER [3074].ISR_STAT_TRIGGER[2:2]	1
0xBB001980	ISR_STAT_TRIGGER [3075].ISR_STAT_TRIGGER[3:3]	1
0xBB001980	ISR_STAT_TRIGGER [3076].ISR_STAT_TRIGGER[4:4]	1
0xBB001980	ISR_STAT_TRIGGER [3077].ISR_STAT_TRIGGER[5:5]	1
0xBB001980	ISR_STAT_TRIGGER [3078].ISR_STAT_TRIGGER[6:6]	1
0xBB001980	ISR_STAT_TRIGGER [3079].ISR_STAT_TRIGGER[7:7]	1
0xBB001980	ISR_STAT_TRIGGER [3080].ISR_STAT_TRIGGER[8:8]	1
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0xBB001980	ISR_STAT_TRIGGER [3083].ISR_STAT_TRIGGER[11:11]	1
0xBB001980	ISR_STAT_TRIGGER [3084].ISR_STAT_TRIGGER[12:12]	1
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0xBB001980	ISR_STAT_TRIGGER [3093].ISR_STAT_TRIGGER[21:21]	1
0xBB001980	ISR_STAT_TRIGGER [3094].ISR_STAT_TRIGGER[22:22]	1
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0xBB001980	ISR_STAT_TRIGGER [3097].ISR_STAT_TRIGGER[25:25]	1
0xBB001980	ISR_STAT_TRIGGER [3098].ISR_STAT_TRIGGER[26:26]	1
0xBB001980	ISR_STAT_TRIGGER [3099].ISR_STAT_TRIGGER[27:27]	1
0xBB001980	ISR_STAT_TRIGGER [3100].ISR_STAT_TRIGGER[28:28]	1
0xBB001980	ISR_STAT_TRIGGER [3101].ISR_STAT_TRIGGER[29:29]	1
0xBB001980	ISR_STAT_TRIGGER [3102].ISR_STAT_TRIGGER[30:30]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001984	ISR_STAT_TRIGGER [3106].ISR_STAT_TRIGGER[2:2]	1
0xBB001984	ISR_STAT_TRIGGER [3107].ISR_STAT_TRIGGER[3:3]	1
0xBB001984	ISR_STAT_TRIGGER [3108].ISR_STAT_TRIGGER[4:4]	1
0xBB001984	ISR_STAT_TRIGGER [3109].ISR_STAT_TRIGGER[5:5]	1
0xBB001984	ISR_STAT_TRIGGER [3110].ISR_STAT_TRIGGER[6:6]	1
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0xBB001984	ISR_STAT_TRIGGER [3114].ISR_STAT_TRIGGER[10:10]	1
0xBB001984	ISR_STAT_TRIGGER [3115].ISR_STAT_TRIGGER[11:11]	1
0xBB001984	ISR_STAT_TRIGGER [3116].ISR_STAT_TRIGGER[12:12]	1
0xBB001984	ISR_STAT_TRIGGER [3117].ISR_STAT_TRIGGER[13:13]	1
0xBB001984	ISR_STAT_TRIGGER [3118].ISR_STAT_TRIGGER[14:14]	1
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0xBB001984	ISR_STAT_TRIGGER [3135].ISR_STAT_TRIGGER[31:31]	1
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0xBB001988	ISR_STAT_TRIGGER [3138].ISR_STAT_TRIGGER[2:2]	1
0xBB001988	ISR_STAT_TRIGGER [3139].ISR_STAT_TRIGGER[3:3]	1
0xBB001988	ISR_STAT_TRIGGER [3140].ISR_STAT_TRIGGER[4:4]	1
0xBB001988	ISR_STAT_TRIGGER [3141].ISR_STAT_TRIGGER[5:5]	1
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0xBB001988	ISR_STAT_TRIGGER [3145].ISR_STAT_TRIGGER[9:9]	1
0xBB001988	ISR_STAT_TRIGGER [3146].ISR_STAT_TRIGGER[10:10]	1
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0xBB001988	ISR_STAT_TRIGGER [3148].ISR_STAT_TRIGGER[12:12]	1
0xBB001988	ISR_STAT_TRIGGER [3149].ISR_STAT_TRIGGER[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001988	ISR_STAT_TRIGGER [3159].ISR_STAT_TRIGGER[23:23]	1
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0xBB001988	ISR_STAT_TRIGGER [3161].ISR_STAT_TRIGGER[25:25]	1
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0xBB001988	ISR_STAT_TRIGGER [3163].ISR_STAT_TRIGGER[27:27]	1
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0xBB00198C	ISR_STAT_TRIGGER [3177].ISR_STAT_TRIGGER[9:9]	1
0xBB00198C	ISR_STAT_TRIGGER [3178].ISR_STAT_TRIGGER[10:10]	1
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0xBB00198C	ISR_STAT_TRIGGER [3189].ISR_STAT_TRIGGER[21:21]	1
0xBB00198C	ISR_STAT_TRIGGER [3190].ISR_STAT_TRIGGER[22:22]	1
0xBB00198C	ISR_STAT_TRIGGER [3191].ISR_STAT_TRIGGER[23:23]	1
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0xBB00198C	ISR_STAT_TRIGGER [3194].ISR_STAT_TRIGGER[26:26]	1
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0xBB001990	ISR_STAT_TRIGGER [3203].ISR_STAT_TRIGGER[3:3]	1
0xBB001990	ISR_STAT_TRIGGER [3204].ISR_STAT_TRIGGER[4:4]	1
0xBB001990	ISR_STAT_TRIGGER [3205].ISR_STAT_TRIGGER[5:5]	1
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0xBB001990	ISR_STAT_TRIGGER [3228].ISR_STAT_TRIGGER[28:28]	1
0xBB001990	ISR_STAT_TRIGGER [3229].ISR_STAT_TRIGGER[29:29]	1
0xBB001990	ISR_STAT_TRIGGER [3230].ISR_STAT_TRIGGER[30:30]	1
0xBB001990	ISR_STAT_TRIGGER [3231].ISR_STAT_TRIGGER[31:31]	1
0xBB001994	ISR_STAT_TRIGGER [3232].ISR_STAT_TRIGGER[0:0]	1
0xBB001994	ISR_STAT_TRIGGER [3233].ISR_STAT_TRIGGER[1:1]	1
0xBB001994	ISR_STAT_TRIGGER [3234].ISR_STAT_TRIGGER[2:2]	1
0xBB001994	ISR_STAT_TRIGGER [3235].ISR_STAT_TRIGGER[3:3]	1
0xBB001994	ISR_STAT_TRIGGER [3236].ISR_STAT_TRIGGER[4:4]	1
0xBB001994	ISR_STAT_TRIGGER [3237].ISR_STAT_TRIGGER[5:5]	1
0xBB001994	ISR_STAT_TRIGGER [3238].ISR_STAT_TRIGGER[6:6]	1
0xBB001994	ISR_STAT_TRIGGER [3239].ISR_STAT_TRIGGER[7:7]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB001994	ISR_STAT_TRIGGER [3251].ISR_STAT_TRIGGER[19:19]	1
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0xBB001994	ISR_STAT_TRIGGER [3253].ISR_STAT_TRIGGER[21:21]	1
0xBB001994	ISR_STAT_TRIGGER [3254].ISR_STAT_TRIGGER[22:22]	1
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0xBB001998	ISR_STAT_TRIGGER [3268].ISR_STAT_TRIGGER[4:4]	1
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0xBB001998	ISR_STAT_TRIGGER [3272].ISR_STAT_TRIGGER[8:8]	1
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0xBB001998	ISR_STAT_TRIGGER [3274].ISR_STAT_TRIGGER[10:10]	1
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0xBB001998	ISR_STAT_TRIGGER [3277].ISR_STAT_TRIGGER[13:13]	1
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0xBB001998	ISR_STAT_TRIGGER [3279].ISR_STAT_TRIGGER[15:15]	1
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0xBB001998	ISR_STAT_TRIGGER [3283].ISR_STAT_TRIGGER[19:19]	1
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0xBB001998	ISR_STAT_TRIGGER [3294].ISR_STAT_TRIGGER[30:30]	1
0xBB001998	ISR_STAT_TRIGGER [3295].ISR_STAT_TRIGGER[31:31]	1
0xBB00199C	ISR_STAT_TRIGGER [3296].ISR_STAT_TRIGGER[0:0]	1
0xBB00199C	ISR_STAT_TRIGGER [3297].ISR_STAT_TRIGGER[1:1]	1
0xBB00199C	ISR_STAT_TRIGGER [3298].ISR_STAT_TRIGGER[2:2]	1
0xBB00199C	ISR_STAT_TRIGGER [3299].ISR_STAT_TRIGGER[3:3]	1
0xBB00199C	ISR_STAT_TRIGGER [3300].ISR_STAT_TRIGGER[4:4]	1
0xBB00199C	ISR_STAT_TRIGGER [3301].ISR_STAT_TRIGGER[5:5]	1
0xBB00199C	ISR_STAT_TRIGGER [3302].ISR_STAT_TRIGGER[6:6]	1
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0xBB00199C	ISR_STAT_TRIGGER [3304].ISR_STAT_TRIGGER[8:8]	1
0xBB00199C	ISR_STAT_TRIGGER [3305].ISR_STAT_TRIGGER[9:9]	1
0xBB00199C	ISR_STAT_TRIGGER [3306].ISR_STAT_TRIGGER[10:10]	1
0xBB00199C	ISR_STAT_TRIGGER [3307].ISR_STAT_TRIGGER[11:11]	1
0xBB00199C	ISR_STAT_TRIGGER [3308].ISR_STAT_TRIGGER[12:12]	1
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0xBB00199C	ISR_STAT_TRIGGER [3327].ISR_STAT_TRIGGER[31:31]	1
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0xBB0019A0	ISR_STAT_TRIGGER [3329].ISR_STAT_TRIGGER[1:1]	1
0xBB0019A0	ISR_STAT_TRIGGER [3330].ISR_STAT_TRIGGER[2:2]	1
0xBB0019A0	ISR_STAT_TRIGGER [3331].ISR_STAT_TRIGGER[3:3]	1
0xBB0019A0	ISR_STAT_TRIGGER [3332].ISR_STAT_TRIGGER[4:4]	1
0xBB0019A0	ISR_STAT_TRIGGER [3333].ISR_STAT_TRIGGER[5:5]	1

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0xBB0019A0	ISR_STAT_TRIGGER [3335].ISR_STAT_TRIGGER[7:7]	1
0xBB0019A0	ISR_STAT_TRIGGER [3336].ISR_STAT_TRIGGER[8:8]	1
0xBB0019A0	ISR_STAT_TRIGGER [3337].ISR_STAT_TRIGGER[9:9]	1
0xBB0019A0	ISR_STAT_TRIGGER [3338].ISR_STAT_TRIGGER[10:10]	1
0xBB0019A0	ISR_STAT_TRIGGER [3339].ISR_STAT_TRIGGER[11:11]	1
0xBB0019A0	ISR_STAT_TRIGGER [3340].ISR_STAT_TRIGGER[12:12]	1
0xBB0019A0	ISR_STAT_TRIGGER [3341].ISR_STAT_TRIGGER[13:13]	1
0xBB0019A0	ISR_STAT_TRIGGER [3342].ISR_STAT_TRIGGER[14:14]	1
0xBB0019A0	ISR_STAT_TRIGGER [3343].ISR_STAT_TRIGGER[15:15]	1
0xBB0019A0	ISR_STAT_TRIGGER [3344].ISR_STAT_TRIGGER[16:16]	1
0xBB0019A0	ISR_STAT_TRIGGER [3345].ISR_STAT_TRIGGER[17:17]	1
0xBB0019A0	ISR_STAT_TRIGGER [3346].ISR_STAT_TRIGGER[18:18]	1
0xBB0019A0	ISR_STAT_TRIGGER [3347].ISR_STAT_TRIGGER[19:19]	1
0xBB0019A0	ISR_STAT_TRIGGER [3348].ISR_STAT_TRIGGER[20:20]	1
0xBB0019A0	ISR_STAT_TRIGGER [3349].ISR_STAT_TRIGGER[21:21]	1
0xBB0019A0	ISR_STAT_TRIGGER [3350].ISR_STAT_TRIGGER[22:22]	1
0xBB0019A0	ISR_STAT_TRIGGER [3351].ISR_STAT_TRIGGER[23:23]	1
0xBB0019A0	ISR_STAT_TRIGGER [3352].ISR_STAT_TRIGGER[24:24]	1
0xBB0019A0	ISR_STAT_TRIGGER [3353].ISR_STAT_TRIGGER[25:25]	1
0xBB0019A0	ISR_STAT_TRIGGER [3354].ISR_STAT_TRIGGER[26:26]	1
0xBB0019A0	ISR_STAT_TRIGGER [3355].ISR_STAT_TRIGGER[27:27]	1
0xBB0019A0	ISR_STAT_TRIGGER [3356].ISR_STAT_TRIGGER[28:28]	1
0xBB0019A0	ISR_STAT_TRIGGER [3357].ISR_STAT_TRIGGER[29:29]	1
0xBB0019A0	ISR_STAT_TRIGGER [3358].ISR_STAT_TRIGGER[30:30]	1
0xBB0019A0	ISR_STAT_TRIGGER [3359].ISR_STAT_TRIGGER[31:31]	1
0xBB0019A4	ISR_STAT_TRIGGER [3360].ISR_STAT_TRIGGER[0:0]	1
0xBB0019A4	ISR_STAT_TRIGGER [3361].ISR_STAT_TRIGGER[1:1]	1
0xBB0019A4	ISR_STAT_TRIGGER [3362].ISR_STAT_TRIGGER[2:2]	1
0xBB0019A4	ISR_STAT_TRIGGER [3363].ISR_STAT_TRIGGER[3:3]	1
0xBB0019A4	ISR_STAT_TRIGGER [3364].ISR_STAT_TRIGGER[4:4]	1
0xBB0019A4	ISR_STAT_TRIGGER [3365].ISR_STAT_TRIGGER[5:5]	1
0xBB0019A4	ISR_STAT_TRIGGER [3366].ISR_STAT_TRIGGER[6:6]	1
0xBB0019A4	ISR_STAT_TRIGGER [3367].ISR_STAT_TRIGGER[7:7]	1
0xBB0019A4	ISR_STAT_TRIGGER [3368].ISR_STAT_TRIGGER[8:8]	1
0xBB0019A4	ISR_STAT_TRIGGER [3369].ISR_STAT_TRIGGER[9:9]	1
0xBB0019A4	ISR_STAT_TRIGGER [3370].ISR_STAT_TRIGGER[10:10]	1
0xBB0019A4	ISR_STAT_TRIGGER [3371].ISR_STAT_TRIGGER[11:11]	1
0xBB0019A4	ISR_STAT_TRIGGER [3372].ISR_STAT_TRIGGER[12:12]	1
0xBB0019A4	ISR_STAT_TRIGGER [3373].ISR_STAT_TRIGGER[13:13]	1
0xBB0019A4	ISR_STAT_TRIGGER [3374].ISR_STAT_TRIGGER[14:14]	1
0xBB0019A4	ISR_STAT_TRIGGER [3375].ISR_STAT_TRIGGER[15:15]	1
0xBB0019A4	ISR_STAT_TRIGGER [3376].ISR_STAT_TRIGGER[16:16]	1
0xBB0019A4	ISR_STAT_TRIGGER [3377].ISR_STAT_TRIGGER[17:17]	1
0xBB0019A4	ISR_STAT_TRIGGER [3378].ISR_STAT_TRIGGER[18:18]	1
0xBB0019A4	ISR_STAT_TRIGGER [3379].ISR_STAT_TRIGGER[19:19]	1

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0xBB0019A4	ISR_STAT_TRIGGER [3382].ISR_STAT_TRIGGER[22:22]	1
0xBB0019A4	ISR_STAT_TRIGGER [3383].ISR_STAT_TRIGGER[23:23]	1
0xBB0019A4	ISR_STAT_TRIGGER [3384].ISR_STAT_TRIGGER[24:24]	1
0xBB0019A4	ISR_STAT_TRIGGER [3385].ISR_STAT_TRIGGER[25:25]	1
0xBB0019A4	ISR_STAT_TRIGGER [3386].ISR_STAT_TRIGGER[26:26]	1
0xBB0019A4	ISR_STAT_TRIGGER [3387].ISR_STAT_TRIGGER[27:27]	1
0xBB0019A4	ISR_STAT_TRIGGER [3388].ISR_STAT_TRIGGER[28:28]	1
0xBB0019A4	ISR_STAT_TRIGGER [3389].ISR_STAT_TRIGGER[29:29]	1
0xBB0019A4	ISR_STAT_TRIGGER [3390].ISR_STAT_TRIGGER[30:30]	1
0xBB0019A4	ISR_STAT_TRIGGER [3391].ISR_STAT_TRIGGER[31:31]	1
0xBB0019A8	ISR_STAT_TRIGGER [3392].ISR_STAT_TRIGGER[0:0]	1
0xBB0019A8	ISR_STAT_TRIGGER [3393].ISR_STAT_TRIGGER[1:1]	1
0xBB0019A8	ISR_STAT_TRIGGER [3394].ISR_STAT_TRIGGER[2:2]	1
0xBB0019A8	ISR_STAT_TRIGGER [3395].ISR_STAT_TRIGGER[3:3]	1
0xBB0019A8	ISR_STAT_TRIGGER [3396].ISR_STAT_TRIGGER[4:4]	1
0xBB0019A8	ISR_STAT_TRIGGER [3397].ISR_STAT_TRIGGER[5:5]	1
0xBB0019A8	ISR_STAT_TRIGGER [3398].ISR_STAT_TRIGGER[6:6]	1
0xBB0019A8	ISR_STAT_TRIGGER [3399].ISR_STAT_TRIGGER[7:7]	1
0xBB0019A8	ISR_STAT_TRIGGER [3400].ISR_STAT_TRIGGER[8:8]	1
0xBB0019A8	ISR_STAT_TRIGGER [3401].ISR_STAT_TRIGGER[9:9]	1
0xBB0019A8	ISR_STAT_TRIGGER [3402].ISR_STAT_TRIGGER[10:10]	1
0xBB0019A8	ISR_STAT_TRIGGER [3403].ISR_STAT_TRIGGER[11:11]	1
0xBB0019A8	ISR_STAT_TRIGGER [3404].ISR_STAT_TRIGGER[12:12]	1
0xBB0019A8	ISR_STAT_TRIGGER [3405].ISR_STAT_TRIGGER[13:13]	1
0xBB0019A8	ISR_STAT_TRIGGER [3406].ISR_STAT_TRIGGER[14:14]	1
0xBB0019A8	ISR_STAT_TRIGGER [3407].ISR_STAT_TRIGGER[15:15]	1
0xBB0019A8	ISR_STAT_TRIGGER [3408].ISR_STAT_TRIGGER[16:16]	1
0xBB0019A8	ISR_STAT_TRIGGER [3409].ISR_STAT_TRIGGER[17:17]	1
0xBB0019A8	ISR_STAT_TRIGGER [3410].ISR_STAT_TRIGGER[18:18]	1
0xBB0019A8	ISR_STAT_TRIGGER [3411].ISR_STAT_TRIGGER[19:19]	1
0xBB0019A8	ISR_STAT_TRIGGER [3412].ISR_STAT_TRIGGER[20:20]	1
0xBB0019A8	ISR_STAT_TRIGGER [3413].ISR_STAT_TRIGGER[21:21]	1
0xBB0019A8	ISR_STAT_TRIGGER [3414].ISR_STAT_TRIGGER[22:22]	1
0xBB0019A8	ISR_STAT_TRIGGER [3415].ISR_STAT_TRIGGER[23:23]	1
0xBB0019A8	ISR_STAT_TRIGGER [3416].ISR_STAT_TRIGGER[24:24]	1
0xBB0019A8	ISR_STAT_TRIGGER [3417].ISR_STAT_TRIGGER[25:25]	1
0xBB0019A8	ISR_STAT_TRIGGER [3418].ISR_STAT_TRIGGER[26:26]	1
0xBB0019A8	ISR_STAT_TRIGGER [3419].ISR_STAT_TRIGGER[27:27]	1
0xBB0019A8	ISR_STAT_TRIGGER [3420].ISR_STAT_TRIGGER[28:28]	1
0xBB0019A8	ISR_STAT_TRIGGER [3421].ISR_STAT_TRIGGER[29:29]	1
0xBB0019A8	ISR_STAT_TRIGGER [3422].ISR_STAT_TRIGGER[30:30]	1
0xBB0019A8	ISR_STAT_TRIGGER [3423].ISR_STAT_TRIGGER[31:31]	1
0xBB0019AC	ISR_STAT_TRIGGER [3424].ISR_STAT_TRIGGER[0:0]	1
0xBB0019AC	ISR_STAT_TRIGGER [3425].ISR_STAT_TRIGGER[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019AC	ISR_STAT_TRIGGER [3427].ISR_STAT_TRIGGER[3:3]	1
0xBB0019AC	ISR_STAT_TRIGGER [3428].ISR_STAT_TRIGGER[4:4]	1
0xBB0019AC	ISR_STAT_TRIGGER [3429].ISR_STAT_TRIGGER[5:5]	1
0xBB0019AC	ISR_STAT_TRIGGER [3430].ISR_STAT_TRIGGER[6:6]	1
0xBB0019AC	ISR_STAT_TRIGGER [3431].ISR_STAT_TRIGGER[7:7]	1
0xBB0019AC	ISR_STAT_TRIGGER [3432].ISR_STAT_TRIGGER[8:8]	1
0xBB0019AC	ISR_STAT_TRIGGER [3433].ISR_STAT_TRIGGER[9:9]	1
0xBB0019AC	ISR_STAT_TRIGGER [3434].ISR_STAT_TRIGGER[10:10]	1
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0xBB0019AC	ISR_STAT_TRIGGER [3436].ISR_STAT_TRIGGER[12:12]	1
0xBB0019AC	ISR_STAT_TRIGGER [3437].ISR_STAT_TRIGGER[13:13]	1
0xBB0019AC	ISR_STAT_TRIGGER [3438].ISR_STAT_TRIGGER[14:14]	1
0xBB0019AC	ISR_STAT_TRIGGER [3439].ISR_STAT_TRIGGER[15:15]	1
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0xBB0019AC	ISR_STAT_TRIGGER [3441].ISR_STAT_TRIGGER[17:17]	1
0xBB0019AC	ISR_STAT_TRIGGER [3442].ISR_STAT_TRIGGER[18:18]	1
0xBB0019AC	ISR_STAT_TRIGGER [3443].ISR_STAT_TRIGGER[19:19]	1
0xBB0019AC	ISR_STAT_TRIGGER [3444].ISR_STAT_TRIGGER[20:20]	1
0xBB0019AC	ISR_STAT_TRIGGER [3445].ISR_STAT_TRIGGER[21:21]	1
0xBB0019AC	ISR_STAT_TRIGGER [3446].ISR_STAT_TRIGGER[22:22]	1
0xBB0019AC	ISR_STAT_TRIGGER [3447].ISR_STAT_TRIGGER[23:23]	1
0xBB0019AC	ISR_STAT_TRIGGER [3448].ISR_STAT_TRIGGER[24:24]	1
0xBB0019AC	ISR_STAT_TRIGGER [3449].ISR_STAT_TRIGGER[25:25]	1
0xBB0019AC	ISR_STAT_TRIGGER [3450].ISR_STAT_TRIGGER[26:26]	1
0xBB0019AC	ISR_STAT_TRIGGER [3451].ISR_STAT_TRIGGER[27:27]	1
0xBB0019AC	ISR_STAT_TRIGGER [3452].ISR_STAT_TRIGGER[28:28]	1
0xBB0019AC	ISR_STAT_TRIGGER [3453].ISR_STAT_TRIGGER[29:29]	1
0xBB0019AC	ISR_STAT_TRIGGER [3454].ISR_STAT_TRIGGER[30:30]	1
0xBB0019AC	ISR_STAT_TRIGGER [3455].ISR_STAT_TRIGGER[31:31]	1
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0xBB0019B0	ISR_STAT_TRIGGER [3457].ISR_STAT_TRIGGER[1:1]	1
0xBB0019B0	ISR_STAT_TRIGGER [3458].ISR_STAT_TRIGGER[2:2]	1
0xBB0019B0	ISR_STAT_TRIGGER [3459].ISR_STAT_TRIGGER[3:3]	1
0xBB0019B0	ISR_STAT_TRIGGER [3460].ISR_STAT_TRIGGER[4:4]	1
0xBB0019B0	ISR_STAT_TRIGGER [3461].ISR_STAT_TRIGGER[5:5]	1
0xBB0019B0	ISR_STAT_TRIGGER [3462].ISR_STAT_TRIGGER[6:6]	1
0xBB0019B0	ISR_STAT_TRIGGER [3463].ISR_STAT_TRIGGER[7:7]	1
0xBB0019B0	ISR_STAT_TRIGGER [3464].ISR_STAT_TRIGGER[8:8]	1
0xBB0019B0	ISR_STAT_TRIGGER [3465].ISR_STAT_TRIGGER[9:9]	1
0xBB0019B0	ISR_STAT_TRIGGER [3466].ISR_STAT_TRIGGER[10:10]	1
0xBB0019B0	ISR_STAT_TRIGGER [3467].ISR_STAT_TRIGGER[11:11]	1
0xBB0019B0	ISR_STAT_TRIGGER [3468].ISR_STAT_TRIGGER[12:12]	1
0xBB0019B0	ISR_STAT_TRIGGER [3469].ISR_STAT_TRIGGER[13:13]	1
0xBB0019B0	ISR_STAT_TRIGGER [3470].ISR_STAT_TRIGGER[14:14]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019B0	ISR_STAT_TRIGGER [3474].ISR_STAT_TRIGGER[18:18]	1
0xBB0019B0	ISR_STAT_TRIGGER [3475].ISR_STAT_TRIGGER[19:19]	1
0xBB0019B0	ISR_STAT_TRIGGER [3476].ISR_STAT_TRIGGER[20:20]	1
0xBB0019B0	ISR_STAT_TRIGGER [3477].ISR_STAT_TRIGGER[21:21]	1
0xBB0019B0	ISR_STAT_TRIGGER [3478].ISR_STAT_TRIGGER[22:22]	1
0xBB0019B0	ISR_STAT_TRIGGER [3479].ISR_STAT_TRIGGER[23:23]	1
0xBB0019B0	ISR_STAT_TRIGGER [3480].ISR_STAT_TRIGGER[24:24]	1
0xBB0019B0	ISR_STAT_TRIGGER [3481].ISR_STAT_TRIGGER[25:25]	1
0xBB0019B0	ISR_STAT_TRIGGER [3482].ISR_STAT_TRIGGER[26:26]	1
0xBB0019B0	ISR_STAT_TRIGGER [3483].ISR_STAT_TRIGGER[27:27]	1
0xBB0019B0	ISR_STAT_TRIGGER [3484].ISR_STAT_TRIGGER[28:28]	1
0xBB0019B0	ISR_STAT_TRIGGER [3485].ISR_STAT_TRIGGER[29:29]	1
0xBB0019B0	ISR_STAT_TRIGGER [3486].ISR_STAT_TRIGGER[30:30]	1
0xBB0019B0	ISR_STAT_TRIGGER [3487].ISR_STAT_TRIGGER[31:31]	1
0xBB0019B4	ISR_STAT_TRIGGER [3488].ISR_STAT_TRIGGER[0:0]	1
0xBB0019B4	ISR_STAT_TRIGGER [3489].ISR_STAT_TRIGGER[1:1]	1
0xBB0019B4	ISR_STAT_TRIGGER [3490].ISR_STAT_TRIGGER[2:2]	1
0xBB0019B4	ISR_STAT_TRIGGER [3491].ISR_STAT_TRIGGER[3:3]	1
0xBB0019B4	ISR_STAT_TRIGGER [3492].ISR_STAT_TRIGGER[4:4]	1
0xBB0019B4	ISR_STAT_TRIGGER [3493].ISR_STAT_TRIGGER[5:5]	1
0xBB0019B4	ISR_STAT_TRIGGER [3494].ISR_STAT_TRIGGER[6:6]	1
0xBB0019B4	ISR_STAT_TRIGGER [3495].ISR_STAT_TRIGGER[7:7]	1
0xBB0019B4	ISR_STAT_TRIGGER [3496].ISR_STAT_TRIGGER[8:8]	1
0xBB0019B4	ISR_STAT_TRIGGER [3497].ISR_STAT_TRIGGER[9:9]	1
0xBB0019B4	ISR_STAT_TRIGGER [3498].ISR_STAT_TRIGGER[10:10]	1
0xBB0019B4	ISR_STAT_TRIGGER [3499].ISR_STAT_TRIGGER[11:11]	1
0xBB0019B4	ISR_STAT_TRIGGER [3500].ISR_STAT_TRIGGER[12:12]	1
0xBB0019B4	ISR_STAT_TRIGGER [3501].ISR_STAT_TRIGGER[13:13]	1
0xBB0019B4	ISR_STAT_TRIGGER [3502].ISR_STAT_TRIGGER[14:14]	1
0xBB0019B4	ISR_STAT_TRIGGER [3503].ISR_STAT_TRIGGER[15:15]	1
0xBB0019B4	ISR_STAT_TRIGGER [3504].ISR_STAT_TRIGGER[16:16]	1
0xBB0019B4	ISR_STAT_TRIGGER [3505].ISR_STAT_TRIGGER[17:17]	1
0xBB0019B4	ISR_STAT_TRIGGER [3506].ISR_STAT_TRIGGER[18:18]	1
0xBB0019B4	ISR_STAT_TRIGGER [3507].ISR_STAT_TRIGGER[19:19]	1
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0xBB0019B4	ISR_STAT_TRIGGER [3511].ISR_STAT_TRIGGER[23:23]	1
0xBB0019B4	ISR_STAT_TRIGGER [3512].ISR_STAT_TRIGGER[24:24]	1
0xBB0019B4	ISR_STAT_TRIGGER [3513].ISR_STAT_TRIGGER[25:25]	1
0xBB0019B4	ISR_STAT_TRIGGER [3514].ISR_STAT_TRIGGER[26:26]	1
0xBB0019B4	ISR_STAT_TRIGGER [3515].ISR_STAT_TRIGGER[27:27]	1
0xBB0019B4	ISR_STAT_TRIGGER [3516].ISR_STAT_TRIGGER[28:28]	1
0xBB0019B4	ISR_STAT_TRIGGER [3517].ISR_STAT_TRIGGER[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019B8	ISR_STAT_TRIGGER [3521].ISR_STAT_TRIGGER[1:1]	1
0xBB0019B8	ISR_STAT_TRIGGER [3522].ISR_STAT_TRIGGER[2:2]	1
0xBB0019B8	ISR_STAT_TRIGGER [3523].ISR_STAT_TRIGGER[3:3]	1
0xBB0019B8	ISR_STAT_TRIGGER [3524].ISR_STAT_TRIGGER[4:4]	1
0xBB0019B8	ISR_STAT_TRIGGER [3525].ISR_STAT_TRIGGER[5:5]	1
0xBB0019B8	ISR_STAT_TRIGGER [3526].ISR_STAT_TRIGGER[6:6]	1
0xBB0019B8	ISR_STAT_TRIGGER [3527].ISR_STAT_TRIGGER[7:7]	1
0xBB0019B8	ISR_STAT_TRIGGER [3528].ISR_STAT_TRIGGER[8:8]	1
0xBB0019B8	ISR_STAT_TRIGGER [3529].ISR_STAT_TRIGGER[9:9]	1
0xBB0019B8	ISR_STAT_TRIGGER [3530].ISR_STAT_TRIGGER[10:10]	1
0xBB0019B8	ISR_STAT_TRIGGER [3531].ISR_STAT_TRIGGER[11:11]	1
0xBB0019B8	ISR_STAT_TRIGGER [3532].ISR_STAT_TRIGGER[12:12]	1
0xBB0019B8	ISR_STAT_TRIGGER [3533].ISR_STAT_TRIGGER[13:13]	1
0xBB0019B8	ISR_STAT_TRIGGER [3534].ISR_STAT_TRIGGER[14:14]	1
0xBB0019B8	ISR_STAT_TRIGGER [3535].ISR_STAT_TRIGGER[15:15]	1
0xBB0019B8	ISR_STAT_TRIGGER [3536].ISR_STAT_TRIGGER[16:16]	1
0xBB0019B8	ISR_STAT_TRIGGER [3537].ISR_STAT_TRIGGER[17:17]	1
0xBB0019B8	ISR_STAT_TRIGGER [3538].ISR_STAT_TRIGGER[18:18]	1
0xBB0019B8	ISR_STAT_TRIGGER [3539].ISR_STAT_TRIGGER[19:19]	1
0xBB0019B8	ISR_STAT_TRIGGER [3540].ISR_STAT_TRIGGER[20:20]	1
0xBB0019B8	ISR_STAT_TRIGGER [3541].ISR_STAT_TRIGGER[21:21]	1
0xBB0019B8	ISR_STAT_TRIGGER [3542].ISR_STAT_TRIGGER[22:22]	1
0xBB0019B8	ISR_STAT_TRIGGER [3543].ISR_STAT_TRIGGER[23:23]	1
0xBB0019B8	ISR_STAT_TRIGGER [3544].ISR_STAT_TRIGGER[24:24]	1
0xBB0019B8	ISR_STAT_TRIGGER [3545].ISR_STAT_TRIGGER[25:25]	1
0xBB0019B8	ISR_STAT_TRIGGER [3546].ISR_STAT_TRIGGER[26:26]	1
0xBB0019B8	ISR_STAT_TRIGGER [3547].ISR_STAT_TRIGGER[27:27]	1
0xBB0019B8	ISR_STAT_TRIGGER [3548].ISR_STAT_TRIGGER[28:28]	1
0xBB0019B8	ISR_STAT_TRIGGER [3549].ISR_STAT_TRIGGER[29:29]	1
0xBB0019B8	ISR_STAT_TRIGGER [3550].ISR_STAT_TRIGGER[30:30]	1
0xBB0019B8	ISR_STAT_TRIGGER [3551].ISR_STAT_TRIGGER[31:31]	1
0xBB0019BC	ISR_STAT_TRIGGER [3552].ISR_STAT_TRIGGER[0:0]	1
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0xBB0019BC	ISR_STAT_TRIGGER [3554].ISR_STAT_TRIGGER[2:2]	1
0xBB0019BC	ISR_STAT_TRIGGER [3555].ISR_STAT_TRIGGER[3:3]	1
0xBB0019BC	ISR_STAT_TRIGGER [3556].ISR_STAT_TRIGGER[4:4]	1
0xBB0019BC	ISR_STAT_TRIGGER [3557].ISR_STAT_TRIGGER[5:5]	1
0xBB0019BC	ISR_STAT_TRIGGER [3558].ISR_STAT_TRIGGER[6:6]	1
0xBB0019BC	ISR_STAT_TRIGGER [3559].ISR_STAT_TRIGGER[7:7]	1
0xBB0019BC	ISR_STAT_TRIGGER [3560].ISR_STAT_TRIGGER[8:8]	1
0xBB0019BC	ISR_STAT_TRIGGER [3561].ISR_STAT_TRIGGER[9:9]	1
0xBB0019BC	ISR_STAT_TRIGGER [3562].ISR_STAT_TRIGGER[10:10]	1
0xBB0019BC	ISR_STAT_TRIGGER [3563].ISR_STAT_TRIGGER[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0019BC	ISR_STAT_TRIGGER [3564].ISR_STAT_TRIGGER[12:12]	1
0xBB0019BC	ISR_STAT_TRIGGER [3565].ISR_STAT_TRIGGER[13:13]	1
0xBB0019BC	ISR_STAT_TRIGGER [3566].ISR_STAT_TRIGGER[14:14]	1
0xBB0019BC	ISR_STAT_TRIGGER [3567].ISR_STAT_TRIGGER[15:15]	1
0xBB0019BC	ISR_STAT_TRIGGER [3568].ISR_STAT_TRIGGER[16:16]	1
0xBB0019BC	ISR_STAT_TRIGGER [3569].ISR_STAT_TRIGGER[17:17]	1
0xBB0019BC	ISR_STAT_TRIGGER [3570].ISR_STAT_TRIGGER[18:18]	1
0xBB0019BC	ISR_STAT_TRIGGER [3571].ISR_STAT_TRIGGER[19:19]	1
0xBB0019BC	ISR_STAT_TRIGGER [3572].ISR_STAT_TRIGGER[20:20]	1
0xBB0019BC	ISR_STAT_TRIGGER [3573].ISR_STAT_TRIGGER[21:21]	1
0xBB0019BC	ISR_STAT_TRIGGER [3574].ISR_STAT_TRIGGER[22:22]	1
0xBB0019BC	ISR_STAT_TRIGGER [3575].ISR_STAT_TRIGGER[23:23]	1
0xBB0019BC	ISR_STAT_TRIGGER [3576].ISR_STAT_TRIGGER[24:24]	1
0xBB0019BC	ISR_STAT_TRIGGER [3577].ISR_STAT_TRIGGER[25:25]	1
0xBB0019BC	ISR_STAT_TRIGGER [3578].ISR_STAT_TRIGGER[26:26]	1
0xBB0019BC	ISR_STAT_TRIGGER [3579].ISR_STAT_TRIGGER[27:27]	1
0xBB0019BC	ISR_STAT_TRIGGER [3580].ISR_STAT_TRIGGER[28:28]	1
0xBB0019BC	ISR_STAT_TRIGGER [3581].ISR_STAT_TRIGGER[29:29]	1
0xBB0019BC	ISR_STAT_TRIGGER [3582].ISR_STAT_TRIGGER[30:30]	1
0xBB0019BC	ISR_STAT_TRIGGER [3583].ISR_STAT_TRIGGER[31:31]	1
0xBB0019C0	ISR_STAT_TRIGGER [3584].ISR_STAT_TRIGGER[0:0]	1
0xBB0019C0	ISR_STAT_TRIGGER [3585].ISR_STAT_TRIGGER[1:1]	1
0xBB0019C0	ISR_STAT_TRIGGER [3586].ISR_STAT_TRIGGER[2:2]	1
0xBB0019C0	ISR_STAT_TRIGGER [3587].ISR_STAT_TRIGGER[3:3]	1
0xBB0019C0	ISR_STAT_TRIGGER [3588].ISR_STAT_TRIGGER[4:4]	1
0xBB0019C0	ISR_STAT_TRIGGER [3589].ISR_STAT_TRIGGER[5:5]	1
0xBB0019C0	ISR_STAT_TRIGGER [3590].ISR_STAT_TRIGGER[6:6]	1
0xBB0019C0	ISR_STAT_TRIGGER [3591].ISR_STAT_TRIGGER[7:7]	1
0xBB0019C0	ISR_STAT_TRIGGER [3592].ISR_STAT_TRIGGER[8:8]	1
0xBB0019C0	ISR_STAT_TRIGGER [3593].ISR_STAT_TRIGGER[9:9]	1
0xBB0019C0	ISR_STAT_TRIGGER [3594].ISR_STAT_TRIGGER[10:10]	1
0xBB0019C0	ISR_STAT_TRIGGER [3595].ISR_STAT_TRIGGER[11:11]	1
0xBB0019C0	ISR_STAT_TRIGGER [3596].ISR_STAT_TRIGGER[12:12]	1
0xBB0019C0	ISR_STAT_TRIGGER [3597].ISR_STAT_TRIGGER[13:13]	1
0xBB0019C0	ISR_STAT_TRIGGER [3598].ISR_STAT_TRIGGER[14:14]	1
0xBB0019C0	ISR_STAT_TRIGGER [3599].ISR_STAT_TRIGGER[15:15]	1
0xBB0019C0	ISR_STAT_TRIGGER [3600].ISR_STAT_TRIGGER[16:16]	1
0xBB0019C0	ISR_STAT_TRIGGER [3601].ISR_STAT_TRIGGER[17:17]	1
0xBB0019C0	ISR_STAT_TRIGGER [3602].ISR_STAT_TRIGGER[18:18]	1
0xBB0019C0	ISR_STAT_TRIGGER [3603].ISR_STAT_TRIGGER[19:19]	1
0xBB0019C0	ISR_STAT_TRIGGER [3604].ISR_STAT_TRIGGER[20:20]	1
0xBB0019C0	ISR_STAT_TRIGGER [3605].ISR_STAT_TRIGGER[21:21]	1
0xBB0019C0	ISR_STAT_TRIGGER [3606].ISR_STAT_TRIGGER[22:22]	1
0xBB0019C0	ISR_STAT_TRIGGER [3607].ISR_STAT_TRIGGER[23:23]	1
0xBB0019C0	ISR_STAT_TRIGGER [3608].ISR_STAT_TRIGGER[24:24]	1
0xBB0019C0	ISR_STAT_TRIGGER [3609].ISR_STAT_TRIGGER[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0019C0	ISR_STAT_TRIGGER [3610].ISR_STAT_TRIGGER[26:26]	1
0xBB0019C0	ISR_STAT_TRIGGER [3611].ISR_STAT_TRIGGER[27:27]	1
0xBB0019C0	ISR_STAT_TRIGGER [3612].ISR_STAT_TRIGGER[28:28]	1
0xBB0019C0	ISR_STAT_TRIGGER [3613].ISR_STAT_TRIGGER[29:29]	1
0xBB0019C0	ISR_STAT_TRIGGER [3614].ISR_STAT_TRIGGER[30:30]	1
0xBB0019C0	ISR_STAT_TRIGGER [3615].ISR_STAT_TRIGGER[31:31]	1
0xBB0019C4	ISR_STAT_TRIGGER [3616].ISR_STAT_TRIGGER[0:0]	1
0xBB0019C4	ISR_STAT_TRIGGER [3617].ISR_STAT_TRIGGER[1:1]	1
0xBB0019C4	ISR_STAT_TRIGGER [3618].ISR_STAT_TRIGGER[2:2]	1
0xBB0019C4	ISR_STAT_TRIGGER [3619].ISR_STAT_TRIGGER[3:3]	1
0xBB0019C4	ISR_STAT_TRIGGER [3620].ISR_STAT_TRIGGER[4:4]	1
0xBB0019C4	ISR_STAT_TRIGGER [3621].ISR_STAT_TRIGGER[5:5]	1
0xBB0019C4	ISR_STAT_TRIGGER [3622].ISR_STAT_TRIGGER[6:6]	1
0xBB0019C4	ISR_STAT_TRIGGER [3623].ISR_STAT_TRIGGER[7:7]	1
0xBB0019C4	ISR_STAT_TRIGGER [3624].ISR_STAT_TRIGGER[8:8]	1
0xBB0019C4	ISR_STAT_TRIGGER [3625].ISR_STAT_TRIGGER[9:9]	1
0xBB0019C4	ISR_STAT_TRIGGER [3626].ISR_STAT_TRIGGER[10:10]	1
0xBB0019C4	ISR_STAT_TRIGGER [3627].ISR_STAT_TRIGGER[11:11]	1
0xBB0019C4	ISR_STAT_TRIGGER [3628].ISR_STAT_TRIGGER[12:12]	1
0xBB0019C4	ISR_STAT_TRIGGER [3629].ISR_STAT_TRIGGER[13:13]	1
0xBB0019C4	ISR_STAT_TRIGGER [3630].ISR_STAT_TRIGGER[14:14]	1
0xBB0019C4	ISR_STAT_TRIGGER [3631].ISR_STAT_TRIGGER[15:15]	1
0xBB0019C4	ISR_STAT_TRIGGER [3632].ISR_STAT_TRIGGER[16:16]	1
0xBB0019C4	ISR_STAT_TRIGGER [3633].ISR_STAT_TRIGGER[17:17]	1
0xBB0019C4	ISR_STAT_TRIGGER [3634].ISR_STAT_TRIGGER[18:18]	1
0xBB0019C4	ISR_STAT_TRIGGER [3635].ISR_STAT_TRIGGER[19:19]	1
0xBB0019C4	ISR_STAT_TRIGGER [3636].ISR_STAT_TRIGGER[20:20]	1
0xBB0019C4	ISR_STAT_TRIGGER [3637].ISR_STAT_TRIGGER[21:21]	1
0xBB0019C4	ISR_STAT_TRIGGER [3638].ISR_STAT_TRIGGER[22:22]	1
0xBB0019C4	ISR_STAT_TRIGGER [3639].ISR_STAT_TRIGGER[23:23]	1
0xBB0019C4	ISR_STAT_TRIGGER [3640].ISR_STAT_TRIGGER[24:24]	1
0xBB0019C4	ISR_STAT_TRIGGER [3641].ISR_STAT_TRIGGER[25:25]	1
0xBB0019C4	ISR_STAT_TRIGGER [3642].ISR_STAT_TRIGGER[26:26]	1
0xBB0019C4	ISR_STAT_TRIGGER [3643].ISR_STAT_TRIGGER[27:27]	1
0xBB0019C4	ISR_STAT_TRIGGER [3644].ISR_STAT_TRIGGER[28:28]	1
0xBB0019C4	ISR_STAT_TRIGGER [3645].ISR_STAT_TRIGGER[29:29]	1
0xBB0019C4	ISR_STAT_TRIGGER [3646].ISR_STAT_TRIGGER[30:30]	1
0xBB0019C4	ISR_STAT_TRIGGER [3647].ISR_STAT_TRIGGER[31:31]	1
0xBB0019C8	ISR_STAT_TRIGGER [3648].ISR_STAT_TRIGGER[0:0]	1
0xBB0019C8	ISR_STAT_TRIGGER [3649].ISR_STAT_TRIGGER[1:1]	1
0xBB0019C8	ISR_STAT_TRIGGER [3650].ISR_STAT_TRIGGER[2:2]	1
0xBB0019C8	ISR_STAT_TRIGGER [3651].ISR_STAT_TRIGGER[3:3]	1
0xBB0019C8	ISR_STAT_TRIGGER [3652].ISR_STAT_TRIGGER[4:4]	1
0xBB0019C8	ISR_STAT_TRIGGER [3653].ISR_STAT_TRIGGER[5:5]	1
0xBB0019C8	ISR_STAT_TRIGGER [3654].ISR_STAT_TRIGGER[6:6]	1
0xBB0019C8	ISR_STAT_TRIGGER [3655].ISR_STAT_TRIGGER[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019C8	ISR_STAT_TRIGGER [3657].ISR_STAT_TRIGGER[9:9]	1
0xBB0019C8	ISR_STAT_TRIGGER [3658].ISR_STAT_TRIGGER[10:10]	1
0xBB0019C8	ISR_STAT_TRIGGER [3659].ISR_STAT_TRIGGER[11:11]	1
0xBB0019C8	ISR_STAT_TRIGGER [3660].ISR_STAT_TRIGGER[12:12]	1
0xBB0019C8	ISR_STAT_TRIGGER [3661].ISR_STAT_TRIGGER[13:13]	1
0xBB0019C8	ISR_STAT_TRIGGER [3662].ISR_STAT_TRIGGER[14:14]	1
0xBB0019C8	ISR_STAT_TRIGGER [3663].ISR_STAT_TRIGGER[15:15]	1
0xBB0019C8	ISR_STAT_TRIGGER [3664].ISR_STAT_TRIGGER[16:16]	1
0xBB0019C8	ISR_STAT_TRIGGER [3665].ISR_STAT_TRIGGER[17:17]	1
0xBB0019C8	ISR_STAT_TRIGGER [3666].ISR_STAT_TRIGGER[18:18]	1
0xBB0019C8	ISR_STAT_TRIGGER [3667].ISR_STAT_TRIGGER[19:19]	1
0xBB0019C8	ISR_STAT_TRIGGER [3668].ISR_STAT_TRIGGER[20:20]	1
0xBB0019C8	ISR_STAT_TRIGGER [3669].ISR_STAT_TRIGGER[21:21]	1
0xBB0019C8	ISR_STAT_TRIGGER [3670].ISR_STAT_TRIGGER[22:22]	1
0xBB0019C8	ISR_STAT_TRIGGER [3671].ISR_STAT_TRIGGER[23:23]	1
0xBB0019C8	ISR_STAT_TRIGGER [3672].ISR_STAT_TRIGGER[24:24]	1
0xBB0019C8	ISR_STAT_TRIGGER [3673].ISR_STAT_TRIGGER[25:25]	1
0xBB0019C8	ISR_STAT_TRIGGER [3674].ISR_STAT_TRIGGER[26:26]	1
0xBB0019C8	ISR_STAT_TRIGGER [3675].ISR_STAT_TRIGGER[27:27]	1
0xBB0019C8	ISR_STAT_TRIGGER [3676].ISR_STAT_TRIGGER[28:28]	1
0xBB0019C8	ISR_STAT_TRIGGER [3677].ISR_STAT_TRIGGER[29:29]	1
0xBB0019C8	ISR_STAT_TRIGGER [3678].ISR_STAT_TRIGGER[30:30]	1
0xBB0019C8	ISR_STAT_TRIGGER [3679].ISR_STAT_TRIGGER[31:31]	1
0xBB0019CC	ISR_STAT_TRIGGER [3680].ISR_STAT_TRIGGER[0:0]	1
0xBB0019CC	ISR_STAT_TRIGGER [3681].ISR_STAT_TRIGGER[1:1]	1
0xBB0019CC	ISR_STAT_TRIGGER [3682].ISR_STAT_TRIGGER[2:2]	1
0xBB0019CC	ISR_STAT_TRIGGER [3683].ISR_STAT_TRIGGER[3:3]	1
0xBB0019CC	ISR_STAT_TRIGGER [3684].ISR_STAT_TRIGGER[4:4]	1
0xBB0019CC	ISR_STAT_TRIGGER [3685].ISR_STAT_TRIGGER[5:5]	1
0xBB0019CC	ISR_STAT_TRIGGER [3686].ISR_STAT_TRIGGER[6:6]	1
0xBB0019CC	ISR_STAT_TRIGGER [3687].ISR_STAT_TRIGGER[7:7]	1
0xBB0019CC	ISR_STAT_TRIGGER [3688].ISR_STAT_TRIGGER[8:8]	1
0xBB0019CC	ISR_STAT_TRIGGER [3689].ISR_STAT_TRIGGER[9:9]	1
0xBB0019CC	ISR_STAT_TRIGGER [3690].ISR_STAT_TRIGGER[10:10]	1
0xBB0019CC	ISR_STAT_TRIGGER [3691].ISR_STAT_TRIGGER[11:11]	1
0xBB0019CC	ISR_STAT_TRIGGER [3692].ISR_STAT_TRIGGER[12:12]	1
0xBB0019CC	ISR_STAT_TRIGGER [3693].ISR_STAT_TRIGGER[13:13]	1
0xBB0019CC	ISR_STAT_TRIGGER [3694].ISR_STAT_TRIGGER[14:14]	1
0xBB0019CC	ISR_STAT_TRIGGER [3695].ISR_STAT_TRIGGER[15:15]	1
0xBB0019CC	ISR_STAT_TRIGGER [3696].ISR_STAT_TRIGGER[16:16]	1
0xBB0019CC	ISR_STAT_TRIGGER [3697].ISR_STAT_TRIGGER[17:17]	1
0xBB0019CC	ISR_STAT_TRIGGER [3698].ISR_STAT_TRIGGER[18:18]	1
0xBB0019CC	ISR_STAT_TRIGGER [3699].ISR_STAT_TRIGGER[19:19]	1
0xBB0019CC	ISR_STAT_TRIGGER [3700].ISR_STAT_TRIGGER[20:20]	1
0xBB0019CC	ISR_STAT_TRIGGER [3701].ISR_STAT_TRIGGER[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019CC	ISR_STAT_TRIGGER [3703].ISR_STAT_TRIGGER[23:23]	1
0xBB0019CC	ISR_STAT_TRIGGER [3704].ISR_STAT_TRIGGER[24:24]	1
0xBB0019CC	ISR_STAT_TRIGGER [3705].ISR_STAT_TRIGGER[25:25]	1
0xBB0019CC	ISR_STAT_TRIGGER [3706].ISR_STAT_TRIGGER[26:26]	1
0xBB0019CC	ISR_STAT_TRIGGER [3707].ISR_STAT_TRIGGER[27:27]	1
0xBB0019CC	ISR_STAT_TRIGGER [3708].ISR_STAT_TRIGGER[28:28]	1
0xBB0019CC	ISR_STAT_TRIGGER [3709].ISR_STAT_TRIGGER[29:29]	1
0xBB0019CC	ISR_STAT_TRIGGER [3710].ISR_STAT_TRIGGER[30:30]	1
0xBB0019CC	ISR_STAT_TRIGGER [3711].ISR_STAT_TRIGGER[31:31]	1
0xBB0019D0	ISR_STAT_TRIGGER [3712].ISR_STAT_TRIGGER[0:0]	1
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0xBB0019D0	ISR_STAT_TRIGGER [3714].ISR_STAT_TRIGGER[2:2]	1
0xBB0019D0	ISR_STAT_TRIGGER [3715].ISR_STAT_TRIGGER[3:3]	1
0xBB0019D0	ISR_STAT_TRIGGER [3716].ISR_STAT_TRIGGER[4:4]	1
0xBB0019D0	ISR_STAT_TRIGGER [3717].ISR_STAT_TRIGGER[5:5]	1
0xBB0019D0	ISR_STAT_TRIGGER [3718].ISR_STAT_TRIGGER[6:6]	1
0xBB0019D0	ISR_STAT_TRIGGER [3719].ISR_STAT_TRIGGER[7:7]	1
0xBB0019D0	ISR_STAT_TRIGGER [3720].ISR_STAT_TRIGGER[8:8]	1
0xBB0019D0	ISR_STAT_TRIGGER [3721].ISR_STAT_TRIGGER[9:9]	1
0xBB0019D0	ISR_STAT_TRIGGER [3722].ISR_STAT_TRIGGER[10:10]	1
0xBB0019D0	ISR_STAT_TRIGGER [3723].ISR_STAT_TRIGGER[11:11]	1
0xBB0019D0	ISR_STAT_TRIGGER [3724].ISR_STAT_TRIGGER[12:12]	1
0xBB0019D0	ISR_STAT_TRIGGER [3725].ISR_STAT_TRIGGER[13:13]	1
0xBB0019D0	ISR_STAT_TRIGGER [3726].ISR_STAT_TRIGGER[14:14]	1
0xBB0019D0	ISR_STAT_TRIGGER [3727].ISR_STAT_TRIGGER[15:15]	1
0xBB0019D0	ISR_STAT_TRIGGER [3728].ISR_STAT_TRIGGER[16:16]	1
0xBB0019D0	ISR_STAT_TRIGGER [3729].ISR_STAT_TRIGGER[17:17]	1
0xBB0019D0	ISR_STAT_TRIGGER [3730].ISR_STAT_TRIGGER[18:18]	1
0xBB0019D0	ISR_STAT_TRIGGER [3731].ISR_STAT_TRIGGER[19:19]	1
0xBB0019D0	ISR_STAT_TRIGGER [3732].ISR_STAT_TRIGGER[20:20]	1
0xBB0019D0	ISR_STAT_TRIGGER [3733].ISR_STAT_TRIGGER[21:21]	1
0xBB0019D0	ISR_STAT_TRIGGER [3734].ISR_STAT_TRIGGER[22:22]	1
0xBB0019D0	ISR_STAT_TRIGGER [3735].ISR_STAT_TRIGGER[23:23]	1
0xBB0019D0	ISR_STAT_TRIGGER [3736].ISR_STAT_TRIGGER[24:24]	1
0xBB0019D0	ISR_STAT_TRIGGER [3737].ISR_STAT_TRIGGER[25:25]	1
0xBB0019D0	ISR_STAT_TRIGGER [3738].ISR_STAT_TRIGGER[26:26]	1
0xBB0019D0	ISR_STAT_TRIGGER [3739].ISR_STAT_TRIGGER[27:27]	1
0xBB0019D0	ISR_STAT_TRIGGER [3740].ISR_STAT_TRIGGER[28:28]	1
0xBB0019D0	ISR_STAT_TRIGGER [3741].ISR_STAT_TRIGGER[29:29]	1
0xBB0019D0	ISR_STAT_TRIGGER [3742].ISR_STAT_TRIGGER[30:30]	1
0xBB0019D0	ISR_STAT_TRIGGER [3743].ISR_STAT_TRIGGER[31:31]	1
0xBB0019D4	ISR_STAT_TRIGGER [3744].ISR_STAT_TRIGGER[0:0]	1
0xBB0019D4	ISR_STAT_TRIGGER [3745].ISR_STAT_TRIGGER[1:1]	1
0xBB0019D4	ISR_STAT_TRIGGER [3746].ISR_STAT_TRIGGER[2:2]	1
0xBB0019D4	ISR_STAT_TRIGGER [3747].ISR_STAT_TRIGGER[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019D4	ISR_STAT_TRIGGER [3749].ISR_STAT_TRIGGER[5:5]	1
0xBB0019D4	ISR_STAT_TRIGGER [3750].ISR_STAT_TRIGGER[6:6]	1
0xBB0019D4	ISR_STAT_TRIGGER [3751].ISR_STAT_TRIGGER[7:7]	1
0xBB0019D4	ISR_STAT_TRIGGER [3752].ISR_STAT_TRIGGER[8:8]	1
0xBB0019D4	ISR_STAT_TRIGGER [3753].ISR_STAT_TRIGGER[9:9]	1
0xBB0019D4	ISR_STAT_TRIGGER [3754].ISR_STAT_TRIGGER[10:10]	1
0xBB0019D4	ISR_STAT_TRIGGER [3755].ISR_STAT_TRIGGER[11:11]	1
0xBB0019D4	ISR_STAT_TRIGGER [3756].ISR_STAT_TRIGGER[12:12]	1
0xBB0019D4	ISR_STAT_TRIGGER [3757].ISR_STAT_TRIGGER[13:13]	1
0xBB0019D4	ISR_STAT_TRIGGER [3758].ISR_STAT_TRIGGER[14:14]	1
0xBB0019D4	ISR_STAT_TRIGGER [3759].ISR_STAT_TRIGGER[15:15]	1
0xBB0019D4	ISR_STAT_TRIGGER [3760].ISR_STAT_TRIGGER[16:16]	1
0xBB0019D4	ISR_STAT_TRIGGER [3761].ISR_STAT_TRIGGER[17:17]	1
0xBB0019D4	ISR_STAT_TRIGGER [3762].ISR_STAT_TRIGGER[18:18]	1
0xBB0019D4	ISR_STAT_TRIGGER [3763].ISR_STAT_TRIGGER[19:19]	1
0xBB0019D4	ISR_STAT_TRIGGER [3764].ISR_STAT_TRIGGER[20:20]	1
0xBB0019D4	ISR_STAT_TRIGGER [3765].ISR_STAT_TRIGGER[21:21]	1
0xBB0019D4	ISR_STAT_TRIGGER [3766].ISR_STAT_TRIGGER[22:22]	1
0xBB0019D4	ISR_STAT_TRIGGER [3767].ISR_STAT_TRIGGER[23:23]	1
0xBB0019D4	ISR_STAT_TRIGGER [3768].ISR_STAT_TRIGGER[24:24]	1
0xBB0019D4	ISR_STAT_TRIGGER [3769].ISR_STAT_TRIGGER[25:25]	1
0xBB0019D4	ISR_STAT_TRIGGER [3770].ISR_STAT_TRIGGER[26:26]	1
0xBB0019D4	ISR_STAT_TRIGGER [3771].ISR_STAT_TRIGGER[27:27]	1
0xBB0019D4	ISR_STAT_TRIGGER [3772].ISR_STAT_TRIGGER[28:28]	1
0xBB0019D4	ISR_STAT_TRIGGER [3773].ISR_STAT_TRIGGER[29:29]	1
0xBB0019D4	ISR_STAT_TRIGGER [3774].ISR_STAT_TRIGGER[30:30]	1
0xBB0019D4	ISR_STAT_TRIGGER [3775].ISR_STAT_TRIGGER[31:31]	1
0xBB0019D8	ISR_STAT_TRIGGER [3776].ISR_STAT_TRIGGER[0:0]	1
0xBB0019D8	ISR_STAT_TRIGGER [3777].ISR_STAT_TRIGGER[1:1]	1
0xBB0019D8	ISR_STAT_TRIGGER [3778].ISR_STAT_TRIGGER[2:2]	1
0xBB0019D8	ISR_STAT_TRIGGER [3779].ISR_STAT_TRIGGER[3:3]	1
0xBB0019D8	ISR_STAT_TRIGGER [3780].ISR_STAT_TRIGGER[4:4]	1
0xBB0019D8	ISR_STAT_TRIGGER [3781].ISR_STAT_TRIGGER[5:5]	1
0xBB0019D8	ISR_STAT_TRIGGER [3782].ISR_STAT_TRIGGER[6:6]	1
0xBB0019D8	ISR_STAT_TRIGGER [3783].ISR_STAT_TRIGGER[7:7]	1
0xBB0019D8	ISR_STAT_TRIGGER [3784].ISR_STAT_TRIGGER[8:8]	1
0xBB0019D8	ISR_STAT_TRIGGER [3785].ISR_STAT_TRIGGER[9:9]	1
0xBB0019D8	ISR_STAT_TRIGGER [3786].ISR_STAT_TRIGGER[10:10]	1
0xBB0019D8	ISR_STAT_TRIGGER [3787].ISR_STAT_TRIGGER[11:11]	1
0xBB0019D8	ISR_STAT_TRIGGER [3788].ISR_STAT_TRIGGER[12:12]	1
0xBB0019D8	ISR_STAT_TRIGGER [3789].ISR_STAT_TRIGGER[13:13]	1
0xBB0019D8	ISR_STAT_TRIGGER [3790].ISR_STAT_TRIGGER[14:14]	1
0xBB0019D8	ISR_STAT_TRIGGER [3791].ISR_STAT_TRIGGER[15:15]	1
0xBB0019D8	ISR_STAT_TRIGGER [3792].ISR_STAT_TRIGGER[16:16]	1
0xBB0019D8	ISR_STAT_TRIGGER [3793].ISR_STAT_TRIGGER[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019D8	ISR_STAT_TRIGGER [3795].ISR_STAT_TRIGGER[19:19]	1
0xBB0019D8	ISR_STAT_TRIGGER [3796].ISR_STAT_TRIGGER[20:20]	1
0xBB0019D8	ISR_STAT_TRIGGER [3797].ISR_STAT_TRIGGER[21:21]	1
0xBB0019D8	ISR_STAT_TRIGGER [3798].ISR_STAT_TRIGGER[22:22]	1
0xBB0019D8	ISR_STAT_TRIGGER [3799].ISR_STAT_TRIGGER[23:23]	1
0xBB0019D8	ISR_STAT_TRIGGER [3800].ISR_STAT_TRIGGER[24:24]	1
0xBB0019D8	ISR_STAT_TRIGGER [3801].ISR_STAT_TRIGGER[25:25]	1
0xBB0019D8	ISR_STAT_TRIGGER [3802].ISR_STAT_TRIGGER[26:26]	1
0xBB0019D8	ISR_STAT_TRIGGER [3803].ISR_STAT_TRIGGER[27:27]	1
0xBB0019D8	ISR_STAT_TRIGGER [3804].ISR_STAT_TRIGGER[28:28]	1
0xBB0019D8	ISR_STAT_TRIGGER [3805].ISR_STAT_TRIGGER[29:29]	1
0xBB0019D8	ISR_STAT_TRIGGER [3806].ISR_STAT_TRIGGER[30:30]	1
0xBB0019D8	ISR_STAT_TRIGGER [3807].ISR_STAT_TRIGGER[31:31]	1
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0xBB0019DC	ISR_STAT_TRIGGER [3809].ISR_STAT_TRIGGER[1:1]	1
0xBB0019DC	ISR_STAT_TRIGGER [3810].ISR_STAT_TRIGGER[2:2]	1
0xBB0019DC	ISR_STAT_TRIGGER [3811].ISR_STAT_TRIGGER[3:3]	1
0xBB0019DC	ISR_STAT_TRIGGER [3812].ISR_STAT_TRIGGER[4:4]	1
0xBB0019DC	ISR_STAT_TRIGGER [3813].ISR_STAT_TRIGGER[5:5]	1
0xBB0019DC	ISR_STAT_TRIGGER [3814].ISR_STAT_TRIGGER[6:6]	1
0xBB0019DC	ISR_STAT_TRIGGER [3815].ISR_STAT_TRIGGER[7:7]	1
0xBB0019DC	ISR_STAT_TRIGGER [3816].ISR_STAT_TRIGGER[8:8]	1
0xBB0019DC	ISR_STAT_TRIGGER [3817].ISR_STAT_TRIGGER[9:9]	1
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0xBB0019DC	ISR_STAT_TRIGGER [3821].ISR_STAT_TRIGGER[13:13]	1
0xBB0019DC	ISR_STAT_TRIGGER [3822].ISR_STAT_TRIGGER[14:14]	1
0xBB0019DC	ISR_STAT_TRIGGER [3823].ISR_STAT_TRIGGER[15:15]	1
0xBB0019DC	ISR_STAT_TRIGGER [3824].ISR_STAT_TRIGGER[16:16]	1
0xBB0019DC	ISR_STAT_TRIGGER [3825].ISR_STAT_TRIGGER[17:17]	1
0xBB0019DC	ISR_STAT_TRIGGER [3826].ISR_STAT_TRIGGER[18:18]	1
0xBB0019DC	ISR_STAT_TRIGGER [3827].ISR_STAT_TRIGGER[19:19]	1
0xBB0019DC	ISR_STAT_TRIGGER [3828].ISR_STAT_TRIGGER[20:20]	1
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0xBB0019DC	ISR_STAT_TRIGGER [3831].ISR_STAT_TRIGGER[23:23]	1
0xBB0019DC	ISR_STAT_TRIGGER [3832].ISR_STAT_TRIGGER[24:24]	1
0xBB0019DC	ISR_STAT_TRIGGER [3833].ISR_STAT_TRIGGER[25:25]	1
0xBB0019DC	ISR_STAT_TRIGGER [3834].ISR_STAT_TRIGGER[26:26]	1
0xBB0019DC	ISR_STAT_TRIGGER [3835].ISR_STAT_TRIGGER[27:27]	1
0xBB0019DC	ISR_STAT_TRIGGER [3836].ISR_STAT_TRIGGER[28:28]	1
0xBB0019DC	ISR_STAT_TRIGGER [3837].ISR_STAT_TRIGGER[29:29]	1
0xBB0019DC	ISR_STAT_TRIGGER [3838].ISR_STAT_TRIGGER[30:30]	1
0xBB0019DC	ISR_STAT_TRIGGER [3839].ISR_STAT_TRIGGER[31:31]	1

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0xBB0019E0	ISR_STAT_TRIGGER [3843].ISR_STAT_TRIGGER[3:3]	1
0xBB0019E0	ISR_STAT_TRIGGER [3844].ISR_STAT_TRIGGER[4:4]	1
0xBB0019E0	ISR_STAT_TRIGGER [3845].ISR_STAT_TRIGGER[5:5]	1
0xBB0019E0	ISR_STAT_TRIGGER [3846].ISR_STAT_TRIGGER[6:6]	1
0xBB0019E0	ISR_STAT_TRIGGER [3847].ISR_STAT_TRIGGER[7:7]	1
0xBB0019E0	ISR_STAT_TRIGGER [3848].ISR_STAT_TRIGGER[8:8]	1
0xBB0019E0	ISR_STAT_TRIGGER [3849].ISR_STAT_TRIGGER[9:9]	1
0xBB0019E0	ISR_STAT_TRIGGER [3850].ISR_STAT_TRIGGER[10:10]	1
0xBB0019E0	ISR_STAT_TRIGGER [3851].ISR_STAT_TRIGGER[11:11]	1
0xBB0019E0	ISR_STAT_TRIGGER [3852].ISR_STAT_TRIGGER[12:12]	1
0xBB0019E0	ISR_STAT_TRIGGER [3853].ISR_STAT_TRIGGER[13:13]	1
0xBB0019E0	ISR_STAT_TRIGGER [3854].ISR_STAT_TRIGGER[14:14]	1
0xBB0019E0	ISR_STAT_TRIGGER [3855].ISR_STAT_TRIGGER[15:15]	1
0xBB0019E0	ISR_STAT_TRIGGER [3856].ISR_STAT_TRIGGER[16:16]	1
0xBB0019E0	ISR_STAT_TRIGGER [3857].ISR_STAT_TRIGGER[17:17]	1
0xBB0019E0	ISR_STAT_TRIGGER [3858].ISR_STAT_TRIGGER[18:18]	1
0xBB0019E0	ISR_STAT_TRIGGER [3859].ISR_STAT_TRIGGER[19:19]	1
0xBB0019E0	ISR_STAT_TRIGGER [3860].ISR_STAT_TRIGGER[20:20]	1
0xBB0019E0	ISR_STAT_TRIGGER [3861].ISR_STAT_TRIGGER[21:21]	1
0xBB0019E0	ISR_STAT_TRIGGER [3862].ISR_STAT_TRIGGER[22:22]	1
0xBB0019E0	ISR_STAT_TRIGGER [3863].ISR_STAT_TRIGGER[23:23]	1
0xBB0019E0	ISR_STAT_TRIGGER [3864].ISR_STAT_TRIGGER[24:24]	1
0xBB0019E0	ISR_STAT_TRIGGER [3865].ISR_STAT_TRIGGER[25:25]	1
0xBB0019E0	ISR_STAT_TRIGGER [3866].ISR_STAT_TRIGGER[26:26]	1
0xBB0019E0	ISR_STAT_TRIGGER [3867].ISR_STAT_TRIGGER[27:27]	1
0xBB0019E0	ISR_STAT_TRIGGER [3868].ISR_STAT_TRIGGER[28:28]	1
0xBB0019E0	ISR_STAT_TRIGGER [3869].ISR_STAT_TRIGGER[29:29]	1
0xBB0019E0	ISR_STAT_TRIGGER [3870].ISR_STAT_TRIGGER[30:30]	1
0xBB0019E0	ISR_STAT_TRIGGER [3871].ISR_STAT_TRIGGER[31:31]	1
0xBB0019E4	ISR_STAT_TRIGGER [3872].ISR_STAT_TRIGGER[0:0]	1
0xBB0019E4	ISR_STAT_TRIGGER [3873].ISR_STAT_TRIGGER[1:1]	1
0xBB0019E4	ISR_STAT_TRIGGER [3874].ISR_STAT_TRIGGER[2:2]	1
0xBB0019E4	ISR_STAT_TRIGGER [3875].ISR_STAT_TRIGGER[3:3]	1
0xBB0019E4	ISR_STAT_TRIGGER [3876].ISR_STAT_TRIGGER[4:4]	1
0xBB0019E4	ISR_STAT_TRIGGER [3877].ISR_STAT_TRIGGER[5:5]	1
0xBB0019E4	ISR_STAT_TRIGGER [3878].ISR_STAT_TRIGGER[6:6]	1
0xBB0019E4	ISR_STAT_TRIGGER [3879].ISR_STAT_TRIGGER[7:7]	1
0xBB0019E4	ISR_STAT_TRIGGER [3880].ISR_STAT_TRIGGER[8:8]	1
0xBB0019E4	ISR_STAT_TRIGGER [3881].ISR_STAT_TRIGGER[9:9]	1
0xBB0019E4	ISR_STAT_TRIGGER [3882].ISR_STAT_TRIGGER[10:10]	1
0xBB0019E4	ISR_STAT_TRIGGER [3883].ISR_STAT_TRIGGER[11:11]	1
0xBB0019E4	ISR_STAT_TRIGGER [3884].ISR_STAT_TRIGGER[12:12]	1
0xBB0019E4	ISR_STAT_TRIGGER [3885].ISR_STAT_TRIGGER[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019E4	ISR_STAT_TRIGGER [3888].ISR_STAT_TRIGGER[16:16]	1
0xBB0019E4	ISR_STAT_TRIGGER [3889].ISR_STAT_TRIGGER[17:17]	1
0xBB0019E4	ISR_STAT_TRIGGER [3890].ISR_STAT_TRIGGER[18:18]	1
0xBB0019E4	ISR_STAT_TRIGGER [3891].ISR_STAT_TRIGGER[19:19]	1
0xBB0019E4	ISR_STAT_TRIGGER [3892].ISR_STAT_TRIGGER[20:20]	1
0xBB0019E4	ISR_STAT_TRIGGER [3893].ISR_STAT_TRIGGER[21:21]	1
0xBB0019E4	ISR_STAT_TRIGGER [3894].ISR_STAT_TRIGGER[22:22]	1
0xBB0019E4	ISR_STAT_TRIGGER [3895].ISR_STAT_TRIGGER[23:23]	1
0xBB0019E4	ISR_STAT_TRIGGER [3896].ISR_STAT_TRIGGER[24:24]	1
0xBB0019E4	ISR_STAT_TRIGGER [3897].ISR_STAT_TRIGGER[25:25]	1
0xBB0019E4	ISR_STAT_TRIGGER [3898].ISR_STAT_TRIGGER[26:26]	1
0xBB0019E4	ISR_STAT_TRIGGER [3899].ISR_STAT_TRIGGER[27:27]	1
0xBB0019E4	ISR_STAT_TRIGGER [3900].ISR_STAT_TRIGGER[28:28]	1
0xBB0019E4	ISR_STAT_TRIGGER [3901].ISR_STAT_TRIGGER[29:29]	1
0xBB0019E4	ISR_STAT_TRIGGER [3902].ISR_STAT_TRIGGER[30:30]	1
0xBB0019E4	ISR_STAT_TRIGGER [3903].ISR_STAT_TRIGGER[31:31]	1
0xBB0019E8	ISR_STAT_TRIGGER [3904].ISR_STAT_TRIGGER[0:0]	1
0xBB0019E8	ISR_STAT_TRIGGER [3905].ISR_STAT_TRIGGER[1:1]	1
0xBB0019E8	ISR_STAT_TRIGGER [3906].ISR_STAT_TRIGGER[2:2]	1
0xBB0019E8	ISR_STAT_TRIGGER [3907].ISR_STAT_TRIGGER[3:3]	1
0xBB0019E8	ISR_STAT_TRIGGER [3908].ISR_STAT_TRIGGER[4:4]	1
0xBB0019E8	ISR_STAT_TRIGGER [3909].ISR_STAT_TRIGGER[5:5]	1
0xBB0019E8	ISR_STAT_TRIGGER [3910].ISR_STAT_TRIGGER[6:6]	1
0xBB0019E8	ISR_STAT_TRIGGER [3911].ISR_STAT_TRIGGER[7:7]	1
0xBB0019E8	ISR_STAT_TRIGGER [3912].ISR_STAT_TRIGGER[8:8]	1
0xBB0019E8	ISR_STAT_TRIGGER [3913].ISR_STAT_TRIGGER[9:9]	1
0xBB0019E8	ISR_STAT_TRIGGER [3914].ISR_STAT_TRIGGER[10:10]	1
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0xBB0019E8	ISR_STAT_TRIGGER [3916].ISR_STAT_TRIGGER[12:12]	1
0xBB0019E8	ISR_STAT_TRIGGER [3917].ISR_STAT_TRIGGER[13:13]	1
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0xBB0019E8	ISR_STAT_TRIGGER [3921].ISR_STAT_TRIGGER[17:17]	1
0xBB0019E8	ISR_STAT_TRIGGER [3922].ISR_STAT_TRIGGER[18:18]	1
0xBB0019E8	ISR_STAT_TRIGGER [3923].ISR_STAT_TRIGGER[19:19]	1
0xBB0019E8	ISR_STAT_TRIGGER [3924].ISR_STAT_TRIGGER[20:20]	1
0xBB0019E8	ISR_STAT_TRIGGER [3925].ISR_STAT_TRIGGER[21:21]	1
0xBB0019E8	ISR_STAT_TRIGGER [3926].ISR_STAT_TRIGGER[22:22]	1
0xBB0019E8	ISR_STAT_TRIGGER [3927].ISR_STAT_TRIGGER[23:23]	1
0xBB0019E8	ISR_STAT_TRIGGER [3928].ISR_STAT_TRIGGER[24:24]	1
0xBB0019E8	ISR_STAT_TRIGGER [3929].ISR_STAT_TRIGGER[25:25]	1
0xBB0019E8	ISR_STAT_TRIGGER [3930].ISR_STAT_TRIGGER[26:26]	1
0xBB0019E8	ISR_STAT_TRIGGER [3931].ISR_STAT_TRIGGER[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019E8	ISR_STAT_TRIGGER [3934].ISR_STAT_TRIGGER[30:30]	1
0xBB0019E8	ISR_STAT_TRIGGER [3935].ISR_STAT_TRIGGER[31:31]	1
0xBB0019EC	ISR_STAT_TRIGGER [3936].ISR_STAT_TRIGGER[0:0]	1
0xBB0019EC	ISR_STAT_TRIGGER [3937].ISR_STAT_TRIGGER[1:1]	1
0xBB0019EC	ISR_STAT_TRIGGER [3938].ISR_STAT_TRIGGER[2:2]	1
0xBB0019EC	ISR_STAT_TRIGGER [3939].ISR_STAT_TRIGGER[3:3]	1
0xBB0019EC	ISR_STAT_TRIGGER [3940].ISR_STAT_TRIGGER[4:4]	1
0xBB0019EC	ISR_STAT_TRIGGER [3941].ISR_STAT_TRIGGER[5:5]	1
0xBB0019EC	ISR_STAT_TRIGGER [3942].ISR_STAT_TRIGGER[6:6]	1
0xBB0019EC	ISR_STAT_TRIGGER [3943].ISR_STAT_TRIGGER[7:7]	1
0xBB0019EC	ISR_STAT_TRIGGER [3944].ISR_STAT_TRIGGER[8:8]	1
0xBB0019EC	ISR_STAT_TRIGGER [3945].ISR_STAT_TRIGGER[9:9]	1
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0xBB0019EC	ISR_STAT_TRIGGER [3947].ISR_STAT_TRIGGER[11:11]	1
0xBB0019EC	ISR_STAT_TRIGGER [3948].ISR_STAT_TRIGGER[12:12]	1
0xBB0019EC	ISR_STAT_TRIGGER [3949].ISR_STAT_TRIGGER[13:13]	1
0xBB0019EC	ISR_STAT_TRIGGER [3950].ISR_STAT_TRIGGER[14:14]	1
0xBB0019EC	ISR_STAT_TRIGGER [3951].ISR_STAT_TRIGGER[15:15]	1
0xBB0019EC	ISR_STAT_TRIGGER [3952].ISR_STAT_TRIGGER[16:16]	1
0xBB0019EC	ISR_STAT_TRIGGER [3953].ISR_STAT_TRIGGER[17:17]	1
0xBB0019EC	ISR_STAT_TRIGGER [3954].ISR_STAT_TRIGGER[18:18]	1
0xBB0019EC	ISR_STAT_TRIGGER [3955].ISR_STAT_TRIGGER[19:19]	1
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0xBB0019EC	ISR_STAT_TRIGGER [3960].ISR_STAT_TRIGGER[24:24]	1
0xBB0019EC	ISR_STAT_TRIGGER [3961].ISR_STAT_TRIGGER[25:25]	1
0xBB0019EC	ISR_STAT_TRIGGER [3962].ISR_STAT_TRIGGER[26:26]	1
0xBB0019EC	ISR_STAT_TRIGGER [3963].ISR_STAT_TRIGGER[27:27]	1
0xBB0019EC	ISR_STAT_TRIGGER [3964].ISR_STAT_TRIGGER[28:28]	1
0xBB0019EC	ISR_STAT_TRIGGER [3965].ISR_STAT_TRIGGER[29:29]	1
0xBB0019EC	ISR_STAT_TRIGGER [3966].ISR_STAT_TRIGGER[30:30]	1
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0xBB0019F0	ISR_STAT_TRIGGER [3969].ISR_STAT_TRIGGER[1:1]	1
0xBB0019F0	ISR_STAT_TRIGGER [3970].ISR_STAT_TRIGGER[2:2]	1
0xBB0019F0	ISR_STAT_TRIGGER [3971].ISR_STAT_TRIGGER[3:3]	1
0xBB0019F0	ISR_STAT_TRIGGER [3972].ISR_STAT_TRIGGER[4:4]	1
0xBB0019F0	ISR_STAT_TRIGGER [3973].ISR_STAT_TRIGGER[5:5]	1
0xBB0019F0	ISR_STAT_TRIGGER [3974].ISR_STAT_TRIGGER[6:6]	1
0xBB0019F0	ISR_STAT_TRIGGER [3975].ISR_STAT_TRIGGER[7:7]	1
0xBB0019F0	ISR_STAT_TRIGGER [3976].ISR_STAT_TRIGGER[8:8]	1
0xBB0019F0	ISR_STAT_TRIGGER [3977].ISR_STAT_TRIGGER[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB0019F0	ISR_STAT_TRIGGER [3980].ISR_STAT_TRIGGER[12:12]	1
0xBB0019F0	ISR_STAT_TRIGGER [3981].ISR_STAT_TRIGGER[13:13]	1
0xBB0019F0	ISR_STAT_TRIGGER [3982].ISR_STAT_TRIGGER[14:14]	1
0xBB0019F0	ISR_STAT_TRIGGER [3983].ISR_STAT_TRIGGER[15:15]	1
0xBB0019F0	ISR_STAT_TRIGGER [3984].ISR_STAT_TRIGGER[16:16]	1
0xBB0019F0	ISR_STAT_TRIGGER [3985].ISR_STAT_TRIGGER[17:17]	1
0xBB0019F0	ISR_STAT_TRIGGER [3986].ISR_STAT_TRIGGER[18:18]	1
0xBB0019F0	ISR_STAT_TRIGGER [3987].ISR_STAT_TRIGGER[19:19]	1
0xBB0019F0	ISR_STAT_TRIGGER [3988].ISR_STAT_TRIGGER[20:20]	1
0xBB0019F0	ISR_STAT_TRIGGER [3989].ISR_STAT_TRIGGER[21:21]	1
0xBB0019F0	ISR_STAT_TRIGGER [3990].ISR_STAT_TRIGGER[22:22]	1
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0xBB0019F0	ISR_STAT_TRIGGER [3992].ISR_STAT_TRIGGER[24:24]	1
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0xBB0019F0	ISR_STAT_TRIGGER [3994].ISR_STAT_TRIGGER[26:26]	1
0xBB0019F0	ISR_STAT_TRIGGER [3995].ISR_STAT_TRIGGER[27:27]	1
0xBB0019F0	ISR_STAT_TRIGGER [3996].ISR_STAT_TRIGGER[28:28]	1
0xBB0019F0	ISR_STAT_TRIGGER [3997].ISR_STAT_TRIGGER[29:29]	1
0xBB0019F0	ISR_STAT_TRIGGER [3998].ISR_STAT_TRIGGER[30:30]	1
0xBB0019F0	ISR_STAT_TRIGGER [3999].ISR_STAT_TRIGGER[31:31]	1
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0xBB0019F4	ISR_STAT_TRIGGER [4004].ISR_STAT_TRIGGER[4:4]	1
0xBB0019F4	ISR_STAT_TRIGGER [4005].ISR_STAT_TRIGGER[5:5]	1
0xBB0019F4	ISR_STAT_TRIGGER [4006].ISR_STAT_TRIGGER[6:6]	1
0xBB0019F4	ISR_STAT_TRIGGER [4007].ISR_STAT_TRIGGER[7:7]	1
0xBB0019F4	ISR_STAT_TRIGGER [4008].ISR_STAT_TRIGGER[8:8]	1
0xBB0019F4	ISR_STAT_TRIGGER [4009].ISR_STAT_TRIGGER[9:9]	1
0xBB0019F4	ISR_STAT_TRIGGER [4010].ISR_STAT_TRIGGER[10:10]	1
0xBB0019F4	ISR_STAT_TRIGGER [4011].ISR_STAT_TRIGGER[11:11]	1
0xBB0019F4	ISR_STAT_TRIGGER [4012].ISR_STAT_TRIGGER[12:12]	1
0xBB0019F4	ISR_STAT_TRIGGER [4013].ISR_STAT_TRIGGER[13:13]	1
0xBB0019F4	ISR_STAT_TRIGGER [4014].ISR_STAT_TRIGGER[14:14]	1
0xBB0019F4	ISR_STAT_TRIGGER [4015].ISR_STAT_TRIGGER[15:15]	1
0xBB0019F4	ISR_STAT_TRIGGER [4016].ISR_STAT_TRIGGER[16:16]	1
0xBB0019F4	ISR_STAT_TRIGGER [4017].ISR_STAT_TRIGGER[17:17]	1
0xBB0019F4	ISR_STAT_TRIGGER [4018].ISR_STAT_TRIGGER[18:18]	1
0xBB0019F4	ISR_STAT_TRIGGER [4019].ISR_STAT_TRIGGER[19:19]	1
0xBB0019F4	ISR_STAT_TRIGGER [4020].ISR_STAT_TRIGGER[20:20]	1
0xBB0019F4	ISR_STAT_TRIGGER [4021].ISR_STAT_TRIGGER[21:21]	1
0xBB0019F4	ISR_STAT_TRIGGER [4022].ISR_STAT_TRIGGER[22:22]	1
0xBB0019F4	ISR_STAT_TRIGGER [4023].ISR_STAT_TRIGGER[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0019F4	ISR_STAT_TRIGGER [4024].ISR_STAT_TRIGGER[24:24]	1
0xBB0019F4	ISR_STAT_TRIGGER [4025].ISR_STAT_TRIGGER[25:25]	1
0xBB0019F4	ISR_STAT_TRIGGER [4026].ISR_STAT_TRIGGER[26:26]	1
0xBB0019F4	ISR_STAT_TRIGGER [4027].ISR_STAT_TRIGGER[27:27]	1
0xBB0019F4	ISR_STAT_TRIGGER [4028].ISR_STAT_TRIGGER[28:28]	1
0xBB0019F4	ISR_STAT_TRIGGER [4029].ISR_STAT_TRIGGER[29:29]	1
0xBB0019F4	ISR_STAT_TRIGGER [4030].ISR_STAT_TRIGGER[30:30]	1
0xBB0019F4	ISR_STAT_TRIGGER [4031].ISR_STAT_TRIGGER[31:31]	1
0xBB0019F8	ISR_STAT_TRIGGER [4032].ISR_STAT_TRIGGER[0:0]	1
0xBB0019F8	ISR_STAT_TRIGGER [4033].ISR_STAT_TRIGGER[1:1]	1
0xBB0019F8	ISR_STAT_TRIGGER [4034].ISR_STAT_TRIGGER[2:2]	1
0xBB0019F8	ISR_STAT_TRIGGER [4035].ISR_STAT_TRIGGER[3:3]	1
0xBB0019F8	ISR_STAT_TRIGGER [4036].ISR_STAT_TRIGGER[4:4]	1
0xBB0019F8	ISR_STAT_TRIGGER [4037].ISR_STAT_TRIGGER[5:5]	1
0xBB0019F8	ISR_STAT_TRIGGER [4038].ISR_STAT_TRIGGER[6:6]	1
0xBB0019F8	ISR_STAT_TRIGGER [4039].ISR_STAT_TRIGGER[7:7]	1
0xBB0019F8	ISR_STAT_TRIGGER [4040].ISR_STAT_TRIGGER[8:8]	1
0xBB0019F8	ISR_STAT_TRIGGER [4041].ISR_STAT_TRIGGER[9:9]	1
0xBB0019F8	ISR_STAT_TRIGGER [4042].ISR_STAT_TRIGGER[10:10]	1
0xBB0019F8	ISR_STAT_TRIGGER [4043].ISR_STAT_TRIGGER[11:11]	1
0xBB0019F8	ISR_STAT_TRIGGER [4044].ISR_STAT_TRIGGER[12:12]	1
0xBB0019F8	ISR_STAT_TRIGGER [4045].ISR_STAT_TRIGGER[13:13]	1
0xBB0019F8	ISR_STAT_TRIGGER [4046].ISR_STAT_TRIGGER[14:14]	1
0xBB0019F8	ISR_STAT_TRIGGER [4047].ISR_STAT_TRIGGER[15:15]	1
0xBB0019F8	ISR_STAT_TRIGGER [4048].ISR_STAT_TRIGGER[16:16]	1
0xBB0019F8	ISR_STAT_TRIGGER [4049].ISR_STAT_TRIGGER[17:17]	1
0xBB0019F8	ISR_STAT_TRIGGER [4050].ISR_STAT_TRIGGER[18:18]	1
0xBB0019F8	ISR_STAT_TRIGGER [4051].ISR_STAT_TRIGGER[19:19]	1
0xBB0019F8	ISR_STAT_TRIGGER [4052].ISR_STAT_TRIGGER[20:20]	1
0xBB0019F8	ISR_STAT_TRIGGER [4053].ISR_STAT_TRIGGER[21:21]	1
0xBB0019F8	ISR_STAT_TRIGGER [4054].ISR_STAT_TRIGGER[22:22]	1
0xBB0019F8	ISR_STAT_TRIGGER [4055].ISR_STAT_TRIGGER[23:23]	1
0xBB0019F8	ISR_STAT_TRIGGER [4056].ISR_STAT_TRIGGER[24:24]	1
0xBB0019F8	ISR_STAT_TRIGGER [4057].ISR_STAT_TRIGGER[25:25]	1
0xBB0019F8	ISR_STAT_TRIGGER [4058].ISR_STAT_TRIGGER[26:26]	1
0xBB0019F8	ISR_STAT_TRIGGER [4059].ISR_STAT_TRIGGER[27:27]	1
0xBB0019F8	ISR_STAT_TRIGGER [4060].ISR_STAT_TRIGGER[28:28]	1
0xBB0019F8	ISR_STAT_TRIGGER [4061].ISR_STAT_TRIGGER[29:29]	1
0xBB0019F8	ISR_STAT_TRIGGER [4062].ISR_STAT_TRIGGER[30:30]	1
0xBB0019F8	ISR_STAT_TRIGGER [4063].ISR_STAT_TRIGGER[31:31]	1
0xBB0019FC	ISR_STAT_TRIGGER [4064].ISR_STAT_TRIGGER[0:0]	1
0xBB0019FC	ISR_STAT_TRIGGER [4065].ISR_STAT_TRIGGER[1:1]	1
0xBB0019FC	ISR_STAT_TRIGGER [4066].ISR_STAT_TRIGGER[2:2]	1
0xBB0019FC	ISR_STAT_TRIGGER [4067].ISR_STAT_TRIGGER[3:3]	1
0xBB0019FC	ISR_STAT_TRIGGER [4068].ISR_STAT_TRIGGER[4:4]	1
0xBB0019FC	ISR_STAT_TRIGGER [4069].ISR_STAT_TRIGGER[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0019FC	ISR_STAT_TRIGGER [4070].ISR_STAT_TRIGGER[6:6]	1
0xBB0019FC	ISR_STAT_TRIGGER [4071].ISR_STAT_TRIGGER[7:7]	1
0xBB0019FC	ISR_STAT_TRIGGER [4072].ISR_STAT_TRIGGER[8:8]	1
0xBB0019FC	ISR_STAT_TRIGGER [4073].ISR_STAT_TRIGGER[9:9]	1
0xBB0019FC	ISR_STAT_TRIGGER [4074].ISR_STAT_TRIGGER[10:10]	1
0xBB0019FC	ISR_STAT_TRIGGER [4075].ISR_STAT_TRIGGER[11:11]	1
0xBB0019FC	ISR_STAT_TRIGGER [4076].ISR_STAT_TRIGGER[12:12]	1
0xBB0019FC	ISR_STAT_TRIGGER [4077].ISR_STAT_TRIGGER[13:13]	1
0xBB0019FC	ISR_STAT_TRIGGER [4078].ISR_STAT_TRIGGER[14:14]	1
0xBB0019FC	ISR_STAT_TRIGGER [4079].ISR_STAT_TRIGGER[15:15]	1
0xBB0019FC	ISR_STAT_TRIGGER [4080].ISR_STAT_TRIGGER[16:16]	1
0xBB0019FC	ISR_STAT_TRIGGER [4081].ISR_STAT_TRIGGER[17:17]	1
0xBB0019FC	ISR_STAT_TRIGGER [4082].ISR_STAT_TRIGGER[18:18]	1
0xBB0019FC	ISR_STAT_TRIGGER [4083].ISR_STAT_TRIGGER[19:19]	1
0xBB0019FC	ISR_STAT_TRIGGER [4084].ISR_STAT_TRIGGER[20:20]	1
0xBB0019FC	ISR_STAT_TRIGGER [4085].ISR_STAT_TRIGGER[21:21]	1
0xBB0019FC	ISR_STAT_TRIGGER [4086].ISR_STAT_TRIGGER[22:22]	1
0xBB0019FC	ISR_STAT_TRIGGER [4087].ISR_STAT_TRIGGER[23:23]	1
0xBB0019FC	ISR_STAT_TRIGGER [4088].ISR_STAT_TRIGGER[24:24]	1
0xBB0019FC	ISR_STAT_TRIGGER [4089].ISR_STAT_TRIGGER[25:25]	1
0xBB0019FC	ISR_STAT_TRIGGER [4090].ISR_STAT_TRIGGER[26:26]	1
0xBB0019FC	ISR_STAT_TRIGGER [4091].ISR_STAT_TRIGGER[27:27]	1
0xBB0019FC	ISR_STAT_TRIGGER [4092].ISR_STAT_TRIGGER[28:28]	1
0xBB0019FC	ISR_STAT_TRIGGER [4093].ISR_STAT_TRIGGER[29:29]	1
0xBB0019FC	ISR_STAT_TRIGGER [4094].ISR_STAT_TRIGGER[30:30]	1
0xBB0019FC	ISR_STAT_TRIGGER [4095].ISR_STAT_TRIGGER[31:31]	1
0xBB00C804	L2_AGE_CTRL.RESERVED[31:26]	6
0xBB00C804	L2_AGE_CTRL.KEEP_AGE_OUT_ENTRY_VALID[25:25]	1
0xBB00C804	L2_AGE_CTRL.SUS_AGE_MAX[24:22]	3
0xBB00C804	L2_AGE_CTRL.RESERVED[21:21]	1
0xBB00C804	L2_AGE_CTRL.AGE_UNIT[20:0]	21
0xBB00C8FC	L2_BC_FLD_PMSK.RESERVED[31:25]	7
0xBB00C8FC	L2_BC_FLD_PMSK.PMSK_1[24:0]	25
0xBB00C900	L2_BC_FLD_PMSK.PMSK_0[31:0]	32
0xBB00C800	L2_CTRL.RESERVED[31:19]	13
0xBB00C800	L2_CTRL.ECID_RPF_FAIL_ACT[18:16]	3
0xBB00C800	L2_CTRL.PE_LM_FLTR[15:15]	1
0xBB00C800	L2_CTRL.RESERVED[14:13]	2
0xBB00C800	L2_CTRL.LUTCAM_EN[12:12]	1
0xBB00C800	L2_CTRL.STK_AUTO_LRN[11:11]	1
0xBB00C800	L2_CTRL.HASH_FULL_ACT[10:8]	3
0xBB00C800	L2_CTRLLINK_DOWN_P_INVLD[7:7]	1
0xBB00C800	L2_CTRL.MC_BC_SA_ACT[6:5]	2
0xBB00C800	L2_CTRL.ZERO_SA_ACT[4:3]	2
0xBB00C800	L2_CTRL.ZERO_SA_LRN[2:2]	1
0xBB00C800	L2_CTRL.L2_HASH_ALGO_BLK1[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C800	L2_CTRL.L2_HASH_ALGO_BLK0[0:0]	1
0xBB00C8CC	L2_GLB_STT_PORT_MV_ACT.RESERVED[31:3]	29
0xBB00C8CC	L2_GLB_STT_PORT_MV_ACT.ACT[2:0]	3
0xBB00C8D0	L2_GLB_STT_PORT_MV_LRNLRN.RESERVED[31:1]	31
0xBB00C8D0	L2_GLB_STT_PORT_MV_LRNLRN.LRN[0:0]	1
0xBB00CDB4	L2_HASH_FULL_CNT.RESERVED[31:16]	16
0xBB00CDB4	L2_HASH_FULL_CNT.CNT[15:0]	16
0xBB00C968	L2_LRNLRN_CONSTRT_CNT.RESERVED[31:16]	16
0xBB00C968	L2_LRNLRN_CONSTRT_CNT.LRN_CNT[15:0]	16
0xBB00C964	L2_LRNLRN_CONSTRT_CTRL.RESERVED[31:19]	13
0xBB00C964	L2_LRNLRN_CONSTRT_CTRL.CONSTRT_NUM[18:3]	16
0xBB00C964	L2_LRNLRN_CONSTRT_CTRL.ACT[2:0]	3
0xBB00CA50	L2_LRNLRN_PORT_CONSTRT_CNT[0].RESERVED[31:16]	16
0xBB00CA50	L2_LRNLRN_PORT_CONSTRT_CNT[0].LRN_CNT[15:0]	16
0xBB00CA54	L2_LRNLRN_PORT_CONSTRT_CNT[1].RESERVED[31:16]	16
0xBB00CA54	L2_LRNLRN_PORT_CONSTRT_CNT[1].LRN_CNT[15:0]	16
0xBB00CA58	L2_LRNLRN_PORT_CONSTRT_CNT[2].RESERVED[31:16]	16
0xBB00CA58	L2_LRNLRN_PORT_CONSTRT_CNT[2].LRN_CNT[15:0]	16
0xBB00CA5C	L2_LRNLRN_PORT_CONSTRT_CNT[3].RESERVED[31:16]	16
0xBB00CA5C	L2_LRNLRN_PORT_CONSTRT_CNT[3].LRN_CNT[15:0]	16
0xBB00CA60	L2_LRNLRN_PORT_CONSTRT_CNT[4].RESERVED[31:16]	16
0xBB00CA60	L2_LRNLRN_PORT_CONSTRT_CNT[4].LRN_CNT[15:0]	16
0xBB00CA64	L2_LRNLRN_PORT_CONSTRT_CNT[5].RESERVED[31:16]	16
0xBB00CA64	L2_LRNLRN_PORT_CONSTRT_CNT[5].LRN_CNT[15:0]	16
0xBB00CA68	L2_LRNLRN_PORT_CONSTRT_CNT[6].RESERVED[31:16]	16
0xBB00CA68	L2_LRNLRN_PORT_CONSTRT_CNT[6].LRN_CNT[15:0]	16
0xBB00CA6C	L2_LRNLRN_PORT_CONSTRT_CNT[7].RESERVED[31:16]	16
0xBB00CA6C	L2_LRNLRN_PORT_CONSTRT_CNT[7].LRN_CNT[15:0]	16
0xBB00CA70	L2_LRNLRN_PORT_CONSTRT_CNT[8].RESERVED[31:16]	16
0xBB00CA70	L2_LRNLRN_PORT_CONSTRT_CNT[8].LRN_CNT[15:0]	16
0xBB00CA74	L2_LRNLRN_PORT_CONSTRT_CNT[9].RESERVED[31:16]	16
0xBB00CA74	L2_LRNLRN_PORT_CONSTRT_CNT[9].LRN_CNT[15:0]	16
0xBB00CA78	L2_LRNLRN_PORT_CONSTRT_CNT[10].RESERVED[31:16]	16
0xBB00CA78	L2_LRNLRN_PORT_CONSTRT_CNT[10].LRN_CNT[15:0]	16
0xBB00CA7C	L2_LRNLRN_PORT_CONSTRT_CNT[11].RESERVED[31:16]	16
0xBB00CA7C	L2_LRNLRN_PORT_CONSTRT_CNT[11].LRN_CNT[15:0]	16
0xBB00CA80	L2_LRNLRN_PORT_CONSTRT_CNT[12].RESERVED[31:16]	16
0xBB00CA80	L2_LRNLRN_PORT_CONSTRT_CNT[12].LRN_CNT[15:0]	16
0xBB00CA84	L2_LRNLRN_PORT_CONSTRT_CNT[13].RESERVED[31:16]	16
0xBB00CA84	L2_LRNLRN_PORT_CONSTRT_CNT[13].LRN_CNT[15:0]	16
0xBB00CA88	L2_LRNLRN_PORT_CONSTRT_CNT[14].RESERVED[31:16]	16
0xBB00CA88	L2_LRNLRN_PORT_CONSTRT_CNT[14].LRN_CNT[15:0]	16
0xBB00CA8C	L2_LRNLRN_PORT_CONSTRT_CNT[15].RESERVED[31:16]	16
0xBB00CA8C	L2_LRNLRN_PORT_CONSTRT_CNT[15].LRN_CNT[15:0]	16
0xBB00CA90	L2_LRNLRN_PORT_CONSTRT_CNT[16].RESERVED[31:16]	16
0xBB00CA90	L2_LRNLRN_PORT_CONSTRT_CNT[16].LRN_CNT[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CA94	L2_LRN_PORT_CONSTRT_CNT [17].RESERVED[31:16]	16
0xBB00CA94	L2_LRN_PORT_CONSTRT_CNT [17].LRN_CNT[15:0]	16
0xBB00CA98	L2_LRN_PORT_CONSTRT_CNT [18].RESERVED[31:16]	16
0xBB00CA98	L2_LRN_PORT_CONSTRT_CNT [18].LRN_CNT[15:0]	16
0xBB00CA9C	L2_LRN_PORT_CONSTRT_CNT [19].RESERVED[31:16]	16
0xBB00CA9C	L2_LRN_PORT_CONSTRT_CNT [19].LRN_CNT[15:0]	16
0xBB00CAA0	L2_LRN_PORT_CONSTRT_CNT [20].RESERVED[31:16]	16
0xBB00CAA0	L2_LRN_PORT_CONSTRT_CNT [20].LRN_CNT[15:0]	16
0xBB00CAA4	L2_LRN_PORT_CONSTRT_CNT [21].RESERVED[31:16]	16
0xBB00CAA4	L2_LRN_PORT_CONSTRT_CNT [21].LRN_CNT[15:0]	16
0xBB00CAA8	L2_LRN_PORT_CONSTRT_CNT [22].RESERVED[31:16]	16
0xBB00CAA8	L2_LRN_PORT_CONSTRT_CNT [22].LRN_CNT[15:0]	16
0xBB00CAAC	L2_LRN_PORT_CONSTRT_CNT [23].RESERVED[31:16]	16
0xBB00CAAC	L2_LRN_PORT_CONSTRT_CNT [23].LRN_CNT[15:0]	16
0xBB00CAB0	L2_LRN_PORT_CONSTRT_CNT [24].RESERVED[31:16]	16
0xBB00CAB0	L2_LRN_PORT_CONSTRT_CNT [24].LRN_CNT[15:0]	16
0xBB00CAB4	L2_LRN_PORT_CONSTRT_CNT [25].RESERVED[31:16]	16
0xBB00CAB4	L2_LRN_PORT_CONSTRT_CNT [25].LRN_CNT[15:0]	16
0xBB00CAB8	L2_LRN_PORT_CONSTRT_CNT [26].RESERVED[31:16]	16
0xBB00CAB8	L2_LRN_PORT_CONSTRT_CNT [26].LRN_CNT[15:0]	16
0xBB00CABC	L2_LRN_PORT_CONSTRT_CNT [27].RESERVED[31:16]	16
0xBB00CABC	L2_LRN_PORT_CONSTRT_CNT [27].LRN_CNT[15:0]	16
0xBB00CAC0	L2_LRN_PORT_CONSTRT_CNT [28].RESERVED[31:16]	16
0xBB00CAC0	L2_LRN_PORT_CONSTRT_CNT [28].LRN_CNT[15:0]	16
0xBB00CAC4	L2_LRN_PORT_CONSTRT_CNT [29].RESERVED[31:16]	16
0xBB00CAC4	L2_LRN_PORT_CONSTRT_CNT [29].LRN_CNT[15:0]	16
0xBB00CAC8	L2_LRN_PORT_CONSTRT_CNT [30].RESERVED[31:16]	16
0xBB00CAC8	L2_LRN_PORT_CONSTRT_CNT [30].LRN_CNT[15:0]	16
0xBB00CACC	L2_LRN_PORT_CONSTRT_CNT [31].RESERVED[31:16]	16
0xBB00CACC	L2_LRN_PORT_CONSTRT_CNT [31].LRN_CNT[15:0]	16
0xBB00CAD0	L2_LRN_PORT_CONSTRT_CNT [32].RESERVED[31:16]	16
0xBB00CAD0	L2_LRN_PORT_CONSTRT_CNT [32].LRN_CNT[15:0]	16
0xBB00CAD4	L2_LRN_PORT_CONSTRT_CNT [33].RESERVED[31:16]	16
0xBB00CAD4	L2_LRN_PORT_CONSTRT_CNT [33].LRN_CNT[15:0]	16
0xBB00CAD8	L2_LRN_PORT_CONSTRT_CNT [34].RESERVED[31:16]	16
0xBB00CAD8	L2_LRN_PORT_CONSTRT_CNT [34].LRN_CNT[15:0]	16
0xBB00CADC	L2_LRN_PORT_CONSTRT_CNT [35].RESERVED[31:16]	16
0xBB00CADC	L2_LRN_PORT_CONSTRT_CNT [35].LRN_CNT[15:0]	16
0xBB00CAE0	L2_LRN_PORT_CONSTRT_CNT [36].RESERVED[31:16]	16
0xBB00CAE0	L2_LRN_PORT_CONSTRT_CNT [36].LRN_CNT[15:0]	16
0xBB00CAE4	L2_LRN_PORT_CONSTRT_CNT [37].RESERVED[31:16]	16
0xBB00CAE4	L2_LRN_PORT_CONSTRT_CNT [37].LRN_CNT[15:0]	16
0xBB00CAE8	L2_LRN_PORT_CONSTRT_CNT [38].RESERVED[31:16]	16
0xBB00CAE8	L2_LRN_PORT_CONSTRT_CNT [38].LRN_CNT[15:0]	16
0xBB00CAEC	L2_LRN_PORT_CONSTRT_CNT [39].RESERVED[31:16]	16
0xBB00CAEC	L2_LRN_PORT_CONSTRT_CNT [39].LRN_CNT[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CAF0	L2_LRN_PORT_CONSTRT_CNT [40].RESERVED[31:16]	16
0xBB00CAF0	L2_LRN_PORT_CONSTRT_CNT [40].LRN_CNT[15:0]	16
0xBB00CAF4	L2_LRN_PORT_CONSTRT_CNT [41].RESERVED[31:16]	16
0xBB00CAF4	L2_LRN_PORT_CONSTRT_CNT [41].LRN_CNT[15:0]	16
0xBB00CAF8	L2_LRN_PORT_CONSTRT_CNT [42].RESERVED[31:16]	16
0xBB00CAF8	L2_LRN_PORT_CONSTRT_CNT [42].LRN_CNT[15:0]	16
0xBB00CAFC	L2_LRN_PORT_CONSTRT_CNT [43].RESERVED[31:16]	16
0xBB00CAFC	L2_LRN_PORT_CONSTRT_CNT [43].LRN_CNT[15:0]	16
0xBB00CB00	L2_LRN_PORT_CONSTRT_CNT [44].RESERVED[31:16]	16
0xBB00CB00	L2_LRN_PORT_CONSTRT_CNT [44].LRN_CNT[15:0]	16
0xBB00CB04	L2_LRN_PORT_CONSTRT_CNT [45].RESERVED[31:16]	16
0xBB00CB04	L2_LRN_PORT_CONSTRT_CNT [45].LRN_CNT[15:0]	16
0xBB00CB08	L2_LRN_PORT_CONSTRT_CNT [46].RESERVED[31:16]	16
0xBB00CB08	L2_LRN_PORT_CONSTRT_CNT [46].LRN_CNT[15:0]	16
0xBB00CB0C	L2_LRN_PORT_CONSTRT_CNT [47].RESERVED[31:16]	16
0xBB00CB0C	L2_LRN_PORT_CONSTRT_CNT [47].LRN_CNT[15:0]	16
0xBB00CB10	L2_LRN_PORT_CONSTRT_CNT [48].RESERVED[31:16]	16
0xBB00CB10	L2_LRN_PORT_CONSTRT_CNT [48].LRN_CNT[15:0]	16
0xBB00CB14	L2_LRN_PORT_CONSTRT_CNT [49].RESERVED[31:16]	16
0xBB00CB14	L2_LRN_PORT_CONSTRT_CNT [49].LRN_CNT[15:0]	16
0xBB00CB18	L2_LRN_PORT_CONSTRT_CNT [50].RESERVED[31:16]	16
0xBB00CB18	L2_LRN_PORT_CONSTRT_CNT [50].LRN_CNT[15:0]	16
0xBB00CB1C	L2_LRN_PORT_CONSTRT_CNT [51].RESERVED[31:16]	16
0xBB00CB1C	L2_LRN_PORT_CONSTRT_CNT [51].LRN_CNT[15:0]	16
0xBB00CB20	L2_LRN_PORT_CONSTRT_CNT [52].RESERVED[31:16]	16
0xBB00CB20	L2_LRN_PORT_CONSTRT_CNT [52].LRN_CNT[15:0]	16
0xBB00CB24	L2_LRN_PORT_CONSTRT_CNT [53].RESERVED[31:16]	16
0xBB00CB24	L2_LRN_PORT_CONSTRT_CNT [53].LRN_CNT[15:0]	16
0xBB00CB28	L2_LRN_PORT_CONSTRT_CNT [54].RESERVED[31:16]	16
0xBB00CB28	L2_LRN_PORT_CONSTRT_CNT [54].LRN_CNT[15:0]	16
0xBB00CB2C	L2_LRN_PORT_CONSTRT_CNT [55].RESERVED[31:16]	16
0xBB00CB2C	L2_LRN_PORT_CONSTRT_CNT [55].LRN_CNT[15:0]	16
0xBB00CB30	L2_LRN_PORT_CONSTRT_CNT [56].RESERVED[31:16]	16
0xBB00CB30	L2_LRN_PORT_CONSTRT_CNT [56].LRN_CNT[15:0]	16
0xBB00C96C	L2_LRN_PORT_CONSTRT_CTRL [0].RESERVED[31:19]	13
0xBB00C96C	L2_LRN_PORT_CONSTRT_CTRL [0].CONSTRT_NUM[18:3]	16
0xBB00C96C	L2_LRN_PORT_CONSTRT_CTRL [0].ACT[2:0]	3
0xBB00C970	L2_LRN_PORT_CONSTRT_CTRL [1].RESERVED[31:19]	13
0xBB00C970	L2_LRN_PORT_CONSTRT_CTRL [1].CONSTRT_NUM[18:3]	16
0xBB00C970	L2_LRN_PORT_CONSTRT_CTRL [1].ACT[2:0]	3
0xBB00C974	L2_LRN_PORT_CONSTRT_CTRL [2].RESERVED[31:19]	13
0xBB00C974	L2_LRN_PORT_CONSTRT_CTRL [2].CONSTRT_NUM[18:3]	16
0xBB00C974	L2_LRN_PORT_CONSTRT_CTRL [2].ACT[2:0]	3
0xBB00C978	L2_LRN_PORT_CONSTRT_CTRL [3].RESERVED[31:19]	13
0xBB00C978	L2_LRN_PORT_CONSTRT_CTRL [3].CONSTRT_NUM[18:3]	16
0xBB00C978	L2_LRN_PORT_CONSTRT_CTRL [3].ACT[2:0]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C97C	L2_LRN_PORT_CONSTRT_CTRL [4].RESERVED[31:19]	13
0xBB00C97C	L2_LRN_PORT_CONSTRT_CTRL [4].CONSTRT_NUM[18:3]	16
0xBB00C97C	L2_LRN_PORT_CONSTRT_CTRL [4].ACT[2:0]	3
0xBB00C980	L2_LRN_PORT_CONSTRT_CTRL [5].RESERVED[31:19]	13
0xBB00C980	L2_LRN_PORT_CONSTRT_CTRL [5].CONSTRT_NUM[18:3]	16
0xBB00C980	L2_LRN_PORT_CONSTRT_CTRL [5].ACT[2:0]	3
0xBB00C984	L2_LRN_PORT_CONSTRT_CTRL [6].RESERVED[31:19]	13
0xBB00C984	L2_LRN_PORT_CONSTRT_CTRL [6].CONSTRT_NUM[18:3]	16
0xBB00C984	L2_LRN_PORT_CONSTRT_CTRL [6].ACT[2:0]	3
0xBB00C988	L2_LRN_PORT_CONSTRT_CTRL [7].RESERVED[31:19]	13
0xBB00C988	L2_LRN_PORT_CONSTRT_CTRL [7].CONSTRT_NUM[18:3]	16
0xBB00C988	L2_LRN_PORT_CONSTRT_CTRL [7].ACT[2:0]	3
0xBB00C98C	L2_LRN_PORT_CONSTRT_CTRL [8].RESERVED[31:19]	13
0xBB00C98C	L2_LRN_PORT_CONSTRT_CTRL [8].CONSTRT_NUM[18:3]	16
0xBB00C98C	L2_LRN_PORT_CONSTRT_CTRL [8].ACT[2:0]	3
0xBB00C990	L2_LRN_PORT_CONSTRT_CTRL [9].RESERVED[31:19]	13
0xBB00C990	L2_LRN_PORT_CONSTRT_CTRL [9].CONSTRT_NUM[18:3]	16
0xBB00C990	L2_LRN_PORT_CONSTRT_CTRL [9].ACT[2:0]	3
0xBB00C994	L2_LRN_PORT_CONSTRT_CTRL [10].RESERVED[31:19]	13
0xBB00C994	L2_LRN_PORT_CONSTRT_CTRL [10].CONSTRT_NUM[18:3]	16
0xBB00C994	L2_LRN_PORT_CONSTRT_CTRL [10].ACT[2:0]	3
0xBB00C998	L2_LRN_PORT_CONSTRT_CTRL [11].RESERVED[31:19]	13
0xBB00C998	L2_LRN_PORT_CONSTRT_CTRL [11].CONSTRT_NUM[18:3]	16
0xBB00C998	L2_LRN_PORT_CONSTRT_CTRL [11].ACT[2:0]	3
0xBB00C99C	L2_LRN_PORT_CONSTRT_CTRL [12].RESERVED[31:19]	13
0xBB00C99C	L2_LRN_PORT_CONSTRT_CTRL [12].CONSTRT_NUM[18:3]	16
0xBB00C99C	L2_LRN_PORT_CONSTRT_CTRL [12].ACT[2:0]	3
0xBB00C9A0	L2_LRN_PORT_CONSTRT_CTRL [13].RESERVED[31:19]	13
0xBB00C9A0	L2_LRN_PORT_CONSTRT_CTRL [13].CONSTRT_NUM[18:3]	16
0xBB00C9A0	L2_LRN_PORT_CONSTRT_CTRL [13].ACT[2:0]	3
0xBB00C9A4	L2_LRN_PORT_CONSTRT_CTRL [14].RESERVED[31:19]	13
0xBB00C9A4	L2_LRN_PORT_CONSTRT_CTRL [14].CONSTRT_NUM[18:3]	16
0xBB00C9A4	L2_LRN_PORT_CONSTRT_CTRL [14].ACT[2:0]	3
0xBB00C9A8	L2_LRN_PORT_CONSTRT_CTRL [15].RESERVED[31:19]	13
0xBB00C9A8	L2_LRN_PORT_CONSTRT_CTRL [15].CONSTRT_NUM[18:3]	16
0xBB00C9A8	L2_LRN_PORT_CONSTRT_CTRL [15].ACT[2:0]	3
0xBB00C9AC	L2_LRN_PORT_CONSTRT_CTRL [16].RESERVED[31:19]	13
0xBB00C9AC	L2_LRN_PORT_CONSTRT_CTRL [16].CONSTRT_NUM[18:3]	16
0xBB00C9AC	L2_LRN_PORT_CONSTRT_CTRL [16].ACT[2:0]	3
0xBB00C9B0	L2_LRN_PORT_CONSTRT_CTRL [17].RESERVED[31:19]	13
0xBB00C9B0	L2_LRN_PORT_CONSTRT_CTRL [17].CONSTRT_NUM[18:3]	16
0xBB00C9B0	L2_LRN_PORT_CONSTRT_CTRL [17].ACT[2:0]	3
0xBB00C9B4	L2_LRN_PORT_CONSTRT_CTRL [18].RESERVED[31:19]	13
0xBB00C9B4	L2_LRN_PORT_CONSTRT_CTRL [18].CONSTRT_NUM[18:3]	16
0xBB00C9B4	L2_LRN_PORT_CONSTRT_CTRL [18].ACT[2:0]	3
0xBB00C9B8	L2_LRN_PORT_CONSTRT_CTRL [19].RESERVED[31:19]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C9B8	L2_LRN_PORT_CONSTRT_CTRL [19].CONSTRT_NUM[18:3]	16
0xBB00C9B8	L2_LRN_PORT_CONSTRT_CTRL [19].ACT[2:0]	3
0xBB00C9BC	L2_LRN_PORT_CONSTRT_CTRL [20].RESERVED[31:19]	13
0xBB00C9BC	L2_LRN_PORT_CONSTRT_CTRL [20].CONSTRT_NUM[18:3]	16
0xBB00C9BC	L2_LRN_PORT_CONSTRT_CTRL [20].ACT[2:0]	3
0xBB00C9C0	L2_LRN_PORT_CONSTRT_CTRL [21].RESERVED[31:19]	13
0xBB00C9C0	L2_LRN_PORT_CONSTRT_CTRL [21].CONSTRT_NUM[18:3]	16
0xBB00C9C0	L2_LRN_PORT_CONSTRT_CTRL [21].ACT[2:0]	3
0xBB00C9C4	L2_LRN_PORT_CONSTRT_CTRL [22].RESERVED[31:19]	13
0xBB00C9C4	L2_LRN_PORT_CONSTRT_CTRL [22].CONSTRT_NUM[18:3]	16
0xBB00C9C4	L2_LRN_PORT_CONSTRT_CTRL [22].ACT[2:0]	3
0xBB00C9C8	L2_LRN_PORT_CONSTRT_CTRL [23].RESERVED[31:19]	13
0xBB00C9C8	L2_LRN_PORT_CONSTRT_CTRL [23].CONSTRT_NUM[18:3]	16
0xBB00C9C8	L2_LRN_PORT_CONSTRT_CTRL [23].ACT[2:0]	3
0xBB00C9CC	L2_LRN_PORT_CONSTRT_CTRL [24].RESERVED[31:19]	13
0xBB00C9CC	L2_LRN_PORT_CONSTRT_CTRL [24].CONSTRT_NUM[18:3]	16
0xBB00C9CC	L2_LRN_PORT_CONSTRT_CTRL [24].ACT[2:0]	3
0xBB00C9D0	L2_LRN_PORT_CONSTRT_CTRL [25].RESERVED[31:19]	13
0xBB00C9D0	L2_LRN_PORT_CONSTRT_CTRL [25].CONSTRT_NUM[18:3]	16
0xBB00C9D0	L2_LRN_PORT_CONSTRT_CTRL [25].ACT[2:0]	3
0xBB00C9D4	L2_LRN_PORT_CONSTRT_CTRL [26].RESERVED[31:19]	13
0xBB00C9D4	L2_LRN_PORT_CONSTRT_CTRL [26].CONSTRT_NUM[18:3]	16
0xBB00C9D4	L2_LRN_PORT_CONSTRT_CTRL [26].ACT[2:0]	3
0xBB00C9D8	L2_LRN_PORT_CONSTRT_CTRL [27].RESERVED[31:19]	13
0xBB00C9D8	L2_LRN_PORT_CONSTRT_CTRL [27].CONSTRT_NUM[18:3]	16
0xBB00C9D8	L2_LRN_PORT_CONSTRT_CTRL [27].ACT[2:0]	3
0xBB00C9DC	L2_LRN_PORT_CONSTRT_CTRL [28].RESERVED[31:19]	13
0xBB00C9DC	L2_LRN_PORT_CONSTRT_CTRL [28].CONSTRT_NUM[18:3]	16
0xBB00C9DC	L2_LRN_PORT_CONSTRT_CTRL [28].ACT[2:0]	3
0xBB00C9E0	L2_LRN_PORT_CONSTRT_CTRL [29].RESERVED[31:19]	13
0xBB00C9E0	L2_LRN_PORT_CONSTRT_CTRL [29].CONSTRT_NUM[18:3]	16
0xBB00C9E0	L2_LRN_PORT_CONSTRT_CTRL [29].ACT[2:0]	3
0xBB00C9E4	L2_LRN_PORT_CONSTRT_CTRL [30].RESERVED[31:19]	13
0xBB00C9E4	L2_LRN_PORT_CONSTRT_CTRL [30].CONSTRT_NUM[18:3]	16
0xBB00C9E4	L2_LRN_PORT_CONSTRT_CTRL [30].ACT[2:0]	3
0xBB00C9E8	L2_LRN_PORT_CONSTRT_CTRL [31].RESERVED[31:19]	13
0xBB00C9E8	L2_LRN_PORT_CONSTRT_CTRL [31].CONSTRT_NUM[18:3]	16
0xBB00C9E8	L2_LRN_PORT_CONSTRT_CTRL [31].ACT[2:0]	3
0xBB00C9EC	L2_LRN_PORT_CONSTRT_CTRL [32].RESERVED[31:19]	13
0xBB00C9EC	L2_LRN_PORT_CONSTRT_CTRL [32].CONSTRT_NUM[18:3]	16
0xBB00C9EC	L2_LRN_PORT_CONSTRT_CTRL [32].ACT[2:0]	3
0xBB00C9F0	L2_LRN_PORT_CONSTRT_CTRL [33].RESERVED[31:19]	13
0xBB00C9F0	L2_LRN_PORT_CONSTRT_CTRL [33].CONSTRT_NUM[18:3]	16
0xBB00C9F0	L2_LRN_PORT_CONSTRT_CTRL [33].ACT[2:0]	3
0xBB00C9F4	L2_LRN_PORT_CONSTRT_CTRL [34].RESERVED[31:19]	13
0xBB00C9F4	L2_LRN_PORT_CONSTRT_CTRL [34].CONSTRT_NUM[18:3]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C9F4	L2_LRN_PORT_CONSTRT_CTRL [34].ACT[2:0]	3
0xBB00C9F8	L2_LRN_PORT_CONSTRT_CTRL [35].RESERVED[31:19]	13
0xBB00C9F8	L2_LRN_PORT_CONSTRT_CTRL [35].CONSTRT_NUM[18:3]	16
0xBB00C9F8	L2_LRN_PORT_CONSTRT_CTRL [35].ACT[2:0]	3
0xBB00C9FC	L2_LRN_PORT_CONSTRT_CTRL [36].RESERVED[31:19]	13
0xBB00C9FC	L2_LRN_PORT_CONSTRT_CTRL [36].CONSTRT_NUM[18:3]	16
0xBB00C9FC	L2_LRN_PORT_CONSTRT_CTRL [36].ACT[2:0]	3
0xBB00CA00	L2_LRN_PORT_CONSTRT_CTRL [37].RESERVED[31:19]	13
0xBB00CA00	L2_LRN_PORT_CONSTRT_CTRL [37].CONSTRT_NUM[18:3]	16
0xBB00CA00	L2_LRN_PORT_CONSTRT_CTRL [37].ACT[2:0]	3
0xBB00CA04	L2_LRN_PORT_CONSTRT_CTRL [38].RESERVED[31:19]	13
0xBB00CA04	L2_LRN_PORT_CONSTRT_CTRL [38].CONSTRT_NUM[18:3]	16
0xBB00CA04	L2_LRN_PORT_CONSTRT_CTRL [38].ACT[2:0]	3
0xBB00CA08	L2_LRN_PORT_CONSTRT_CTRL [39].RESERVED[31:19]	13
0xBB00CA08	L2_LRN_PORT_CONSTRT_CTRL [39].CONSTRT_NUM[18:3]	16
0xBB00CA08	L2_LRN_PORT_CONSTRT_CTRL [39].ACT[2:0]	3
0xBB00CA0C	L2_LRN_PORT_CONSTRT_CTRL [40].RESERVED[31:19]	13
0xBB00CA0C	L2_LRN_PORT_CONSTRT_CTRL [40].CONSTRT_NUM[18:3]	16
0xBB00CA0C	L2_LRN_PORT_CONSTRT_CTRL [40].ACT[2:0]	3
0xBB00CA10	L2_LRN_PORT_CONSTRT_CTRL [41].RESERVED[31:19]	13
0xBB00CA10	L2_LRN_PORT_CONSTRT_CTRL [41].CONSTRT_NUM[18:3]	16
0xBB00CA10	L2_LRN_PORT_CONSTRT_CTRL [41].ACT[2:0]	3
0xBB00CA14	L2_LRN_PORT_CONSTRT_CTRL [42].RESERVED[31:19]	13
0xBB00CA14	L2_LRN_PORT_CONSTRT_CTRL [42].CONSTRT_NUM[18:3]	16
0xBB00CA14	L2_LRN_PORT_CONSTRT_CTRL [42].ACT[2:0]	3
0xBB00CA18	L2_LRN_PORT_CONSTRT_CTRL [43].RESERVED[31:19]	13
0xBB00CA18	L2_LRN_PORT_CONSTRT_CTRL [43].CONSTRT_NUM[18:3]	16
0xBB00CA18	L2_LRN_PORT_CONSTRT_CTRL [43].ACT[2:0]	3
0xBB00CA1C	L2_LRN_PORT_CONSTRT_CTRL [44].RESERVED[31:19]	13
0xBB00CA1C	L2_LRN_PORT_CONSTRT_CTRL [44].CONSTRT_NUM[18:3]	16
0xBB00CA1C	L2_LRN_PORT_CONSTRT_CTRL [44].ACT[2:0]	3
0xBB00CA20	L2_LRN_PORT_CONSTRT_CTRL [45].RESERVED[31:19]	13
0xBB00CA20	L2_LRN_PORT_CONSTRT_CTRL [45].CONSTRT_NUM[18:3]	16
0xBB00CA20	L2_LRN_PORT_CONSTRT_CTRL [45].ACT[2:0]	3
0xBB00CA24	L2_LRN_PORT_CONSTRT_CTRL [46].RESERVED[31:19]	13
0xBB00CA24	L2_LRN_PORT_CONSTRT_CTRL [46].CONSTRT_NUM[18:3]	16
0xBB00CA24	L2_LRN_PORT_CONSTRT_CTRL [46].ACT[2:0]	3
0xBB00CA28	L2_LRN_PORT_CONSTRT_CTRL [47].RESERVED[31:19]	13
0xBB00CA28	L2_LRN_PORT_CONSTRT_CTRL [47].CONSTRT_NUM[18:3]	16
0xBB00CA28	L2_LRN_PORT_CONSTRT_CTRL [47].ACT[2:0]	3
0xBB00CA2C	L2_LRN_PORT_CONSTRT_CTRL [48].RESERVED[31:19]	13
0xBB00CA2C	L2_LRN_PORT_CONSTRT_CTRL [48].CONSTRT_NUM[18:3]	16
0xBB00CA2C	L2_LRN_PORT_CONSTRT_CTRL [48].ACT[2:0]	3
0xBB00CA30	L2_LRN_PORT_CONSTRT_CTRL [49].RESERVED[31:19]	13
0xBB00CA30	L2_LRN_PORT_CONSTRT_CTRL [49].CONSTRT_NUM[18:3]	16
0xBB00CA30	L2_LRN_PORT_CONSTRT_CTRL [49].ACT[2:0]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CA34	L2_LRN_PORT_CONSTRT_CTRL [50].RESERVED[31:19]	13
0xBB00CA34	L2_LRN_PORT_CONSTRT_CTRL [50].CONSTRT_NUM[18:3]	16
0xBB00CA34	L2_LRN_PORT_CONSTRT_CTRL [50].ACT[2:0]	3
0xBB00CA38	L2_LRN_PORT_CONSTRT_CTRL [51].RESERVED[31:19]	13
0xBB00CA38	L2_LRN_PORT_CONSTRT_CTRL [51].CONSTRT_NUM[18:3]	16
0xBB00CA38	L2_LRN_PORT_CONSTRT_CTRL [51].ACT[2:0]	3
0xBB00CA3C	L2_LRN_PORT_CONSTRT_CTRL [52].RESERVED[31:19]	13
0xBB00CA3C	L2_LRN_PORT_CONSTRT_CTRL [52].CONSTRT_NUM[18:3]	16
0xBB00CA3C	L2_LRN_PORT_CONSTRT_CTRL [52].ACT[2:0]	3
0xBB00CA40	L2_LRN_PORT_CONSTRT_CTRL [53].RESERVED[31:19]	13
0xBB00CA40	L2_LRN_PORT_CONSTRT_CTRL [53].CONSTRT_NUM[18:3]	16
0xBB00CA40	L2_LRN_PORT_CONSTRT_CTRL [53].ACT[2:0]	3
0xBB00CA44	L2_LRN_PORT_CONSTRT_CTRL [54].RESERVED[31:19]	13
0xBB00CA44	L2_LRN_PORT_CONSTRT_CTRL [54].CONSTRT_NUM[18:3]	16
0xBB00CA44	L2_LRN_PORT_CONSTRT_CTRL [54].ACT[2:0]	3
0xBB00CA48	L2_LRN_PORT_CONSTRT_CTRL [55].RESERVED[31:19]	13
0xBB00CA48	L2_LRN_PORT_CONSTRT_CTRL [55].CONSTRT_NUM[18:3]	16
0xBB00CA48	L2_LRN_PORT_CONSTRT_CTRL [55].ACT[2:0]	3
0xBB00CA4C	L2_LRN_PORT_CONSTRT_CTRL [56].RESERVED[31:19]	13
0xBB00CA4C	L2_LRN_PORT_CONSTRT_CTRL [56].CONSTRT_NUM[18:3]	16
0xBB00CA4C	L2_LRN_PORT_CONSTRT_CTRL [56].ACT[2:0]	3
0xBB00CB9C	L2_LRN_TRK_CONSTRT_CNT [0].RESERVED[31:16]	16
0xBB00CB9C	L2_LRN_TRK_CONSTRT_CNT [0].LRN_CNT[15:0]	16
0xBB00CBA0	L2_LRN_TRK_CONSTRT_CNT [1].RESERVED[31:16]	16
0xBB00CBA0	L2_LRN_TRK_CONSTRT_CNT [1].LRN_CNT[15:0]	16
0xBB00CBA4	L2_LRN_TRK_CONSTRT_CNT [2].RESERVED[31:16]	16
0xBB00CBA4	L2_LRN_TRK_CONSTRT_CNT [2].LRN_CNT[15:0]	16
0xBB00CBA8	L2_LRN_TRK_CONSTRT_CNT [3].RESERVED[31:16]	16
0xBB00CBA8	L2_LRN_TRK_CONSTRT_CNT [3].LRN_CNT[15:0]	16
0xBB00CBAC	L2_LRN_TRK_CONSTRT_CNT [4].RESERVED[31:16]	16
0xBB00CBAC	L2_LRN_TRK_CONSTRT_CNT [4].LRN_CNT[15:0]	16
0xBB00CBB0	L2_LRN_TRK_CONSTRT_CNT [5].RESERVED[31:16]	16
0xBB00CBB0	L2_LRN_TRK_CONSTRT_CNT [5].LRN_CNT[15:0]	16
0xBB00CBB4	L2_LRN_TRK_CONSTRT_CNT [6].RESERVED[31:16]	16
0xBB00CBB4	L2_LRN_TRK_CONSTRT_CNT [6].LRN_CNT[15:0]	16
0xBB00CBB8	L2_LRN_TRK_CONSTRT_CNT [7].RESERVED[31:16]	16
0xBB00CBB8	L2_LRN_TRK_CONSTRT_CNT [7].LRN_CNT[15:0]	16
0xBB00CBC0	L2_LRN_TRK_CONSTRT_CNT [8].RESERVED[31:16]	16
0xBB00CBC0	L2_LRN_TRK_CONSTRT_CNT [8].LRN_CNT[15:0]	16
0xBB00CBC4	L2_LRN_TRK_CONSTRT_CNT [9].RESERVED[31:16]	16
0xBB00CBC4	L2_LRN_TRK_CONSTRT_CNT [9].LRN_CNT[15:0]	16
0xBB00CBC8	L2_LRN_TRK_CONSTRT_CNT [10].RESERVED[31:16]	16
0xBB00CBC8	L2_LRN_TRK_CONSTRT_CNT [10].LRN_CNT[15:0]	16
0xBB00CBCC	L2_LRN_TRK_CONSTRT_CNT [11].RESERVED[31:16]	16
0xBB00CBCC	L2_LRN_TRK_CONSTRT_CNT [11].LRN_CNT[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CBCC	L2_LRN_TRK_CONSTRT_CNT [12].LRN_CNT[15:0]	16
0xBB00CBD0	L2_LRN_TRK_CONSTRT_CNT [13].RESERVED[31:16]	16
0xBB00CBD0	L2_LRN_TRK_CONSTRT_CNT [13].LRN_CNT[15:0]	16
0xBB00CBD4	L2_LRN_TRK_CONSTRT_CNT [14].RESERVED[31:16]	16
0xBB00CBD4	L2_LRN_TRK_CONSTRT_CNT [14].LRN_CNT[15:0]	16
0xBB00CBD8	L2_LRN_TRK_CONSTRT_CNT [15].RESERVED[31:16]	16
0xBB00CBD8	L2_LRN_TRK_CONSTRT_CNT [15].LRN_CNT[15:0]	16
0xBB00CBDC	L2_LRN_TRK_CONSTRT_CNT [16].RESERVED[31:16]	16
0xBB00CBDC	L2_LRN_TRK_CONSTRT_CNT [16].LRN_CNT[15:0]	16
0xBB00CBE0	L2_LRN_TRK_CONSTRT_CNT [17].RESERVED[31:16]	16
0xBB00CBE0	L2_LRN_TRK_CONSTRT_CNT [17].LRN_CNT[15:0]	16
0xBB00CBE4	L2_LRN_TRK_CONSTRT_CNT [18].RESERVED[31:16]	16
0xBB00CBE4	L2_LRN_TRK_CONSTRT_CNT [18].LRN_CNT[15:0]	16
0xBB00CBE8	L2_LRN_TRK_CONSTRT_CNT [19].RESERVED[31:16]	16
0xBB00CBE8	L2_LRN_TRK_CONSTRT_CNT [19].LRN_CNT[15:0]	16
0xBB00CBEC	L2_LRN_TRK_CONSTRT_CNT [20].RESERVED[31:16]	16
0xBB00CBEC	L2_LRN_TRK_CONSTRT_CNT [20].LRN_CNT[15:0]	16
0xBB00CBF0	L2_LRN_TRK_CONSTRT_CNT [21].RESERVED[31:16]	16
0xBB00CBF0	L2_LRN_TRK_CONSTRT_CNT [21].LRN_CNT[15:0]	16
0xBB00CBF4	L2_LRN_TRK_CONSTRT_CNT [22].RESERVED[31:16]	16
0xBB00CBF4	L2_LRN_TRK_CONSTRT_CNT [22].LRN_CNT[15:0]	16
0xBB00CBF8	L2_LRN_TRK_CONSTRT_CNT [23].RESERVED[31:16]	16
0xBB00CBF8	L2_LRN_TRK_CONSTRT_CNT [23].LRN_CNT[15:0]	16
0xBB00CBFC	L2_LRN_TRK_CONSTRT_CNT [24].RESERVED[31:16]	16
0xBB00CBFC	L2_LRN_TRK_CONSTRT_CNT [24].LRN_CNT[15:0]	16
0xBB00CC00	L2_LRN_TRK_CONSTRT_CNT [25].RESERVED[31:16]	16
0xBB00CC00	L2_LRN_TRK_CONSTRT_CNT [25].LRN_CNT[15:0]	16
0xBB00CB34	L2_LRN_TRK_CONSTRT_CTRL [0].RESERVED[31:19]	13
0xBB00CB34	L2_LRN_TRK_CONSTRT_CTRL [0].CONSTRT_NUM[18:3]	16
0xBB00CB34	L2_LRN_TRK_CONSTRT_CTRL [0].ACT[2:0]	3
0xBB00CB38	L2_LRN_TRK_CONSTRT_CTRL [1].RESERVED[31:19]	13
0xBB00CB38	L2_LRN_TRK_CONSTRT_CTRL [1].CONSTRT_NUM[18:3]	16
0xBB00CB38	L2_LRN_TRK_CONSTRT_CTRL [1].ACT[2:0]	3
0xBB00CB3C	L2_LRN_TRK_CONSTRT_CTRL [2].RESERVED[31:19]	13
0xBB00CB3C	L2_LRN_TRK_CONSTRT_CTRL [2].CONSTRT_NUM[18:3]	16
0xBB00CB3C	L2_LRN_TRK_CONSTRT_CTRL [2].ACT[2:0]	3
0xBB00CB40	L2_LRN_TRK_CONSTRT_CTRL [3].RESERVED[31:19]	13
0xBB00CB40	L2_LRN_TRK_CONSTRT_CTRL [3].CONSTRT_NUM[18:3]	16
0xBB00CB40	L2_LRN_TRK_CONSTRT_CTRL [3].ACT[2:0]	3
0xBB00CB44	L2_LRN_TRK_CONSTRT_CTRL [4].RESERVED[31:19]	13
0xBB00CB44	L2_LRN_TRK_CONSTRT_CTRL [4].CONSTRT_NUM[18:3]	16
0xBB00CB44	L2_LRN_TRK_CONSTRT_CTRL [4].ACT[2:0]	3
0xBB00CB48	L2_LRN_TRK_CONSTRT_CTRL [5].RESERVED[31:19]	13
0xBB00CB48	L2_LRN_TRK_CONSTRT_CTRL [5].CONSTRT_NUM[18:3]	16
0xBB00CB48	L2_LRN_TRK_CONSTRT_CTRL [5].ACT[2:0]	3
0xBB00CB4C	L2_LRN_TRK_CONSTRT_CTRL [6].RESERVED[31:19]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CB4C	L2_LRN_TRK_CONSTRT_CTRL [6].CONSTRT_NUM[18:3]	16
0xBB00CB4C	L2_LRN_TRK_CONSTRT_CTRL [6].ACT[2:0]	3
0xBB00CB50	L2_LRN_TRK_CONSTRT_CTRL [7].RESERVED[31:19]	13
0xBB00CB50	L2_LRN_TRK_CONSTRT_CTRL [7].CONSTRT_NUM[18:3]	16
0xBB00CB50	L2_LRN_TRK_CONSTRT_CTRL [7].ACT[2:0]	3
0xBB00CB54	L2_LRN_TRK_CONSTRT_CTRL [8].RESERVED[31:19]	13
0xBB00CB54	L2_LRN_TRK_CONSTRT_CTRL [8].CONSTRT_NUM[18:3]	16
0xBB00CB54	L2_LRN_TRK_CONSTRT_CTRL [8].ACT[2:0]	3
0xBB00CB58	L2_LRN_TRK_CONSTRT_CTRL [9].RESERVED[31:19]	13
0xBB00CB58	L2_LRN_TRK_CONSTRT_CTRL [9].CONSTRT_NUM[18:3]	16
0xBB00CB58	L2_LRN_TRK_CONSTRT_CTRL [9].ACT[2:0]	3
0xBB00CB5C	L2_LRN_TRK_CONSTRT_CTRL [10].RESERVED[31:19]	13
0xBB00CB5C	L2_LRN_TRK_CONSTRT_CTRL [10].CONSTRT_NUM[18:3]	16
0xBB00CB5C	L2_LRN_TRK_CONSTRT_CTRL [10].ACT[2:0]	3
0xBB00CB60	L2_LRN_TRK_CONSTRT_CTRL [11].RESERVED[31:19]	13
0xBB00CB60	L2_LRN_TRK_CONSTRT_CTRL [11].CONSTRT_NUM[18:3]	16
0xBB00CB60	L2_LRN_TRK_CONSTRT_CTRL [11].ACT[2:0]	3
0xBB00CB64	L2_LRN_TRK_CONSTRT_CTRL [12].RESERVED[31:19]	13
0xBB00CB64	L2_LRN_TRK_CONSTRT_CTRL [12].CONSTRT_NUM[18:3]	16
0xBB00CB64	L2_LRN_TRK_CONSTRT_CTRL [12].ACT[2:0]	3
0xBB00CB68	L2_LRN_TRK_CONSTRT_CTRL [13].RESERVED[31:19]	13
0xBB00CB68	L2_LRN_TRK_CONSTRT_CTRL [13].CONSTRT_NUM[18:3]	16
0xBB00CB68	L2_LRN_TRK_CONSTRT_CTRL [13].ACT[2:0]	3
0xBB00CB6C	L2_LRN_TRK_CONSTRT_CTRL [14].RESERVED[31:19]	13
0xBB00CB6C	L2_LRN_TRK_CONSTRT_CTRL [14].CONSTRT_NUM[18:3]	16
0xBB00CB6C	L2_LRN_TRK_CONSTRT_CTRL [14].ACT[2:0]	3
0xBB00CB70	L2_LRN_TRK_CONSTRT_CTRL [15].RESERVED[31:19]	13
0xBB00CB70	L2_LRN_TRK_CONSTRT_CTRL [15].CONSTRT_NUM[18:3]	16
0xBB00CB70	L2_LRN_TRK_CONSTRT_CTRL [15].ACT[2:0]	3
0xBB00CB74	L2_LRN_TRK_CONSTRT_CTRL [16].RESERVED[31:19]	13
0xBB00CB74	L2_LRN_TRK_CONSTRT_CTRL [16].CONSTRT_NUM[18:3]	16
0xBB00CB74	L2_LRN_TRK_CONSTRT_CTRL [16].ACT[2:0]	3
0xBB00CB78	L2_LRN_TRK_CONSTRT_CTRL [17].RESERVED[31:19]	13
0xBB00CB78	L2_LRN_TRK_CONSTRT_CTRL [17].CONSTRT_NUM[18:3]	16
0xBB00CB78	L2_LRN_TRK_CONSTRT_CTRL [17].ACT[2:0]	3
0xBB00CB7C	L2_LRN_TRK_CONSTRT_CTRL [18].RESERVED[31:19]	13
0xBB00CB7C	L2_LRN_TRK_CONSTRT_CTRL [18].CONSTRT_NUM[18:3]	16
0xBB00CB7C	L2_LRN_TRK_CONSTRT_CTRL [18].ACT[2:0]	3
0xBB00CB80	L2_LRN_TRK_CONSTRT_CTRL [19].RESERVED[31:19]	13
0xBB00CB80	L2_LRN_TRK_CONSTRT_CTRL [19].CONSTRT_NUM[18:3]	16
0xBB00CB80	L2_LRN_TRK_CONSTRT_CTRL [19].ACT[2:0]	3
0xBB00CB84	L2_LRN_TRK_CONSTRT_CTRL [20].RESERVED[31:19]	13
0xBB00CB84	L2_LRN_TRK_CONSTRT_CTRL [20].CONSTRT_NUM[18:3]	16
0xBB00CB84	L2_LRN_TRK_CONSTRT_CTRL [20].ACT[2:0]	3
0xBB00CB88	L2_LRN_TRK_CONSTRT_CTRL [21].RESERVED[31:19]	13
0xBB00CB88	L2_LRN_TRK_CONSTRT_CTRL [21].CONSTRT_NUM[18:3]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CB88	L2_LRN_TRK_CONSTRT_CTRL [21].ACT[2:0]	3
0xBB00CB8C	L2_LRN_TRK_CONSTRT_CTRL [22].RESERVED[31:19]	13
0xBB00CB8C	L2_LRN_TRK_CONSTRT_CTRL [22].CONSTRT_NUM[18:3]	16
0xBB00CB8C	L2_LRN_TRK_CONSTRT_CTRL [22].ACT[2:0]	3
0xBB00CB90	L2_LRN_TRK_CONSTRT_CTRL [23].RESERVED[31:19]	13
0xBB00CB90	L2_LRN_TRK_CONSTRT_CTRL [23].CONSTRT_NUM[18:3]	16
0xBB00CB90	L2_LRN_TRK_CONSTRT_CTRL [23].ACT[2:0]	3
0xBB00CB94	L2_LRN_TRK_CONSTRT_CTRL [24].RESERVED[31:19]	13
0xBB00CB94	L2_LRN_TRK_CONSTRT_CTRL [24].CONSTRT_NUM[18:3]	16
0xBB00CB94	L2_LRN_TRK_CONSTRT_CTRL [24].ACT[2:0]	3
0xBB00CB98	L2_LRN_TRK_CONSTRT_CTRL [25].RESERVED[31:19]	13
0xBB00CB98	L2_LRN_TRK_CONSTRT_CTRL [25].CONSTRT_NUM[18:3]	16
0xBB00CB98	L2_LRN_TRK_CONSTRT_CTRL [25].ACT[2:0]	3
0xBB00CD04	L2_LRN_VLAN_CONSTRT_CNT [0].RESERVED[31:16]	16
0xBB00CD04	L2_LRN_VLAN_CONSTRT_CNT [0].LRN_CNT[15:0]	16
0xBB00CD08	L2_LRN_VLAN_CONSTRT_CNT [1].RESERVED[31:16]	16
0xBB00CD08	L2_LRN_VLAN_CONSTRT_CNT [1].LRN_CNT[15:0]	16
0xBB00CD0C	L2_LRN_VLAN_CONSTRT_CNT [2].RESERVED[31:16]	16
0xBB00CD0C	L2_LRN_VLAN_CONSTRT_CNT [2].LRN_CNT[15:0]	16
0xBB00CD10	L2_LRN_VLAN_CONSTRT_CNT [3].RESERVED[31:16]	16
0xBB00CD10	L2_LRN_VLAN_CONSTRT_CNT [3].LRN_CNT[15:0]	16
0xBB00CD14	L2_LRN_VLAN_CONSTRT_CNT [4].RESERVED[31:16]	16
0xBB00CD14	L2_LRN_VLAN_CONSTRT_CNT [4].LRN_CNT[15:0]	16
0xBB00CD18	L2_LRN_VLAN_CONSTRT_CNT [5].RESERVED[31:16]	16
0xBB00CD1C	L2_LRN_VLAN_CONSTRT_CNT [6].RESERVED[31:16]	16
0xBB00CD1C	L2_LRN_VLAN_CONSTRT_CNT [6].LRN_CNT[15:0]	16
0xBB00CD20	L2_LRN_VLAN_CONSTRT_CNT [7].RESERVED[31:16]	16
0xBB00CD20	L2_LRN_VLAN_CONSTRT_CNT [7].LRN_CNT[15:0]	16
0xBB00CD24	L2_LRN_VLAN_CONSTRT_CNT [8].RESERVED[31:16]	16
0xBB00CD24	L2_LRN_VLAN_CONSTRT_CNT [8].LRN_CNT[15:0]	16
0xBB00CD28	L2_LRN_VLAN_CONSTRT_CNT [9].RESERVED[31:16]	16
0xBB00CD28	L2_LRN_VLAN_CONSTRT_CNT [9].LRN_CNT[15:0]	16
0xBB00CD2C	L2_LRN_VLAN_CONSTRT_CNT [10].RESERVED[31:16]	16
0xBB00CD2C	L2_LRN_VLAN_CONSTRT_CNT [10].LRN_CNT[15:0]	16
0xBB00CD30	L2_LRN_VLAN_CONSTRT_CNT [11].RESERVED[31:16]	16
0xBB00CD30	L2_LRN_VLAN_CONSTRT_CNT [11].LRN_CNT[15:0]	16
0xBB00CD34	L2_LRN_VLAN_CONSTRT_CNT [12].RESERVED[31:16]	16
0xBB00CD34	L2_LRN_VLAN_CONSTRT_CNT [12].LRN_CNT[15:0]	16
0xBB00CD38	L2_LRN_VLAN_CONSTRT_CNT [13].RESERVED[31:16]	16
0xBB00CD38	L2_LRN_VLAN_CONSTRT_CNT [13].LRN_CNT[15:0]	16
0xBB00CD3C	L2_LRN_VLAN_CONSTRT_CNT [14].RESERVED[31:16]	16
0xBB00CD3C	L2_LRN_VLAN_CONSTRT_CNT [14].LRN_CNT[15:0]	16
0xBB00CD40	L2_LRN_VLAN_CONSTRT_CNT [15].RESERVED[31:16]	16
0xBB00CD40	L2_LRN_VLAN_CONSTRT_CNT [15].LRN_CNT[15:0]	16
0xBB00CD44	L2_LRN_VLAN_CONSTRT_CNT [16].RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CD44	L2_LRN_VLAN_CONSTRT_CNT [16].LRN_CNT[15:0]	16
0xBB00CD48	L2_LRN_VLAN_CONSTRT_CNT [17].RESERVED[31:16]	16
0xBB00CD48	L2_LRN_VLAN_CONSTRT_CNT [17].LRN_CNT[15:0]	16
0xBB00CD4C	L2_LRN_VLAN_CONSTRT_CNT [18].RESERVED[31:16]	16
0xBB00CD4C	L2_LRN_VLAN_CONSTRT_CNT [18].LRN_CNT[15:0]	16
0xBB00CD50	L2_LRN_VLAN_CONSTRT_CNT [19].RESERVED[31:16]	16
0xBB00CD50	L2_LRN_VLAN_CONSTRT_CNT [19].LRN_CNT[15:0]	16
0xBB00CD54	L2_LRN_VLAN_CONSTRT_CNT [20].RESERVED[31:16]	16
0xBB00CD54	L2_LRN_VLAN_CONSTRT_CNT [20].LRN_CNT[15:0]	16
0xBB00CD58	L2_LRN_VLAN_CONSTRT_CNT [21].RESERVED[31:16]	16
0xBB00CD5C	L2_LRN_VLAN_CONSTRT_CNT [22].RESERVED[31:16]	16
0xBB00CD5C	L2_LRN_VLAN_CONSTRT_CNT [22].LRN_CNT[15:0]	16
0xBB00CD60	L2_LRN_VLAN_CONSTRT_CNT [23].RESERVED[31:16]	16
0xBB00CD60	L2_LRN_VLAN_CONSTRT_CNT [23].LRN_CNT[15:0]	16
0xBB00CD64	L2_LRN_VLAN_CONSTRT_CNT [24].RESERVED[31:16]	16
0xBB00CD64	L2_LRN_VLAN_CONSTRT_CNT [24].LRN_CNT[15:0]	16
0xBB00CD68	L2_LRN_VLAN_CONSTRT_CNT [25].RESERVED[31:16]	16
0xBB00CD68	L2_LRN_VLAN_CONSTRT_CNT [25].LRN_CNT[15:0]	16
0xBB00CD6C	L2_LRN_VLAN_CONSTRT_CNT [26].RESERVED[31:16]	16
0xBB00CD6C	L2_LRN_VLAN_CONSTRT_CNT [26].LRN_CNT[15:0]	16
0xBB00CD70	L2_LRN_VLAN_CONSTRT_CNT [27].RESERVED[31:16]	16
0xBB00CD70	L2_LRN_VLAN_CONSTRT_CNT [27].LRN_CNT[15:0]	16
0xBB00CD74	L2_LRN_VLAN_CONSTRT_CNT [28].RESERVED[31:16]	16
0xBB00CD78	L2_LRN_VLAN_CONSTRT_CNT [29].RESERVED[31:16]	16
0xBB00CD78	L2_LRN_VLAN_CONSTRT_CNT [29].LRN_CNT[15:0]	16
0xBB00CD7C	L2_LRN_VLAN_CONSTRT_CNT [30].RESERVED[31:16]	16
0xBB00CD7C	L2_LRN_VLAN_CONSTRT_CNT [30].LRN_CNT[15:0]	16
0xBB00CD80	L2_LRN_VLAN_CONSTRT_CNT [31].RESERVED[31:16]	16
0xBB00CD80	L2_LRN_VLAN_CONSTRT_CNT [31].LRN_CNT[15:0]	16
0xBB00CC04	L2_LRN_VLAN_CONSTRT_ENTRY [0].RESERVED[31:12]	20
0xBB00CC04	L2_LRN_VLAN_CONSTRT_ENTRY [0].FVID[11:0]	12
0xBB00CC08	L2_LRN_VLAN_CONSTRT_ENTRY [0].RESERVED[31:23]	9
0xBB00CC08	L2_LRN_VLAN_CONSTRT_ENTRY [0].PORT_ID[22:16]	7
0xBB00CC08	L2_LRN_VLAN_CONSTRT_ENTRY [0].CONSTRT_NUM[15:0]	16
0xBB00CC0C	L2_LRN_VLAN_CONSTRT_ENTRY [1].RESERVED[31:12]	20
0xBB00CC0C	L2_LRN_VLAN_CONSTRT_ENTRY [1].FVID[11:0]	12
0xBB00CC10	L2_LRN_VLAN_CONSTRT_ENTRY [1].RESERVED[31:23]	9
0xBB00CC10	L2_LRN_VLAN_CONSTRT_ENTRY [1].PORT_ID[22:16]	7
0xBB00CC10	L2_LRN_VLAN_CONSTRT_ENTRY [1].CONSTRT_NUM[15:0]	16
0xBB00CC14	L2_LRN_VLAN_CONSTRT_ENTRY [2].RESERVED[31:12]	20
0xBB00CC14	L2_LRN_VLAN_CONSTRT_ENTRY [2].FVID[11:0]	12
0xBB00CC18	L2_LRN_VLAN_CONSTRT_ENTRY [2].RESERVED[31:23]	9
0xBB00CC18	L2_LRN_VLAN_CONSTRT_ENTRY [2].PORT_ID[22:16]	7
0xBB00CC18	L2_LRN_VLAN_CONSTRT_ENTRY [2].CONSTRT_NUM[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CC1C	L2_LRN_VLAN_CONSTRT_ENTRY [3].RESERVED[31:12]	20
0xBB00CC1C	L2_LRN_VLAN_CONSTRT_ENTRY [3].FVID[11:0]	12
0xBB00CC20	L2_LRN_VLAN_CONSTRT_ENTRY [3].RESERVED[31:23]	9
0xBB00CC20	L2_LRN_VLAN_CONSTRT_ENTRY [3].PORT_ID[22:16]	7
0xBB00CC20	L2_LRN_VLAN_CONSTRT_ENTRY [3].CONSTRT_NUM[15:0]	16
0xBB00CC24	L2_LRN_VLAN_CONSTRT_ENTRY [4].RESERVED[31:12]	20
0xBB00CC24	L2_LRN_VLAN_CONSTRT_ENTRY [4].FVID[11:0]	12
0xBB00CC28	L2_LRN_VLAN_CONSTRT_ENTRY [4].RESERVED[31:23]	9
0xBB00CC28	L2_LRN_VLAN_CONSTRT_ENTRY [4].PORT_ID[22:16]	7
0xBB00CC28	L2_LRN_VLAN_CONSTRT_ENTRY [4].CONSTRT_NUM[15:0]	16
0xBB00CC2C	L2_LRN_VLAN_CONSTRT_ENTRY [5].RESERVED[31:12]	20
0xBB00CC2C	L2_LRN_VLAN_CONSTRT_ENTRY [5].FVID[11:0]	12
0xBB00CC30	L2_LRN_VLAN_CONSTRT_ENTRY [5].RESERVED[31:23]	9
0xBB00CC30	L2_LRN_VLAN_CONSTRT_ENTRY [5].PORT_ID[22:16]	7
0xBB00CC30	L2_LRN_VLAN_CONSTRT_ENTRY [5].CONSTRT_NUM[15:0]	16
0xBB00CC34	L2_LRN_VLAN_CONSTRT_ENTRY [6].RESERVED[31:12]	20
0xBB00CC34	L2_LRN_VLAN_CONSTRT_ENTRY [6].FVID[11:0]	12
0xBB00CC38	L2_LRN_VLAN_CONSTRT_ENTRY [6].RESERVED[31:23]	9
0xBB00CC38	L2_LRN_VLAN_CONSTRT_ENTRY [6].PORT_ID[22:16]	7
0xBB00CC38	L2_LRN_VLAN_CONSTRT_ENTRY [6].CONSTRT_NUM[15:0]	16
0xBB00CC3C	L2_LRN_VLAN_CONSTRT_ENTRY [7].RESERVED[31:12]	20
0xBB00CC3C	L2_LRN_VLAN_CONSTRT_ENTRY [7].FVID[11:0]	12
0xBB00CC40	L2_LRN_VLAN_CONSTRT_ENTRY [7].RESERVED[31:23]	9
0xBB00CC40	L2_LRN_VLAN_CONSTRT_ENTRY [7].PORT_ID[22:16]	7
0xBB00CC40	L2_LRN_VLAN_CONSTRT_ENTRY [7].CONSTRT_NUM[15:0]	16
0xBB00CC44	L2_LRN_VLAN_CONSTRT_ENTRY [8].RESERVED[31:12]	20
0xBB00CC44	L2_LRN_VLAN_CONSTRT_ENTRY [8].FVID[11:0]	12
0xBB00CC48	L2_LRN_VLAN_CONSTRT_ENTRY [8].RESERVED[31:23]	9
0xBB00CC48	L2_LRN_VLAN_CONSTRT_ENTRY [8].PORT_ID[22:16]	7
0xBB00CC48	L2_LRN_VLAN_CONSTRT_ENTRY [8].CONSTRT_NUM[15:0]	16
0xBB00CC4C	L2_LRN_VLAN_CONSTRT_ENTRY [9].RESERVED[31:12]	20
0xBB00CC4C	L2_LRN_VLAN_CONSTRT_ENTRY [9].FVID[11:0]	12
0xBB00CC50	L2_LRN_VLAN_CONSTRT_ENTRY [9].RESERVED[31:23]	9
0xBB00CC50	L2_LRN_VLAN_CONSTRT_ENTRY [9].PORT_ID[22:16]	7
0xBB00CC50	L2_LRN_VLAN_CONSTRT_ENTRY [9].CONSTRT_NUM[15:0]	16
0xBB00CC54	L2_LRN_VLAN_CONSTRT_ENTRY [10].RESERVED[31:12]	20
0xBB00CC54	L2_LRN_VLAN_CONSTRT_ENTRY [10].FVID[11:0]	12
0xBB00CC58	L2_LRN_VLAN_CONSTRT_ENTRY [10].RESERVED[31:23]	9
0xBB00CC58	L2_LRN_VLAN_CONSTRT_ENTRY [10].PORT_ID[22:16]	7
0xBB00CC58	L2_LRN_VLAN_CONSTRT_ENTRY [10].CONSTRT_NUM[15:0]	16
0xBB00CC5C	L2_LRN_VLAN_CONSTRT_ENTRY [11].RESERVED[31:12]	20
0xBB00CC5C	L2_LRN_VLAN_CONSTRT_ENTRY [11].FVID[11:0]	12
0xBB00CC60	L2_LRN_VLAN_CONSTRT_ENTRY [11].RESERVED[31:23]	9
0xBB00CC60	L2_LRN_VLAN_CONSTRT_ENTRY [11].PORT_ID[22:16]	7
0xBB00CC60	L2_LRN_VLAN_CONSTRT_ENTRY [11].CONSTRT_NUM[15:0]	16
0xBB00CC64	L2_LRN_VLAN_CONSTRT_ENTRY [12].RESERVED[31:12]	20

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CC64	L2_LRN_VLAN_CONSTRT_ENTRY [12].FVID[11:0]	12
0xBB00CC68	L2_LRN_VLAN_CONSTRT_ENTRY [12].RESERVED[31:23]	9
0xBB00CC68	L2_LRN_VLAN_CONSTRT_ENTRY [12].PORT_ID[22:16]	7
0xBB00CC68	L2_LRN_VLAN_CONSTRT_ENTRY [12].CONSTRT_NUM[15:0]	16
0xBB00CC6C	L2_LRN_VLAN_CONSTRT_ENTRY [13].RESERVED[31:12]	20
0xBB00CC6C	L2_LRN_VLAN_CONSTRT_ENTRY [13].FVID[11:0]	12
0xBB00CC70	L2_LRN_VLAN_CONSTRT_ENTRY [13].RESERVED[31:23]	9
0xBB00CC70	L2_LRN_VLAN_CONSTRT_ENTRY [13].PORT_ID[22:16]	7
0xBB00CC70	L2_LRN_VLAN_CONSTRT_ENTRY [13].CONSTRT_NUM[15:0]	16
0xBB00CC74	L2_LRN_VLAN_CONSTRT_ENTRY [14].RESERVED[31:12]	20
0xBB00CC74	L2_LRN_VLAN_CONSTRT_ENTRY [14].FVID[11:0]	12
0xBB00CC78	L2_LRN_VLAN_CONSTRT_ENTRY [14].RESERVED[31:23]	9
0xBB00CC78	L2_LRN_VLAN_CONSTRT_ENTRY [14].PORT_ID[22:16]	7
0xBB00CC78	L2_LRN_VLAN_CONSTRT_ENTRY [14].CONSTRT_NUM[15:0]	16
0xBB00CC7C	L2_LRN_VLAN_CONSTRT_ENTRY [15].RESERVED[31:12]	20
0xBB00CC7C	L2_LRN_VLAN_CONSTRT_ENTRY [15].FVID[11:0]	12
0xBB00CC80	L2_LRN_VLAN_CONSTRT_ENTRY [15].RESERVED[31:23]	9
0xBB00CC80	L2_LRN_VLAN_CONSTRT_ENTRY [15].PORT_ID[22:16]	7
0xBB00CC80	L2_LRN_VLAN_CONSTRT_ENTRY [15].CONSTRT_NUM[15:0]	16
0xBB00CC84	L2_LRN_VLAN_CONSTRT_ENTRY [16].RESERVED[31:12]	20
0xBB00CC84	L2_LRN_VLAN_CONSTRT_ENTRY [16].FVID[11:0]	12
0xBB00CC88	L2_LRN_VLAN_CONSTRT_ENTRY [16].RESERVED[31:23]	9
0xBB00CC88	L2_LRN_VLAN_CONSTRT_ENTRY [16].PORT_ID[22:16]	7
0xBB00CC88	L2_LRN_VLAN_CONSTRT_ENTRY [16].CONSTRT_NUM[15:0]	16
0xBB00CC8C	L2_LRN_VLAN_CONSTRT_ENTRY [17].RESERVED[31:12]	20
0xBB00CC8C	L2_LRN_VLAN_CONSTRT_ENTRY [17].FVID[11:0]	12
0xBB00CC90	L2_LRN_VLAN_CONSTRT_ENTRY [17].RESERVED[31:23]	9
0xBB00CC90	L2_LRN_VLAN_CONSTRT_ENTRY [17].PORT_ID[22:16]	7
0xBB00CC90	L2_LRN_VLAN_CONSTRT_ENTRY [17].CONSTRT_NUM[15:0]	16
0xBB00CC94	L2_LRN_VLAN_CONSTRT_ENTRY [18].RESERVED[31:12]	20
0xBB00CC94	L2_LRN_VLAN_CONSTRT_ENTRY [18].FVID[11:0]	12
0xBB00CC98	L2_LRN_VLAN_CONSTRT_ENTRY [18].RESERVED[31:23]	9
0xBB00CC98	L2_LRN_VLAN_CONSTRT_ENTRY [18].PORT_ID[22:16]	7
0xBB00CC98	L2_LRN_VLAN_CONSTRT_ENTRY [18].CONSTRT_NUM[15:0]	16
0xBB00CC9C	L2_LRN_VLAN_CONSTRT_ENTRY [19].RESERVED[31:12]	20
0xBB00CC9C	L2_LRN_VLAN_CONSTRT_ENTRY [19].FVID[11:0]	12
0xBB00CCA0	L2_LRN_VLAN_CONSTRT_ENTRY [19].RESERVED[31:23]	9
0xBB00CCA0	L2_LRN_VLAN_CONSTRT_ENTRY [19].PORT_ID[22:16]	7
0xBB00CCA0	L2_LRN_VLAN_CONSTRT_ENTRY [19].CONSTRT_NUM[15:0]	16
0xBB00CCA4	L2_LRN_VLAN_CONSTRT_ENTRY [20].RESERVED[31:12]	20
0xBB00CCA4	L2_LRN_VLAN_CONSTRT_ENTRY [20].FVID[11:0]	12
0xBB00CCA8	L2_LRN_VLAN_CONSTRT_ENTRY [20].RESERVED[31:23]	9
0xBB00CCA8	L2_LRN_VLAN_CONSTRT_ENTRY [20].PORT_ID[22:16]	7
0xBB00CCA8	L2_LRN_VLAN_CONSTRT_ENTRY [20].CONSTRT_NUM[15:0]	16
0xBB00CCAC	L2_LRN_VLAN_CONSTRT_ENTRY [21].RESERVED[31:12]	20
0xBB00CCAC	L2_LRN_VLAN_CONSTRT_ENTRY [21].FVID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CCB0	L2_LRN_VLAN_CONSTRT_ENTRY [21].RESERVED[31:23]	9
0xBB00CCB0	L2_LRN_VLAN_CONSTRT_ENTRY [21].PORT_ID[22:16]	7
0xBB00CCB0	L2_LRN_VLAN_CONSTRT_ENTRY [21].CONSTRT_NUM[15:0]	16
0xBB00CCB4	L2_LRN_VLAN_CONSTRT_ENTRY [22].RESERVED[31:12]	20
0xBB00CCB4	L2_LRN_VLAN_CONSTRT_ENTRY [22].FVID[11:0]	12
0xBB00CCB8	L2_LRN_VLAN_CONSTRT_ENTRY [22].RESERVED[31:23]	9
0xBB00CCB8	L2_LRN_VLAN_CONSTRT_ENTRY [22].PORT_ID[22:16]	7
0xBB00CCB8	L2_LRN_VLAN_CONSTRT_ENTRY [22].CONSTRT_NUM[15:0]	16
0xBB00CCBC	L2_LRN_VLAN_CONSTRT_ENTRY [23].RESERVED[31:12]	20
0xBB00CCBC	L2_LRN_VLAN_CONSTRT_ENTRY [23].FVID[11:0]	12
0xBB00CCC0	L2_LRN_VLAN_CONSTRT_ENTRY [23].RESERVED[31:23]	9
0xBB00CCC0	L2_LRN_VLAN_CONSTRT_ENTRY [23].PORT_ID[22:16]	7
0xBB00CCC0	L2_LRN_VLAN_CONSTRT_ENTRY [23].CONSTRT_NUM[15:0]	16
0xBB00CCC4	L2_LRN_VLAN_CONSTRT_ENTRY [24].RESERVED[31:12]	20
0xBB00CCC4	L2_LRN_VLAN_CONSTRT_ENTRY [24].FVID[11:0]	12
0xBB00CCC8	L2_LRN_VLAN_CONSTRT_ENTRY [24].RESERVED[31:23]	9
0xBB00CCC8	L2_LRN_VLAN_CONSTRT_ENTRY [24].PORT_ID[22:16]	7
0xBB00CCC8	L2_LRN_VLAN_CONSTRT_ENTRY [24].CONSTRT_NUM[15:0]	16
0xBB00CCCC	L2_LRN_VLAN_CONSTRT_ENTRY [25].RESERVED[31:12]	20
0xBB00CCCC	L2_LRN_VLAN_CONSTRT_ENTRY [25].FVID[11:0]	12
0xBB00CCD0	L2_LRN_VLAN_CONSTRT_ENTRY [25].RESERVED[31:23]	9
0xBB00CCD0	L2_LRN_VLAN_CONSTRT_ENTRY [25].PORT_ID[22:16]	7
0xBB00CCD0	L2_LRN_VLAN_CONSTRT_ENTRY [25].CONSTRT_NUM[15:0]	16
0xBB00CCD4	L2_LRN_VLAN_CONSTRT_ENTRY [26].RESERVED[31:12]	20
0xBB00CCD4	L2_LRN_VLAN_CONSTRT_ENTRY [26].FVID[11:0]	12
0xBB00CCD8	L2_LRN_VLAN_CONSTRT_ENTRY [26].RESERVED[31:23]	9
0xBB00CCD8	L2_LRN_VLAN_CONSTRT_ENTRY [26].PORT_ID[22:16]	7
0xBB00CCD8	L2_LRN_VLAN_CONSTRT_ENTRY [26].CONSTRT_NUM[15:0]	16
0xBB00CCDC	L2_LRN_VLAN_CONSTRT_ENTRY [27].RESERVED[31:12]	20
0xBB00CCDC	L2_LRN_VLAN_CONSTRT_ENTRY [27].FVID[11:0]	12
0xBB00CCE0	L2_LRN_VLAN_CONSTRT_ENTRY [27].RESERVED[31:23]	9
0xBB00CCE0	L2_LRN_VLAN_CONSTRT_ENTRY [27].PORT_ID[22:16]	7
0xBB00CCE0	L2_LRN_VLAN_CONSTRT_ENTRY [27].CONSTRT_NUM[15:0]	16
0xBB00CCE4	L2_LRN_VLAN_CONSTRT_ENTRY [28].RESERVED[31:12]	20
0xBB00CCE4	L2_LRN_VLAN_CONSTRT_ENTRY [28].FVID[11:0]	12
0xBB00CCE8	L2_LRN_VLAN_CONSTRT_ENTRY [28].RESERVED[31:23]	9
0xBB00CCE8	L2_LRN_VLAN_CONSTRT_ENTRY [28].PORT_ID[22:16]	7
0xBB00CCE8	L2_LRN_VLAN_CONSTRT_ENTRY [28].CONSTRT_NUM[15:0]	16
0xBB00CCEC	L2_LRN_VLAN_CONSTRT_ENTRY [29].RESERVED[31:12]	20
0xBB00CCEC	L2_LRN_VLAN_CONSTRT_ENTRY [29].FVID[11:0]	12
0xBB00CCF0	L2_LRN_VLAN_CONSTRT_ENTRY [29].RESERVED[31:23]	9
0xBB00CCF0	L2_LRN_VLAN_CONSTRT_ENTRY [29].PORT_ID[22:16]	7
0xBB00CCF0	L2_LRN_VLAN_CONSTRT_ENTRY [29].CONSTRT_NUM[15:0]	16
0xBB00CCF4	L2_LRN_VLAN_CONSTRT_ENTRY [30].RESERVED[31:12]	20
0xBB00CCF4	L2_LRN_VLAN_CONSTRT_ENTRY [30].FVID[11:0]	12
0xBB00CCF8	L2_LRN_VLAN_CONSTRT_ENTRY [30].RESERVED[31:23]	9

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CCF8	L2_LRN_VLAN_CONSTRT_ENTRY [30].PORT_ID[22:16]	7
0xBB00CCF8	L2_LRN_VLAN_CONSTRT_ENTRY [30].CONSTRT_NUM[15:0]	16
0xBB00CCFC	L2_LRN_VLAN_CONSTRT_ENTRY [31].RESERVED[31:12]	20
0xBB00CCFC	L2_LRN_VLAN_CONSTRT_ENTRY [31].FVID[11:0]	12
0xBB00CD00	L2_LRN_VLAN_CONSTRT_ENTRY [31].RESERVED[31:23]	9
0xBB00CD00	L2_LRN_VLAN_CONSTRT_ENTRY [31].PORT_ID[22:16]	7
0xBB00CD00	L2_LRN_VLAN_CONSTRT_ENTRY [31].CONSTRT_NUM[15:0]	16
0xBB00CDC8	L2_NTFY_CTRL.RESERVED[31:24]	8
0xBB00CDC8	L2_NTFY_CTRL.DYN_NTFY_AGE_OUT_EN[23:23]	1
0xBB00CDC8	L2_NTFY_CTRL.DYN_NTFY_PORT_MV_EN[22:22]	1
0xBB00CDC8	L2_NTFY_CTRL.DYN_NTFY_NEW_LRN_EN[21:21]	1
0xBB00CDC8	L2_NTFY_CTRL.NTFY_DST_TPYE[20:19]	2
0xBB00CDC8	L2_NTFY_CTRL.HASHFULL_NTFY_EN[18:18]	1
0xBB00CDC8	L2_NTFY_CTRL.LD_FLUSH_NTFY_EN[17:17]	1
0xBB00CDC8	L2_NTFY_CTRL.TAGSTS_NTFY_EN[16:16]	1
0xBB00CDC8	L2_NTFY_CTRL.SUS_NTFY_EN[15:15]	1
0xBB00CDC8	L2_NTFY_CTRL.DASABLK_NTFY_EN[14:14]	1
0xBB00CDC8	L2_NTFY_CTRL.STTC_NTFY_EN[13:13]	1
0xBB00CDC8	L2_NTFY_CTRL.DYN_NTFY_EN[12:12]	1
0xBB00CDC8	L2_NTFY_CTRL.BP_THR[11:1]	11
0xBB00CDC8	L2_NTFY_CTRL.NTFY_EN[0:0]	1
0xBB0009E4	L2_NTFY_IF_INTR_MSK.RESERVED[31:3]	29
0xBB0009E4	L2_NTFY_IF_INTR_MSK.NTFY_DONE[2:2]	1
0xBB0009E4	L2_NTFY_IF_INTR_MSK.NTFY_BUF_RUN_OUT[1:1]	1
0xBB0009E4	L2_NTFY_IF_INTR_MSK.LOCAL_NTFY_BUF_RUN_OUT[0:0]	1
0xBB0009E8	L2_NTFY_IF_INTR_STS.RESERVED[31:3]	29
0xBB0009E8	L2_NTFY_IF_INTR_STS.NTFY_DONE[2:2]	1
0xBB0009E8	L2_NTFY_IF_INTR_STS.NTFY_BUF_RUN_OUT[1:1]	1
0xBB0009E8	L2_NTFY_IF_INTR_STS.LOCAL_NTFY_BUF_RUN_OUT[0:0]	1
0xBB0009EC	L2_NTFY_NIC_FIFO_STS.RESERVED[31:25]	7
0xBB0009EC	L2_NTFY_NIC_FIFO_STS.FIFO_EMPTY[24:24]	1
0xBB0009EC	L2_NTFY_NIC_FIFO_STS.PEAK[23:12]	12
0xBB0009EC	L2_NTFY_NIC_FIFO_STS.CNT[11:0]	12
0xBB0009F0	L2_NTFY_NIC_TIMEOUT.RESERVED[31:17]	15
0xBB0009F0	L2_NTFY_NIC_TIMEOUT.CFG_L2MSG_TMRROUT_EN[16:16]	1
0xBB0009F0	L2_NTFY_NIC_TIMEOUT.TIMEOUT[15:0]	16
0xBB005704	L2_NTFY_PKT_CTRL.RESERVED[31:15]	17
0xBB005704	L2_NTFY_PKT_CTRL.ITAGIF[14:14]	1
0xBB005704	L2_NTFY_PKT_CTRL.MAX_EVENT[13:7]	7
0xBB005704	L2_NTFY_PKT_CTRL.MIN_LEN[6:0]	7
0xBB000584	L2_NTFY_PKT_FIFO_STS.RESERVED[31:25]	7
0xBB000584	L2_NTFY_PKT_FIFO_STS.FIFO_EMPTY[24:24]	1
0xBB000584	L2_NTFY_PKT_FIFO_STS.PEAK[23:12]	12
0xBB000584	L2_NTFY_PKT_FIFO_STS.CNT[11:0]	12
0xBB00571C	L2_NTFY_PKT_ITAG.RESERVED[31:0]	32
0xBB00570C	L2_NTFY_PKT_MAC.RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00570C	L2_NTFY_PKT_MAC.DMAC_HI[15:0]	16
0xBB005710	L2_NTFY_PKT_MAC.DMAC_LO[31:0]	32
0xBB005714	L2_NTFY_PKT_MAC.RESERVED[31:16]	16
0xBB005714	L2_NTFY_PKT_MAC.SMAC_HI[15:0]	16
0xBB005718	L2_NTFY_PKT_MAC.SMAC_LO[31:0]	32
0xBB000580	L2_NTFY_PKT_MAGIC_NUM.RESERVED[31:8]	24
0xBB000580	L2_NTFY_PKT_MAGIC_NUM.NUM[7:0]	8
0xBB000588	L2_NTFY_PKT_QUE_ID.RESERVED[31:5]	27
0xBB000588	L2_NTFY_PKT_QUE_ID.QID[4:0]	5
0xBB005708	L2_NTFY_PKT_TIMEOUT.RESERVED[31:20]	12
0xBB005708	L2_NTFY_PKT_TIMEOUT.TIMEOUT[19:0]	20
0xBB0009DC	L2_NTFY_RING_BASE_ADDR.BASE_ADDR[31:0]	32
0xBB0009E0	L2_NTFY_RING_CUR_ADDR.CUR_ADDR[31:0]	32
0xBB00140C	L2_NTFY_RST.RESERVED[31:1]	31
0xBB00140C	L2_NTFY_RST.RST[0:0]	1
0xBB00C808	L2_PORT_AGE_CTRL [0].AGE_EN[0:0]	1
0xBB00C808	L2_PORT_AGE_CTRL [1].AGE_EN[1:1]	1
0xBB00C808	L2_PORT_AGE_CTRL [2].AGE_EN[2:2]	1
0xBB00C808	L2_PORT_AGE_CTRL [3].AGE_EN[3:3]	1
0xBB00C808	L2_PORT_AGE_CTRL [4].AGE_EN[4:4]	1
0xBB00C808	L2_PORT_AGE_CTRL [5].AGE_EN[5:5]	1
0xBB00C808	L2_PORT_AGE_CTRL [6].AGE_EN[6:6]	1
0xBB00C808	L2_PORT_AGE_CTRL [7].AGE_EN[7:7]	1
0xBB00C808	L2_PORT_AGE_CTRL [8].AGE_EN[8:8]	1
0xBB00C808	L2_PORT_AGE_CTRL [9].AGE_EN[9:9]	1
0xBB00C808	L2_PORT_AGE_CTRL [10].AGE_EN[10:10]	1
0xBB00C808	L2_PORT_AGE_CTRL [11].AGE_EN[11:11]	1
0xBB00C808	L2_PORT_AGE_CTRL [12].AGE_EN[12:12]	1
0xBB00C808	L2_PORT_AGE_CTRL [13].AGE_EN[13:13]	1
0xBB00C808	L2_PORT_AGE_CTRL [14].AGE_EN[14:14]	1
0xBB00C808	L2_PORT_AGE_CTRL [15].AGE_EN[15:15]	1
0xBB00C808	L2_PORT_AGE_CTRL [16].AGE_EN[16:16]	1
0xBB00C808	L2_PORT_AGE_CTRL [17].AGE_EN[17:17]	1
0xBB00C808	L2_PORT_AGE_CTRL [18].AGE_EN[18:18]	1
0xBB00C808	L2_PORT_AGE_CTRL [19].AGE_EN[19:19]	1
0xBB00C808	L2_PORT_AGE_CTRL [20].AGE_EN[20:20]	1
0xBB00C808	L2_PORT_AGE_CTRL [21].AGE_EN[21:21]	1
0xBB00C808	L2_PORT_AGE_CTRL [22].AGE_EN[22:22]	1
0xBB00C808	L2_PORT_AGE_CTRL [23].AGE_EN[23:23]	1
0xBB00C808	L2_PORT_AGE_CTRL [24].AGE_EN[24:24]	1
0xBB00C808	L2_PORT_AGE_CTRL [25].AGE_EN[25:25]	1
0xBB00C808	L2_PORT_AGE_CTRL [26].AGE_EN[26:26]	1
0xBB00C808	L2_PORT_AGE_CTRL [27].AGE_EN[27:27]	1
0xBB00C808	L2_PORT_AGE_CTRL [28].AGE_EN[28:28]	1
0xBB00C808	L2_PORT_AGE_CTRL [29].AGE_EN[29:29]	1
0xBB00C808	L2_PORT_AGE_CTRL [30].AGE_EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C808	L2_PORT_AGE_CTRL [31].AGE_EN[31:31]	1
0xBB00C80C	L2_PORT_AGE_CTRL [32].AGE_EN[0:0]	1
0xBB00C80C	L2_PORT_AGE_CTRL [33].AGE_EN[1:1]	1
0xBB00C80C	L2_PORT_AGE_CTRL [34].AGE_EN[2:2]	1
0xBB00C80C	L2_PORT_AGE_CTRL [35].AGE_EN[3:3]	1
0xBB00C80C	L2_PORT_AGE_CTRL [36].AGE_EN[4:4]	1
0xBB00C80C	L2_PORT_AGE_CTRL [37].AGE_EN[5:5]	1
0xBB00C80C	L2_PORT_AGE_CTRL [38].AGE_EN[6:6]	1
0xBB00C80C	L2_PORT_AGE_CTRL [39].AGE_EN[7:7]	1
0xBB00C80C	L2_PORT_AGE_CTRL [40].AGE_EN[8:8]	1
0xBB00C80C	L2_PORT_AGE_CTRL [41].AGE_EN[9:9]	1
0xBB00C80C	L2_PORT_AGE_CTRL [42].AGE_EN[10:10]	1
0xBB00C80C	L2_PORT_AGE_CTRL [43].AGE_EN[11:11]	1
0xBB00C80C	L2_PORT_AGE_CTRL [44].AGE_EN[12:12]	1
0xBB00C80C	L2_PORT_AGE_CTRL [45].AGE_EN[13:13]	1
0xBB00C80C	L2_PORT_AGE_CTRL [46].AGE_EN[14:14]	1
0xBB00C80C	L2_PORT_AGE_CTRL [47].AGE_EN[15:15]	1
0xBB00C80C	L2_PORT_AGE_CTRL [48].AGE_EN[16:16]	1
0xBB00C80C	L2_PORT_AGE_CTRL [49].AGE_EN[17:17]	1
0xBB00C80C	L2_PORT_AGE_CTRL [50].AGE_EN[18:18]	1
0xBB00C80C	L2_PORT_AGE_CTRL [51].AGE_EN[19:19]	1
0xBB00C80C	L2_PORT_AGE_CTRL [52].AGE_EN[20:20]	1
0xBB00C80C	L2_PORT_AGE_CTRL [53].AGE_EN[21:21]	1
0xBB00C80C	L2_PORT_AGE_CTRL [54].AGE_EN[22:22]	1
0xBB00C80C	L2_PORT_AGE_CTRL [55].AGE_EN[23:23]	1
0xBB00C80C	L2_PORT_AGE_CTRL [56].AGE_EN[24:24]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [0].EN[0:0]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [1].EN[1:1]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [2].EN[2:2]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [3].EN[3:3]	1
0xBB00C8FC	L2_PORT_DABLK_CTRL [4].EN[4:4]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [5].EN[5:5]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [6].EN[6:6]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [7].EN[7:7]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [8].EN[8:8]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [9].EN[9:9]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [10].EN[10:10]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [11].EN[11:11]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [12].EN[12:12]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [13].EN[13:13]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [14].EN[14:14]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [15].EN[15:15]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [16].EN[16:16]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [17].EN[17:17]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [18].EN[18:18]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [19].EN[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C8EC	L2_PORT_DABLK_CTRL [20].EN[20:20]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [21].EN[21:21]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [22].EN[22:22]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [23].EN[23:23]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [24].EN[24:24]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [25].EN[25:25]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [26].EN[26:26]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [27].EN[27:27]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [28].EN[28:28]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [29].EN[29:29]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [30].EN[30:30]	1
0xBB00C8EC	L2_PORT_DABLK_CTRL [31].EN[31:31]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [32].EN[0:0]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [33].EN[1:1]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [34].EN[2:2]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [35].EN[3:3]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [36].EN[4:4]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [37].EN[5:5]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [38].EN[6:6]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [39].EN[7:7]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [40].EN[8:8]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [41].EN[9:9]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [42].EN[10:10]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [43].EN[11:11]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [44].EN[12:12]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [45].EN[13:13]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [46].EN[14:14]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [47].EN[15:15]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [48].EN[16:16]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [49].EN[17:17]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [50].EN[18:18]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [51].EN[19:19]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [52].EN[20:20]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [53].EN[21:21]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [54].EN[22:22]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [55].EN[23:23]	1
0xBB00C8F0	L2_PORT_DABLK_CTRL [56].EN[24:24]	1
0xBB00C848	L2_PORT_DYN_MV_ACT [0].ACT[2:0]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [1].ACT[5:3]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [2].ACT[8:6]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [3].ACT[11:9]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [4].ACT[14:12]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [5].ACT[17:15]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [6].ACT[20:18]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [7].ACT[23:21]	3
0xBB00C848	L2_PORT_DYN_MV_ACT [8].ACT[26:24]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C848	L2_PORT_DYN_MV_ACT [9].ACT[29:27]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [10].ACT[2:0]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [11].ACT[5:3]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [12].ACT[8:6]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [13].ACT[11:9]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [14].ACT[14:12]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [15].ACT[17:15]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [16].ACT[20:18]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [17].ACT[23:21]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [18].ACT[26:24]	3
0xBB00C84C	L2_PORT_DYN_MV_ACT [19].ACT[29:27]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [20].ACT[2:0]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [21].ACT[5:3]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [22].ACT[8:6]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [23].ACT[11:9]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [24].ACT[14:12]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [25].ACT[17:15]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [26].ACT[20:18]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [27].ACT[23:21]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [28].ACT[26:24]	3
0xBB00C850	L2_PORT_DYN_MV_ACT [29].ACT[29:27]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [30].ACT[2:0]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [31].ACT[5:3]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [32].ACT[8:6]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [33].ACT[11:9]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [34].ACT[14:12]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [35].ACT[17:15]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [36].ACT[20:18]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [37].ACT[23:21]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [38].ACT[26:24]	3
0xBB00C854	L2_PORT_DYN_MV_ACT [39].ACT[29:27]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [40].ACT[2:0]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [41].ACT[5:3]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [42].ACT[8:6]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [43].ACT[11:9]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [44].ACT[14:12]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [45].ACT[17:15]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [46].ACT[20:18]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [47].ACT[23:21]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [48].ACT[26:24]	3
0xBB00C858	L2_PORT_DYN_MV_ACT [49].ACT[29:27]	3
0xBB00C85C	L2_PORT_DYN_MV_ACT [50].ACT[2:0]	3
0xBB00C85C	L2_PORT_DYN_MV_ACT [51].ACT[5:3]	3
0xBB00C85C	L2_PORT_DYN_MV_ACT [52].ACT[8:6]	3
0xBB00C85C	L2_PORT_DYN_MV_ACT [53].ACT[11:9]	3
0xBB00C85C	L2_PORT_DYN_MV_ACT [54].ACT[14:12]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C85C	L2_PORT_DYN_MV_ACT [55].ACT[17:15]	3
0xBB00C85C	L2_PORT_DYN_MV_ACT [56].ACT[20:18]	3
0xBB00C860	L2_PORT_DYN_MV_LRN [0].LRN[0:0]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [1].LRN[1:1]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [2].LRN[2:2]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [3].LRN[3:3]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [4].LRN[4:4]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [5].LRN[5:5]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [6].LRN[6:6]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [7].LRN[7:7]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [8].LRN[8:8]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [9].LRN[9:9]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [10].LRN[10:10]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [11].LRN[11:11]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [12].LRN[12:12]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [13].LRN[13:13]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [14].LRN[14:14]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [15].LRN[15:15]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [16].LRN[16:16]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [17].LRN[17:17]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [18].LRN[18:18]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [19].LRN[19:19]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [20].LRN[20:20]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [21].LRN[21:21]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [22].LRN[22:22]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [23].LRN[23:23]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [24].LRN[24:24]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [25].LRN[25:25]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [26].LRN[26:26]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [27].LRN[27:27]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [28].LRN[28:28]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [29].LRN[29:29]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [30].LRN[30:30]	1
0xBB00C860	L2_PORT_DYN_MV_LRN [31].LRN[31:31]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [32].LRN[0:0]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [33].LRN[1:1]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [34].LRN[2:2]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [35].LRN[3:3]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [36].LRN[4:4]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [37].LRN[5:5]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [38].LRN[6:6]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [39].LRN[7:7]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [40].LRN[8:8]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [41].LRN[9:9]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [42].LRN[10:10]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [43].LRN[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C864	L2_PORT_DYN_MV_LRN [44].LRN[12:12]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [45].LRN[13:13]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [46].LRN[14:14]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [47].LRN[15:15]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [48].LRN[16:16]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [49].LRN[17:17]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [50].LRN[18:18]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [51].LRN[19:19]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [52].LRN[20:20]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [53].LRN[21:21]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [54].LRN[22:22]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [55].LRN[23:23]	1
0xBB00C864	L2_PORT_DYN_MV_LRN [56].LRN[24:24]	1
0xBB00C8D4	L2_PORT_MV_FORBID [0].EN[0:0]	1
0xBB00C8D4	L2_PORT_MV_FORBID [1].EN[1:1]	1
0xBB00C8D4	L2_PORT_MV_FORBID [2].EN[2:2]	1
0xBB00C8D4	L2_PORT_MV_FORBID [3].EN[3:3]	1
0xBB00C8D4	L2_PORT_MV_FORBID [4].EN[4:4]	1
0xBB00C8D4	L2_PORT_MV_FORBID [5].EN[5:5]	1
0xBB00C8D4	L2_PORT_MV_FORBID [6].EN[6:6]	1
0xBB00C8D4	L2_PORT_MV_FORBID [7].EN[7:7]	1
0xBB00C8D4	L2_PORT_MV_FORBID [8].EN[8:8]	1
0xBB00C8D4	L2_PORT_MV_FORBID [9].EN[9:9]	1
0xBB00C8D4	L2_PORT_MV_FORBID [10].EN[10:10]	1
0xBB00C8D4	L2_PORT_MV_FORBID [11].EN[11:11]	1
0xBB00C8D4	L2_PORT_MV_FORBID [12].EN[12:12]	1
0xBB00C8D4	L2_PORT_MV_FORBID [13].EN[13:13]	1
0xBB00C8D4	L2_PORT_MV_FORBID [14].EN[14:14]	1
0xBB00C8D4	L2_PORT_MV_FORBID [15].EN[15:15]	1
0xBB00C8D4	L2_PORT_MV_FORBID [16].EN[16:16]	1
0xBB00C8D4	L2_PORT_MV_FORBID [17].EN[17:17]	1
0xBB00C8D4	L2_PORT_MV_FORBID [18].EN[18:18]	1
0xBB00C8D4	L2_PORT_MV_FORBID [19].EN[19:19]	1
0xBB00C8D4	L2_PORT_MV_FORBID [20].EN[20:20]	1
0xBB00C8D4	L2_PORT_MV_FORBID [21].EN[21:21]	1
0xBB00C8D4	L2_PORT_MV_FORBID [22].EN[22:22]	1
0xBB00C8D4	L2_PORT_MV_FORBID [23].EN[23:23]	1
0xBB00C8D4	L2_PORT_MV_FORBID [24].EN[24:24]	1
0xBB00C8D4	L2_PORT_MV_FORBID [25].EN[25:25]	1
0xBB00C8D4	L2_PORT_MV_FORBID [26].EN[26:26]	1
0xBB00C8D4	L2_PORT_MV_FORBID [27].EN[27:27]	1
0xBB00C8D4	L2_PORT_MV_FORBID [28].EN[28:28]	1
0xBB00C8D4	L2_PORT_MV_FORBID [29].EN[29:29]	1
0xBB00C8D4	L2_PORT_MV_FORBID [30].EN[30:30]	1
0xBB00C8D4	L2_PORT_MV_FORBID [31].EN[31:31]	1
0xBB00C8D8	L2_PORT_MV_FORBID [32].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C8D8	L2_PORT_MV_FORBID [33].EN[1:1]	1
0xBB00C8D8	L2_PORT_MV_FORBID [34].EN[2:2]	1
0xBB00C8D8	L2_PORT_MV_FORBID [35].EN[3:3]	1
0xBB00C8D8	L2_PORT_MV_FORBID [36].EN[4:4]	1
0xBB00C8D8	L2_PORT_MV_FORBID [37].EN[5:5]	1
0xBB00C8D8	L2_PORT_MV_FORBID [38].EN[6:6]	1
0xBB00C8D8	L2_PORT_MV_FORBID [39].EN[7:7]	1
0xBB00C8D8	L2_PORT_MV_FORBID [40].EN[8:8]	1
0xBB00C8D8	L2_PORT_MV_FORBID [41].EN[9:9]	1
0xBB00C8D8	L2_PORT_MV_FORBID [42].EN[10:10]	1
0xBB00C8D8	L2_PORT_MV_FORBID [43].EN[11:11]	1
0xBB00C8D8	L2_PORT_MV_FORBID [44].EN[12:12]	1
0xBB00C8D8	L2_PORT_MV_FORBID [45].EN[13:13]	1
0xBB00C8D8	L2_PORT_MV_FORBID [46].EN[14:14]	1
0xBB00C8D8	L2_PORT_MV_FORBID [47].EN[15:15]	1
0xBB00C8D8	L2_PORT_MV_FORBID [48].EN[16:16]	1
0xBB00C8D8	L2_PORT_MV_FORBID [49].EN[17:17]	1
0xBB00C8D8	L2_PORT_MV_FORBID [50].EN[18:18]	1
0xBB00C8D8	L2_PORT_MV_FORBID [51].EN[19:19]	1
0xBB00C8D8	L2_PORT_MV_FORBID [52].EN[20:20]	1
0xBB00C8D8	L2_PORT_MV_FORBID [53].EN[21:21]	1
0xBB00C8D8	L2_PORT_MV_FORBID [54].EN[22:22]	1
0xBB00C8D8	L2_PORT_MV_FORBID [55].EN[23:23]	1
0xBB00C8D8	L2_PORT_MV_FORBID [56].EN[24:24]	1
0xBB00C8E0	L2_PORT_MV_FORBID_CTRL.RESERVED[31:2]	30
0xBB00C8E0	L2_PORT_MV_FORBID_CTRL.FORBID_ACT[1:0]	2
0xBB00C830	L2_PORT_NEW_SA_FWD [0].NEW_SA_FWD[2:0]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [1].NEW_SA_FWD[5:3]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [2].NEW_SA_FWD[8:6]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [3].NEW_SA_FWD[11:9]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [4].NEW_SA_FWD[14:12]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [5].NEW_SA_FWD[17:15]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [6].NEW_SA_FWD[20:18]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [7].NEW_SA_FWD[23:21]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [8].NEW_SA_FWD[26:24]	3
0xBB00C830	L2_PORT_NEW_SA_FWD [9].NEW_SA_FWD[29:27]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [10].NEW_SA_FWD[2:0]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [11].NEW_SA_FWD[5:3]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [12].NEW_SA_FWD[8:6]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [13].NEW_SA_FWD[11:9]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [14].NEW_SA_FWD[14:12]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [15].NEW_SA_FWD[17:15]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [16].NEW_SA_FWD[20:18]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [17].NEW_SA_FWD[23:21]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [18].NEW_SA_FWD[26:24]	3
0xBB00C834	L2_PORT_NEW_SA_FWD [19].NEW_SA_FWD[29:27]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C838	L2_PORT_NEW_SA_FWD [20].NEW_SA_FWD[2:0]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [21].NEW_SA_FWD[5:3]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [22].NEW_SA_FWD[8:6]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [23].NEW_SA_FWD[11:9]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [24].NEW_SA_FWD[14:12]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [25].NEW_SA_FWD[17:15]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [26].NEW_SA_FWD[20:18]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [27].NEW_SA_FWD[23:21]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [28].NEW_SA_FWD[26:24]	3
0xBB00C838	L2_PORT_NEW_SA_FWD [29].NEW_SA_FWD[29:27]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [30].NEW_SA_FWD[2:0]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [31].NEW_SA_FWD[5:3]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [32].NEW_SA_FWD[8:6]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [33].NEW_SA_FWD[11:9]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [34].NEW_SA_FWD[14:12]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [35].NEW_SA_FWD[17:15]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [36].NEW_SA_FWD[20:18]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [37].NEW_SA_FWD[23:21]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [38].NEW_SA_FWD[26:24]	3
0xBB00C83C	L2_PORT_NEW_SA_FWD [39].NEW_SA_FWD[29:27]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [40].NEW_SA_FWD[2:0]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [41].NEW_SA_FWD[5:3]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [42].NEW_SA_FWD[8:6]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [43].NEW_SA_FWD[11:9]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [44].NEW_SA_FWD[14:12]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [45].NEW_SA_FWD[17:15]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [46].NEW_SA_FWD[20:18]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [47].NEW_SA_FWD[23:21]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [48].NEW_SA_FWD[26:24]	3
0xBB00C840	L2_PORT_NEW_SA_FWD [49].NEW_SA_FWD[29:27]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [50].NEW_SA_FWD[2:0]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [51].NEW_SA_FWD[5:3]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [52].NEW_SA_FWD[8:6]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [53].NEW_SA_FWD[11:9]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [54].NEW_SA_FWD[14:12]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [55].NEW_SA_FWD[17:15]	3
0xBB00C844	L2_PORT_NEW_SA_FWD [56].NEW_SA_FWD[20:18]	3
0xBB00C8E4	L2_PORT_SABLK_CTRL [0].EN[0:0]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [1].EN[1:1]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [2].EN[2:2]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [3].EN[3:3]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [4].EN[4:4]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [5].EN[5:5]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [6].EN[6:6]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [7].EN[7:7]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [8].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C8E4	L2_PORT_SABLK_CTRL [9].EN[9:9]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [10].EN[10:10]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [11].EN[11:11]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [12].EN[12:12]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [13].EN[13:13]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [14].EN[14:14]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [15].EN[15:15]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [16].EN[16:16]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [17].EN[17:17]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [18].EN[18:18]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [19].EN[19:19]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [20].EN[20:20]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [21].EN[21:21]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [22].EN[22:22]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [23].EN[23:23]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [24].EN[24:24]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [25].EN[25:25]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [26].EN[26:26]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [27].EN[27:27]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [28].EN[28:28]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [29].EN[29:29]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [30].EN[30:30]	1
0xBB00C8E4	L2_PORT_SABLK_CTRL [31].EN[31:31]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [32].EN[0:0]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [33].EN[1:1]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [34].EN[2:2]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [35].EN[3:3]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [36].EN[4:4]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [37].EN[5:5]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [38].EN[6:6]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [39].EN[7:7]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [40].EN[8:8]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [41].EN[9:9]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [42].EN[10:10]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [43].EN[11:11]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [44].EN[12:12]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [45].EN[13:13]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [46].EN[14:14]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [47].EN[15:15]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [48].EN[16:16]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [49].EN[17:17]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [50].EN[18:18]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [51].EN[19:19]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [52].EN[20:20]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [53].EN[21:21]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [54].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C8E8	L2_PORT_SABLK_CTRL [55].EN[23:23]	1
0xBB00C8E8	L2_PORT_SABLK_CTRL [56].EN[24:24]	1
0xBB00C820	L2_PORT_SALRN [0].SALRN[1:0]	2
0xBB00C820	L2_PORT_SALRN [1].SALRN[3:2]	2
0xBB00C820	L2_PORT_SALRN [2].SALRN[5:4]	2
0xBB00C820	L2_PORT_SALRN [3].SALRN[7:6]	2
0xBB00C820	L2_PORT_SALRN [4].SALRN[9:8]	2
0xBB00C820	L2_PORT_SALRN [5].SALRN[11:10]	2
0xBB00C820	L2_PORT_SALRN [6].SALRN[13:12]	2
0xBB00C820	L2_PORT_SALRN [7].SALRN[15:14]	2
0xBB00C820	L2_PORT_SALRN [8].SALRN[17:16]	2
0xBB00C820	L2_PORT_SALRN [9].SALRN[19:18]	2
0xBB00C820	L2_PORT_SALRN [10].SALRN[21:20]	2
0xBB00C820	L2_PORT_SALRN [11].SALRN[23:22]	2
0xBB00C820	L2_PORT_SALRN [12].SALRN[25:24]	2
0xBB00C820	L2_PORT_SALRN [13].SALRN[27:26]	2
0xBB00C820	L2_PORT_SALRN [14].SALRN[29:28]	2
0xBB00C820	L2_PORT_SALRN [15].SALRN[31:30]	2
0xBB00C824	L2_PORT_SALRN [16].SALRN[1:0]	2
0xBB00C824	L2_PORT_SALRN [17].SALRN[3:2]	2
0xBB00C824	L2_PORT_SALRN [18].SALRN[5:4]	2
0xBB00C824	L2_PORT_SALRN [19].SALRN[7:6]	2
0xBB00C824	L2_PORT_SALRN [20].SALRN[9:8]	2
0xBB00C824	L2_PORT_SALRN [21].SALRN[11:10]	2
0xBB00C824	L2_PORT_SALRN [22].SALRN[13:12]	2
0xBB00C824	L2_PORT_SALRN [23].SALRN[15:14]	2
0xBB00C824	L2_PORT_SALRN [24].SALRN[17:16]	2
0xBB00C824	L2_PORT_SALRN [25].SALRN[19:18]	2
0xBB00C824	L2_PORT_SALRN [26].SALRN[21:20]	2
0xBB00C824	L2_PORT_SALRN [27].SALRN[23:22]	2
0xBB00C824	L2_PORT_SALRN [28].SALRN[25:24]	2
0xBB00C824	L2_PORT_SALRN [29].SALRN[27:26]	2
0xBB00C824	L2_PORT_SALRN [30].SALRN[29:28]	2
0xBB00C824	L2_PORT_SALRN [31].SALRN[31:30]	2
0xBB00C828	L2_PORT_SALRN [32].SALRN[1:0]	2
0xBB00C828	L2_PORT_SALRN [33].SALRN[3:2]	2
0xBB00C828	L2_PORT_SALRN [34].SALRN[5:4]	2
0xBB00C828	L2_PORT_SALRN [35].SALRN[7:6]	2
0xBB00C828	L2_PORT_SALRN [36].SALRN[9:8]	2
0xBB00C828	L2_PORT_SALRN [37].SALRN[11:10]	2
0xBB00C828	L2_PORT_SALRN [38].SALRN[13:12]	2
0xBB00C828	L2_PORT_SALRN [39].SALRN[15:14]	2
0xBB00C828	L2_PORT_SALRN [40].SALRN[17:16]	2
0xBB00C828	L2_PORT_SALRN [41].SALRN[19:18]	2
0xBB00C828	L2_PORT_SALRN [42].SALRN[21:20]	2
0xBB00C828	L2_PORT_SALRN [43].SALRN[23:22]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C828	L2_PORT_SALRN [44].SALRN[25:24]	2
0xBB00C828	L2_PORT_SALRN [45].SALRN[27:26]	2
0xBB00C828	L2_PORT_SALRN [46].SALRN[29:28]	2
0xBB00C828	L2_PORT_SALRN [47].SALRN[31:30]	2
0xBB00C82C	L2_PORT_SALRN [48].SALRN[1:0]	2
0xBB00C82C	L2_PORT_SALRN [49].SALRN[3:2]	2
0xBB00C82C	L2_PORT_SALRN [50].SALRN[5:4]	2
0xBB00C82C	L2_PORT_SALRN [51].SALRN[7:6]	2
0xBB00C82C	L2_PORT_SALRN [52].SALRN[9:8]	2
0xBB00C82C	L2_PORT_SALRN [53].SALRN[11:10]	2
0xBB00C82C	L2_PORT_SALRN [54].SALRN[13:12]	2
0xBB00C82C	L2_PORT_SALRN [55].SALRN[15:14]	2
0xBB00C82C	L2_PORT_SALRN [56].SALRN[17:16]	2
0xBB00C904	L2_PORT_UC_LM_ACT [0].ACT[2:0]	3
0xBB00C904	L2_PORT_UC_LM_ACT [1].ACT[5:3]	3
0xBB00C904	L2_PORT_UC_LM_ACT [2].ACT[8:6]	3
0xBB00C904	L2_PORT_UC_LM_ACT [3].ACT[11:9]	3
0xBB00C904	L2_PORT_UC_LM_ACT [4].ACT[14:12]	3
0xBB00C904	L2_PORT_UC_LM_ACT [5].ACT[17:15]	3
0xBB00C904	L2_PORT_UC_LM_ACT [6].ACT[20:18]	3
0xBB00C904	L2_PORT_UC_LM_ACT [7].ACT[23:21]	3
0xBB00C904	L2_PORT_UC_LM_ACT [8].ACT[26:24]	3
0xBB00C904	L2_PORT_UC_LM_ACT [9].ACT[29:27]	3
0xBB00C908	L2_PORT_UC_LM_ACT [10].ACT[2:0]	3
0xBB00C908	L2_PORT_UC_LM_ACT [11].ACT[5:3]	3
0xBB00C908	L2_PORT_UC_LM_ACT [12].ACT[8:6]	3
0xBB00C908	L2_PORT_UC_LM_ACT [13].ACT[11:9]	3
0xBB00C908	L2_PORT_UC_LM_ACT [14].ACT[14:12]	3
0xBB00C908	L2_PORT_UC_LM_ACT [15].ACT[17:15]	3
0xBB00C908	L2_PORT_UC_LM_ACT [16].ACT[20:18]	3
0xBB00C908	L2_PORT_UC_LM_ACT [17].ACT[23:21]	3
0xBB00C908	L2_PORT_UC_LM_ACT [18].ACT[26:24]	3
0xBB00C908	L2_PORT_UC_LM_ACT [19].ACT[29:27]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [20].ACT[2:0]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [21].ACT[5:3]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [22].ACT[8:6]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [23].ACT[11:9]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [24].ACT[14:12]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [25].ACT[17:15]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [26].ACT[20:18]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [27].ACT[23:21]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [28].ACT[26:24]	3
0xBB00C90C	L2_PORT_UC_LM_ACT [29].ACT[29:27]	3
0xBB00C910	L2_PORT_UC_LM_ACT [30].ACT[2:0]	3
0xBB00C910	L2_PORT_UC_LM_ACT [31].ACT[5:3]	3
0xBB00C910	L2_PORT_UC_LM_ACT [32].ACT[8:6]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C910	L2_PORT_UC_LM_ACT [33].ACT[11:9]	3
0xBB00C910	L2_PORT_UC_LM_ACT [34].ACT[14:12]	3
0xBB00C910	L2_PORT_UC_LM_ACT [35].ACT[17:15]	3
0xBB00C910	L2_PORT_UC_LM_ACT [36].ACT[20:18]	3
0xBB00C910	L2_PORT_UC_LM_ACT [37].ACT[23:21]	3
0xBB00C910	L2_PORT_UC_LM_ACT [38].ACT[26:24]	3
0xBB00C910	L2_PORT_UC_LM_ACT [39].ACT[29:27]	3
0xBB00C914	L2_PORT_UC_LM_ACT [40].ACT[2:0]	3
0xBB00C914	L2_PORT_UC_LM_ACT [41].ACT[5:3]	3
0xBB00C914	L2_PORT_UC_LM_ACT [42].ACT[8:6]	3
0xBB00C914	L2_PORT_UC_LM_ACT [43].ACT[11:9]	3
0xBB00C914	L2_PORT_UC_LM_ACT [44].ACT[14:12]	3
0xBB00C914	L2_PORT_UC_LM_ACT [45].ACT[17:15]	3
0xBB00C914	L2_PORT_UC_LM_ACT [46].ACT[20:18]	3
0xBB00C914	L2_PORT_UC_LM_ACT [47].ACT[23:21]	3
0xBB00C914	L2_PORT_UC_LM_ACT [48].ACT[26:24]	3
0xBB00C914	L2_PORT_UC_LM_ACT [49].ACT[29:27]	3
0xBB00C918	L2_PORT_UC_LM_ACT [50].ACT[2:0]	3
0xBB00C918	L2_PORT_UC_LM_ACT [51].ACT[5:3]	3
0xBB00C918	L2_PORT_UC_LM_ACT [52].ACT[8:6]	3
0xBB00C918	L2_PORT_UC_LM_ACT [53].ACT[11:9]	3
0xBB00C918	L2_PORT_UC_LM_ACT [54].ACT[14:12]	3
0xBB00C918	L2_PORT_UC_LM_ACT [55].ACT[17:15]	3
0xBB00C918	L2_PORT_UC_LM_ACT [56].ACT[20:18]	3
0xBB00CDAC	L2_SA_ACT_REF [0].SRC[0:0]	1
0xBB00CDAC	L2_SA_ACT_REF [1].SRC[1:1]	1
0xBB00CDAC	L2_SA_ACT_REF [2].SRC[2:2]	1
0xBB00CDAC	L2_SA_ACT_REF [3].SRC[3:3]	1
0xBB00CDAC	L2_SA_ACT_REF [4].SRC[4:4]	1
0xBB00CDAC	L2_SA_ACT_REF [5].SRC[5:5]	1
0xBB00CDAC	L2_SA_ACT_REF [6].SRC[6:6]	1
0xBB00CDAC	L2_SA_ACT_REF [7].SRC[7:7]	1
0xBB00CDAC	L2_SA_ACT_REF [8].SRC[8:8]	1
0xBB00CDAC	L2_SA_ACT_REF [9].SRC[9:9]	1
0xBB00CDAC	L2_SA_ACT_REF [10].SRC[10:10]	1
0xBB00CDAC	L2_SA_ACT_REF [11].SRC[11:11]	1
0xBB00CDAC	L2_SA_ACT_REF [12].SRC[12:12]	1
0xBB00CDAC	L2_SA_ACT_REF [13].SRC[13:13]	1
0xBB00CDAC	L2_SA_ACT_REF [14].SRC[14:14]	1
0xBB00CDAC	L2_SA_ACT_REF [15].SRC[15:15]	1
0xBB00CDAC	L2_SA_ACT_REF [16].SRC[16:16]	1
0xBB00CDAC	L2_SA_ACT_REF [17].SRC[17:17]	1
0xBB00CDAC	L2_SA_ACT_REF [18].SRC[18:18]	1
0xBB00CDAC	L2_SA_ACT_REF [19].SRC[19:19]	1
0xBB00CDAC	L2_SA_ACT_REF [20].SRC[20:20]	1
0xBB00CDAC	L2_SA_ACT_REF [21].SRC[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CDAC	L2_SA_ACT_REF [22].SRC[22:22]	1
0xBB00CDAC	L2_SA_ACT_REF [23].SRC[23:23]	1
0xBB00CDAC	L2_SA_ACT_REF [24].SRC[24:24]	1
0xBB00CDAC	L2_SA_ACT_REF [25].SRC[25:25]	1
0xBB00CDAC	L2_SA_ACT_REF [26].SRC[26:26]	1
0xBB00CDAC	L2_SA_ACT_REF [27].SRC[27:27]	1
0xBB00CDAC	L2_SA_ACT_REF [28].SRC[28:28]	1
0xBB00CDAC	L2_SA_ACT_REF [29].SRC[29:29]	1
0xBB00CDAC	L2_SA_ACT_REF [30].SRC[30:30]	1
0xBB00CDAC	L2_SA_ACT_REF [31].SRC[31:31]	1
0xBB00CDB0	L2_SA_ACT_REF [32].SRC[0:0]	1
0xBB00CDB0	L2_SA_ACT_REF [33].SRC[1:1]	1
0xBB00CDB0	L2_SA_ACT_REF [34].SRC[2:2]	1
0xBB00CDB0	L2_SA_ACT_REF [35].SRC[3:3]	1
0xBB00CDB0	L2_SA_ACT_REF [36].SRC[4:4]	1
0xBB00CDB0	L2_SA_ACT_REF [37].SRC[5:5]	1
0xBB00CDB0	L2_SA_ACT_REF [38].SRC[6:6]	1
0xBB00CDB0	L2_SA_ACT_REF [39].SRC[7:7]	1
0xBB00CDB0	L2_SA_ACT_REF [40].SRC[8:8]	1
0xBB00CDB0	L2_SA_ACT_REF [41].SRC[9:9]	1
0xBB00CDB0	L2_SA_ACT_REF [42].SRC[10:10]	1
0xBB00CDB0	L2_SA_ACT_REF [43].SRC[11:11]	1
0xBB00CDB0	L2_SA_ACT_REF [44].SRC[12:12]	1
0xBB00CDB0	L2_SA_ACT_REF [45].SRC[13:13]	1
0xBB00CDB0	L2_SA_ACT_REF [46].SRC[14:14]	1
0xBB00CDB0	L2_SA_ACT_REF [47].SRC[15:15]	1
0xBB00CDB0	L2_SA_ACT_REF [48].SRC[16:16]	1
0xBB00CDB0	L2_SA_ACT_REF [49].SRC[17:17]	1
0xBB00CDB0	L2_SA_ACT_REF [50].SRC[18:18]	1
0xBB00CDB0	L2_SA_ACT_REF [51].SRC[19:19]	1
0xBB00CDB0	L2_SA_ACT_REF [52].SRC[20:20]	1
0xBB00CDB0	L2_SA_ACT_REF [53].SRC[21:21]	1
0xBB00CDB0	L2_SA_ACT_REF [54].SRC[22:22]	1
0xBB00CDB0	L2_SA_ACT_REF [55].SRC[23:23]	1
0xBB00CDB0	L2_SA_ACT_REF [56].SRC[24:24]	1
0xBB00CDA4	L2_SRC_P_FLTR [0].SRC_FLTR_EN[0:0]	1
0xBB00CDA4	L2_SRC_P_FLTR [1].SRC_FLTR_EN[1:1]	1
0xBB00CDA4	L2_SRC_P_FLTR [2].SRC_FLTR_EN[2:2]	1
0xBB00CDA4	L2_SRC_P_FLTR [3].SRC_FLTR_EN[3:3]	1
0xBB00CDA4	L2_SRC_P_FLTR [4].SRC_FLTR_EN[4:4]	1
0xBB00CDA4	L2_SRC_P_FLTR [5].SRC_FLTR_EN[5:5]	1
0xBB00CDA4	L2_SRC_P_FLTR [6].SRC_FLTR_EN[6:6]	1
0xBB00CDA4	L2_SRC_P_FLTR [7].SRC_FLTR_EN[7:7]	1
0xBB00CDA4	L2_SRC_P_FLTR [8].SRC_FLTR_EN[8:8]	1
0xBB00CDA4	L2_SRC_P_FLTR [9].SRC_FLTR_EN[9:9]	1
0xBB00CDA4	L2_SRC_P_FLTR [10].SRC_FLTR_EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CDA4	L2_SRC_P_FLTR [11].SRC_FLTR_EN[11:11]	1
0xBB00CDA4	L2_SRC_P_FLTR [12].SRC_FLTR_EN[12:12]	1
0xBB00CDA4	L2_SRC_P_FLTR [13].SRC_FLTR_EN[13:13]	1
0xBB00CDA4	L2_SRC_P_FLTR [14].SRC_FLTR_EN[14:14]	1
0xBB00CDA4	L2_SRC_P_FLTR [15].SRC_FLTR_EN[15:15]	1
0xBB00CDA4	L2_SRC_P_FLTR [16].SRC_FLTR_EN[16:16]	1
0xBB00CDA4	L2_SRC_P_FLTR [17].SRC_FLTR_EN[17:17]	1
0xBB00CDA4	L2_SRC_P_FLTR [18].SRC_FLTR_EN[18:18]	1
0xBB00CDA4	L2_SRC_P_FLTR [19].SRC_FLTR_EN[19:19]	1
0xBB00CDA4	L2_SRC_P_FLTR [20].SRC_FLTR_EN[20:20]	1
0xBB00CDA4	L2_SRC_P_FLTR [21].SRC_FLTR_EN[21:21]	1
0xBB00CDA4	L2_SRC_P_FLTR [22].SRC_FLTR_EN[22:22]	1
0xBB00CDA4	L2_SRC_P_FLTR [23].SRC_FLTR_EN[23:23]	1
0xBB00CDA4	L2_SRC_P_FLTR [24].SRC_FLTR_EN[24:24]	1
0xBB00CDA4	L2_SRC_P_FLTR [25].SRC_FLTR_EN[25:25]	1
0xBB00CDA4	L2_SRC_P_FLTR [26].SRC_FLTR_EN[26:26]	1
0xBB00CDA4	L2_SRC_P_FLTR [27].SRC_FLTR_EN[27:27]	1
0xBB00CDA4	L2_SRC_P_FLTR [28].SRC_FLTR_EN[28:28]	1
0xBB00CDA4	L2_SRC_P_FLTR [29].SRC_FLTR_EN[29:29]	1
0xBB00CDA4	L2_SRC_P_FLTR [30].SRC_FLTR_EN[30:30]	1
0xBB00CDA4	L2_SRC_P_FLTR [31].SRC_FLTR_EN[31:31]	1
0xBB00CDA8	L2_SRC_P_FLTR [32].SRC_FLTR_EN[0:0]	1
0xBB00CDA8	L2_SRC_P_FLTR [33].SRC_FLTR_EN[1:1]	1
0xBB00CDA8	L2_SRC_P_FLTR [34].SRC_FLTR_EN[2:2]	1
0xBB00CDA8	L2_SRC_P_FLTR [35].SRC_FLTR_EN[3:3]	1
0xBB00CDA8	L2_SRC_P_FLTR [36].SRC_FLTR_EN[4:4]	1
0xBB00CDA8	L2_SRC_P_FLTR [37].SRC_FLTR_EN[5:5]	1
0xBB00CDA8	L2_SRC_P_FLTR [38].SRC_FLTR_EN[6:6]	1
0xBB00CDA8	L2_SRC_P_FLTR [39].SRC_FLTR_EN[7:7]	1
0xBB00CDA8	L2_SRC_P_FLTR [40].SRC_FLTR_EN[8:8]	1
0xBB00CDA8	L2_SRC_P_FLTR [41].SRC_FLTR_EN[9:9]	1
0xBB00CDA8	L2_SRC_P_FLTR [42].SRC_FLTR_EN[10:10]	1
0xBB00CDA8	L2_SRC_P_FLTR [43].SRC_FLTR_EN[11:11]	1
0xBB00CDA8	L2_SRC_P_FLTR [44].SRC_FLTR_EN[12:12]	1
0xBB00CDA8	L2_SRC_P_FLTR [45].SRC_FLTR_EN[13:13]	1
0xBB00CDA8	L2_SRC_P_FLTR [46].SRC_FLTR_EN[14:14]	1
0xBB00CDA8	L2_SRC_P_FLTR [47].SRC_FLTR_EN[15:15]	1
0xBB00CDA8	L2_SRC_P_FLTR [48].SRC_FLTR_EN[16:16]	1
0xBB00CDA8	L2_SRC_P_FLTR [49].SRC_FLTR_EN[17:17]	1
0xBB00CDA8	L2_SRC_P_FLTR [50].SRC_FLTR_EN[18:18]	1
0xBB00CDA8	L2_SRC_P_FLTR [51].SRC_FLTR_EN[19:19]	1
0xBB00CDA8	L2_SRC_P_FLTR [52].SRC_FLTR_EN[20:20]	1
0xBB00CDA8	L2_SRC_P_FLTR [53].SRC_FLTR_EN[21:21]	1
0xBB00CDA8	L2_SRC_P_FLTR [54].SRC_FLTR_EN[22:22]	1
0xBB00CDA8	L2_SRC_P_FLTR [55].SRC_FLTR_EN[23:23]	1
0xBB00CDA8	L2_SRC_P_FLTR [56].SRC_FLTR_EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CD9C	L2_TBL_FLUSH_CTRL.RESERVED[31:29]	3
0xBB00CD9C	L2_TBL_FLUSH_CTRL.STS[28:28]	1
0xBB00CD9C	L2_TBL_FLUSH_CTRL.ACT[27:27]	1
0xBB00CD9C	L2_TBL_FLUSH_CTRL.FVID_CMP[26:26]	1
0xBB00CD9C	L2_TBL_FLUSH_CTRL.AGG_VID_CMP[25:25]	1
0xBB00CD9C	L2_TBL_FLUSH_CTRL.PORT_CMP[24:24]	1
0xBB00CD9C	L2_TBL_FLUSH_CTRL.ENTRY_TYPE[23:22]	2
0xBB00CD9C	L2_TBL_FLUSH_CTRL.PORT_ID[21:11]	11
0xBB00CD9C	L2_TBL_FLUSH_CTRL.REPLACING_PORT_ID[10:0]	11
0xBB00CDA0	L2_TBL_FLUSH_CTRL.FVID[31:20]	12
0xBB00CDA0	L2_TBL_FLUSH_CTRL.AGG_VID[19:0]	20
0xBB00C810	L2_TRK_AGE_CTRL [0].AGE_EN[0:0]	1
0xBB00C810	L2_TRK_AGE_CTRL [1].AGE_EN[1:1]	1
0xBB00C810	L2_TRK_AGE_CTRL [2].AGE_EN[2:2]	1
0xBB00C810	L2_TRK_AGE_CTRL [3].AGE_EN[3:3]	1
0xBB00C810	L2_TRK_AGE_CTRL [4].AGE_EN[4:4]	1
0xBB00C810	L2_TRK_AGE_CTRL [5].AGE_EN[5:5]	1
0xBB00C810	L2_TRK_AGE_CTRL [6].AGE_EN[6:6]	1
0xBB00C810	L2_TRK_AGE_CTRL [7].AGE_EN[7:7]	1
0xBB00C810	L2_TRK_AGE_CTRL [8].AGE_EN[8:8]	1
0xBB00C810	L2_TRK_AGE_CTRL [9].AGE_EN[9:9]	1
0xBB00C810	L2_TRK_AGE_CTRL [10].AGE_EN[10:10]	1
0xBB00C810	L2_TRK_AGE_CTRL [11].AGE_EN[11:11]	1
0xBB00C810	L2_TRK_AGE_CTRL [12].AGE_EN[12:12]	1
0xBB00C810	L2_TRK_AGE_CTRL [13].AGE_EN[13:13]	1
0xBB00C810	L2_TRK_AGE_CTRL [14].AGE_EN[14:14]	1
0xBB00C810	L2_TRK_AGE_CTRL [15].AGE_EN[15:15]	1
0xBB00C810	L2_TRK_AGE_CTRL [16].AGE_EN[16:16]	1
0xBB00C810	L2_TRK_AGE_CTRL [17].AGE_EN[17:17]	1
0xBB00C810	L2_TRK_AGE_CTRL [18].AGE_EN[18:18]	1
0xBB00C810	L2_TRK_AGE_CTRL [19].AGE_EN[19:19]	1
0xBB00C810	L2_TRK_AGE_CTRL [20].AGE_EN[20:20]	1
0xBB00C810	L2_TRK_AGE_CTRL [21].AGE_EN[21:21]	1
0xBB00C810	L2_TRK_AGE_CTRL [22].AGE_EN[22:22]	1
0xBB00C810	L2_TRK_AGE_CTRL [23].AGE_EN[23:23]	1
0xBB00C810	L2_TRK_AGE_CTRL [24].AGE_EN[24:24]	1
0xBB00C810	L2_TRK_AGE_CTRL [25].AGE_EN[25:25]	1
0xBB00C810	L2_TRK_AGE_CTRL [26].AGE_EN[26:26]	1
0xBB00C810	L2_TRK_AGE_CTRL [27].AGE_EN[27:27]	1
0xBB00C810	L2_TRK_AGE_CTRL [28].AGE_EN[28:28]	1
0xBB00C810	L2_TRK_AGE_CTRL [29].AGE_EN[29:29]	1
0xBB00C810	L2_TRK_AGE_CTRL [30].AGE_EN[30:30]	1
0xBB00C810	L2_TRK_AGE_CTRL [31].AGE_EN[31:31]	1
0xBB00C814	L2_TRK_AGE_CTRL [32].AGE_EN[0:0]	1
0xBB00C814	L2_TRK_AGE_CTRL [33].AGE_EN[1:1]	1
0xBB00C814	L2_TRK_AGE_CTRL [34].AGE_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C814	L2_TRK_AGE_CTRL [35].AGE_EN[3:3]	1
0xBB00C814	L2_TRK_AGE_CTRL [36].AGE_EN[4:4]	1
0xBB00C814	L2_TRK_AGE_CTRL [37].AGE_EN[5:5]	1
0xBB00C814	L2_TRK_AGE_CTRL [38].AGE_EN[6:6]	1
0xBB00C814	L2_TRK_AGE_CTRL [39].AGE_EN[7:7]	1
0xBB00C814	L2_TRK_AGE_CTRL [40].AGE_EN[8:8]	1
0xBB00C814	L2_TRK_AGE_CTRL [41].AGE_EN[9:9]	1
0xBB00C814	L2_TRK_AGE_CTRL [42].AGE_EN[10:10]	1
0xBB00C814	L2_TRK_AGE_CTRL [43].AGE_EN[11:11]	1
0xBB00C814	L2_TRK_AGE_CTRL [44].AGE_EN[12:12]	1
0xBB00C814	L2_TRK_AGE_CTRL [45].AGE_EN[13:13]	1
0xBB00C814	L2_TRK_AGE_CTRL [46].AGE_EN[14:14]	1
0xBB00C814	L2_TRK_AGE_CTRL [47].AGE_EN[15:15]	1
0xBB00C814	L2_TRK_AGE_CTRL [48].AGE_EN[16:16]	1
0xBB00C814	L2_TRK_AGE_CTRL [49].AGE_EN[17:17]	1
0xBB00C814	L2_TRK_AGE_CTRL [50].AGE_EN[18:18]	1
0xBB00C814	L2_TRK_AGE_CTRL [51].AGE_EN[19:19]	1
0xBB00C814	L2_TRK_AGE_CTRL [52].AGE_EN[20:20]	1
0xBB00C814	L2_TRK_AGE_CTRL [53].AGE_EN[21:21]	1
0xBB00C814	L2_TRK_AGE_CTRL [54].AGE_EN[22:22]	1
0xBB00C814	L2_TRK_AGE_CTRL [55].AGE_EN[23:23]	1
0xBB00C814	L2_TRK_AGE_CTRL [56].AGE_EN[24:24]	1
0xBB00C814	L2_TRK_AGE_CTRL [57].AGE_EN[25:25]	1
0xBB00C814	L2_TRK_AGE_CTRL [58].AGE_EN[26:26]	1
0xBB00C814	L2_TRK_AGE_CTRL [59].AGE_EN[27:27]	1
0xBB00C814	L2_TRK_AGE_CTRL [60].AGE_EN[28:28]	1
0xBB00C814	L2_TRK_AGE_CTRL [61].AGE_EN[29:29]	1
0xBB00C814	L2_TRK_AGE_CTRL [62].AGE_EN[30:30]	1
0xBB00C814	L2_TRK_AGE_CTRL [63].AGE_EN[31:31]	1
0xBB00C818	L2_TRK_AGE_CTRL [64].AGE_EN[0:0]	1
0xBB00C818	L2_TRK_AGE_CTRL [65].AGE_EN[1:1]	1
0xBB00C818	L2_TRK_AGE_CTRL [66].AGE_EN[2:2]	1
0xBB00C818	L2_TRK_AGE_CTRL [67].AGE_EN[3:3]	1
0xBB00C818	L2_TRK_AGE_CTRL [68].AGE_EN[4:4]	1
0xBB00C818	L2_TRK_AGE_CTRL [69].AGE_EN[5:5]	1
0xBB00C818	L2_TRK_AGE_CTRL [70].AGE_EN[6:6]	1
0xBB00C818	L2_TRK_AGE_CTRL [71].AGE_EN[7:7]	1
0xBB00C818	L2_TRK_AGE_CTRL [72].AGE_EN[8:8]	1
0xBB00C818	L2_TRK_AGE_CTRL [73].AGE_EN[9:9]	1
0xBB00C818	L2_TRK_AGE_CTRL [74].AGE_EN[10:10]	1
0xBB00C818	L2_TRK_AGE_CTRL [75].AGE_EN[11:11]	1
0xBB00C818	L2_TRK_AGE_CTRL [76].AGE_EN[12:12]	1
0xBB00C818	L2_TRK_AGE_CTRL [77].AGE_EN[13:13]	1
0xBB00C818	L2_TRK_AGE_CTRL [78].AGE_EN[14:14]	1
0xBB00C818	L2_TRK_AGE_CTRL [79].AGE_EN[15:15]	1
0xBB00C818	L2_TRK_AGE_CTRL [80].AGE_EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C818	L2_TRK_AGE_CTRL [81].AGE_EN[17:17]	1
0xBB00C818	L2_TRK_AGE_CTRL [82].AGE_EN[18:18]	1
0xBB00C818	L2_TRK_AGE_CTRL [83].AGE_EN[19:19]	1
0xBB00C818	L2_TRK_AGE_CTRL [84].AGE_EN[20:20]	1
0xBB00C818	L2_TRK_AGE_CTRL [85].AGE_EN[21:21]	1
0xBB00C818	L2_TRK_AGE_CTRL [86].AGE_EN[22:22]	1
0xBB00C818	L2_TRK_AGE_CTRL [87].AGE_EN[23:23]	1
0xBB00C818	L2_TRK_AGE_CTRL [88].AGE_EN[24:24]	1
0xBB00C818	L2_TRK_AGE_CTRL [89].AGE_EN[25:25]	1
0xBB00C818	L2_TRK_AGE_CTRL [90].AGE_EN[26:26]	1
0xBB00C818	L2_TRK_AGE_CTRL [91].AGE_EN[27:27]	1
0xBB00C818	L2_TRK_AGE_CTRL [92].AGE_EN[28:28]	1
0xBB00C818	L2_TRK_AGE_CTRL [93].AGE_EN[29:29]	1
0xBB00C818	L2_TRK_AGE_CTRL [94].AGE_EN[30:30]	1
0xBB00C818	L2_TRK_AGE_CTRL [95].AGE_EN[31:31]	1
0xBB00C81C	L2_TRK_AGE_CTRL [96].AGE_EN[0:0]	1
0xBB00C81C	L2_TRK_AGE_CTRL [97].AGE_EN[1:1]	1
0xBB00C81C	L2_TRK_AGE_CTRL [98].AGE_EN[2:2]	1
0xBB00C81C	L2_TRK_AGE_CTRL [99].AGE_EN[3:3]	1
0xBB00C81C	L2_TRK_AGE_CTRL [100].AGE_EN[4:4]	1
0xBB00C81C	L2_TRK_AGE_CTRL [101].AGE_EN[5:5]	1
0xBB00C81C	L2_TRK_AGE_CTRL [102].AGE_EN[6:6]	1
0xBB00C81C	L2_TRK_AGE_CTRL [103].AGE_EN[7:7]	1
0xBB00C81C	L2_TRK_AGE_CTRL [104].AGE_EN[8:8]	1
0xBB00C81C	L2_TRK_AGE_CTRL [105].AGE_EN[9:9]	1
0xBB00C81C	L2_TRK_AGE_CTRL [106].AGE_EN[10:10]	1
0xBB00C81C	L2_TRK_AGE_CTRL [107].AGE_EN[11:11]	1
0xBB00C81C	L2_TRK_AGE_CTRL [108].AGE_EN[12:12]	1
0xBB00C81C	L2_TRK_AGE_CTRL [109].AGE_EN[13:13]	1
0xBB00C81C	L2_TRK_AGE_CTRL [110].AGE_EN[14:14]	1
0xBB00C81C	L2_TRK_AGE_CTRL [111].AGE_EN[15:15]	1
0xBB00C81C	L2_TRK_AGE_CTRL [112].AGE_EN[16:16]	1
0xBB00C81C	L2_TRK_AGE_CTRL [113].AGE_EN[17:17]	1
0xBB00C81C	L2_TRK_AGE_CTRL [114].AGE_EN[18:18]	1
0xBB00C81C	L2_TRK_AGE_CTRL [115].AGE_EN[19:19]	1
0xBB00C81C	L2_TRK_AGE_CTRL [116].AGE_EN[20:20]	1
0xBB00C81C	L2_TRK_AGE_CTRL [117].AGE_EN[21:21]	1
0xBB00C81C	L2_TRK_AGE_CTRL [118].AGE_EN[22:22]	1
0xBB00C81C	L2_TRK_AGE_CTRL [119].AGE_EN[23:23]	1
0xBB00C81C	L2_TRK_AGE_CTRL [120].AGE_EN[24:24]	1
0xBB00C81C	L2_TRK_AGE_CTRL [121].AGE_EN[25:25]	1
0xBB00C81C	L2_TRK_AGE_CTRL [122].AGE_EN[26:26]	1
0xBB00C81C	L2_TRK_AGE_CTRL [123].AGE_EN[27:27]	1
0xBB00C81C	L2_TRK_AGE_CTRL [124].AGE_EN[28:28]	1
0xBB00C81C	L2_TRK_AGE_CTRL [125].AGE_EN[29:29]	1
0xBB00C81C	L2_TRK_AGE_CTRL [126].AGE_EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C81C	L2_TRK_AGE_CTRL [127].AGE_EN[31:31]	1
0xBB00C8DC	L2_TRK_MV_FORBID [0].EN[0:0]	1
0xBB00C8DC	L2_TRK_MV_FORBID [1].EN[1:1]	1
0xBB00C8DC	L2_TRK_MV_FORBID [2].EN[2:2]	1
0xBB00C8DC	L2_TRK_MV_FORBID [3].EN[3:3]	1
0xBB00C8DC	L2_TRK_MV_FORBID [4].EN[4:4]	1
0xBB00C8DC	L2_TRK_MV_FORBID [5].EN[5:5]	1
0xBB00C8DC	L2_TRK_MV_FORBID [6].EN[6:6]	1
0xBB00C8DC	L2_TRK_MV_FORBID [7].EN[7:7]	1
0xBB00C8DC	L2_TRK_MV_FORBID [8].EN[8:8]	1
0xBB00C8DC	L2_TRK_MV_FORBID [9].EN[9:9]	1
0xBB00C8DC	L2_TRK_MV_FORBID [10].EN[10:10]	1
0xBB00C8DC	L2_TRK_MV_FORBID [11].EN[11:11]	1
0xBB00C8DC	L2_TRK_MV_FORBID [12].EN[12:12]	1
0xBB00C8DC	L2_TRK_MV_FORBID [13].EN[13:13]	1
0xBB00C8DC	L2_TRK_MV_FORBID [14].EN[14:14]	1
0xBB00C8DC	L2_TRK_MV_FORBID [15].EN[15:15]	1
0xBB00C8DC	L2_TRK_MV_FORBID [16].EN[16:16]	1
0xBB00C8DC	L2_TRK_MV_FORBID [17].EN[17:17]	1
0xBB00C8DC	L2_TRK_MV_FORBID [18].EN[18:18]	1
0xBB00C8DC	L2_TRK_MV_FORBID [19].EN[19:19]	1
0xBB00C8DC	L2_TRK_MV_FORBID [20].EN[20:20]	1
0xBB00C8DC	L2_TRK_MV_FORBID [21].EN[21:21]	1
0xBB00C8DC	L2_TRK_MV_FORBID [22].EN[22:22]	1
0xBB00C8DC	L2_TRK_MV_FORBID [23].EN[23:23]	1
0xBB00C8DC	L2_TRK_MV_FORBID [24].EN[24:24]	1
0xBB00C8DC	L2_TRK_MV_FORBID [25].EN[25:25]	1
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_GPE_EXCPT_FLAGS[31:24]	8
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_EXCPT_FLAGS[23:16]	8
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_EXCPT_ACT[15:14]	2
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_CTRL_PKT_ACT[13:13]	1
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_INVLD_HDR_ACT[12:12]	1
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_TRAP_TARGET[11:11]	1
0xBB00CDD0	L2_TUNNEL_CTRL.VXLAN_VNI_LU_MIS_ACT[10:10]	1
0xBB00CDD0	L2_TUNNEL_CTRL.RESERVED[9:0]	10
0xBB00C8F4	L2_UNKN_UC_FLD_PMSK.RESERVED[31:25]	7
0xBB00C8F4	L2_UNKN_UC_FLD_PMSK.PMSK_1[24:0]	25
0xBB00C8F8	L2_UNKN_UC_FLD_PMSK.PMSK_0[31:0]	32
0xBB00CD84	L2_VLAN_CONSTRCT_CTRL.RESERVED[31:3]	29
0xBB00CD84	L2_VLAN_CONSTRCT_CTRL.ACT[2:0]	3
0xBB00F268	L3_ECMP_HASH_CTRL.RESERVED[31:21]	11
0xBB00F268	L3_ECMP_HASH_CTRL.L4_DPORT_BIT_OFFSET[20:19]	2
0xBB00F268	L3_ECMP_HASH_CTRL.L4_SPORT_BIT_OFFSET[18:17]	2
0xBB00F268	L3_ECMP_HASH_CTRL.DIP_BIT_OFFSET[16:15]	2
0xBB00F268	L3_ECMP_HASH_CTRL.SIP_BIT_OFFSET[14:13]	2
0xBB00F268	L3_ECMP_HASH_CTRL.L4_DPORT_INC[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F268	L3_ECMP_HASH_CTRL.L4_SPORT_INC[11:11]	1
0xBB00F268	L3_ECMP_HASH_CTRL.L4_PROTO_INC[10:10]	1
0xBB00F268	L3_ECMP_HASH_CTRL.IP6_LABEL_INC[9:9]	1
0xBB00F268	L3_ECMP_HASH_CTRL.DSCP_INC[8:8]	1
0xBB00F268	L3_ECMP_HASH_CTRL.DIP_INC[7:7]	1
0xBB00F268	L3_ECMP_HASH_CTRL.SIP_INC[6:6]	1
0xBB00F268	L3_ECMP_HASH_CTRL.TRUNK_ID_INC[5:5]	1
0xBB00F268	L3_ECMP_HASH_CTRL.RX_PORT_INC[4:4]	1
0xBB00F268	L3_ECMP_HASH_CTRL.UNIVERSAL_ID[3:0]	4
0xBB00F260	L3_ENTRY_MV_CTRL.RESERVED[31:30]	2
0xBB00F260	L3_ENTRY_MV_CTRL.TO[29:16]	14
0xBB00F260	L3_ENTRY_MV_CTRL.FROM[15:2]	14
0xBB00F260	L3_ENTRY_MV_CTRL.CMD[1:1]	1
0xBB00F260	L3_ENTRY_MV_CTRL.EXEC[0:0]	1
0xBB00F264	L3_ENTRY_MV_PARAM.RESERVED[31:14]	18
0xBB00F264	L3_ENTRY_MV_PARAM.LEN[13:0]	14
0xBB00F004	L3_HOST_TBL_CTRL.RESERVED[31:6]	26
0xBB00F004	L3_HOST_TBL_CTRL.MC_HASH_ALG_SEL_1[5:5]	1
0xBB00F004	L3_HOST_TBL_CTRL.MC_HASH_ALG_SEL_0[4:4]	1
0xBB00F004	L3_HOST_TBL_CTRL.UC_HASH_ALG_SEL_1[3:3]	1
0xBB00F004	L3_HOST_TBL_CTRL.UC_HASH_ALG_SEL_0[2:2]	1
0xBB00F004	L3_HOST_TBL_CTRL.LU_FORCE_MODE_HASH_KEY_SEL[1:1]	1
0xBB00F004	L3_HOST_TBL_CTRL.LU_MODE_SEL[0:0]	1
0xBB00F2C0	L3_HW_LU_CTRL.RESERVED[31:16]	16
0xBB00F2C0	L3_HW_LU_CTRL.EXEC_TCAM[15:15]	1
0xBB00F2C0	L3_HW_LU_CTRL.RESULT_TCAM[14:14]	1
0xBB00F2C0	L3_HW_LU_CTRL.ENTRY_IDX_TCAM[13:0]	14
0xBB00F29C	L3_HW_LU_KEY_CTRL.RESERVED[31:25]	7
0xBB00F29C	L3_HW_LU_KEY_CTRL.ROUND[24:24]	1
0xBB00F29C	L3_HW_LU_KEY_CTRL.ENTRY_TYPE[23:22]	2
0xBB00F29C	L3_HW_LU_KEY_CTRL.IPMC_TYPE[21:21]	1
0xBB00F29C	L3_HW_LU_KEY_CTRL.VRF[20:13]	8
0xBB00F29C	L3_HW_LU_KEY_CTRL.MC_KEY_SEL[12:12]	1
0xBB00F29C	L3_HW_LU_KEY_CTRL.VID_INTF_ID[11:0]	12
0xBB00F2B0	L3_HW_LU_KEY_DIP_CTRL.IP3[31:0]	32
0xBB00F2B4	L3_HW_LU_KEY_DIP_CTRL.IP2[31:0]	32
0xBB00F2B8	L3_HW_LU_KEY_DIP_CTRL.IP1[31:0]	32
0xBB00F2BC	L3_HW_LU_KEY_DIP_CTRL.IP[31:0]	32
0xBB00F2A0	L3_HW_LU_KEY_SIP_CTRL.IP3[31:0]	32
0xBB00F2A4	L3_HW_LU_KEY_SIP_CTRL.IP2[31:0]	32
0xBB00F2A8	L3_HW_LU_KEY_SIP_CTRL.IP1[31:0]	32
0xBB00F2AC	L3_HW_LU_KEY_SIP_CTRL.IP[31:0]	32
0xBB00F220	L3_intf_IP6_MTU [0].RESERVED[31:14]	18
0xBB00F220	L3_intf_IP6_MTU [0].MTU[13:0]	14
0xBB00F224	L3_intf_IP6_MTU [1].RESERVED[31:14]	18
0xBB00F224	L3_intf_IP6_MTU [1].MTU[13:0]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F228	L3_INTF_IP6_MTU [2].RESERVED[31:14]	18
0xBB00F228	L3_INTF_IP6_MTU [2].MTU[13:0]	14
0xBB00F22C	L3_INTF_IP6_MTU [3].RESERVED[31:14]	18
0xBB00F22C	L3_INTF_IP6_MTU [3].MTU[13:0]	14
0xBB00F230	L3_INTF_IP6_MTU [4].RESERVED[31:14]	18
0xBB00F230	L3_INTF_IP6_MTU [4].MTU[13:0]	14
0xBB00F234	L3_INTF_IP6_MTU [5].RESERVED[31:14]	18
0xBB00F234	L3_INTF_IP6_MTU [5].MTU[13:0]	14
0xBB00F238	L3_INTF_IP6_MTU [6].RESERVED[31:14]	18
0xBB00F238	L3_INTF_IP6_MTU [6].MTU[13:0]	14
0xBB00F23C	L3_INTF_IP6_MTU [7].RESERVED[31:14]	18
0xBB00F23C	L3_INTF_IP6_MTU [7].MTU[13:0]	14
0xBB00F240	L3_INTF_IP6_MTU [8].RESERVED[31:14]	18
0xBB00F240	L3_INTF_IP6_MTU [8].MTU[13:0]	14
0xBB00F244	L3_INTF_IP6_MTU [9].RESERVED[31:14]	18
0xBB00F244	L3_INTF_IP6_MTU [9].MTU[13:0]	14
0xBB00F248	L3_INTF_IP6_MTU [10].RESERVED[31:14]	18
0xBB00F248	L3_INTF_IP6_MTU [10].MTU[13:0]	14
0xBB00F24C	L3_INTF_IP6_MTU [11].RESERVED[31:14]	18
0xBB00F24C	L3_INTF_IP6_MTU [11].MTU[13:0]	14
0xBB00F250	L3_INTF_IP6_MTU [12].RESERVED[31:14]	18
0xBB00F250	L3_INTF_IP6_MTU [12].MTU[13:0]	14
0xBB00F254	L3_INTF_IP6_MTU [13].RESERVED[31:14]	18
0xBB00F254	L3_INTF_IP6_MTU [13].MTU[13:0]	14
0xBB00F258	L3_INTF_IP6_MTU [14].RESERVED[31:14]	18
0xBB00F258	L3_INTF_IP6_MTU [14].MTU[13:0]	14
0xBB00F25C	L3_INTF_IP6_MTU [15].RESERVED[31:14]	18
0xBB00F25C	L3_INTF_IP6_MTU [15].MTU[13:0]	14
0xBB00F1E0	L3_INTF_IP_MTU [0].RESERVED[31:14]	18
0xBB00F1E0	L3_INTF_IP_MTU [0].MTU[13:0]	14
0xBB00F1E4	L3_INTF_IP_MTU [1].RESERVED[31:14]	18
0xBB00F1E4	L3_INTF_IP_MTU [1].MTU[13:0]	14
0xBB00F1E8	L3_INTF_IP_MTU [2].RESERVED[31:14]	18
0xBB00F1E8	L3_INTF_IP_MTU [2].MTU[13:0]	14
0xBB00F1EC	L3_INTF_IP_MTU [3].RESERVED[31:14]	18
0xBB00F1EC	L3_INTF_IP_MTU [3].MTU[13:0]	14
0xBB00F1F0	L3_INTF_IP_MTU [4].RESERVED[31:14]	18
0xBB00F1F0	L3_INTF_IP_MTU [4].MTU[13:0]	14
0xBB00F1F4	L3_INTF_IP_MTU [5].RESERVED[31:14]	18
0xBB00F1F4	L3_INTF_IP_MTU [5].MTU[13:0]	14
0xBB00F1F8	L3_INTF_IP_MTU [6].RESERVED[31:14]	18
0xBB00F1F8	L3_INTF_IP_MTU [6].MTU[13:0]	14
0xBB00F1FC	L3_INTF_IP_MTU [7].RESERVED[31:14]	18
0xBB00F1FC	L3_INTF_IP_MTU [7].MTU[13:0]	14
0xBB00F200	L3_INTF_IP_MTU [8].RESERVED[31:14]	18
0xBB00F200	L3_INTF_IP_MTU [8].MTU[13:0]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F204	L3_INTF_IP_MTU [9].RESERVED[31:14]	18
0xBB00F204	L3_INTF_IP_MTU [9].MTU[13:0]	14
0xBB00F208	L3_INTF_IP_MTU [10].RESERVED[31:14]	18
0xBB00F208	L3_INTF_IP_MTU [10].MTU[13:0]	14
0xBB00F20C	L3_INTF_IP_MTU [11].RESERVED[31:14]	18
0xBB00F20C	L3_INTF_IP_MTU [11].MTU[13:0]	14
0xBB00F210	L3_INTF_IP_MTU [12].RESERVED[31:14]	18
0xBB00F210	L3_INTF_IP_MTU [12].MTU[13:0]	14
0xBB00F214	L3_INTF_IP_MTU [13].RESERVED[31:14]	18
0xBB00F214	L3_INTF_IP_MTU [13].MTU[13:0]	14
0xBB00F218	L3_INTF_IP_MTU [14].RESERVED[31:14]	18
0xBB00F218	L3_INTF_IP_MTU [14].MTU[13:0]	14
0xBB00F21C	L3_INTF_IP_MTU [15].RESERVED[31:14]	18
0xBB00F21C	L3_INTF_IP_MTU [15].MTU[13:0]	14
0xBB00F014	L3_IP6MC_ROUTE_CTRL.RESERVED[31:22]	10
0xBB00F014	L3_IP6MC_ROUTE_CTRL.PKT_TO_CPU_TARGET[21:21]	1
0xBB00F014	L3_IP6MC_ROUTE_CTRL.HL_FAIL_ACT[20:19]	2
0xBB00F014	L3_IP6MC_ROUTE_CTRL.MTU_FAIL_ACT[18:17]	2
0xBB00F014	L3_IP6MC_ROUTE_CTRL.SRC_INTF_FLTR_EN[16:16]	1
0xBB00F014	L3_IP6MC_ROUTE_CTRL.HDR_ROUTE_ACT[15:13]	3
0xBB00F014	L3_IP6MC_ROUTE_CTRL.HBH_ERR_ACT[12:10]	3
0xBB00F014	L3_IP6MC_ROUTE_CTRL.HBH_ACT[9:7]	3
0xBB00F014	L3_IP6MC_ROUTE_CTRL.DMAC_MISMATCH_ACT[6:5]	2
0xBB00F014	L3_IP6MC_ROUTE_CTRL.ZERO_SIP_ACT[4:3]	2
0xBB00F014	L3_IP6MC_ROUTE_CTRL.BAD_SIP_ACT[2:1]	2
0xBB00F014	L3_IP6MC_ROUTE_CTRL.GLB_EN[0:0]	1
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.RESERVED[31:23]	9
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.PKT_TO_CPU_TARGET[22:22]	1
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.HL_FAIL_ACT[21:20]	2
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.MTU_FAIL_ACT[19:18]	2
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.HDR_ROUTE_ACT[17:15]	3
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.HBH_ERR_ACT[14:12]	3
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.HBH_ACT[11:9]	3
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.DMAC_MISMATCH_ACT[8:7]	2
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.ZERO_SIP_ACT[6:5]	2
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.BAD_DIP_ACT[4:3]	2
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.BAD_SIP_ACT[2:1]	2
0xBB00F00C	L3_IP6UC_ROUTE_CTRL.GLB_EN[0:0]	1
0xBB00F010	L3_IPMC_ROUTE_CTRL.RESERVED[31:16]	16
0xBB00F010	L3_IPMC_ROUTE_CTRL.PKT_TO_CPU_TARGET[15:15]	1
0xBB00F010	L3_IPMC_ROUTE_CTRL.TTL_FAIL_ACT[14:13]	2
0xBB00F010	L3_IPMC_ROUTE_CTRL.MTU_FAIL_ACT[12:11]	2
0xBB00F010	L3_IPMC_ROUTE_CTRL.SRC_INTF_FLTR_EN[10:10]	1
0xBB00F010	L3_IPMC_ROUTE_CTRL.HDR_OPT_ACT[9:7]	3
0xBB00F010	L3_IPMC_ROUTE_CTRL.DMAC_MISMATCH_ACT[6:5]	2
0xBB00F010	L3_IPMC_ROUTE_CTRL.ZERO_SIP_ACT[4:3]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F010	L3_IPMC_ROUTE_CTRL.BAD_SIP_ACT[2:1]	2
0xBB00F010	L3_IPMC_ROUTE_CTRL.GLB_EN[0:0]	1
0xBB00F008	L3_IPUC_ROUTE_CTRL.RESERVED[31:19]	13
0xBB00F008	L3_IPUC_ROUTE_CTRL.PKT_TO_CPU_TARGET[18:18]	1
0xBB00F008	L3_IPUC_ROUTE_CTRL.TTL_FAIL_ACT[17:16]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.MTU_FAIL_ACT[15:14]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.HDR_OPT_ACT[13:11]	3
0xBB00F008	L3_IPUC_ROUTE_CTRL.DMAC_MC_ACT[10:9]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.DMAC_BC_ACT[8:7]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.ZERO_SIP_ACT[6:5]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.BAD_DIP_ACT[4:3]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.BAD_SIP_ACT[2:1]	2
0xBB00F008	L3_IPUC_ROUTE_CTRL.GLB_EN[0:0]	1
0xBB00F000	L3_IP_ROUTE_CTRL.RESERVED[31:12]	20
0xBB00F000	L3_IP_ROUTE_CTRL.NON_IP_ACT[11:10]	2
0xBB00F000	L3_IP_ROUTE_CTRL.URPF_BASE_SEL[9:9]	1
0xBB00F000	L3_IP_ROUTE_CTRL.NH_AGE_OUT_ACT[8:6]	3
0xBB00F000	L3_IP_ROUTE_CTRL.NH_ERR_ACT[5:4]	2
0xBB00F000	L3_IP_ROUTE_CTRL.IP6_HDR_ERR_ACT[3:2]	2
0xBB00F000	L3_IP_ROUTE_CTRL.IP_HDR_ERR_ACT[1:0]	2
0xBB00F26C	L3_MONT_CNTR_CTRL [0].RESERVED[31:18]	14
0xBB00F26C	L3_MONT_CNTR_CTRL [0].RST[17:17]	1
0xBB00F26C	L3_MONT_CNTR_CTRL [0].LATCH[16:16]	1
0xBB00F26C	L3_MONT_CNTR_CTRL [0].MODE[15:15]	1
0xBB00F26C	L3_MONT_CNTR_CTRL [0].INDEX[14:0]	15
0xBB00F270	L3_MONT_CNTR_CTRL [1].RESERVED[31:18]	14
0xBB00F270	L3_MONT_CNTR_CTRL [1].RST[17:17]	1
0xBB00F270	L3_MONT_CNTR_CTRL [1].LATCH[16:16]	1
0xBB00F270	L3_MONT_CNTR_CTRL [1].MODE[15:15]	1
0xBB00F270	L3_MONT_CNTR_CTRL [1].INDEX[14:0]	15
0xBB00F274	L3_MONT_CNTR_CTRL [2].RESERVED[31:18]	14
0xBB00F274	L3_MONT_CNTR_CTRL [2].RST[17:17]	1
0xBB00F274	L3_MONT_CNTR_CTRL [2].LATCH[16:16]	1
0xBB00F274	L3_MONT_CNTR_CTRL [2].MODE[15:15]	1
0xBB00F274	L3_MONT_CNTR_CTRL [2].INDEX[14:0]	15
0xBB00F278	L3_MONT_CNTR_CTRL [3].RESERVED[31:18]	14
0xBB00F278	L3_MONT_CNTR_CTRL [3].RST[17:17]	1
0xBB00F278	L3_MONT_CNTR_CTRL [3].LATCH[16:16]	1
0xBB00F278	L3_MONT_CNTR_CTRL [3].MODE[15:15]	1
0xBB00F278	L3_MONT_CNTR_CTRL [3].INDEX[14:0]	15
0xBB00F27C	L3_MONT_CNTR_DATA [0].RESERVED[31:10]	22
0xBB00F27C	L3_MONT_CNTR_DATA [0].CNT_HI[9:0]	10
0xBB00F280	L3_MONT_CNTR_DATA [0].CNT_LO[31:0]	32
0xBB00F284	L3_MONT_CNTR_DATA [1].RESERVED[31:10]	22
0xBB00F284	L3_MONT_CNTR_DATA [1].CNT_HI[9:0]	10
0xBB00F288	L3_MONT_CNTR_DATA [1].CNT_LO[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F28C	L3_MONT_CNTR_DATA [2].RESERVED[31:10]	22
0xBB00F28C	L3_MONT_CNTR_DATA [2].CNT_HI[9:0]	10
0xBB00F290	L3_MONT_CNTR_DATA [2].CNT_LO[31:0]	32
0xBB00F294	L3_MONT_CNTR_DATA [3].RESERVED[31:10]	22
0xBB00F294	L3_MONT_CNTR_DATA [3].CNT_HI[9:0]	10
0xBB00F298	L3_MONT_CNTR_DATA [3].CNT_LO[31:0]	32
0xBB00F0FC	L3_PORT_IP6_ROUTE_CTRL [0].RESERVED[31:6]	26
0xBB00F0FC	L3_PORT_IP6_ROUTE_CTRL [0].URPF_FAIL_ACT[5:3]	3
0xBB00F0FC	L3_PORT_IP6_ROUTE_CTRL [0].URPF_CHK_MODE[2:2]	1
0xBB00F0FC	L3_PORT_IP6_ROUTE_CTRL [0].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0FC	L3_PORT_IP6_ROUTE_CTRL [0].URPF_CHK_EN[0:0]	1
0xBB00F100	L3_PORT_IP6_ROUTE_CTRL [1].RESERVED[31:6]	26
0xBB00F100	L3_PORT_IP6_ROUTE_CTRL [1].URPF_FAIL_ACT[5:3]	3
0xBB00F100	L3_PORT_IP6_ROUTE_CTRL [1].URPF_CHK_MODE[2:2]	1
0xBB00F100	L3_PORT_IP6_ROUTE_CTRL [1].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F100	L3_PORT_IP6_ROUTE_CTRL [1].URPF_CHK_EN[0:0]	1
0xBB00F104	L3_PORT_IP6_ROUTE_CTRL [2].RESERVED[31:6]	26
0xBB00F104	L3_PORT_IP6_ROUTE_CTRL [2].URPF_FAIL_ACT[5:3]	3
0xBB00F104	L3_PORT_IP6_ROUTE_CTRL [2].URPF_CHK_MODE[2:2]	1
0xBB00F104	L3_PORT_IP6_ROUTE_CTRL [2].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F104	L3_PORT_IP6_ROUTE_CTRL [2].URPF_CHK_EN[0:0]	1
0xBB00F108	L3_PORT_IP6_ROUTE_CTRL [3].RESERVED[31:6]	26
0xBB00F108	L3_PORT_IP6_ROUTE_CTRL [3].URPF_FAIL_ACT[5:3]	3
0xBB00F108	L3_PORT_IP6_ROUTE_CTRL [3].URPF_CHK_MODE[2:2]	1
0xBB00F108	L3_PORT_IP6_ROUTE_CTRL [3].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F108	L3_PORT_IP6_ROUTE_CTRL [3].URPF_CHK_EN[0:0]	1
0xBB00F10C	L3_PORT_IP6_ROUTE_CTRL [4].RESERVED[31:6]	26
0xBB00F10C	L3_PORT_IP6_ROUTE_CTRL [4].URPF_FAIL_ACT[5:3]	3
0xBB00F10C	L3_PORT_IP6_ROUTE_CTRL [4].URPF_CHK_MODE[2:2]	1
0xBB00F10C	L3_PORT_IP6_ROUTE_CTRL [4].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F10C	L3_PORT_IP6_ROUTE_CTRL [4].URPF_CHK_EN[0:0]	1
0xBB00F110	L3_PORT_IP6_ROUTE_CTRL [5].RESERVED[31:6]	26
0xBB00F110	L3_PORT_IP6_ROUTE_CTRL [5].URPF_FAIL_ACT[5:3]	3
0xBB00F110	L3_PORT_IP6_ROUTE_CTRL [5].URPF_CHK_MODE[2:2]	1
0xBB00F110	L3_PORT_IP6_ROUTE_CTRL [5].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F110	L3_PORT_IP6_ROUTE_CTRL [5].URPF_CHK_EN[0:0]	1
0xBB00F114	L3_PORT_IP6_ROUTE_CTRL [6].RESERVED[31:6]	26
0xBB00F114	L3_PORT_IP6_ROUTE_CTRL [6].URPF_FAIL_ACT[5:3]	3
0xBB00F114	L3_PORT_IP6_ROUTE_CTRL [6].URPF_CHK_MODE[2:2]	1
0xBB00F114	L3_PORT_IP6_ROUTE_CTRL [6].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F114	L3_PORT_IP6_ROUTE_CTRL [6].URPF_CHK_EN[0:0]	1
0xBB00F118	L3_PORT_IP6_ROUTE_CTRL [7].RESERVED[31:6]	26
0xBB00F118	L3_PORT_IP6_ROUTE_CTRL [7].URPF_FAIL_ACT[5:3]	3
0xBB00F118	L3_PORT_IP6_ROUTE_CTRL [7].URPF_CHK_MODE[2:2]	1
0xBB00F118	L3_PORT_IP6_ROUTE_CTRL [7].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F118	L3_PORT_IP6_ROUTE_CTRL [7].URPF_CHK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F11C	L3_PORT_IP6_ROUTE_CTRL [8].RESERVED[31:6]	26
0xBB00F11C	L3_PORT_IP6_ROUTE_CTRL [8].URPF_FAIL_ACT[5:3]	3
0xBB00F11C	L3_PORT_IP6_ROUTE_CTRL [8].URPF_CHK_MODE[2:2]	1
0xBB00F11C	L3_PORT_IP6_ROUTE_CTRL [8].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F11C	L3_PORT_IP6_ROUTE_CTRL [8].URPF_CHK_EN[0:0]	1
0xBB00F120	L3_PORT_IP6_ROUTE_CTRL [9].RESERVED[31:6]	26
0xBB00F120	L3_PORT_IP6_ROUTE_CTRL [9].URPF_FAIL_ACT[5:3]	3
0xBB00F120	L3_PORT_IP6_ROUTE_CTRL [9].URPF_CHK_MODE[2:2]	1
0xBB00F120	L3_PORT_IP6_ROUTE_CTRL [9].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F120	L3_PORT_IP6_ROUTE_CTRL [9].URPF_CHK_EN[0:0]	1
0xBB00F124	L3_PORT_IP6_ROUTE_CTRL [10].RESERVED[31:6]	26
0xBB00F124	L3_PORT_IP6_ROUTE_CTRL [10].URPF_FAIL_ACT[5:3]	3
0xBB00F124	L3_PORT_IP6_ROUTE_CTRL [10].URPF_CHK_MODE[2:2]	1
0xBB00F124	L3_PORT_IP6_ROUTE_CTRL [10].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F124	L3_PORT_IP6_ROUTE_CTRL [10].URPF_CHK_EN[0:0]	1
0xBB00F128	L3_PORT_IP6_ROUTE_CTRL [11].RESERVED[31:6]	26
0xBB00F128	L3_PORT_IP6_ROUTE_CTRL [11].URPF_FAIL_ACT[5:3]	3
0xBB00F128	L3_PORT_IP6_ROUTE_CTRL [11].URPF_CHK_MODE[2:2]	1
0xBB00F128	L3_PORT_IP6_ROUTE_CTRL [11].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F128	L3_PORT_IP6_ROUTE_CTRL [11].URPF_CHK_EN[0:0]	1
0xBB00F12C	L3_PORT_IP6_ROUTE_CTRL [12].RESERVED[31:6]	26
0xBB00F12C	L3_PORT_IP6_ROUTE_CTRL [12].URPF_FAIL_ACT[5:3]	3
0xBB00F12C	L3_PORT_IP6_ROUTE_CTRL [12].URPF_CHK_MODE[2:2]	1
0xBB00F12C	L3_PORT_IP6_ROUTE_CTRL [12].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F12C	L3_PORT_IP6_ROUTE_CTRL [12].URPF_CHK_EN[0:0]	1
0xBB00F130	L3_PORT_IP6_ROUTE_CTRL [13].RESERVED[31:6]	26
0xBB00F130	L3_PORT_IP6_ROUTE_CTRL [13].URPF_FAIL_ACT[5:3]	3
0xBB00F130	L3_PORT_IP6_ROUTE_CTRL [13].URPF_CHK_MODE[2:2]	1
0xBB00F130	L3_PORT_IP6_ROUTE_CTRL [13].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F130	L3_PORT_IP6_ROUTE_CTRL [13].URPF_CHK_EN[0:0]	1
0xBB00F134	L3_PORT_IP6_ROUTE_CTRL [14].RESERVED[31:6]	26
0xBB00F134	L3_PORT_IP6_ROUTE_CTRL [14].URPF_FAIL_ACT[5:3]	3
0xBB00F134	L3_PORT_IP6_ROUTE_CTRL [14].URPF_CHK_MODE[2:2]	1
0xBB00F134	L3_PORT_IP6_ROUTE_CTRL [14].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F134	L3_PORT_IP6_ROUTE_CTRL [14].URPF_CHK_EN[0:0]	1
0xBB00F138	L3_PORT_IP6_ROUTE_CTRL [15].RESERVED[31:6]	26
0xBB00F138	L3_PORT_IP6_ROUTE_CTRL [15].URPF_FAIL_ACT[5:3]	3
0xBB00F138	L3_PORT_IP6_ROUTE_CTRL [15].URPF_CHK_MODE[2:2]	1
0xBB00F138	L3_PORT_IP6_ROUTE_CTRL [15].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F138	L3_PORT_IP6_ROUTE_CTRL [15].URPF_CHK_EN[0:0]	1
0xBB00F13C	L3_PORT_IP6_ROUTE_CTRL [16].RESERVED[31:6]	26
0xBB00F13C	L3_PORT_IP6_ROUTE_CTRL [16].URPF_FAIL_ACT[5:3]	3
0xBB00F13C	L3_PORT_IP6_ROUTE_CTRL [16].URPF_CHK_MODE[2:2]	1
0xBB00F13C	L3_PORT_IP6_ROUTE_CTRL [16].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F13C	L3_PORT_IP6_ROUTE_CTRL [16].URPF_CHK_EN[0:0]	1
0xBB00F140	L3_PORT_IP6_ROUTE_CTRL [17].RESERVED[31:6]	26

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F140	L3_PORT_IP6_ROUTE_CTRL [17].URPF_FAIL_ACT[5:3]	3
0xBB00F140	L3_PORT_IP6_ROUTE_CTRL [17].URPF_CHK_MODE[2:2]	1
0xBB00F140	L3_PORT_IP6_ROUTE_CTRL [17].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F140	L3_PORT_IP6_ROUTE_CTRL [17].URPF_CHK_EN[0:0]	1
0xBB00F144	L3_PORT_IP6_ROUTE_CTRL [18].RESERVED[31:6]	26
0xBB00F144	L3_PORT_IP6_ROUTE_CTRL [18].URPF_FAIL_ACT[5:3]	3
0xBB00F144	L3_PORT_IP6_ROUTE_CTRL [18].URPF_CHK_MODE[2:2]	1
0xBB00F144	L3_PORT_IP6_ROUTE_CTRL [18].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F144	L3_PORT_IP6_ROUTE_CTRL [18].URPF_CHK_EN[0:0]	1
0xBB00F148	L3_PORT_IP6_ROUTE_CTRL [19].RESERVED[31:6]	26
0xBB00F148	L3_PORT_IP6_ROUTE_CTRL [19].URPF_FAIL_ACT[5:3]	3
0xBB00F148	L3_PORT_IP6_ROUTE_CTRL [19].URPF_CHK_MODE[2:2]	1
0xBB00F148	L3_PORT_IP6_ROUTE_CTRL [19].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F148	L3_PORT_IP6_ROUTE_CTRL [19].URPF_CHK_EN[0:0]	1
0xBB00F14C	L3_PORT_IP6_ROUTE_CTRL [20].RESERVED[31:6]	26
0xBB00F14C	L3_PORT_IP6_ROUTE_CTRL [20].URPF_FAIL_ACT[5:3]	3
0xBB00F14C	L3_PORT_IP6_ROUTE_CTRL [20].URPF_CHK_MODE[2:2]	1
0xBB00F14C	L3_PORT_IP6_ROUTE_CTRL [20].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F14C	L3_PORT_IP6_ROUTE_CTRL [20].URPF_CHK_EN[0:0]	1
0xBB00F150	L3_PORT_IP6_ROUTE_CTRL [21].RESERVED[31:6]	26
0xBB00F150	L3_PORT_IP6_ROUTE_CTRL [21].URPF_FAIL_ACT[5:3]	3
0xBB00F150	L3_PORT_IP6_ROUTE_CTRL [21].URPF_CHK_MODE[2:2]	1
0xBB00F150	L3_PORT_IP6_ROUTE_CTRL [21].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F150	L3_PORT_IP6_ROUTE_CTRL [21].URPF_CHK_EN[0:0]	1
0xBB00F154	L3_PORT_IP6_ROUTE_CTRL [22].RESERVED[31:6]	26
0xBB00F154	L3_PORT_IP6_ROUTE_CTRL [22].URPF_FAIL_ACT[5:3]	3
0xBB00F154	L3_PORT_IP6_ROUTE_CTRL [22].URPF_CHK_MODE[2:2]	1
0xBB00F154	L3_PORT_IP6_ROUTE_CTRL [22].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F154	L3_PORT_IP6_ROUTE_CTRL [22].URPF_CHK_EN[0:0]	1
0xBB00F158	L3_PORT_IP6_ROUTE_CTRL [23].RESERVED[31:6]	26
0xBB00F158	L3_PORT_IP6_ROUTE_CTRL [23].URPF_FAIL_ACT[5:3]	3
0xBB00F158	L3_PORT_IP6_ROUTE_CTRL [23].URPF_CHK_MODE[2:2]	1
0xBB00F158	L3_PORT_IP6_ROUTE_CTRL [23].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F158	L3_PORT_IP6_ROUTE_CTRL [23].URPF_CHK_EN[0:0]	1
0xBB00F15C	L3_PORT_IP6_ROUTE_CTRL [24].RESERVED[31:6]	26
0xBB00F15C	L3_PORT_IP6_ROUTE_CTRL [24].URPF_FAIL_ACT[5:3]	3
0xBB00F15C	L3_PORT_IP6_ROUTE_CTRL [24].URPF_CHK_MODE[2:2]	1
0xBB00F15C	L3_PORT_IP6_ROUTE_CTRL [24].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F15C	L3_PORT_IP6_ROUTE_CTRL [24].URPF_CHK_EN[0:0]	1
0xBB00F160	L3_PORT_IP6_ROUTE_CTRL [25].RESERVED[31:6]	26
0xBB00F160	L3_PORT_IP6_ROUTE_CTRL [25].URPF_FAIL_ACT[5:3]	3
0xBB00F160	L3_PORT_IP6_ROUTE_CTRL [25].URPF_CHK_MODE[2:2]	1
0xBB00F160	L3_PORT_IP6_ROUTE_CTRL [25].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F160	L3_PORT_IP6_ROUTE_CTRL [25].URPF_CHK_EN[0:0]	1
0xBB00F164	L3_PORT_IP6_ROUTE_CTRL [26].RESERVED[31:6]	26
0xBB00F164	L3_PORT_IP6_ROUTE_CTRL [26].URPF_FAIL_ACT[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F164	L3_PORT_IP6_ROUTE_CTRL [26].URPF_CHK_MODE[2:2]	1
0xBB00F164	L3_PORT_IP6_ROUTE_CTRL [26].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F164	L3_PORT_IP6_ROUTE_CTRL [26].URPF_CHK_EN[0:0]	1
0xBB00F168	L3_PORT_IP6_ROUTE_CTRL [27].RESERVED[31:6]	26
0xBB00F168	L3_PORT_IP6_ROUTE_CTRL [27].URPF_FAIL_ACT[5:3]	3
0xBB00F168	L3_PORT_IP6_ROUTE_CTRL [27].URPF_CHK_MODE[2:2]	1
0xBB00F168	L3_PORT_IP6_ROUTE_CTRL [27].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F168	L3_PORT_IP6_ROUTE_CTRL [27].URPF_CHK_EN[0:0]	1
0xBB00F16C	L3_PORT_IP6_ROUTE_CTRL [28].RESERVED[31:6]	26
0xBB00F16C	L3_PORT_IP6_ROUTE_CTRL [28].URPF_FAIL_ACT[5:3]	3
0xBB00F16C	L3_PORT_IP6_ROUTE_CTRL [28].URPF_CHK_MODE[2:2]	1
0xBB00F16C	L3_PORT_IP6_ROUTE_CTRL [28].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F16C	L3_PORT_IP6_ROUTE_CTRL [28].URPF_CHK_EN[0:0]	1
0xBB00F170	L3_PORT_IP6_ROUTE_CTRL [29].RESERVED[31:6]	26
0xBB00F170	L3_PORT_IP6_ROUTE_CTRL [29].URPF_FAIL_ACT[5:3]	3
0xBB00F170	L3_PORT_IP6_ROUTE_CTRL [29].URPF_CHK_MODE[2:2]	1
0xBB00F170	L3_PORT_IP6_ROUTE_CTRL [29].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F170	L3_PORT_IP6_ROUTE_CTRL [29].URPF_CHK_EN[0:0]	1
0xBB00F174	L3_PORT_IP6_ROUTE_CTRL [30].RESERVED[31:6]	26
0xBB00F174	L3_PORT_IP6_ROUTE_CTRL [30].URPF_FAIL_ACT[5:3]	3
0xBB00F174	L3_PORT_IP6_ROUTE_CTRL [30].URPF_CHK_MODE[2:2]	1
0xBB00F174	L3_PORT_IP6_ROUTE_CTRL [30].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F174	L3_PORT_IP6_ROUTE_CTRL [30].URPF_CHK_EN[0:0]	1
0xBB00F178	L3_PORT_IP6_ROUTE_CTRL [31].RESERVED[31:6]	26
0xBB00F178	L3_PORT_IP6_ROUTE_CTRL [31].URPF_FAIL_ACT[5:3]	3
0xBB00F178	L3_PORT_IP6_ROUTE_CTRL [31].URPF_CHK_MODE[2:2]	1
0xBB00F178	L3_PORT_IP6_ROUTE_CTRL [31].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F178	L3_PORT_IP6_ROUTE_CTRL [31].URPF_CHK_EN[0:0]	1
0xBB00F17C	L3_PORT_IP6_ROUTE_CTRL [32].RESERVED[31:6]	26
0xBB00F17C	L3_PORT_IP6_ROUTE_CTRL [32].URPF_FAIL_ACT[5:3]	3
0xBB00F17C	L3_PORT_IP6_ROUTE_CTRL [32].URPF_CHK_MODE[2:2]	1
0xBB00F17C	L3_PORT_IP6_ROUTE_CTRL [32].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F17C	L3_PORT_IP6_ROUTE_CTRL [32].URPF_CHK_EN[0:0]	1
0xBB00F180	L3_PORT_IP6_ROUTE_CTRL [33].RESERVED[31:6]	26
0xBB00F180	L3_PORT_IP6_ROUTE_CTRL [33].URPF_FAIL_ACT[5:3]	3
0xBB00F180	L3_PORT_IP6_ROUTE_CTRL [33].URPF_CHK_MODE[2:2]	1
0xBB00F180	L3_PORT_IP6_ROUTE_CTRL [33].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F180	L3_PORT_IP6_ROUTE_CTRL [33].URPF_CHK_EN[0:0]	1
0xBB00F184	L3_PORT_IP6_ROUTE_CTRL [34].RESERVED[31:6]	26
0xBB00F184	L3_PORT_IP6_ROUTE_CTRL [34].URPF_FAIL_ACT[5:3]	3
0xBB00F184	L3_PORT_IP6_ROUTE_CTRL [34].URPF_CHK_MODE[2:2]	1
0xBB00F184	L3_PORT_IP6_ROUTE_CTRL [34].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F184	L3_PORT_IP6_ROUTE_CTRL [34].URPF_CHK_EN[0:0]	1
0xBB00F188	L3_PORT_IP6_ROUTE_CTRL [35].RESERVED[31:6]	26
0xBB00F188	L3_PORT_IP6_ROUTE_CTRL [35].URPF_FAIL_ACT[5:3]	3
0xBB00F188	L3_PORT_IP6_ROUTE_CTRL [35].URPF_CHK_MODE[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F188	L3_PORT_IP6_ROUTE_CTRL [35].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F188	L3_PORT_IP6_ROUTE_CTRL [35].URPF_CHK_EN[0:0]	1
0xBB00F18C	L3_PORT_IP6_ROUTE_CTRL [36].RESERVED[31:6]	26
0xBB00F18C	L3_PORT_IP6_ROUTE_CTRL [36].URPF_FAIL_ACT[5:3]	3
0xBB00F18C	L3_PORT_IP6_ROUTE_CTRL [36].URPF_CHK_MODE[2:2]	1
0xBB00F18C	L3_PORT_IP6_ROUTE_CTRL [36].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F18C	L3_PORT_IP6_ROUTE_CTRL [36].URPF_CHK_EN[0:0]	1
0xBB00F190	L3_PORT_IP6_ROUTE_CTRL [37].RESERVED[31:6]	26
0xBB00F190	L3_PORT_IP6_ROUTE_CTRL [37].URPF_FAIL_ACT[5:3]	3
0xBB00F190	L3_PORT_IP6_ROUTE_CTRL [37].URPF_CHK_MODE[2:2]	1
0xBB00F190	L3_PORT_IP6_ROUTE_CTRL [37].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F190	L3_PORT_IP6_ROUTE_CTRL [37].URPF_CHK_EN[0:0]	1
0xBB00F194	L3_PORT_IP6_ROUTE_CTRL [38].RESERVED[31:6]	26
0xBB00F194	L3_PORT_IP6_ROUTE_CTRL [38].URPF_FAIL_ACT[5:3]	3
0xBB00F194	L3_PORT_IP6_ROUTE_CTRL [38].URPF_CHK_MODE[2:2]	1
0xBB00F194	L3_PORT_IP6_ROUTE_CTRL [38].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F194	L3_PORT_IP6_ROUTE_CTRL [38].URPF_CHK_EN[0:0]	1
0xBB00F198	L3_PORT_IP6_ROUTE_CTRL [39].RESERVED[31:6]	26
0xBB00F198	L3_PORT_IP6_ROUTE_CTRL [39].URPF_FAIL_ACT[5:3]	3
0xBB00F198	L3_PORT_IP6_ROUTE_CTRL [39].URPF_CHK_MODE[2:2]	1
0xBB00F198	L3_PORT_IP6_ROUTE_CTRL [39].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F198	L3_PORT_IP6_ROUTE_CTRL [39].URPF_CHK_EN[0:0]	1
0xBB00F19C	L3_PORT_IP6_ROUTE_CTRL [40].RESERVED[31:6]	26
0xBB00F19C	L3_PORT_IP6_ROUTE_CTRL [40].URPF_FAIL_ACT[5:3]	3
0xBB00F19C	L3_PORT_IP6_ROUTE_CTRL [40].URPF_CHK_MODE[2:2]	1
0xBB00F19C	L3_PORT_IP6_ROUTE_CTRL [40].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F19C	L3_PORT_IP6_ROUTE_CTRL [40].URPF_CHK_EN[0:0]	1
0xBB00F1A0	L3_PORT_IP6_ROUTE_CTRL [41].RESERVED[31:6]	26
0xBB00F1A0	L3_PORT_IP6_ROUTE_CTRL [41].URPF_FAIL_ACT[5:3]	3
0xBB00F1A0	L3_PORT_IP6_ROUTE_CTRL [41].URPF_CHK_MODE[2:2]	1
0xBB00F1A0	L3_PORT_IP6_ROUTE_CTRL [41].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1A0	L3_PORT_IP6_ROUTE_CTRL [41].URPF_CHK_EN[0:0]	1
0xBB00F1A4	L3_PORT_IP6_ROUTE_CTRL [42].RESERVED[31:6]	26
0xBB00F1A4	L3_PORT_IP6_ROUTE_CTRL [42].URPF_FAIL_ACT[5:3]	3
0xBB00F1A4	L3_PORT_IP6_ROUTE_CTRL [42].URPF_CHK_MODE[2:2]	1
0xBB00F1A4	L3_PORT_IP6_ROUTE_CTRL [42].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1A4	L3_PORT_IP6_ROUTE_CTRL [42].URPF_CHK_EN[0:0]	1
0xBB00F1A8	L3_PORT_IP6_ROUTE_CTRL [43].RESERVED[31:6]	26
0xBB00F1A8	L3_PORT_IP6_ROUTE_CTRL [43].URPF_FAIL_ACT[5:3]	3
0xBB00F1A8	L3_PORT_IP6_ROUTE_CTRL [43].URPF_CHK_MODE[2:2]	1
0xBB00F1A8	L3_PORT_IP6_ROUTE_CTRL [43].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1A8	L3_PORT_IP6_ROUTE_CTRL [43].URPF_CHK_EN[0:0]	1
0xBB00F1AC	L3_PORT_IP6_ROUTE_CTRL [44].RESERVED[31:6]	26
0xBB00F1AC	L3_PORT_IP6_ROUTE_CTRL [44].URPF_FAIL_ACT[5:3]	3
0xBB00F1AC	L3_PORT_IP6_ROUTE_CTRL [44].URPF_CHK_MODE[2:2]	1
0xBB00F1AC	L3_PORT_IP6_ROUTE_CTRL [44].URPF_DFLT_ROUTE_EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F1AC	L3_PORT_IP6_ROUTE_CTRL [44].URPF_CHK_EN[0:0]	1
0xBB00F1B0	L3_PORT_IP6_ROUTE_CTRL [45].RESERVED[31:6]	26
0xBB00F1B0	L3_PORT_IP6_ROUTE_CTRL [45].URPF_FAIL_ACT[5:3]	3
0xBB00F1B0	L3_PORT_IP6_ROUTE_CTRL [45].URPF_CHK_MODE[2:2]	1
0xBB00F1B0	L3_PORT_IP6_ROUTE_CTRL [45].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1B0	L3_PORT_IP6_ROUTE_CTRL [45].URPF_CHK_EN[0:0]	1
0xBB00F1B4	L3_PORT_IP6_ROUTE_CTRL [46].RESERVED[31:6]	26
0xBB00F1B4	L3_PORT_IP6_ROUTE_CTRL [46].URPF_FAIL_ACT[5:3]	3
0xBB00F1B4	L3_PORT_IP6_ROUTE_CTRL [46].URPF_CHK_MODE[2:2]	1
0xBB00F1B4	L3_PORT_IP6_ROUTE_CTRL [46].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1B4	L3_PORT_IP6_ROUTE_CTRL [46].URPF_CHK_EN[0:0]	1
0xBB00F1B8	L3_PORT_IP6_ROUTE_CTRL [47].RESERVED[31:6]	26
0xBB00F1B8	L3_PORT_IP6_ROUTE_CTRL [47].URPF_FAIL_ACT[5:3]	3
0xBB00F1B8	L3_PORT_IP6_ROUTE_CTRL [47].URPF_CHK_MODE[2:2]	1
0xBB00F1B8	L3_PORT_IP6_ROUTE_CTRL [47].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1B8	L3_PORT_IP6_ROUTE_CTRL [47].URPF_CHK_EN[0:0]	1
0xBB00F1BC	L3_PORT_IP6_ROUTE_CTRL [48].RESERVED[31:6]	26
0xBB00F1BC	L3_PORT_IP6_ROUTE_CTRL [48].URPF_FAIL_ACT[5:3]	3
0xBB00F1BC	L3_PORT_IP6_ROUTE_CTRL [48].URPF_CHK_MODE[2:2]	1
0xBB00F1BC	L3_PORT_IP6_ROUTE_CTRL [48].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1BC	L3_PORT_IP6_ROUTE_CTRL [48].URPF_CHK_EN[0:0]	1
0xBB00F1C0	L3_PORT_IP6_ROUTE_CTRL [49].RESERVED[31:6]	26
0xBB00F1C0	L3_PORT_IP6_ROUTE_CTRL [49].URPF_FAIL_ACT[5:3]	3
0xBB00F1C0	L3_PORT_IP6_ROUTE_CTRL [49].URPF_CHK_MODE[2:2]	1
0xBB00F1C0	L3_PORT_IP6_ROUTE_CTRL [49].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1C0	L3_PORT_IP6_ROUTE_CTRL [49].URPF_CHK_EN[0:0]	1
0xBB00F1C4	L3_PORT_IP6_ROUTE_CTRL [50].RESERVED[31:6]	26
0xBB00F1C4	L3_PORT_IP6_ROUTE_CTRL [50].URPF_FAIL_ACT[5:3]	3
0xBB00F1C4	L3_PORT_IP6_ROUTE_CTRL [50].URPF_CHK_MODE[2:2]	1
0xBB00F1C4	L3_PORT_IP6_ROUTE_CTRL [50].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1C4	L3_PORT_IP6_ROUTE_CTRL [50].URPF_CHK_EN[0:0]	1
0xBB00F1C8	L3_PORT_IP6_ROUTE_CTRL [51].RESERVED[31:6]	26
0xBB00F1C8	L3_PORT_IP6_ROUTE_CTRL [51].URPF_FAIL_ACT[5:3]	3
0xBB00F1C8	L3_PORT_IP6_ROUTE_CTRL [51].URPF_CHK_MODE[2:2]	1
0xBB00F1C8	L3_PORT_IP6_ROUTE_CTRL [51].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1C8	L3_PORT_IP6_ROUTE_CTRL [51].URPF_CHK_EN[0:0]	1
0xBB00F1CC	L3_PORT_IP6_ROUTE_CTRL [52].RESERVED[31:6]	26
0xBB00F1CC	L3_PORT_IP6_ROUTE_CTRL [52].URPF_FAIL_ACT[5:3]	3
0xBB00F1CC	L3_PORT_IP6_ROUTE_CTRL [52].URPF_CHK_MODE[2:2]	1
0xBB00F1CC	L3_PORT_IP6_ROUTE_CTRL [52].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1CC	L3_PORT_IP6_ROUTE_CTRL [52].URPF_CHK_EN[0:0]	1
0xBB00F1D0	L3_PORT_IP6_ROUTE_CTRL [53].RESERVED[31:6]	26
0xBB00F1D0	L3_PORT_IP6_ROUTE_CTRL [53].URPF_FAIL_ACT[5:3]	3
0xBB00F1D0	L3_PORT_IP6_ROUTE_CTRL [53].URPF_CHK_MODE[2:2]	1
0xBB00F1D0	L3_PORT_IP6_ROUTE_CTRL [53].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1D0	L3_PORT_IP6_ROUTE_CTRL [53].URPF_CHK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F1D4	L3_PORT_IP6_ROUTE_CTRL [54].RESERVED[31:6]	26
0xBB00F1D4	L3_PORT_IP6_ROUTE_CTRL [54].URPF_FAIL_ACT[5:3]	3
0xBB00F1D4	L3_PORT_IP6_ROUTE_CTRL [54].URPF_CHK_MODE[2:2]	1
0xBB00F1D4	L3_PORT_IP6_ROUTE_CTRL [54].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1D4	L3_PORT_IP6_ROUTE_CTRL [54].URPF_CHK_EN[0:0]	1
0xBB00F1D8	L3_PORT_IP6_ROUTE_CTRL [55].RESERVED[31:6]	26
0xBB00F1D8	L3_PORT_IP6_ROUTE_CTRL [55].URPF_FAIL_ACT[5:3]	3
0xBB00F1D8	L3_PORT_IP6_ROUTE_CTRL [55].URPF_CHK_MODE[2:2]	1
0xBB00F1D8	L3_PORT_IP6_ROUTE_CTRL [55].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1D8	L3_PORT_IP6_ROUTE_CTRL [55].URPF_CHK_EN[0:0]	1
0xBB00F1DC	L3_PORT_IP6_ROUTE_CTRL [56].RESERVED[31:6]	26
0xBB00F1DC	L3_PORT_IP6_ROUTE_CTRL [56].URPF_FAIL_ACT[5:3]	3
0xBB00F1DC	L3_PORT_IP6_ROUTE_CTRL [56].URPF_CHK_MODE[2:2]	1
0xBB00F1DC	L3_PORT_IP6_ROUTE_CTRL [56].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F1DC	L3_PORT_IP6_ROUTE_CTRL [56].URPF_CHK_EN[0:0]	1
0xBB00F018	L3_PORT_IP_ROUTE_CTRL [0].RESERVED[31:6]	26
0xBB00F018	L3_PORT_IP_ROUTE_CTRL [0].URPF_FAIL_ACT[5:3]	3
0xBB00F018	L3_PORT_IP_ROUTE_CTRL [0].URPF_CHK_MODE[2:2]	1
0xBB00F018	L3_PORT_IP_ROUTE_CTRL [0].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F018	L3_PORT_IP_ROUTE_CTRL [0].URPF_CHK_EN[0:0]	1
0xBB00F01C	L3_PORT_IP_ROUTE_CTRL [1].RESERVED[31:6]	26
0xBB00F01C	L3_PORT_IP_ROUTE_CTRL [1].URPF_FAIL_ACT[5:3]	3
0xBB00F01C	L3_PORT_IP_ROUTE_CTRL [1].URPF_CHK_MODE[2:2]	1
0xBB00F01C	L3_PORT_IP_ROUTE_CTRL [1].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F01C	L3_PORT_IP_ROUTE_CTRL [1].URPF_CHK_EN[0:0]	1
0xBB00F020	L3_PORT_IP_ROUTE_CTRL [2].RESERVED[31:6]	26
0xBB00F020	L3_PORT_IP_ROUTE_CTRL [2].URPF_FAIL_ACT[5:3]	3
0xBB00F020	L3_PORT_IP_ROUTE_CTRL [2].URPF_CHK_MODE[2:2]	1
0xBB00F020	L3_PORT_IP_ROUTE_CTRL [2].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F020	L3_PORT_IP_ROUTE_CTRL [2].URPF_CHK_EN[0:0]	1
0xBB00F024	L3_PORT_IP_ROUTE_CTRL [3].RESERVED[31:6]	26
0xBB00F024	L3_PORT_IP_ROUTE_CTRL [3].URPF_FAIL_ACT[5:3]	3
0xBB00F024	L3_PORT_IP_ROUTE_CTRL [3].URPF_CHK_MODE[2:2]	1
0xBB00F024	L3_PORT_IP_ROUTE_CTRL [3].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F024	L3_PORT_IP_ROUTE_CTRL [3].URPF_CHK_EN[0:0]	1
0xBB00F028	L3_PORT_IP_ROUTE_CTRL [4].RESERVED[31:6]	26
0xBB00F028	L3_PORT_IP_ROUTE_CTRL [4].URPF_FAIL_ACT[5:3]	3
0xBB00F028	L3_PORT_IP_ROUTE_CTRL [4].URPF_CHK_MODE[2:2]	1
0xBB00F028	L3_PORT_IP_ROUTE_CTRL [4].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F028	L3_PORT_IP_ROUTE_CTRL [4].URPF_CHK_EN[0:0]	1
0xBB00F02C	L3_PORT_IP_ROUTE_CTRL [5].RESERVED[31:6]	26
0xBB00F02C	L3_PORT_IP_ROUTE_CTRL [5].URPF_FAIL_ACT[5:3]	3
0xBB00F02C	L3_PORT_IP_ROUTE_CTRL [5].URPF_CHK_MODE[2:2]	1
0xBB00F02C	L3_PORT_IP_ROUTE_CTRL [5].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F02C	L3_PORT_IP_ROUTE_CTRL [5].URPF_CHK_EN[0:0]	1
0xBB00F030	L3_PORT_IP_ROUTE_CTRL [6].RESERVED[31:6]	26

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F030	L3_PORT_IP_ROUTE_CTRL [6].URPF_FAIL_ACT[5:3]	3
0xBB00F030	L3_PORT_IP_ROUTE_CTRL [6].URPF_CHK_MODE[2:2]	1
0xBB00F030	L3_PORT_IP_ROUTE_CTRL [6].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F030	L3_PORT_IP_ROUTE_CTRL [6].URPF_CHK_EN[0:0]	1
0xBB00F034	L3_PORT_IP_ROUTE_CTRL [7].RESERVED[31:6]	26
0xBB00F034	L3_PORT_IP_ROUTE_CTRL [7].URPF_FAIL_ACT[5:3]	3
0xBB00F034	L3_PORT_IP_ROUTE_CTRL [7].URPF_CHK_MODE[2:2]	1
0xBB00F034	L3_PORT_IP_ROUTE_CTRL [7].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F034	L3_PORT_IP_ROUTE_CTRL [7].URPF_CHK_EN[0:0]	1
0xBB00F038	L3_PORT_IP_ROUTE_CTRL [8].RESERVED[31:6]	26
0xBB00F038	L3_PORT_IP_ROUTE_CTRL [8].URPF_FAIL_ACT[5:3]	3
0xBB00F038	L3_PORT_IP_ROUTE_CTRL [8].URPF_CHK_MODE[2:2]	1
0xBB00F038	L3_PORT_IP_ROUTE_CTRL [8].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F038	L3_PORT_IP_ROUTE_CTRL [8].URPF_CHK_EN[0:0]	1
0xBB00F03C	L3_PORT_IP_ROUTE_CTRL [9].RESERVED[31:6]	26
0xBB00F03C	L3_PORT_IP_ROUTE_CTRL [9].URPF_FAIL_ACT[5:3]	3
0xBB00F03C	L3_PORT_IP_ROUTE_CTRL [9].URPF_CHK_MODE[2:2]	1
0xBB00F03C	L3_PORT_IP_ROUTE_CTRL [9].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F03C	L3_PORT_IP_ROUTE_CTRL [9].URPF_CHK_EN[0:0]	1
0xBB00F040	L3_PORT_IP_ROUTE_CTRL [10].RESERVED[31:6]	26
0xBB00F040	L3_PORT_IP_ROUTE_CTRL [10].URPF_FAIL_ACT[5:3]	3
0xBB00F040	L3_PORT_IP_ROUTE_CTRL [10].URPF_CHK_MODE[2:2]	1
0xBB00F040	L3_PORT_IP_ROUTE_CTRL [10].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F040	L3_PORT_IP_ROUTE_CTRL [10].URPF_CHK_EN[0:0]	1
0xBB00F044	L3_PORT_IP_ROUTE_CTRL [11].RESERVED[31:6]	26
0xBB00F044	L3_PORT_IP_ROUTE_CTRL [11].URPF_FAIL_ACT[5:3]	3
0xBB00F044	L3_PORT_IP_ROUTE_CTRL [11].URPF_CHK_MODE[2:2]	1
0xBB00F044	L3_PORT_IP_ROUTE_CTRL [11].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F044	L3_PORT_IP_ROUTE_CTRL [11].URPF_CHK_EN[0:0]	1
0xBB00F048	L3_PORT_IP_ROUTE_CTRL [12].RESERVED[31:6]	26
0xBB00F048	L3_PORT_IP_ROUTE_CTRL [12].URPF_FAIL_ACT[5:3]	3
0xBB00F048	L3_PORT_IP_ROUTE_CTRL [12].URPF_CHK_MODE[2:2]	1
0xBB00F048	L3_PORT_IP_ROUTE_CTRL [12].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F048	L3_PORT_IP_ROUTE_CTRL [12].URPF_CHK_EN[0:0]	1
0xBB00F04C	L3_PORT_IP_ROUTE_CTRL [13].RESERVED[31:6]	26
0xBB00F04C	L3_PORT_IP_ROUTE_CTRL [13].URPF_FAIL_ACT[5:3]	3
0xBB00F04C	L3_PORT_IP_ROUTE_CTRL [13].URPF_CHK_MODE[2:2]	1
0xBB00F04C	L3_PORT_IP_ROUTE_CTRL [13].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F04C	L3_PORT_IP_ROUTE_CTRL [13].URPF_CHK_EN[0:0]	1
0xBB00F050	L3_PORT_IP_ROUTE_CTRL [14].RESERVED[31:6]	26
0xBB00F050	L3_PORT_IP_ROUTE_CTRL [14].URPF_FAIL_ACT[5:3]	3
0xBB00F050	L3_PORT_IP_ROUTE_CTRL [14].URPF_CHK_MODE[2:2]	1
0xBB00F050	L3_PORT_IP_ROUTE_CTRL [14].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F050	L3_PORT_IP_ROUTE_CTRL [14].URPF_CHK_EN[0:0]	1
0xBB00F054	L3_PORT_IP_ROUTE_CTRL [15].RESERVED[31:6]	26
0xBB00F054	L3_PORT_IP_ROUTE_CTRL [15].URPF_FAIL_ACT[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F054	L3_PORT_IP_ROUTE_CTRL [15].URPF_CHK_MODE[2:2]	1
0xBB00F054	L3_PORT_IP_ROUTE_CTRL [15].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F054	L3_PORT_IP_ROUTE_CTRL [15].URPF_CHK_EN[0:0]	1
0xBB00F058	L3_PORT_IP_ROUTE_CTRL [16].RESERVED[31:6]	26
0xBB00F058	L3_PORT_IP_ROUTE_CTRL [16].URPF_FAIL_ACT[5:3]	3
0xBB00F058	L3_PORT_IP_ROUTE_CTRL [16].URPF_CHK_MODE[2:2]	1
0xBB00F058	L3_PORT_IP_ROUTE_CTRL [16].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F058	L3_PORT_IP_ROUTE_CTRL [16].URPF_CHK_EN[0:0]	1
0xBB00F05C	L3_PORT_IP_ROUTE_CTRL [17].RESERVED[31:6]	26
0xBB00F05C	L3_PORT_IP_ROUTE_CTRL [17].URPF_FAIL_ACT[5:3]	3
0xBB00F05C	L3_PORT_IP_ROUTE_CTRL [17].URPF_CHK_MODE[2:2]	1
0xBB00F05C	L3_PORT_IP_ROUTE_CTRL [17].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F05C	L3_PORT_IP_ROUTE_CTRL [17].URPF_CHK_EN[0:0]	1
0xBB00F060	L3_PORT_IP_ROUTE_CTRL [18].RESERVED[31:6]	26
0xBB00F060	L3_PORT_IP_ROUTE_CTRL [18].URPF_FAIL_ACT[5:3]	3
0xBB00F060	L3_PORT_IP_ROUTE_CTRL [18].URPF_CHK_MODE[2:2]	1
0xBB00F060	L3_PORT_IP_ROUTE_CTRL [18].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F060	L3_PORT_IP_ROUTE_CTRL [18].URPF_CHK_EN[0:0]	1
0xBB00F064	L3_PORT_IP_ROUTE_CTRL [19].RESERVED[31:6]	26
0xBB00F064	L3_PORT_IP_ROUTE_CTRL [19].URPF_FAIL_ACT[5:3]	3
0xBB00F064	L3_PORT_IP_ROUTE_CTRL [19].URPF_CHK_MODE[2:2]	1
0xBB00F064	L3_PORT_IP_ROUTE_CTRL [19].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F064	L3_PORT_IP_ROUTE_CTRL [19].URPF_CHK_EN[0:0]	1
0xBB00F068	L3_PORT_IP_ROUTE_CTRL [20].RESERVED[31:6]	26
0xBB00F068	L3_PORT_IP_ROUTE_CTRL [20].URPF_FAIL_ACT[5:3]	3
0xBB00F068	L3_PORT_IP_ROUTE_CTRL [20].URPF_CHK_MODE[2:2]	1
0xBB00F068	L3_PORT_IP_ROUTE_CTRL [20].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F068	L3_PORT_IP_ROUTE_CTRL [20].URPF_CHK_EN[0:0]	1
0xBB00F06C	L3_PORT_IP_ROUTE_CTRL [21].RESERVED[31:6]	26
0xBB00F06C	L3_PORT_IP_ROUTE_CTRL [21].URPF_FAIL_ACT[5:3]	3
0xBB00F06C	L3_PORT_IP_ROUTE_CTRL [21].URPF_CHK_MODE[2:2]	1
0xBB00F06C	L3_PORT_IP_ROUTE_CTRL [21].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F06C	L3_PORT_IP_ROUTE_CTRL [21].URPF_CHK_EN[0:0]	1
0xBB00F070	L3_PORT_IP_ROUTE_CTRL [22].RESERVED[31:6]	26
0xBB00F070	L3_PORT_IP_ROUTE_CTRL [22].URPF_FAIL_ACT[5:3]	3
0xBB00F070	L3_PORT_IP_ROUTE_CTRL [22].URPF_CHK_MODE[2:2]	1
0xBB00F070	L3_PORT_IP_ROUTE_CTRL [22].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F070	L3_PORT_IP_ROUTE_CTRL [22].URPF_CHK_EN[0:0]	1
0xBB00F074	L3_PORT_IP_ROUTE_CTRL [23].RESERVED[31:6]	26
0xBB00F074	L3_PORT_IP_ROUTE_CTRL [23].URPF_FAIL_ACT[5:3]	3
0xBB00F074	L3_PORT_IP_ROUTE_CTRL [23].URPF_CHK_MODE[2:2]	1
0xBB00F074	L3_PORT_IP_ROUTE_CTRL [23].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F074	L3_PORT_IP_ROUTE_CTRL [23].URPF_CHK_EN[0:0]	1
0xBB00F078	L3_PORT_IP_ROUTE_CTRL [24].RESERVED[31:6]	26
0xBB00F078	L3_PORT_IP_ROUTE_CTRL [24].URPF_FAIL_ACT[5:3]	3
0xBB00F078	L3_PORT_IP_ROUTE_CTRL [24].URPF_CHK_MODE[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F078	L3_PORT_IP_ROUTE_CTRL [24].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F078	L3_PORT_IP_ROUTE_CTRL [24].URPF_CHK_EN[0:0]	1
0xBB00F07C	L3_PORT_IP_ROUTE_CTRL [25].RESERVED[31:6]	26
0xBB00F07C	L3_PORT_IP_ROUTE_CTRL [25].URPF_FAIL_ACT[5:3]	3
0xBB00F07C	L3_PORT_IP_ROUTE_CTRL [25].URPF_CHK_MODE[2:2]	1
0xBB00F07C	L3_PORT_IP_ROUTE_CTRL [25].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F07C	L3_PORT_IP_ROUTE_CTRL [25].URPF_CHK_EN[0:0]	1
0xBB00F080	L3_PORT_IP_ROUTE_CTRL [26].RESERVED[31:6]	26
0xBB00F080	L3_PORT_IP_ROUTE_CTRL [26].URPF_FAIL_ACT[5:3]	3
0xBB00F080	L3_PORT_IP_ROUTE_CTRL [26].URPF_CHK_MODE[2:2]	1
0xBB00F080	L3_PORT_IP_ROUTE_CTRL [26].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F080	L3_PORT_IP_ROUTE_CTRL [26].URPF_CHK_EN[0:0]	1
0xBB00F084	L3_PORT_IP_ROUTE_CTRL [27].RESERVED[31:6]	26
0xBB00F084	L3_PORT_IP_ROUTE_CTRL [27].URPF_FAIL_ACT[5:3]	3
0xBB00F084	L3_PORT_IP_ROUTE_CTRL [27].URPF_CHK_MODE[2:2]	1
0xBB00F084	L3_PORT_IP_ROUTE_CTRL [27].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F084	L3_PORT_IP_ROUTE_CTRL [27].URPF_CHK_EN[0:0]	1
0xBB00F088	L3_PORT_IP_ROUTE_CTRL [28].RESERVED[31:6]	26
0xBB00F088	L3_PORT_IP_ROUTE_CTRL [28].URPF_FAIL_ACT[5:3]	3
0xBB00F088	L3_PORT_IP_ROUTE_CTRL [28].URPF_CHK_MODE[2:2]	1
0xBB00F088	L3_PORT_IP_ROUTE_CTRL [28].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F088	L3_PORT_IP_ROUTE_CTRL [28].URPF_CHK_EN[0:0]	1
0xBB00F08C	L3_PORT_IP_ROUTE_CTRL [29].RESERVED[31:6]	26
0xBB00F08C	L3_PORT_IP_ROUTE_CTRL [29].URPF_FAIL_ACT[5:3]	3
0xBB00F08C	L3_PORT_IP_ROUTE_CTRL [29].URPF_CHK_MODE[2:2]	1
0xBB00F08C	L3_PORT_IP_ROUTE_CTRL [29].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F08C	L3_PORT_IP_ROUTE_CTRL [29].URPF_CHK_EN[0:0]	1
0xBB00F090	L3_PORT_IP_ROUTE_CTRL [30].RESERVED[31:6]	26
0xBB00F090	L3_PORT_IP_ROUTE_CTRL [30].URPF_FAIL_ACT[5:3]	3
0xBB00F090	L3_PORT_IP_ROUTE_CTRL [30].URPF_CHK_MODE[2:2]	1
0xBB00F090	L3_PORT_IP_ROUTE_CTRL [30].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F090	L3_PORT_IP_ROUTE_CTRL [30].URPF_CHK_EN[0:0]	1
0xBB00F094	L3_PORT_IP_ROUTE_CTRL [31].RESERVED[31:6]	26
0xBB00F094	L3_PORT_IP_ROUTE_CTRL [31].URPF_FAIL_ACT[5:3]	3
0xBB00F094	L3_PORT_IP_ROUTE_CTRL [31].URPF_CHK_MODE[2:2]	1
0xBB00F094	L3_PORT_IP_ROUTE_CTRL [31].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F094	L3_PORT_IP_ROUTE_CTRL [31].URPF_CHK_EN[0:0]	1
0xBB00F098	L3_PORT_IP_ROUTE_CTRL [32].RESERVED[31:6]	26
0xBB00F098	L3_PORT_IP_ROUTE_CTRL [32].URPF_FAIL_ACT[5:3]	3
0xBB00F098	L3_PORT_IP_ROUTE_CTRL [32].URPF_CHK_MODE[2:2]	1
0xBB00F098	L3_PORT_IP_ROUTE_CTRL [32].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F098	L3_PORT_IP_ROUTE_CTRL [32].URPF_CHK_EN[0:0]	1
0xBB00F09C	L3_PORT_IP_ROUTE_CTRL [33].RESERVED[31:6]	26
0xBB00F09C	L3_PORT_IP_ROUTE_CTRL [33].URPF_FAIL_ACT[5:3]	3
0xBB00F09C	L3_PORT_IP_ROUTE_CTRL [33].URPF_CHK_MODE[2:2]	1
0xBB00F09C	L3_PORT_IP_ROUTE_CTRL [33].URPF_DFLT_ROUTE_EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F09C	L3_PORT_IP_ROUTE_CTRL [33].URPF_CHK_EN[0:0]	1
0xBB00F0A0	L3_PORT_IP_ROUTE_CTRL [34].RESERVED[31:6]	26
0xBB00F0A0	L3_PORT_IP_ROUTE_CTRL [34].URPF_FAIL_ACT[5:3]	3
0xBB00F0A0	L3_PORT_IP_ROUTE_CTRL [34].URPF_CHK_MODE[2:2]	1
0xBB00F0A0	L3_PORT_IP_ROUTE_CTRL [34].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0A0	L3_PORT_IP_ROUTE_CTRL [34].URPF_CHK_EN[0:0]	1
0xBB00F0A4	L3_PORT_IP_ROUTE_CTRL [35].RESERVED[31:6]	26
0xBB00F0A4	L3_PORT_IP_ROUTE_CTRL [35].URPF_FAIL_ACT[5:3]	3
0xBB00F0A4	L3_PORT_IP_ROUTE_CTRL [35].URPF_CHK_MODE[2:2]	1
0xBB00F0A4	L3_PORT_IP_ROUTE_CTRL [35].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0A4	L3_PORT_IP_ROUTE_CTRL [35].URPF_CHK_EN[0:0]	1
0xBB00F0A8	L3_PORT_IP_ROUTE_CTRL [36].RESERVED[31:6]	26
0xBB00F0A8	L3_PORT_IP_ROUTE_CTRL [36].URPF_FAIL_ACT[5:3]	3
0xBB00F0A8	L3_PORT_IP_ROUTE_CTRL [36].URPF_CHK_MODE[2:2]	1
0xBB00F0A8	L3_PORT_IP_ROUTE_CTRL [36].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0A8	L3_PORT_IP_ROUTE_CTRL [36].URPF_CHK_EN[0:0]	1
0xBB00F0AC	L3_PORT_IP_ROUTE_CTRL [37].RESERVED[31:6]	26
0xBB00F0AC	L3_PORT_IP_ROUTE_CTRL [37].URPF_FAIL_ACT[5:3]	3
0xBB00F0AC	L3_PORT_IP_ROUTE_CTRL [37].URPF_CHK_MODE[2:2]	1
0xBB00F0AC	L3_PORT_IP_ROUTE_CTRL [37].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0AC	L3_PORT_IP_ROUTE_CTRL [37].URPF_CHK_EN[0:0]	1
0xBB00F0B0	L3_PORT_IP_ROUTE_CTRL [38].RESERVED[31:6]	26
0xBB00F0B0	L3_PORT_IP_ROUTE_CTRL [38].URPF_FAIL_ACT[5:3]	3
0xBB00F0B0	L3_PORT_IP_ROUTE_CTRL [38].URPF_CHK_MODE[2:2]	1
0xBB00F0B0	L3_PORT_IP_ROUTE_CTRL [38].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0B0	L3_PORT_IP_ROUTE_CTRL [38].URPF_CHK_EN[0:0]	1
0xBB00F0B4	L3_PORT_IP_ROUTE_CTRL [39].RESERVED[31:6]	26
0xBB00F0B4	L3_PORT_IP_ROUTE_CTRL [39].URPF_FAIL_ACT[5:3]	3
0xBB00F0B4	L3_PORT_IP_ROUTE_CTRL [39].URPF_CHK_MODE[2:2]	1
0xBB00F0B4	L3_PORT_IP_ROUTE_CTRL [39].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0B4	L3_PORT_IP_ROUTE_CTRL [39].URPF_CHK_EN[0:0]	1
0xBB00F0B8	L3_PORT_IP_ROUTE_CTRL [40].RESERVED[31:6]	26
0xBB00F0B8	L3_PORT_IP_ROUTE_CTRL [40].URPF_FAIL_ACT[5:3]	3
0xBB00F0B8	L3_PORT_IP_ROUTE_CTRL [40].URPF_CHK_MODE[2:2]	1
0xBB00F0B8	L3_PORT_IP_ROUTE_CTRL [40].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0B8	L3_PORT_IP_ROUTE_CTRL [40].URPF_CHK_EN[0:0]	1
0xBB00F0BC	L3_PORT_IP_ROUTE_CTRL [41].RESERVED[31:6]	26
0xBB00F0BC	L3_PORT_IP_ROUTE_CTRL [41].URPF_FAIL_ACT[5:3]	3
0xBB00F0BC	L3_PORT_IP_ROUTE_CTRL [41].URPF_CHK_MODE[2:2]	1
0xBB00F0BC	L3_PORT_IP_ROUTE_CTRL [41].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0BC	L3_PORT_IP_ROUTE_CTRL [41].URPF_CHK_EN[0:0]	1
0xBB00F0C0	L3_PORT_IP_ROUTE_CTRL [42].RESERVED[31:6]	26
0xBB00F0C0	L3_PORT_IP_ROUTE_CTRL [42].URPF_FAIL_ACT[5:3]	3
0xBB00F0C0	L3_PORT_IP_ROUTE_CTRL [42].URPF_CHK_MODE[2:2]	1
0xBB00F0C0	L3_PORT_IP_ROUTE_CTRL [42].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0C0	L3_PORT_IP_ROUTE_CTRL [42].URPF_CHK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F0C4	L3_PORT_IP_ROUTE_CTRL [43].RESERVED[31:6]	26
0xBB00F0C4	L3_PORT_IP_ROUTE_CTRL [43].URPF_FAIL_ACT[5:3]	3
0xBB00F0C4	L3_PORT_IP_ROUTE_CTRL [43].URPF_CHK_MODE[2:2]	1
0xBB00F0C4	L3_PORT_IP_ROUTE_CTRL [43].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0C4	L3_PORT_IP_ROUTE_CTRL [43].URPF_CHK_EN[0:0]	1
0xBB00F0C8	L3_PORT_IP_ROUTE_CTRL [44].RESERVED[31:6]	26
0xBB00F0C8	L3_PORT_IP_ROUTE_CTRL [44].URPF_FAIL_ACT[5:3]	3
0xBB00F0C8	L3_PORT_IP_ROUTE_CTRL [44].URPF_CHK_MODE[2:2]	1
0xBB00F0C8	L3_PORT_IP_ROUTE_CTRL [44].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0C8	L3_PORT_IP_ROUTE_CTRL [44].URPF_CHK_EN[0:0]	1
0xBB00F0CC	L3_PORT_IP_ROUTE_CTRL [45].RESERVED[31:6]	26
0xBB00F0CC	L3_PORT_IP_ROUTE_CTRL [45].URPF_FAIL_ACT[5:3]	3
0xBB00F0CC	L3_PORT_IP_ROUTE_CTRL [45].URPF_CHK_MODE[2:2]	1
0xBB00F0CC	L3_PORT_IP_ROUTE_CTRL [45].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0CC	L3_PORT_IP_ROUTE_CTRL [45].URPF_CHK_EN[0:0]	1
0xBB00F0D0	L3_PORT_IP_ROUTE_CTRL [46].RESERVED[31:6]	26
0xBB00F0D0	L3_PORT_IP_ROUTE_CTRL [46].URPF_FAIL_ACT[5:3]	3
0xBB00F0D0	L3_PORT_IP_ROUTE_CTRL [46].URPF_CHK_MODE[2:2]	1
0xBB00F0D0	L3_PORT_IP_ROUTE_CTRL [46].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0D0	L3_PORT_IP_ROUTE_CTRL [46].URPF_CHK_EN[0:0]	1
0xBB00F0D4	L3_PORT_IP_ROUTE_CTRL [47].RESERVED[31:6]	26
0xBB00F0D4	L3_PORT_IP_ROUTE_CTRL [47].URPF_FAIL_ACT[5:3]	3
0xBB00F0D4	L3_PORT_IP_ROUTE_CTRL [47].URPF_CHK_MODE[2:2]	1
0xBB00F0D4	L3_PORT_IP_ROUTE_CTRL [47].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0D4	L3_PORT_IP_ROUTE_CTRL [47].URPF_CHK_EN[0:0]	1
0xBB00F0D8	L3_PORT_IP_ROUTE_CTRL [48].RESERVED[31:6]	26
0xBB00F0D8	L3_PORT_IP_ROUTE_CTRL [48].URPF_FAIL_ACT[5:3]	3
0xBB00F0D8	L3_PORT_IP_ROUTE_CTRL [48].URPF_CHK_MODE[2:2]	1
0xBB00F0D8	L3_PORT_IP_ROUTE_CTRL [48].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0D8	L3_PORT_IP_ROUTE_CTRL [48].URPF_CHK_EN[0:0]	1
0xBB00F0DC	L3_PORT_IP_ROUTE_CTRL [49].RESERVED[31:6]	26
0xBB00F0DC	L3_PORT_IP_ROUTE_CTRL [49].URPF_FAIL_ACT[5:3]	3
0xBB00F0DC	L3_PORT_IP_ROUTE_CTRL [49].URPF_CHK_MODE[2:2]	1
0xBB00F0DC	L3_PORT_IP_ROUTE_CTRL [49].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0DC	L3_PORT_IP_ROUTE_CTRL [49].URPF_CHK_EN[0:0]	1
0xBB00F0E0	L3_PORT_IP_ROUTE_CTRL [50].RESERVED[31:6]	26
0xBB00F0E0	L3_PORT_IP_ROUTE_CTRL [50].URPF_FAIL_ACT[5:3]	3
0xBB00F0E0	L3_PORT_IP_ROUTE_CTRL [50].URPF_CHK_MODE[2:2]	1
0xBB00F0E0	L3_PORT_IP_ROUTE_CTRL [50].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0E0	L3_PORT_IP_ROUTE_CTRL [50].URPF_CHK_EN[0:0]	1
0xBB00F0E4	L3_PORT_IP_ROUTE_CTRL [51].RESERVED[31:6]	26
0xBB00F0E4	L3_PORT_IP_ROUTE_CTRL [51].URPF_FAIL_ACT[5:3]	3
0xBB00F0E4	L3_PORT_IP_ROUTE_CTRL [51].URPF_CHK_MODE[2:2]	1
0xBB00F0E4	L3_PORT_IP_ROUTE_CTRL [51].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0E4	L3_PORT_IP_ROUTE_CTRL [51].URPF_CHK_EN[0:0]	1
0xBB00F0E8	L3_PORT_IP_ROUTE_CTRL [52].RESERVED[31:6]	26

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F0E8	L3_PORT_IP_ROUTE_CTRL [52].URPF_FAIL_ACT[5:3]	3
0xBB00F0E8	L3_PORT_IP_ROUTE_CTRL [52].URPF_CHK_MODE[2:2]	1
0xBB00F0E8	L3_PORT_IP_ROUTE_CTRL [52].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0E8	L3_PORT_IP_ROUTE_CTRL [52].URPF_CHK_EN[0:0]	1
0xBB00F0EC	L3_PORT_IP_ROUTE_CTRL [53].RESERVED[31:6]	26
0xBB00F0EC	L3_PORT_IP_ROUTE_CTRL [53].URPF_FAIL_ACT[5:3]	3
0xBB00F0EC	L3_PORT_IP_ROUTE_CTRL [53].URPF_CHK_MODE[2:2]	1
0xBB00F0EC	L3_PORT_IP_ROUTE_CTRL [53].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0EC	L3_PORT_IP_ROUTE_CTRL [53].URPF_CHK_EN[0:0]	1
0xBB00F0F0	L3_PORT_IP_ROUTE_CTRL [54].RESERVED[31:6]	26
0xBB00F0F0	L3_PORT_IP_ROUTE_CTRL [54].URPF_FAIL_ACT[5:3]	3
0xBB00F0F0	L3_PORT_IP_ROUTE_CTRL [54].URPF_CHK_MODE[2:2]	1
0xBB00F0F0	L3_PORT_IP_ROUTE_CTRL [54].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0F0	L3_PORT_IP_ROUTE_CTRL [54].URPF_CHK_EN[0:0]	1
0xBB00F0F4	L3_PORT_IP_ROUTE_CTRL [55].RESERVED[31:6]	26
0xBB00F0F4	L3_PORT_IP_ROUTE_CTRL [55].URPF_FAIL_ACT[5:3]	3
0xBB00F0F4	L3_PORT_IP_ROUTE_CTRL [55].URPF_CHK_MODE[2:2]	1
0xBB00F0F4	L3_PORT_IP_ROUTE_CTRL [55].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0F4	L3_PORT_IP_ROUTE_CTRL [55].URPF_CHK_EN[0:0]	1
0xBB00F0F8	L3_PORT_IP_ROUTE_CTRL [56].RESERVED[31:6]	26
0xBB00F0F8	L3_PORT_IP_ROUTE_CTRL [56].URPF_FAIL_ACT[5:3]	3
0xBB00F0F8	L3_PORT_IP_ROUTE_CTRL [56].URPF_CHK_MODE[2:2]	1
0xBB00F0F8	L3_PORT_IP_ROUTE_CTRL [56].URPF_DFLT_ROUTE_EN[1:1]	1
0xBB00F0F8	L3_PORT_IP_ROUTE_CTRL [56].URPF_CHK_EN[0:0]	1
0xBB000600	LED_GLB_CTRL.RESERVED[31:25]	7
0xBB000600	LED_GLB_CTRL.LED_STREAM_XCHG[24:24]	1
0xBB000600	LED_GLB_CTRL.LEDIC_RESTART[23:23]	1
0xBB000600	LED_GLB_CTRL.LEDIC_PAGE_ACCESS[22:22]	1
0xBB000600	LED_GLB_CTRL.LED_ACTIVE[21:21]	1
0xBB000600	LED_GLB_CTRL.LED_SIGNAL_INVERT[20:20]	1
0xBB000600	LED_GLB_CTRL.BLINK_TIME_SEL[19:17]	3
0xBB000600	LED_GLB_CTRL.LED_CLK_SEL[16:15]	2
0xBB000600	LED_GLB_CTRL.RESERVED[14:14]	1
0xBB000600	LED_GLB_CTRL.SYS_LED_MODE[13:12]	2
0xBB000600	LED_GLB_CTRL.STP2_PWR_ON_LED[11:8]	4
0xBB000600	LED_GLB_CTRL.STP1_PWR_ON_LED[7:4]	4
0xBB000600	LED_GLB_CTRL.PWR_ON_BLINK_SEL[3:2]	2
0xBB000600	LED_GLB_CTRL.LED_MOD[1:0]	2
0xBB00076C	LED_INDRT_ACCESS_CTRL.RESERVED[31:28]	4
0xBB00076C	LED_INDRT_ACCESS_CTRL.DATA[27:12]	16
0xBB00076C	LED_INDRT_ACCESS_CTRL.REG[11:7]	5
0xBB00076C	LED_INDRT_ACCESS_CTRL.PHYADDR[6:2]	5
0xBB00076C	LED_INDRT_ACCESS_CTRL.RWOP[1:1]	1
0xBB00076C	LED_INDRT_ACCESS_CTRL.CMD[0:0]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [0].LED_COMBO_PMASK[0:0]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [1].LED_COMBO_PMASK[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000664	LED_PORT_COMBO_MASK_CTRL [2].LED_COMBO_PMASK[2:2]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [3].LED_COMBO_PMASK[3:3]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [4].LED_COMBO_PMASK[4:4]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [5].LED_COMBO_PMASK[5:5]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [6].LED_COMBO_PMASK[6:6]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [7].LED_COMBO_PMASK[7:7]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [8].LED_COMBO_PMASK[8:8]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [9].LED_COMBO_PMASK[9:9]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [10].LED_COMBO_PMASK[10:10]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [11].LED_COMBO_PMASK[11:11]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [12].LED_COMBO_PMASK[12:12]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [13].LED_COMBO_PMASK[13:13]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [14].LED_COMBO_PMASK[14:14]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [15].LED_COMBO_PMASK[15:15]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [16].LED_COMBO_PMASK[16:16]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [17].LED_COMBO_PMASK[17:17]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [18].LED_COMBO_PMASK[18:18]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [19].LED_COMBO_PMASK[19:19]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [20].LED_COMBO_PMASK[20:20]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [21].LED_COMBO_PMASK[21:21]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [22].LED_COMBO_PMASK[22:22]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [23].LED_COMBO_PMASK[23:23]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [24].LED_COMBO_PMASK[24:24]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [25].LED_COMBO_PMASK[25:25]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [26].LED_COMBO_PMASK[26:26]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [27].LED_COMBO_PMASK[27:27]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [28].LED_COMBO_PMASK[28:28]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [29].LED_COMBO_PMASK[29:29]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [30].LED_COMBO_PMASK[30:30]	1
0xBB000664	LED_PORT_COMBO_MASK_CTRL [31].LED_COMBO_PMASK[31:31]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [32].LED_COMBO_PMASK[0:0]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [33].LED_COMBO_PMASK[1:1]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [34].LED_COMBO_PMASK[2:2]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [35].LED_COMBO_PMASK[3:3]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [36].LED_COMBO_PMASK[4:4]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [37].LED_COMBO_PMASK[5:5]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [38].LED_COMBO_PMASK[6:6]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [39].LED_COMBO_PMASK[7:7]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [40].LED_COMBO_PMASK[8:8]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [41].LED_COMBO_PMASK[9:9]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [42].LED_COMBO_PMASK[10:10]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [43].LED_COMBO_PMASK[11:11]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [44].LED_COMBO_PMASK[12:12]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [45].LED_COMBO_PMASK[13:13]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [46].LED_COMBO_PMASK[14:14]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [47].LED_COMBO_PMASK[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000668	LED_PORT_COMBO_MASK_CTRL [48].LED_COMBO_PMASK[16:16]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [49].LED_COMBO_PMASK[17:17]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [50].LED_COMBO_PMASK[18:18]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [51].LED_COMBO_PMASK[19:19]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [52].LED_COMBO_PMASK[20:20]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [53].LED_COMBO_PMASK[21:21]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [54].LED_COMBO_PMASK[22:22]	1
0xBB000668	LED_PORT_COMBO_MASK_CTRL [55].LED_COMBO_PMASK[23:23]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [0].LED_COPR_PMASK[0:0]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [1].LED_COPR_PMASK[1:1]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [2].LED_COPR_PMASK[2:2]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [3].LED_COPR_PMASK[3:3]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [4].LED_COPR_PMASK[4:4]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [5].LED_COPR_PMASK[5:5]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [6].LED_COPR_PMASK[6:6]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [7].LED_COPR_PMASK[7:7]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [8].LED_COPR_PMASK[8:8]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [9].LED_COPR_PMASK[9:9]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [10].LED_COPR_PMASK[10:10]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [11].LED_COPR_PMASK[11:11]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [12].LED_COPR_PMASK[12:12]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [13].LED_COPR_PMASK[13:13]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [14].LED_COPR_PMASK[14:14]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [15].LED_COPR_PMASK[15:15]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [16].LED_COPR_PMASK[16:16]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [17].LED_COPR_PMASK[17:17]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [18].LED_COPR_PMASK[18:18]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [19].LED_COPR_PMASK[19:19]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [20].LED_COPR_PMASK[20:20]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [21].LED_COPR_PMASK[21:21]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [22].LED_COPR_PMASK[22:22]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [23].LED_COPR_PMASK[23:23]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [24].LED_COPR_PMASK[24:24]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [25].LED_COPR_PMASK[25:25]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [26].LED_COPR_PMASK[26:26]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [27].LED_COPR_PMASK[27:27]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [28].LED_COPR_PMASK[28:28]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [29].LED_COPR_PMASK[29:29]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [30].LED_COPR_PMASK[30:30]	1
0xBB000654	LED_PORT_COPR_MASK_CTRL [31].LED_COPR_PMASK[31:31]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [32].LED_COPR_PMASK[0:0]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [33].LED_COPR_PMASK[1:1]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [34].LED_COPR_PMASK[2:2]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [35].LED_COPR_PMASK[3:3]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [36].LED_COPR_PMASK[4:4]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [37].LED_COPR_PMASK[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000658	LED_PORT_COPR_MASK_CTRL [38].LED_COPR_PMASK[6:6]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [39].LED_COPR_PMASK[7:7]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [40].LED_COPR_PMASK[8:8]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [41].LED_COPR_PMASK[9:9]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [42].LED_COPR_PMASK[10:10]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [43].LED_COPR_PMASK[11:11]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [44].LED_COPR_PMASK[12:12]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [45].LED_COPR_PMASK[13:13]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [46].LED_COPR_PMASK[14:14]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [47].LED_COPR_PMASK[15:15]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [48].LED_COPR_PMASK[16:16]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [49].LED_COPR_PMASK[17:17]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [50].LED_COPR_PMASK[18:18]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [51].LED_COPR_PMASK[19:19]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [52].LED_COPR_PMASK[20:20]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [53].LED_COPR_PMASK[21:21]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [54].LED_COPR_PMASK[22:22]	1
0xBB000658	LED_PORT_COPR_MASK_CTRL [55].LED_COPR_PMASK[23:23]	1
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [0].LED_COPR_SET_PSEL[1:0]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [1].LED_COPR_SET_PSEL[3:2]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [2].LED_COPR_SET_PSEL[5:4]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [3].LED_COPR_SET_PSEL[7:6]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [4].LED_COPR_SET_PSEL[9:8]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [5].LED_COPR_SET_PSEL[11:10]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [6].LED_COPR_SET_PSEL[13:12]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [7].LED_COPR_SET_PSEL[15:14]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [8].LED_COPR_SET_PSEL[17:16]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [9].LED_COPR_SET_PSEL[19:18]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [10].LED_COPR_SET_PSEL[21:20]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [11].LED_COPR_SET_PSEL[23:22]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [12].LED_COPR_SET_PSEL[25:24]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [13].LED_COPR_SET_PSEL[27:26]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [14].LED_COPR_SET_PSEL[29:28]	2
0xBB000634	LED_PORT_COPR_SET_SEL_CTRL [15].LED_COPR_SET_PSEL[31:30]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [16].LED_COPR_SET_PSEL[1:0]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [17].LED_COPR_SET_PSEL[3:2]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [18].LED_COPR_SET_PSEL[5:4]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [19].LED_COPR_SET_PSEL[7:6]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [20].LED_COPR_SET_PSEL[9:8]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [21].LED_COPR_SET_PSEL[11:10]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [22].LED_COPR_SET_PSEL[13:12]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [23].LED_COPR_SET_PSEL[15:14]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [24].LED_COPR_SET_PSEL[17:16]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [25].LED_COPR_SET_PSEL[19:18]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [26].LED_COPR_SET_PSEL[21:20]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [27].LED_COPR_SET_PSEL[23:22]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [28].LED_COPR_SET_PSEL[25:24]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [29].LED_COPR_SET_PSEL[27:26]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [30].LED_COPR_SET_PSEL[29:28]	2
0xBB000638	LED_PORT_COPR_SET_SEL_CTRL [31].LED_COPR_SET_PSEL[31:30]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [32].LED_COPR_SET_PSEL[1:0]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [33].LED_COPR_SET_PSEL[3:2]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [34].LED_COPR_SET_PSEL[5:4]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [35].LED_COPR_SET_PSEL[7:6]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [36].LED_COPR_SET_PSEL[9:8]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [37].LED_COPR_SET_PSEL[11:10]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [38].LED_COPR_SET_PSEL[13:12]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [39].LED_COPR_SET_PSEL[15:14]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [40].LED_COPR_SET_PSEL[17:16]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [41].LED_COPR_SET_PSEL[19:18]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [42].LED_COPR_SET_PSEL[21:20]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [43].LED_COPR_SET_PSEL[23:22]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [44].LED_COPR_SET_PSEL[25:24]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [45].LED_COPR_SET_PSEL[27:26]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [46].LED_COPR_SET_PSEL[29:28]	2
0xBB00063C	LED_PORT_COPR_SET_SEL_CTRL [47].LED_COPR_SET_PSEL[31:30]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [48].LED_COPR_SET_PSEL[1:0]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [49].LED_COPR_SET_PSEL[3:2]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [50].LED_COPR_SET_PSEL[5:4]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [51].LED_COPR_SET_PSEL[7:6]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [52].LED_COPR_SET_PSEL[9:8]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [53].LED_COPR_SET_PSEL[11:10]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [54].LED_COPR_SET_PSEL[13:12]	2
0xBB000640	LED_PORT_COPR_SET_SEL_CTRL [55].LED_COPR_SET_PSEL[15:14]	2
0xBB00065C	LED_PORT_FIB_MASK_CTRL [0].LED_FIB_PMASK[0:0]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [1].LED_FIB_PMASK[1:1]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [2].LED_FIB_PMASK[2:2]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [3].LED_FIB_PMASK[3:3]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [4].LED_FIB_PMASK[4:4]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [5].LED_FIB_PMASK[5:5]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [6].LED_FIB_PMASK[6:6]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [7].LED_FIB_PMASK[7:7]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [8].LED_FIB_PMASK[8:8]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [9].LED_FIB_PMASK[9:9]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [10].LED_FIB_PMASK[10:10]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [11].LED_FIB_PMASK[11:11]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [12].LED_FIB_PMASK[12:12]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [13].LED_FIB_PMASK[13:13]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [14].LED_FIB_PMASK[14:14]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [15].LED_FIB_PMASK[15:15]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [16].LED_FIB_PMASK[16:16]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL [17].LED_FIB_PMASK[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00065C	LED_PORT_FIB_MASK_CTRL[18].LED_FIB_PMASK[18:18]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[19].LED_FIB_PMASK[19:19]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[20].LED_FIB_PMASK[20:20]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[21].LED_FIB_PMASK[21:21]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[22].LED_FIB_PMASK[22:22]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[23].LED_FIB_PMASK[23:23]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[24].LED_FIB_PMASK[24:24]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[25].LED_FIB_PMASK[25:25]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[26].LED_FIB_PMASK[26:26]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[27].LED_FIB_PMASK[27:27]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[28].LED_FIB_PMASK[28:28]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[29].LED_FIB_PMASK[29:29]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[30].LED_FIB_PMASK[30:30]	1
0xBB00065C	LED_PORT_FIB_MASK_CTRL[31].LED_FIB_PMASK[31:31]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[32].LED_FIB_PMASK[0:0]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[33].LED_FIB_PMASK[1:1]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[34].LED_FIB_PMASK[2:2]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[35].LED_FIB_PMASK[3:3]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[36].LED_FIB_PMASK[4:4]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[37].LED_FIB_PMASK[5:5]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[38].LED_FIB_PMASK[6:6]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[39].LED_FIB_PMASK[7:7]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[40].LED_FIB_PMASK[8:8]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[41].LED_FIB_PMASK[9:9]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[42].LED_FIB_PMASK[10:10]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[43].LED_FIB_PMASK[11:11]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[44].LED_FIB_PMASK[12:12]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[45].LED_FIB_PMASK[13:13]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[46].LED_FIB_PMASK[14:14]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[47].LED_FIB_PMASK[15:15]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[48].LED_FIB_PMASK[16:16]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[49].LED_FIB_PMASK[17:17]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[50].LED_FIB_PMASK[18:18]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[51].LED_FIB_PMASK[19:19]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[52].LED_FIB_PMASK[20:20]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[53].LED_FIB_PMASK[21:21]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[54].LED_FIB_PMASK[22:22]	1
0xBB000660	LED_PORT_FIB_MASK_CTRL[55].LED_FIB_PMASK[23:23]	1
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[0].LED_FIB_SET_PSEL[1:0]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[1].LED_FIB_SET_PSEL[3:2]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[2].LED_FIB_SET_PSEL[5:4]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[3].LED_FIB_SET_PSEL[7:6]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[4].LED_FIB_SET_PSEL[9:8]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[5].LED_FIB_SET_PSEL[11:10]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[6].LED_FIB_SET_PSEL[13:12]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL[7].LED_FIB_SET_PSEL[15:14]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [8].LED_FIB_SET_PSEL[17:16]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [9].LED_FIB_SET_PSEL[19:18]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [10].LED_FIB_SET_PSEL[21:20]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [11].LED_FIB_SET_PSEL[23:22]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [12].LED_FIB_SET_PSEL[25:24]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [13].LED_FIB_SET_PSEL[27:26]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [14].LED_FIB_SET_PSEL[29:28]	2
0xBB000644	LED_PORT_FIB_SET_SEL_CTRL [15].LED_FIB_SET_PSEL[31:30]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [16].LED_FIB_SET_PSEL[1:0]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [17].LED_FIB_SET_PSEL[3:2]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [18].LED_FIB_SET_PSEL[5:4]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [19].LED_FIB_SET_PSEL[7:6]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [20].LED_FIB_SET_PSEL[9:8]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [21].LED_FIB_SET_PSEL[11:10]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [22].LED_FIB_SET_PSEL[13:12]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [23].LED_FIB_SET_PSEL[15:14]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [24].LED_FIB_SET_PSEL[17:16]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [25].LED_FIB_SET_PSEL[19:18]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [26].LED_FIB_SET_PSEL[21:20]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [27].LED_FIB_SET_PSEL[23:22]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [28].LED_FIB_SET_PSEL[25:24]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [29].LED_FIB_SET_PSEL[27:26]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [30].LED_FIB_SET_PSEL[29:28]	2
0xBB000648	LED_PORT_FIB_SET_SEL_CTRL [31].LED_FIB_SET_PSEL[31:30]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [32].LED_FIB_SET_PSEL[1:0]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [33].LED_FIB_SET_PSEL[3:2]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [34].LED_FIB_SET_PSEL[5:4]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [35].LED_FIB_SET_PSEL[7:6]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [36].LED_FIB_SET_PSEL[9:8]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [37].LED_FIB_SET_PSEL[11:10]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [38].LED_FIB_SET_PSEL[13:12]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [39].LED_FIB_SET_PSEL[15:14]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [40].LED_FIB_SET_PSEL[17:16]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [41].LED_FIB_SET_PSEL[19:18]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [42].LED_FIB_SET_PSEL[21:20]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [43].LED_FIB_SET_PSEL[23:22]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [44].LED_FIB_SET_PSEL[25:24]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [45].LED_FIB_SET_PSEL[27:26]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [46].LED_FIB_SET_PSEL[29:28]	2
0xBB00064C	LED_PORT_FIB_SET_SEL_CTRL [47].LED_FIB_SET_PSEL[31:30]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [48].LED_FIB_SET_PSEL[1:0]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [49].LED_FIB_SET_PSEL[3:2]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [50].LED_FIB_SET_PSEL[5:4]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [51].LED_FIB_SET_PSEL[7:6]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [52].LED_FIB_SET_PSEL[9:8]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [53].LED_FIB_SET_PSEL[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [54].LED_FIB_SET_PSEL[13:12]	2
0xBB000650	LED_PORT_FIB_SET_SEL_CTRL [55].LED_FIB_SET_PSEL[15:14]	2
0xBB000604	LED_PORT_NUM_CTRL [0].LED_NUM_SEL[1:0]	2
0xBB000604	LED_PORT_NUM_CTRL [1].LED_NUM_SEL[3:2]	2
0xBB000604	LED_PORT_NUM_CTRL [2].LED_NUM_SEL[5:4]	2
0xBB000604	LED_PORT_NUM_CTRL [3].LED_NUM_SEL[7:6]	2
0xBB000604	LED_PORT_NUM_CTRL [4].LED_NUM_SEL[9:8]	2
0xBB000604	LED_PORT_NUM_CTRL [5].LED_NUM_SEL[11:10]	2
0xBB000604	LED_PORT_NUM_CTRL [6].LED_NUM_SEL[13:12]	2
0xBB000604	LED_PORT_NUM_CTRL [7].LED_NUM_SEL[15:14]	2
0xBB000604	LED_PORT_NUM_CTRL [8].LED_NUM_SEL[17:16]	2
0xBB000604	LED_PORT_NUM_CTRL [9].LED_NUM_SEL[19:18]	2
0xBB000604	LED_PORT_NUM_CTRL [10].LED_NUM_SEL[21:20]	2
0xBB000604	LED_PORT_NUM_CTRL [11].LED_NUM_SEL[23:22]	2
0xBB000604	LED_PORT_NUM_CTRL [12].LED_NUM_SEL[25:24]	2
0xBB000604	LED_PORT_NUM_CTRL [13].LED_NUM_SEL[27:26]	2
0xBB000604	LED_PORT_NUM_CTRL [14].LED_NUM_SEL[29:28]	2
0xBB000604	LED_PORT_NUM_CTRL [15].LED_NUM_SEL[31:30]	2
0xBB000608	LED_PORT_NUM_CTRL [16].LED_NUM_SEL[1:0]	2
0xBB000608	LED_PORT_NUM_CTRL [17].LED_NUM_SEL[3:2]	2
0xBB000608	LED_PORT_NUM_CTRL [18].LED_NUM_SEL[5:4]	2
0xBB000608	LED_PORT_NUM_CTRL [19].LED_NUM_SEL[7:6]	2
0xBB000608	LED_PORT_NUM_CTRL [20].LED_NUM_SEL[9:8]	2
0xBB000608	LED_PORT_NUM_CTRL [21].LED_NUM_SEL[11:10]	2
0xBB000608	LED_PORT_NUM_CTRL [22].LED_NUM_SEL[13:12]	2
0xBB000608	LED_PORT_NUM_CTRL [23].LED_NUM_SEL[15:14]	2
0xBB000608	LED_PORT_NUM_CTRL [24].LED_NUM_SEL[17:16]	2
0xBB000608	LED_PORT_NUM_CTRL [25].LED_NUM_SEL[19:18]	2
0xBB000608	LED_PORT_NUM_CTRL [26].LED_NUM_SEL[21:20]	2
0xBB000608	LED_PORT_NUM_CTRL [27].LED_NUM_SEL[23:22]	2
0xBB000608	LED_PORT_NUM_CTRL [28].LED_NUM_SEL[25:24]	2
0xBB000608	LED_PORT_NUM_CTRL [29].LED_NUM_SEL[27:26]	2
0xBB000608	LED_PORT_NUM_CTRL [30].LED_NUM_SEL[29:28]	2
0xBB000608	LED_PORT_NUM_CTRL [31].LED_NUM_SEL[31:30]	2
0xBB00060C	LED_PORT_NUM_CTRL [32].LED_NUM_SEL[1:0]	2
0xBB00060C	LED_PORT_NUM_CTRL [33].LED_NUM_SEL[3:2]	2
0xBB00060C	LED_PORT_NUM_CTRL [34].LED_NUM_SEL[5:4]	2
0xBB00060C	LED_PORT_NUM_CTRL [35].LED_NUM_SEL[7:6]	2
0xBB00060C	LED_PORT_NUM_CTRL [36].LED_NUM_SEL[9:8]	2
0xBB00060C	LED_PORT_NUM_CTRL [37].LED_NUM_SEL[11:10]	2
0xBB00060C	LED_PORT_NUM_CTRL [38].LED_NUM_SEL[13:12]	2
0xBB00060C	LED_PORT_NUM_CTRL [39].LED_NUM_SEL[15:14]	2
0xBB00060C	LED_PORT_NUM_CTRL [40].LED_NUM_SEL[17:16]	2
0xBB00060C	LED_PORT_NUM_CTRL [41].LED_NUM_SEL[19:18]	2
0xBB00060C	LED_PORT_NUM_CTRL [42].LED_NUM_SEL[21:20]	2
0xBB00060C	LED_PORT_NUM_CTRL [43].LED_NUM_SEL[23:22]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00060C	LED_PORT_NUM_CTRL [44].LED_NUM_SEL[25:24]	2
0xBB00060C	LED_PORT_NUM_CTRL [45].LED_NUM_SEL[27:26]	2
0xBB00060C	LED_PORT_NUM_CTRL [46].LED_NUM_SEL[29:28]	2
0xBB00060C	LED_PORT_NUM_CTRL [47].LED_NUM_SEL[31:30]	2
0xBB000610	LED_PORT_NUM_CTRL [48].LED_NUM_SEL[1:0]	2
0xBB000610	LED_PORT_NUM_CTRL [49].LED_NUM_SEL[3:2]	2
0xBB000610	LED_PORT_NUM_CTRL [50].LED_NUM_SEL[5:4]	2
0xBB000610	LED_PORT_NUM_CTRL [51].LED_NUM_SEL[7:6]	2
0xBB000610	LED_PORT_NUM_CTRL [52].LED_NUM_SEL[9:8]	2
0xBB000610	LED_PORT_NUM_CTRL [53].LED_NUM_SEL[11:10]	2
0xBB000610	LED_PORT_NUM_CTRL [54].LED_NUM_SEL[13:12]	2
0xBB000610	LED_PORT_NUM_CTRL [55].LED_NUM_SEL[15:14]	2
0xBB00068C	LED_PORT_SW_CTRL [0].RESERVED[31:24]	8
0xBB00068C	LED_PORT_SW_CTRL [0].SW_FIB_LED3_MODE[23:21]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_FIB_LED2_MODE[20:18]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_FIB_LED1_MODE[17:15]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_FIB_LED0_MODE[14:12]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_COPR_LED3_MODE[11:9]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_COPR_LED2_MODE[8:6]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_COPR_LED1_MODE[5:3]	3
0xBB00068C	LED_PORT_SW_CTRL [0].SW_COPR_LED0_MODE[2:0]	3
0xBB000690	LED_PORT_SW_CTRL [1].RESERVED[31:24]	8
0xBB000690	LED_PORT_SW_CTRL [1].SW_FIB_LED3_MODE[23:21]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_FIB_LED2_MODE[20:18]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_FIB_LED1_MODE[17:15]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_FIB_LED0_MODE[14:12]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_COPR_LED3_MODE[11:9]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_COPR_LED2_MODE[8:6]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_COPR_LED1_MODE[5:3]	3
0xBB000690	LED_PORT_SW_CTRL [1].SW_COPR_LED0_MODE[2:0]	3
0xBB000694	LED_PORT_SW_CTRL [2].RESERVED[31:24]	8
0xBB000694	LED_PORT_SW_CTRL [2].SW_FIB_LED3_MODE[23:21]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_FIB_LED2_MODE[20:18]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_FIB_LED1_MODE[17:15]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_FIB_LED0_MODE[14:12]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_COPR_LED3_MODE[11:9]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_COPR_LED2_MODE[8:6]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_COPR_LED1_MODE[5:3]	3
0xBB000694	LED_PORT_SW_CTRL [2].SW_COPR_LED0_MODE[2:0]	3
0xBB000698	LED_PORT_SW_CTRL [3].RESERVED[31:24]	8
0xBB000698	LED_PORT_SW_CTRL [3].SW_FIB_LED3_MODE[23:21]	3
0xBB000698	LED_PORT_SW_CTRL [3].SW_FIB_LED2_MODE[20:18]	3
0xBB000698	LED_PORT_SW_CTRL [3].SW_FIB_LED1_MODE[17:15]	3
0xBB000698	LED_PORT_SW_CTRL [3].SW_FIB_LED0_MODE[14:12]	3
0xBB000698	LED_PORT_SW_CTRL [3].SW_COPR_LED3_MODE[11:9]	3
0xBB000698	LED_PORT_SW_CTRL [3].SW_COPR_LED2_MODE[8:6]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000698	LED_PORT_SW_CTRL [3].SW_COPR_LED1_MODE[5:3]	3
0xBB000698	LED_PORT_SW_CTRL [3].SW_COPR_LED0_MODE[2:0]	3
0xBB00069C	LED_PORT_SW_CTRL [4].RESERVED[31:24]	8
0xBB00069C	LED_PORT_SW_CTRL [4].SW_FIB_LED3_MODE[23:21]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_FIB_LED2_MODE[20:18]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_FIB_LED1_MODE[17:15]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_FIB_LED0_MODE[14:12]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_COPR_LED3_MODE[11:9]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_COPR_LED2_MODE[8:6]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_COPR_LED1_MODE[5:3]	3
0xBB00069C	LED_PORT_SW_CTRL [4].SW_COPR_LED0_MODE[2:0]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].RESERVED[31:24]	8
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_FIB_LED3_MODE[23:21]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_FIB_LED2_MODE[20:18]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_FIB_LED1_MODE[17:15]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_FIB_LED0_MODE[14:12]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_COPR_LED3_MODE[11:9]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_COPR_LED2_MODE[8:6]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_COPR_LED1_MODE[5:3]	3
0xBB0006A0	LED_PORT_SW_CTRL [5].SW_COPR_LED0_MODE[2:0]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].RESERVED[31:24]	8
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_FIB_LED3_MODE[23:21]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_FIB_LED2_MODE[20:18]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_FIB_LED1_MODE[17:15]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_FIB_LED0_MODE[14:12]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_COPR_LED3_MODE[11:9]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_COPR_LED2_MODE[8:6]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_COPR_LED1_MODE[5:3]	3
0xBB0006A4	LED_PORT_SW_CTRL [6].SW_COPR_LED0_MODE[2:0]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].RESERVED[31:24]	8
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_FIB_LED3_MODE[23:21]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_FIB_LED2_MODE[20:18]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_FIB_LED1_MODE[17:15]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_FIB_LED0_MODE[14:12]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_COPR_LED3_MODE[11:9]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_COPR_LED2_MODE[8:6]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_COPR_LED1_MODE[5:3]	3
0xBB0006A8	LED_PORT_SW_CTRL [7].SW_COPR_LED0_MODE[2:0]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].RESERVED[31:24]	8
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_FIB_LED3_MODE[23:21]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_FIB_LED2_MODE[20:18]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_FIB_LED1_MODE[17:15]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_FIB_LED0_MODE[14:12]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_COPR_LED3_MODE[11:9]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_COPR_LED2_MODE[8:6]	3
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_COPR_LED1_MODE[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0006AC	LED_PORT_SW_CTRL [8].SW_COPR_LED0_MODE[2:0]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].RESERVED[31:24]	8
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_FIB_LED3_MODE[23:21]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_FIB_LED2_MODE[20:18]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_FIB_LED1_MODE[17:15]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_FIB_LED0_MODE[14:12]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_COPR_LED3_MODE[11:9]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_COPR_LED2_MODE[8:6]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_COPR_LED1_MODE[5:3]	3
0xBB0006B0	LED_PORT_SW_CTRL [9].SW_COPR_LED0_MODE[2:0]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].RESERVED[31:24]	8
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_FIB_LED3_MODE[23:21]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_FIB_LED2_MODE[20:18]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_FIB_LED1_MODE[17:15]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_FIB_LED0_MODE[14:12]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_COPR_LED3_MODE[11:9]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_COPR_LED2_MODE[8:6]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_COPR_LED1_MODE[5:3]	3
0xBB0006B4	LED_PORT_SW_CTRL [10].SW_COPR_LED0_MODE[2:0]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].RESERVED[31:24]	8
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_FIB_LED3_MODE[23:21]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_FIB_LED2_MODE[20:18]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_FIB_LED1_MODE[17:15]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_FIB_LED0_MODE[14:12]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_COPR_LED3_MODE[11:9]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_COPR_LED2_MODE[8:6]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_COPR_LED1_MODE[5:3]	3
0xBB0006B8	LED_PORT_SW_CTRL [11].SW_COPR_LED0_MODE[2:0]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].RESERVED[31:24]	8
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_FIB_LED3_MODE[23:21]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_FIB_LED2_MODE[20:18]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_FIB_LED1_MODE[17:15]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_FIB_LED0_MODE[14:12]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_COPR_LED3_MODE[11:9]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_COPR_LED2_MODE[8:6]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_COPR_LED1_MODE[5:3]	3
0xBB0006BC	LED_PORT_SW_CTRL [12].SW_COPR_LED0_MODE[2:0]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].RESERVED[31:24]	8
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_FIB_LED3_MODE[23:21]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_FIB_LED2_MODE[20:18]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_FIB_LED1_MODE[17:15]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_FIB_LED0_MODE[14:12]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_COPR_LED3_MODE[11:9]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_COPR_LED2_MODE[8:6]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_COPR_LED1_MODE[5:3]	3
0xBB0006C0	LED_PORT_SW_CTRL [13].SW_COPR_LED0_MODE[2:0]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0006C4	LED_PORT_SW_CTRL [14].RESERVED[31:24]	8
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_FIB_LED3_MODE[23:21]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_FIB_LED2_MODE[20:18]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_FIB_LED1_MODE[17:15]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_FIB_LED0_MODE[14:12]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_COPR_LED3_MODE[11:9]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_COPR_LED2_MODE[8:6]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_COPR_LED1_MODE[5:3]	3
0xBB0006C4	LED_PORT_SW_CTRL [14].SW_COPR_LED0_MODE[2:0]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].RESERVED[31:24]	8
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_FIB_LED3_MODE[23:21]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_FIB_LED2_MODE[20:18]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_FIB_LED1_MODE[17:15]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_FIB_LED0_MODE[14:12]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_COPR_LED3_MODE[11:9]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_COPR_LED2_MODE[8:6]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_COPR_LED1_MODE[5:3]	3
0xBB0006C8	LED_PORT_SW_CTRL [15].SW_COPR_LED0_MODE[2:0]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].RESERVED[31:24]	8
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_FIB_LED3_MODE[23:21]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_FIB_LED2_MODE[20:18]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_FIB_LED1_MODE[17:15]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_FIB_LED0_MODE[14:12]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_COPR_LED3_MODE[11:9]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_COPR_LED2_MODE[8:6]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_COPR_LED1_MODE[5:3]	3
0xBB0006CC	LED_PORT_SW_CTRL [16].SW_COPR_LED0_MODE[2:0]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].RESERVED[31:24]	8
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_FIB_LED3_MODE[23:21]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_FIB_LED2_MODE[20:18]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_FIB_LED1_MODE[17:15]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_FIB_LED0_MODE[14:12]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_COPR_LED3_MODE[11:9]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_COPR_LED2_MODE[8:6]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_COPR_LED1_MODE[5:3]	3
0xBB0006D0	LED_PORT_SW_CTRL [17].SW_COPR_LED0_MODE[2:0]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].RESERVED[31:24]	8
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_FIB_LED3_MODE[23:21]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_FIB_LED2_MODE[20:18]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_FIB_LED1_MODE[17:15]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_FIB_LED0_MODE[14:12]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_COPR_LED3_MODE[11:9]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_COPR_LED2_MODE[8:6]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_COPR_LED1_MODE[5:3]	3
0xBB0006D4	LED_PORT_SW_CTRL [18].SW_COPR_LED0_MODE[2:0]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].RESERVED[31:24]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_FIB_LED3_MODE[23:21]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_FIB_LED2_MODE[20:18]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_FIB_LED1_MODE[17:15]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_FIB_LED0_MODE[14:12]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_COPR_LED3_MODE[11:9]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_COPR_LED2_MODE[8:6]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_COPR_LED1_MODE[5:3]	3
0xBB0006D8	LED_PORT_SW_CTRL [19].SW_COPR_LED0_MODE[2:0]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].RESERVED[31:24]	8
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_FIB_LED3_MODE[23:21]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_FIB_LED2_MODE[20:18]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_FIB_LED1_MODE[17:15]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_FIB_LED0_MODE[14:12]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_COPR_LED3_MODE[11:9]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_COPR_LED2_MODE[8:6]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_COPR_LED1_MODE[5:3]	3
0xBB0006DC	LED_PORT_SW_CTRL [20].SW_COPR_LED0_MODE[2:0]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].RESERVED[31:24]	8
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_FIB_LED3_MODE[23:21]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_FIB_LED2_MODE[20:18]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_FIB_LED1_MODE[17:15]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_FIB_LED0_MODE[14:12]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_COPR_LED3_MODE[11:9]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_COPR_LED2_MODE[8:6]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_COPR_LED1_MODE[5:3]	3
0xBB0006E0	LED_PORT_SW_CTRL [21].SW_COPR_LED0_MODE[2:0]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].RESERVED[31:24]	8
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_FIB_LED3_MODE[23:21]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_FIB_LED2_MODE[20:18]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_FIB_LED1_MODE[17:15]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_FIB_LED0_MODE[14:12]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_COPR_LED3_MODE[11:9]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_COPR_LED2_MODE[8:6]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_COPR_LED1_MODE[5:3]	3
0xBB0006E4	LED_PORT_SW_CTRL [22].SW_COPR_LED0_MODE[2:0]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].RESERVED[31:24]	8
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_FIB_LED3_MODE[23:21]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_FIB_LED2_MODE[20:18]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_FIB_LED1_MODE[17:15]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_FIB_LED0_MODE[14:12]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_COPR_LED3_MODE[11:9]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_COPR_LED2_MODE[8:6]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_COPR_LED1_MODE[5:3]	3
0xBB0006E8	LED_PORT_SW_CTRL [23].SW_COPR_LED0_MODE[2:0]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].RESERVED[31:24]	8
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_FIB_LED3_MODE[23:21]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_FIB_LED2_MODE[20:18]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_FIB_LED1_MODE[17:15]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_FIB_LED0_MODE[14:12]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_COPR_LED3_MODE[11:9]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_COPR_LED2_MODE[8:6]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_COPR_LED1_MODE[5:3]	3
0xBB0006EC	LED_PORT_SW_CTRL [24].SW_COPR_LED0_MODE[2:0]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].RESERVED[31:24]	8
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_FIB_LED3_MODE[23:21]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_FIB_LED2_MODE[20:18]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_FIB_LED1_MODE[17:15]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_FIB_LED0_MODE[14:12]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_COPR_LED3_MODE[11:9]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_COPR_LED2_MODE[8:6]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_COPR_LED1_MODE[5:3]	3
0xBB0006F0	LED_PORT_SW_CTRL [25].SW_COPR_LED0_MODE[2:0]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].RESERVED[31:24]	8
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_FIB_LED3_MODE[23:21]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_FIB_LED2_MODE[20:18]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_FIB_LED1_MODE[17:15]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_FIB_LED0_MODE[14:12]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_COPR_LED3_MODE[11:9]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_COPR_LED2_MODE[8:6]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_COPR_LED1_MODE[5:3]	3
0xBB0006F4	LED_PORT_SW_CTRL [26].SW_COPR_LED0_MODE[2:0]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].RESERVED[31:24]	8
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_FIB_LED3_MODE[23:21]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_FIB_LED2_MODE[20:18]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_FIB_LED1_MODE[17:15]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_FIB_LED0_MODE[14:12]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_COPR_LED3_MODE[11:9]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_COPR_LED2_MODE[8:6]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_COPR_LED1_MODE[5:3]	3
0xBB0006F8	LED_PORT_SW_CTRL [27].SW_COPR_LED0_MODE[2:0]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].RESERVED[31:24]	8
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_FIB_LED3_MODE[23:21]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_FIB_LED2_MODE[20:18]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_FIB_LED1_MODE[17:15]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_FIB_LED0_MODE[14:12]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_COPR_LED3_MODE[11:9]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_COPR_LED2_MODE[8:6]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_COPR_LED1_MODE[5:3]	3
0xBB0006FC	LED_PORT_SW_CTRL [28].SW_COPR_LED0_MODE[2:0]	3
0xBB000700	LED_PORT_SW_CTRL [29].RESERVED[31:24]	8
0xBB000700	LED_PORT_SW_CTRL [29].SW_FIB_LED3_MODE[23:21]	3
0xBB000700	LED_PORT_SW_CTRL [29].SW_FIB_LED2_MODE[20:18]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000700	LED_PORT_SW_CTRL [29].SW_FIB_LED1_MODE[17:15]	3
0xBB000700	LED_PORT_SW_CTRL [29].SW_FIB_LED0_MODE[14:12]	3
0xBB000700	LED_PORT_SW_CTRL [29].SW_COPR_LED3_MODE[11:9]	3
0xBB000700	LED_PORT_SW_CTRL [29].SW_COPR_LED2_MODE[8:6]	3
0xBB000700	LED_PORT_SW_CTRL [29].SW_COPR_LED1_MODE[5:3]	3
0xBB000700	LED_PORT_SW_CTRL [29].SW_COPR_LED0_MODE[2:0]	3
0xBB000704	LED_PORT_SW_CTRL [30].RESERVED[31:24]	8
0xBB000704	LED_PORT_SW_CTRL [30].SW_FIB_LED3_MODE[23:21]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_FIB_LED2_MODE[20:18]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_FIB_LED1_MODE[17:15]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_FIB_LED0_MODE[14:12]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_COPR_LED3_MODE[11:9]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_COPR_LED2_MODE[8:6]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_COPR_LED1_MODE[5:3]	3
0xBB000704	LED_PORT_SW_CTRL [30].SW_COPR_LED0_MODE[2:0]	3
0xBB000708	LED_PORT_SW_CTRL [31].RESERVED[31:24]	8
0xBB000708	LED_PORT_SW_CTRL [31].SW_FIB_LED3_MODE[23:21]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_FIB_LED2_MODE[20:18]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_FIB_LED1_MODE[17:15]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_FIB_LED0_MODE[14:12]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_COPR_LED3_MODE[11:9]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_COPR_LED2_MODE[8:6]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_COPR_LED1_MODE[5:3]	3
0xBB000708	LED_PORT_SW_CTRL [31].SW_COPR_LED0_MODE[2:0]	3
0xBB00070C	LED_PORT_SW_CTRL [32].RESERVED[31:24]	8
0xBB00070C	LED_PORT_SW_CTRL [32].SW_FIB_LED3_MODE[23:21]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_FIB_LED2_MODE[20:18]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_FIB_LED1_MODE[17:15]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_FIB_LED0_MODE[14:12]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_COPR_LED3_MODE[11:9]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_COPR_LED2_MODE[8:6]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_COPR_LED1_MODE[5:3]	3
0xBB00070C	LED_PORT_SW_CTRL [32].SW_COPR_LED0_MODE[2:0]	3
0xBB000710	LED_PORT_SW_CTRL [33].RESERVED[31:24]	8
0xBB000710	LED_PORT_SW_CTRL [33].SW_FIB_LED3_MODE[23:21]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_FIB_LED2_MODE[20:18]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_FIB_LED1_MODE[17:15]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_FIB_LED0_MODE[14:12]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_COPR_LED3_MODE[11:9]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_COPR_LED2_MODE[8:6]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_COPR_LED1_MODE[5:3]	3
0xBB000710	LED_PORT_SW_CTRL [33].SW_COPR_LED0_MODE[2:0]	3
0xBB000714	LED_PORT_SW_CTRL [34].RESERVED[31:24]	8
0xBB000714	LED_PORT_SW_CTRL [34].SW_FIB_LED3_MODE[23:21]	3
0xBB000714	LED_PORT_SW_CTRL [34].SW_FIB_LED2_MODE[20:18]	3
0xBB000714	LED_PORT_SW_CTRL [34].SW_FIB_LED1_MODE[17:15]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000714	LED_PORT_SW_CTRL [34].SW_FIB_LED0_MODE[14:12]	3
0xBB000714	LED_PORT_SW_CTRL [34].SW_COPR_LED3_MODE[11:9]	3
0xBB000714	LED_PORT_SW_CTRL [34].SW_COPR_LED2_MODE[8:6]	3
0xBB000714	LED_PORT_SW_CTRL [34].SW_COPR_LED1_MODE[5:3]	3
0xBB000714	LED_PORT_SW_CTRL [34].SW_COPR_LED0_MODE[2:0]	3
0xBB000718	LED_PORT_SW_CTRL [35].RESERVED[31:24]	8
0xBB000718	LED_PORT_SW_CTRL [35].SW_FIB_LED3_MODE[23:21]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_FIB_LED2_MODE[20:18]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_FIB_LED1_MODE[17:15]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_FIB_LED0_MODE[14:12]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_COPR_LED3_MODE[11:9]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_COPR_LED2_MODE[8:6]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_COPR_LED1_MODE[5:3]	3
0xBB000718	LED_PORT_SW_CTRL [35].SW_COPR_LED0_MODE[2:0]	3
0xBB00071C	LED_PORT_SW_CTRL [36].RESERVED[31:24]	8
0xBB00071C	LED_PORT_SW_CTRL [36].SW_FIB_LED3_MODE[23:21]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_FIB_LED2_MODE[20:18]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_FIB_LED1_MODE[17:15]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_FIB_LED0_MODE[14:12]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_COPR_LED3_MODE[11:9]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_COPR_LED2_MODE[8:6]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_COPR_LED1_MODE[5:3]	3
0xBB00071C	LED_PORT_SW_CTRL [36].SW_COPR_LED0_MODE[2:0]	3
0xBB000720	LED_PORT_SW_CTRL [37].RESERVED[31:24]	8
0xBB000720	LED_PORT_SW_CTRL [37].SW_FIB_LED3_MODE[23:21]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_FIB_LED2_MODE[20:18]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_FIB_LED1_MODE[17:15]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_FIB_LED0_MODE[14:12]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_COPR_LED3_MODE[11:9]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_COPR_LED2_MODE[8:6]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_COPR_LED1_MODE[5:3]	3
0xBB000720	LED_PORT_SW_CTRL [37].SW_COPR_LED0_MODE[2:0]	3
0xBB000724	LED_PORT_SW_CTRL [38].RESERVED[31:24]	8
0xBB000724	LED_PORT_SW_CTRL [38].SW_FIB_LED3_MODE[23:21]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_FIB_LED2_MODE[20:18]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_FIB_LED1_MODE[17:15]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_FIB_LED0_MODE[14:12]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_COPR_LED3_MODE[11:9]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_COPR_LED2_MODE[8:6]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_COPR_LED1_MODE[5:3]	3
0xBB000724	LED_PORT_SW_CTRL [38].SW_COPR_LED0_MODE[2:0]	3
0xBB000728	LED_PORT_SW_CTRL [39].RESERVED[31:24]	8
0xBB000728	LED_PORT_SW_CTRL [39].SW_FIB_LED3_MODE[23:21]	3
0xBB000728	LED_PORT_SW_CTRL [39].SW_FIB_LED2_MODE[20:18]	3
0xBB000728	LED_PORT_SW_CTRL [39].SW_FIB_LED1_MODE[17:15]	3
0xBB000728	LED_PORT_SW_CTRL [39].SW_FIB_LED0_MODE[14:12]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000728	LED_PORT_SW_CTRL [39].SW_COPR_LED3_MODE[11:9]	3
0xBB000728	LED_PORT_SW_CTRL [39].SW_COPR_LED2_MODE[8:6]	3
0xBB000728	LED_PORT_SW_CTRL [39].SW_COPR_LED1_MODE[5:3]	3
0xBB000728	LED_PORT_SW_CTRL [39].SW_COPR_LED0_MODE[2:0]	3
0xBB00072C	LED_PORT_SW_CTRL [40].RESERVED[31:24]	8
0xBB00072C	LED_PORT_SW_CTRL [40].SW_FIB_LED3_MODE[23:21]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_FIB_LED2_MODE[20:18]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_FIB_LED1_MODE[17:15]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_FIB_LED0_MODE[14:12]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_COPR_LED3_MODE[11:9]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_COPR_LED2_MODE[8:6]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_COPR_LED1_MODE[5:3]	3
0xBB00072C	LED_PORT_SW_CTRL [40].SW_COPR_LED0_MODE[2:0]	3
0xBB000730	LED_PORT_SW_CTRL [41].RESERVED[31:24]	8
0xBB000730	LED_PORT_SW_CTRL [41].SW_FIB_LED3_MODE[23:21]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_FIB_LED2_MODE[20:18]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_FIB_LED1_MODE[17:15]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_FIB_LED0_MODE[14:12]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_COPR_LED3_MODE[11:9]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_COPR_LED2_MODE[8:6]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_COPR_LED1_MODE[5:3]	3
0xBB000730	LED_PORT_SW_CTRL [41].SW_COPR_LED0_MODE[2:0]	3
0xBB000734	LED_PORT_SW_CTRL [42].RESERVED[31:24]	8
0xBB000734	LED_PORT_SW_CTRL [42].SW_FIB_LED3_MODE[23:21]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_FIB_LED2_MODE[20:18]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_FIB_LED1_MODE[17:15]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_FIB_LED0_MODE[14:12]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_COPR_LED3_MODE[11:9]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_COPR_LED2_MODE[8:6]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_COPR_LED1_MODE[5:3]	3
0xBB000734	LED_PORT_SW_CTRL [42].SW_COPR_LED0_MODE[2:0]	3
0xBB000738	LED_PORT_SW_CTRL [43].RESERVED[31:24]	8
0xBB000738	LED_PORT_SW_CTRL [43].SW_FIB_LED3_MODE[23:21]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_FIB_LED2_MODE[20:18]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_FIB_LED1_MODE[17:15]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_FIB_LED0_MODE[14:12]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_COPR_LED3_MODE[11:9]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_COPR_LED2_MODE[8:6]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_COPR_LED1_MODE[5:3]	3
0xBB000738	LED_PORT_SW_CTRL [43].SW_COPR_LED0_MODE[2:0]	3
0xBB00073C	LED_PORT_SW_CTRL [44].RESERVED[31:24]	8
0xBB00073C	LED_PORT_SW_CTRL [44].SW_FIB_LED3_MODE[23:21]	3
0xBB00073C	LED_PORT_SW_CTRL [44].SW_FIB_LED2_MODE[20:18]	3
0xBB00073C	LED_PORT_SW_CTRL [44].SW_FIB_LED1_MODE[17:15]	3
0xBB00073C	LED_PORT_SW_CTRL [44].SW_FIB_LED0_MODE[14:12]	3
0xBB00073C	LED_PORT_SW_CTRL [44].SW_COPR_LED3_MODE[11:9]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00073C	LED_PORT_SW_CTRL [44].SW_COPR_LED2_MODE[8:6]	3
0xBB00073C	LED_PORT_SW_CTRL [44].SW_COPR_LED1_MODE[5:3]	3
0xBB00073C	LED_PORT_SW_CTRL [44].SW_COPR_LED0_MODE[2:0]	3
0xBB000740	LED_PORT_SW_CTRL [45].RESERVED[31:24]	8
0xBB000740	LED_PORT_SW_CTRL [45].SW_FIB_LED3_MODE[23:21]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_FIB_LED2_MODE[20:18]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_FIB_LED1_MODE[17:15]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_FIB_LED0_MODE[14:12]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_COPR_LED3_MODE[11:9]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_COPR_LED2_MODE[8:6]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_COPR_LED1_MODE[5:3]	3
0xBB000740	LED_PORT_SW_CTRL [45].SW_COPR_LED0_MODE[2:0]	3
0xBB000744	LED_PORT_SW_CTRL [46].RESERVED[31:24]	8
0xBB000744	LED_PORT_SW_CTRL [46].SW_FIB_LED3_MODE[23:21]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_FIB_LED2_MODE[20:18]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_FIB_LED1_MODE[17:15]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_FIB_LED0_MODE[14:12]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_COPR_LED3_MODE[11:9]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_COPR_LED2_MODE[8:6]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_COPR_LED1_MODE[5:3]	3
0xBB000744	LED_PORT_SW_CTRL [46].SW_COPR_LED0_MODE[2:0]	3
0xBB000748	LED_PORT_SW_CTRL [47].RESERVED[31:24]	8
0xBB000748	LED_PORT_SW_CTRL [47].SW_FIB_LED3_MODE[23:21]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_FIB_LED2_MODE[20:18]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_FIB_LED1_MODE[17:15]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_FIB_LED0_MODE[14:12]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_COPR_LED3_MODE[11:9]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_COPR_LED2_MODE[8:6]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_COPR_LED1_MODE[5:3]	3
0xBB000748	LED_PORT_SW_CTRL [47].SW_COPR_LED0_MODE[2:0]	3
0xBB00074C	LED_PORT_SW_CTRL [48].RESERVED[31:24]	8
0xBB00074C	LED_PORT_SW_CTRL [48].SW_FIB_LED3_MODE[23:21]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_FIB_LED2_MODE[20:18]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_FIB_LED1_MODE[17:15]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_FIB_LED0_MODE[14:12]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_COPR_LED3_MODE[11:9]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_COPR_LED2_MODE[8:6]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_COPR_LED1_MODE[5:3]	3
0xBB00074C	LED_PORT_SW_CTRL [48].SW_COPR_LED0_MODE[2:0]	3
0xBB000750	LED_PORT_SW_CTRL [49].RESERVED[31:24]	8
0xBB000750	LED_PORT_SW_CTRL [49].SW_FIB_LED3_MODE[23:21]	3
0xBB000750	LED_PORT_SW_CTRL [49].SW_FIB_LED2_MODE[20:18]	3
0xBB000750	LED_PORT_SW_CTRL [49].SW_FIB_LED1_MODE[17:15]	3
0xBB000750	LED_PORT_SW_CTRL [49].SW_FIB_LED0_MODE[14:12]	3
0xBB000750	LED_PORT_SW_CTRL [49].SW_COPR_LED3_MODE[11:9]	3
0xBB000750	LED_PORT_SW_CTRL [49].SW_COPR_LED2_MODE[8:6]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000750	LED_PORT_SW_CTRL [49].SW_COPR_LED1_MODE[5:3]	3
0xBB000750	LED_PORT_SW_CTRL [49].SW_COPR_LED0_MODE[2:0]	3
0xBB000754	LED_PORT_SW_CTRL [50].RESERVED[31:24]	8
0xBB000754	LED_PORT_SW_CTRL [50].SW_FIB_LED3_MODE[23:21]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_FIB_LED2_MODE[20:18]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_FIB_LED1_MODE[17:15]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_FIB_LED0_MODE[14:12]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_COPR_LED3_MODE[11:9]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_COPR_LED2_MODE[8:6]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_COPR_LED1_MODE[5:3]	3
0xBB000754	LED_PORT_SW_CTRL [50].SW_COPR_LED0_MODE[2:0]	3
0xBB000758	LED_PORT_SW_CTRL [51].RESERVED[31:24]	8
0xBB000758	LED_PORT_SW_CTRL [51].SW_FIB_LED3_MODE[23:21]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_FIB_LED2_MODE[20:18]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_FIB_LED1_MODE[17:15]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_FIB_LED0_MODE[14:12]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_COPR_LED3_MODE[11:9]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_COPR_LED2_MODE[8:6]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_COPR_LED1_MODE[5:3]	3
0xBB000758	LED_PORT_SW_CTRL [51].SW_COPR_LED0_MODE[2:0]	3
0xBB00075C	LED_PORT_SW_CTRL [52].RESERVED[31:24]	8
0xBB00075C	LED_PORT_SW_CTRL [52].SW_FIB_LED3_MODE[23:21]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_FIB_LED2_MODE[20:18]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_FIB_LED1_MODE[17:15]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_FIB_LED0_MODE[14:12]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_COPR_LED3_MODE[11:9]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_COPR_LED2_MODE[8:6]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_COPR_LED1_MODE[5:3]	3
0xBB00075C	LED_PORT_SW_CTRL [52].SW_COPR_LED0_MODE[2:0]	3
0xBB000760	LED_PORT_SW_CTRL [53].RESERVED[31:24]	8
0xBB000760	LED_PORT_SW_CTRL [53].SW_FIB_LED3_MODE[23:21]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_FIB_LED2_MODE[20:18]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_FIB_LED1_MODE[17:15]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_FIB_LED0_MODE[14:12]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_COPR_LED3_MODE[11:9]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_COPR_LED2_MODE[8:6]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_COPR_LED1_MODE[5:3]	3
0xBB000760	LED_PORT_SW_CTRL [53].SW_COPR_LED0_MODE[2:0]	3
0xBB000764	LED_PORT_SW_CTRL [54].RESERVED[31:24]	8
0xBB000764	LED_PORT_SW_CTRL [54].SW_FIB_LED3_MODE[23:21]	3
0xBB000764	LED_PORT_SW_CTRL [54].SW_FIB_LED2_MODE[20:18]	3
0xBB000764	LED_PORT_SW_CTRL [54].SW_FIB_LED1_MODE[17:15]	3
0xBB000764	LED_PORT_SW_CTRL [54].SW_FIB_LED0_MODE[14:12]	3
0xBB000764	LED_PORT_SW_CTRL [54].SW_COPR_LED3_MODE[11:9]	3
0xBB000764	LED_PORT_SW_CTRL [54].SW_COPR_LED2_MODE[8:6]	3
0xBB000764	LED_PORT_SW_CTRL [54].SW_COPR_LED1_MODE[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000764	LED_PORT_SW_CTRL [54].SW_COPR_LED0_MODE[2:0]	3
0xBB000768	LED_PORT_SW_CTRL [55].RESERVED[31:24]	8
0xBB000768	LED_PORT_SW_CTRL [55].SW_FIB_LED3_MODE[23:21]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_FIB_LED2_MODE[20:18]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_FIB_LED1_MODE[17:15]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_FIB_LED0_MODE[14:12]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_COPR_LED3_MODE[11:9]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_COPR_LED2_MODE[8:6]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_COPR_LED1_MODE[5:3]	3
0xBB000768	LED_PORT_SW_CTRL [55].SW_COPR_LED0_MODE[2:0]	3
0xBB000670	LED_PORT_SW_EN_CTRL [0].SW_CTRL_LED_EN[3:0]	4
0xBB000670	LED_PORT_SW_EN_CTRL [1].SW_CTRL_LED_EN[7:4]	4
0xBB000670	LED_PORT_SW_EN_CTRL [2].SW_CTRL_LED_EN[11:8]	4
0xBB000670	LED_PORT_SW_EN_CTRL [3].SW_CTRL_LED_EN[15:12]	4
0xBB000670	LED_PORT_SW_EN_CTRL [4].SW_CTRL_LED_EN[19:16]	4
0xBB000670	LED_PORT_SW_EN_CTRL [5].SW_CTRL_LED_EN[23:20]	4
0xBB000670	LED_PORT_SW_EN_CTRL [6].SW_CTRL_LED_EN[27:24]	4
0xBB000670	LED_PORT_SW_EN_CTRL [7].SW_CTRL_LED_EN[31:28]	4
0xBB000674	LED_PORT_SW_EN_CTRL [8].SW_CTRL_LED_EN[3:0]	4
0xBB000674	LED_PORT_SW_EN_CTRL [9].SW_CTRL_LED_EN[7:4]	4
0xBB000674	LED_PORT_SW_EN_CTRL [10].SW_CTRL_LED_EN[11:8]	4
0xBB000674	LED_PORT_SW_EN_CTRL [11].SW_CTRL_LED_EN[15:12]	4
0xBB000674	LED_PORT_SW_EN_CTRL [12].SW_CTRL_LED_EN[19:16]	4
0xBB000674	LED_PORT_SW_EN_CTRL [13].SW_CTRL_LED_EN[23:20]	4
0xBB000674	LED_PORT_SW_EN_CTRL [14].SW_CTRL_LED_EN[27:24]	4
0xBB000674	LED_PORT_SW_EN_CTRL [15].SW_CTRL_LED_EN[31:28]	4
0xBB000678	LED_PORT_SW_EN_CTRL [16].SW_CTRL_LED_EN[3:0]	4
0xBB000678	LED_PORT_SW_EN_CTRL [17].SW_CTRL_LED_EN[7:4]	4
0xBB000678	LED_PORT_SW_EN_CTRL [18].SW_CTRL_LED_EN[11:8]	4
0xBB000678	LED_PORT_SW_EN_CTRL [19].SW_CTRL_LED_EN[15:12]	4
0xBB000678	LED_PORT_SW_EN_CTRL [20].SW_CTRL_LED_EN[19:16]	4
0xBB000678	LED_PORT_SW_EN_CTRL [21].SW_CTRL_LED_EN[23:20]	4
0xBB000678	LED_PORT_SW_EN_CTRL [22].SW_CTRL_LED_EN[27:24]	4
0xBB000678	LED_PORT_SW_EN_CTRL [23].SW_CTRL_LED_EN[31:28]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [24].SW_CTRL_LED_EN[3:0]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [25].SW_CTRL_LED_EN[7:4]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [26].SW_CTRL_LED_EN[11:8]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [27].SW_CTRL_LED_EN[15:12]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [28].SW_CTRL_LED_EN[19:16]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [29].SW_CTRL_LED_EN[23:20]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [30].SW_CTRL_LED_EN[27:24]	4
0xBB00067C	LED_PORT_SW_EN_CTRL [31].SW_CTRL_LED_EN[31:28]	4
0xBB000680	LED_PORT_SW_EN_CTRL [32].SW_CTRL_LED_EN[3:0]	4
0xBB000680	LED_PORT_SW_EN_CTRL [33].SW_CTRL_LED_EN[7:4]	4
0xBB000680	LED_PORT_SW_EN_CTRL [34].SW_CTRL_LED_EN[11:8]	4
0xBB000680	LED_PORT_SW_EN_CTRL [35].SW_CTRL_LED_EN[15:12]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000680	LED_PORT_SW_EN_CTRL [36].SW_CTRL_LED_EN[19:16]	4
0xBB000680	LED_PORT_SW_EN_CTRL [37].SW_CTRL_LED_EN[23:20]	4
0xBB000680	LED_PORT_SW_EN_CTRL [38].SW_CTRL_LED_EN[27:24]	4
0xBB000680	LED_PORT_SW_EN_CTRL [39].SW_CTRL_LED_EN[31:28]	4
0xBB000684	LED_PORT_SW_EN_CTRL [40].SW_CTRL_LED_EN[3:0]	4
0xBB000684	LED_PORT_SW_EN_CTRL [41].SW_CTRL_LED_EN[7:4]	4
0xBB000684	LED_PORT_SW_EN_CTRL [42].SW_CTRL_LED_EN[11:8]	4
0xBB000684	LED_PORT_SW_EN_CTRL [43].SW_CTRL_LED_EN[15:12]	4
0xBB000684	LED_PORT_SW_EN_CTRL [44].SW_CTRL_LED_EN[19:16]	4
0xBB000684	LED_PORT_SW_EN_CTRL [45].SW_CTRL_LED_EN[23:20]	4
0xBB000684	LED_PORT_SW_EN_CTRL [46].SW_CTRL_LED_EN[27:24]	4
0xBB000684	LED_PORT_SW_EN_CTRL [47].SW_CTRL_LED_EN[31:28]	4
0xBB000688	LED_PORT_SW_EN_CTRL [48].SW_CTRL_LED_EN[3:0]	4
0xBB000688	LED_PORT_SW_EN_CTRL [49].SW_CTRL_LED_EN[7:4]	4
0xBB000688	LED_PORT_SW_EN_CTRL [50].SW_CTRL_LED_EN[11:8]	4
0xBB000688	LED_PORT_SW_EN_CTRL [51].SW_CTRL_LED_EN[15:12]	4
0xBB000688	LED_PORT_SW_EN_CTRL [52].SW_CTRL_LED_EN[19:16]	4
0xBB000688	LED_PORT_SW_EN_CTRL [53].SW_CTRL_LED_EN[23:20]	4
0xBB000688	LED_PORT_SW_EN_CTRL [54].SW_CTRL_LED_EN[27:24]	4
0xBB000688	LED_PORT_SW_EN_CTRL [55].SW_CTRL_LED_EN[31:28]	4
0xBB000630	LED_SET_0_CTRL0.SET0_LED1_SEL[31:16]	16
0xBB000630	LED_SET_0_CTRL0.SET0_LED0_SEL[15:0]	16
0xBB00062C	LED_SET_0_CTRL1.SET0_LED3_SEL[31:16]	16
0xBB00062C	LED_SET_0_CTRL1.SET0_LED2_SEL[15:0]	16
0xBB000628	LED_SET_1_CTRL0.SET1_LED1_SEL[31:16]	16
0xBB000628	LED_SET_1_CTRL0.SET1_LED0_SEL[15:0]	16
0xBB000624	LED_SET_1_CTRL1.SET1_LED3_SEL[31:16]	16
0xBB000624	LED_SET_1_CTRL1.SET1_LED2_SEL[15:0]	16
0xBB000620	LED_SET_2_CTRL0.SET2_LED1_SEL[31:16]	16
0xBB000620	LED_SET_2_CTRL0.SET2_LED0_SEL[15:0]	16
0xBB00061C	LED_SET_2_CTRL1.SET2_LED3_SEL[31:16]	16
0xBB00061C	LED_SET_2_CTRL1.SET2_LED2_SEL[15:0]	16
0xBB000618	LED_SET_3_CTRL0.SET3_LED1_SEL[31:16]	16
0xBB000618	LED_SET_3_CTRL0.SET3_LED0_SEL[15:0]	16
0xBB000614	LED_SET_3_CTRL1.SET3_LED3_SEL[31:16]	16
0xBB000614	LED_SET_3_CTRL1.SET3_LED2_SEL[15:0]	16
0xBB000CD8	LINK_DELAY_CTRL.RESERVED[31:19]	13
0xBB000CD8	LINK_DELAY_CTRL.DBG_SEL_ABLTY_RSL[18:17]	2
0xBB000CD8	LINK_DELAY_CTRL.LNKDN_FRC_DIS[16:16]	1
0xBB000CD8	LINK_DELAY_CTRL.LINK_DOWN_TIME[15:8]	8
0xBB000CD8	LINK_DELAY_CTRL.LINK_DOWN_TIME_EN[7:7]	1
0xBB000CD8	LINK_DELAY_CTRL.LINKUP_DELAY_EN[6:6]	1
0xBB000CD8	LINK_DELAY_CTRL.LINKUP_DELAY_10G_5G[5:4]	2
0xBB000CD8	LINK_DELAY_CTRL.LINKUP_DELAY_2P5G_1000M_100M[3:2]	2
0xBB000CD8	LINK_DELAY_CTRL.LINKUP_DELAY_10M[1:0]	2
0xBB004CAC	LOCAL_PORT_TRK_MAP [0].RESERVED[31:8]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004CAC	LOCAL_PORT_TRK_MAP [0].IS_TRK_MBR[7:7]	1
0xBB004CAC	LOCAL_PORT_TRK_MAP [0].TRK_ID[6:0]	7
0xBB004CB0	LOCAL_PORT_TRK_MAP [1].RESERVED[31:8]	24
0xBB004CB0	LOCAL_PORT_TRK_MAP [1].IS_TRK_MBR[7:7]	1
0xBB004CB0	LOCAL_PORT_TRK_MAP [1].TRK_ID[6:0]	7
0xBB004CB4	LOCAL_PORT_TRK_MAP [2].RESERVED[31:8]	24
0xBB004CB4	LOCAL_PORT_TRK_MAP [2].IS_TRK_MBR[7:7]	1
0xBB004CB4	LOCAL_PORT_TRK_MAP [2].TRK_ID[6:0]	7
0xBB004CB8	LOCAL_PORT_TRK_MAP [3].RESERVED[31:8]	24
0xBB004CB8	LOCAL_PORT_TRK_MAP [3].IS_TRK_MBR[7:7]	1
0xBB004CB8	LOCAL_PORT_TRK_MAP [3].TRK_ID[6:0]	7
0xBB004CBC	LOCAL_PORT_TRK_MAP [4].RESERVED[31:8]	24
0xBB004CBC	LOCAL_PORT_TRK_MAP [4].IS_TRK_MBR[7:7]	1
0xBB004CBC	LOCAL_PORT_TRK_MAP [4].TRK_ID[6:0]	7
0xBB004CC0	LOCAL_PORT_TRK_MAP [5].RESERVED[31:8]	24
0xBB004CC0	LOCAL_PORT_TRK_MAP [5].IS_TRK_MBR[7:7]	1
0xBB004CC0	LOCAL_PORT_TRK_MAP [5].TRK_ID[6:0]	7
0xBB004CC4	LOCAL_PORT_TRK_MAP [6].RESERVED[31:8]	24
0xBB004CC4	LOCAL_PORT_TRK_MAP [6].IS_TRK_MBR[7:7]	1
0xBB004CC4	LOCAL_PORT_TRK_MAP [6].TRK_ID[6:0]	7
0xBB004CC8	LOCAL_PORT_TRK_MAP [7].RESERVED[31:8]	24
0xBB004CC8	LOCAL_PORT_TRK_MAP [7].IS_TRK_MBR[7:7]	1
0xBB004CC8	LOCAL_PORT_TRK_MAP [7].TRK_ID[6:0]	7
0xBB004CCC	LOCAL_PORT_TRK_MAP [8].RESERVED[31:8]	24
0xBB004CCC	LOCAL_PORT_TRK_MAP [8].IS_TRK_MBR[7:7]	1
0xBB004CCC	LOCAL_PORT_TRK_MAP [8].TRK_ID[6:0]	7
0xBB004CD0	LOCAL_PORT_TRK_MAP [9].RESERVED[31:8]	24
0xBB004CD0	LOCAL_PORT_TRK_MAP [9].IS_TRK_MBR[7:7]	1
0xBB004CD0	LOCAL_PORT_TRK_MAP [9].TRK_ID[6:0]	7
0xBB004CD4	LOCAL_PORT_TRK_MAP [10].RESERVED[31:8]	24
0xBB004CD4	LOCAL_PORT_TRK_MAP [10].IS_TRK_MBR[7:7]	1
0xBB004CD4	LOCAL_PORT_TRK_MAP [10].TRK_ID[6:0]	7
0xBB004CD8	LOCAL_PORT_TRK_MAP [11].RESERVED[31:8]	24
0xBB004CD8	LOCAL_PORT_TRK_MAP [11].IS_TRK_MBR[7:7]	1
0xBB004CD8	LOCAL_PORT_TRK_MAP [11].TRK_ID[6:0]	7
0xBB004CDC	LOCAL_PORT_TRK_MAP [12].RESERVED[31:8]	24
0xBB004CDC	LOCAL_PORT_TRK_MAP [12].IS_TRK_MBR[7:7]	1
0xBB004CDC	LOCAL_PORT_TRK_MAP [12].TRK_ID[6:0]	7
0xBB004CE0	LOCAL_PORT_TRK_MAP [13].RESERVED[31:8]	24
0xBB004CE0	LOCAL_PORT_TRK_MAP [13].IS_TRK_MBR[7:7]	1
0xBB004CE0	LOCAL_PORT_TRK_MAP [13].TRK_ID[6:0]	7
0xBB004CE4	LOCAL_PORT_TRK_MAP [14].RESERVED[31:8]	24
0xBB004CE4	LOCAL_PORT_TRK_MAP [14].IS_TRK_MBR[7:7]	1
0xBB004CE4	LOCAL_PORT_TRK_MAP [14].TRK_ID[6:0]	7
0xBB004CE8	LOCAL_PORT_TRK_MAP [15].RESERVED[31:8]	24
0xBB004CE8	LOCAL_PORT_TRK_MAP [15].IS_TRK_MBR[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004CE8	LOCAL_PORT_TRK_MAP [15].TRK_ID[6:0]	7
0xBB004CEC	LOCAL_PORT_TRK_MAP [16].RESERVED[31:8]	24
0xBB004CEC	LOCAL_PORT_TRK_MAP [16].IS_TRK_MBR[7:7]	1
0xBB004CEC	LOCAL_PORT_TRK_MAP [16].TRK_ID[6:0]	7
0xBB004CF0	LOCAL_PORT_TRK_MAP [17].RESERVED[31:8]	24
0xBB004CF0	LOCAL_PORT_TRK_MAP [17].IS_TRK_MBR[7:7]	1
0xBB004CF0	LOCAL_PORT_TRK_MAP [17].TRK_ID[6:0]	7
0xBB004CF4	LOCAL_PORT_TRK_MAP [18].RESERVED[31:8]	24
0xBB004CF4	LOCAL_PORT_TRK_MAP [18].IS_TRK_MBR[7:7]	1
0xBB004CF4	LOCAL_PORT_TRK_MAP [18].TRK_ID[6:0]	7
0xBB004CF8	LOCAL_PORT_TRK_MAP [19].RESERVED[31:8]	24
0xBB004CF8	LOCAL_PORT_TRK_MAP [19].IS_TRK_MBR[7:7]	1
0xBB004CF8	LOCAL_PORT_TRK_MAP [19].TRK_ID[6:0]	7
0xBB004CFC	LOCAL_PORT_TRK_MAP [20].RESERVED[31:8]	24
0xBB004CFC	LOCAL_PORT_TRK_MAP [20].IS_TRK_MBR[7:7]	1
0xBB004CFC	LOCAL_PORT_TRK_MAP [20].TRK_ID[6:0]	7
0xBB004D00	LOCAL_PORT_TRK_MAP [21].RESERVED[31:8]	24
0xBB004D00	LOCAL_PORT_TRK_MAP [21].IS_TRK_MBR[7:7]	1
0xBB004D00	LOCAL_PORT_TRK_MAP [21].TRK_ID[6:0]	7
0xBB004D04	LOCAL_PORT_TRK_MAP [22].RESERVED[31:8]	24
0xBB004D04	LOCAL_PORT_TRK_MAP [22].IS_TRK_MBR[7:7]	1
0xBB004D04	LOCAL_PORT_TRK_MAP [22].TRK_ID[6:0]	7
0xBB004D08	LOCAL_PORT_TRK_MAP [23].RESERVED[31:8]	24
0xBB004D08	LOCAL_PORT_TRK_MAP [23].IS_TRK_MBR[7:7]	1
0xBB004D08	LOCAL_PORT_TRK_MAP [23].TRK_ID[6:0]	7
0xBB004D0C	LOCAL_PORT_TRK_MAP [24].RESERVED[31:8]	24
0xBB004D0C	LOCAL_PORT_TRK_MAP [24].IS_TRK_MBR[7:7]	1
0xBB004D0C	LOCAL_PORT_TRK_MAP [24].TRK_ID[6:0]	7
0xBB004D10	LOCAL_PORT_TRK_MAP [25].RESERVED[31:8]	24
0xBB004D10	LOCAL_PORT_TRK_MAP [25].IS_TRK_MBR[7:7]	1
0xBB004D10	LOCAL_PORT_TRK_MAP [25].TRK_ID[6:0]	7
0xBB004D14	LOCAL_PORT_TRK_MAP [26].RESERVED[31:8]	24
0xBB004D14	LOCAL_PORT_TRK_MAP [26].IS_TRK_MBR[7:7]	1
0xBB004D14	LOCAL_PORT_TRK_MAP [26].TRK_ID[6:0]	7
0xBB004D18	LOCAL_PORT_TRK_MAP [27].RESERVED[31:8]	24
0xBB004D18	LOCAL_PORT_TRK_MAP [27].IS_TRK_MBR[7:7]	1
0xBB004D18	LOCAL_PORT_TRK_MAP [27].TRK_ID[6:0]	7
0xBB004D1C	LOCAL_PORT_TRK_MAP [28].RESERVED[31:8]	24
0xBB004D1C	LOCAL_PORT_TRK_MAP [28].IS_TRK_MBR[7:7]	1
0xBB004D1C	LOCAL_PORT_TRK_MAP [28].TRK_ID[6:0]	7
0xBB004D20	LOCAL_PORT_TRK_MAP [29].RESERVED[31:8]	24
0xBB004D20	LOCAL_PORT_TRK_MAP [29].IS_TRK_MBR[7:7]	1
0xBB004D20	LOCAL_PORT_TRK_MAP [29].TRK_ID[6:0]	7
0xBB004D24	LOCAL_PORT_TRK_MAP [30].RESERVED[31:8]	24
0xBB004D24	LOCAL_PORT_TRK_MAP [30].IS_TRK_MBR[7:7]	1
0xBB004D24	LOCAL_PORT_TRK_MAP [30].TRK_ID[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004D28	LOCAL_PORT_TRK_MAP [31].RESERVED[31:8]	24
0xBB004D28	LOCAL_PORT_TRK_MAP [31].IS_TRK_MBR[7:7]	1
0xBB004D28	LOCAL_PORT_TRK_MAP [31].TRK_ID[6:0]	7
0xBB004D2C	LOCAL_PORT_TRK_MAP [32].RESERVED[31:8]	24
0xBB004D2C	LOCAL_PORT_TRK_MAP [32].IS_TRK_MBR[7:7]	1
0xBB004D2C	LOCAL_PORT_TRK_MAP [32].TRK_ID[6:0]	7
0xBB004D30	LOCAL_PORT_TRK_MAP [33].RESERVED[31:8]	24
0xBB004D30	LOCAL_PORT_TRK_MAP [33].IS_TRK_MBR[7:7]	1
0xBB004D30	LOCAL_PORT_TRK_MAP [33].TRK_ID[6:0]	7
0xBB004D34	LOCAL_PORT_TRK_MAP [34].RESERVED[31:8]	24
0xBB004D34	LOCAL_PORT_TRK_MAP [34].IS_TRK_MBR[7:7]	1
0xBB004D34	LOCAL_PORT_TRK_MAP [34].TRK_ID[6:0]	7
0xBB004D38	LOCAL_PORT_TRK_MAP [35].RESERVED[31:8]	24
0xBB004D38	LOCAL_PORT_TRK_MAP [35].IS_TRK_MBR[7:7]	1
0xBB004D38	LOCAL_PORT_TRK_MAP [35].TRK_ID[6:0]	7
0xBB004D3C	LOCAL_PORT_TRK_MAP [36].RESERVED[31:8]	24
0xBB004D3C	LOCAL_PORT_TRK_MAP [36].IS_TRK_MBR[7:7]	1
0xBB004D3C	LOCAL_PORT_TRK_MAP [36].TRK_ID[6:0]	7
0xBB004D40	LOCAL_PORT_TRK_MAP [37].RESERVED[31:8]	24
0xBB004D40	LOCAL_PORT_TRK_MAP [37].IS_TRK_MBR[7:7]	1
0xBB004D40	LOCAL_PORT_TRK_MAP [37].TRK_ID[6:0]	7
0xBB004D44	LOCAL_PORT_TRK_MAP [38].RESERVED[31:8]	24
0xBB004D44	LOCAL_PORT_TRK_MAP [38].IS_TRK_MBR[7:7]	1
0xBB004D44	LOCAL_PORT_TRK_MAP [38].TRK_ID[6:0]	7
0xBB004D48	LOCAL_PORT_TRK_MAP [39].RESERVED[31:8]	24
0xBB004D48	LOCAL_PORT_TRK_MAP [39].IS_TRK_MBR[7:7]	1
0xBB004D48	LOCAL_PORT_TRK_MAP [39].TRK_ID[6:0]	7
0xBB004D4C	LOCAL_PORT_TRK_MAP [40].RESERVED[31:8]	24
0xBB004D4C	LOCAL_PORT_TRK_MAP [40].IS_TRK_MBR[7:7]	1
0xBB004D4C	LOCAL_PORT_TRK_MAP [40].TRK_ID[6:0]	7
0xBB004D50	LOCAL_PORT_TRK_MAP [41].RESERVED[31:8]	24
0xBB004D50	LOCAL_PORT_TRK_MAP [41].IS_TRK_MBR[7:7]	1
0xBB004D50	LOCAL_PORT_TRK_MAP [41].TRK_ID[6:0]	7
0xBB004D54	LOCAL_PORT_TRK_MAP [42].RESERVED[31:8]	24
0xBB004D54	LOCAL_PORT_TRK_MAP [42].IS_TRK_MBR[7:7]	1
0xBB004D54	LOCAL_PORT_TRK_MAP [42].TRK_ID[6:0]	7
0xBB004D58	LOCAL_PORT_TRK_MAP [43].RESERVED[31:8]	24
0xBB004D58	LOCAL_PORT_TRK_MAP [43].IS_TRK_MBR[7:7]	1
0xBB004D58	LOCAL_PORT_TRK_MAP [43].TRK_ID[6:0]	7
0xBB004D5C	LOCAL_PORT_TRK_MAP [44].RESERVED[31:8]	24
0xBB004D5C	LOCAL_PORT_TRK_MAP [44].IS_TRK_MBR[7:7]	1
0xBB004D5C	LOCAL_PORT_TRK_MAP [44].TRK_ID[6:0]	7
0xBB004D60	LOCAL_PORT_TRK_MAP [45].RESERVED[31:8]	24
0xBB004D60	LOCAL_PORT_TRK_MAP [45].IS_TRK_MBR[7:7]	1
0xBB004D60	LOCAL_PORT_TRK_MAP [45].TRK_ID[6:0]	7
0xBB004D64	LOCAL_PORT_TRK_MAP [46].RESERVED[31:8]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004D64	LOCAL_PORT_TRK_MAP [46].IS_TRK_MBR[7:7]	1
0xBB004D64	LOCAL_PORT_TRK_MAP [46].TRK_ID[6:0]	7
0xBB004D68	LOCAL_PORT_TRK_MAP [47].RESERVED[31:8]	24
0xBB004D68	LOCAL_PORT_TRK_MAP [47].IS_TRK_MBR[7:7]	1
0xBB004D68	LOCAL_PORT_TRK_MAP [47].TRK_ID[6:0]	7
0xBB004D6C	LOCAL_PORT_TRK_MAP [48].RESERVED[31:8]	24
0xBB004D6C	LOCAL_PORT_TRK_MAP [48].IS_TRK_MBR[7:7]	1
0xBB004D6C	LOCAL_PORT_TRK_MAP [48].TRK_ID[6:0]	7
0xBB004D70	LOCAL_PORT_TRK_MAP [49].RESERVED[31:8]	24
0xBB004D70	LOCAL_PORT_TRK_MAP [49].IS_TRK_MBR[7:7]	1
0xBB004D70	LOCAL_PORT_TRK_MAP [49].TRK_ID[6:0]	7
0xBB004D74	LOCAL_PORT_TRK_MAP [50].RESERVED[31:8]	24
0xBB004D74	LOCAL_PORT_TRK_MAP [50].IS_TRK_MBR[7:7]	1
0xBB004D74	LOCAL_PORT_TRK_MAP [50].TRK_ID[6:0]	7
0xBB004D78	LOCAL_PORT_TRK_MAP [51].RESERVED[31:8]	24
0xBB004D78	LOCAL_PORT_TRK_MAP [51].IS_TRK_MBR[7:7]	1
0xBB004D78	LOCAL_PORT_TRK_MAP [51].TRK_ID[6:0]	7
0xBB004D7C	LOCAL_PORT_TRK_MAP [52].RESERVED[31:8]	24
0xBB004D7C	LOCAL_PORT_TRK_MAP [52].IS_TRK_MBR[7:7]	1
0xBB004D7C	LOCAL_PORT_TRK_MAP [52].TRK_ID[6:0]	7
0xBB004D80	LOCAL_PORT_TRK_MAP [53].RESERVED[31:8]	24
0xBB004D80	LOCAL_PORT_TRK_MAP [53].IS_TRK_MBR[7:7]	1
0xBB004D80	LOCAL_PORT_TRK_MAP [53].TRK_ID[6:0]	7
0xBB004D84	LOCAL_PORT_TRK_MAP [54].RESERVED[31:8]	24
0xBB004D84	LOCAL_PORT_TRK_MAP [54].IS_TRK_MBR[7:7]	1
0xBB004D84	LOCAL_PORT_TRK_MAP [54].TRK_ID[6:0]	7
0xBB004D88	LOCAL_PORT_TRK_MAP [55].RESERVED[31:8]	24
0xBB004D88	LOCAL_PORT_TRK_MAP [55].IS_TRK_MBR[7:7]	1
0xBB004D88	LOCAL_PORT_TRK_MAP [55].TRK_ID[6:0]	7
0xBB001364	MAC_CPU_TAG_ID_CTRL.RESERVED[31:16]	16
0xBB001364	MAC_CPU_TAG_ID_CTRL.CPU_TAG_ID[15:0]	16
0xBB000F08	MAC_EEE_ABLTY [0].EEE_ABLTY[0:0]	1
0xBB000F08	MAC_EEE_ABLTY [1].EEE_ABLTY[1:1]	1
0xBB000F08	MAC_EEE_ABLTY [2].EEE_ABLTY[2:2]	1
0xBB000F08	MAC_EEE_ABLTY [3].EEE_ABLTY[3:3]	1
0xBB000F08	MAC_EEE_ABLTY [4].EEE_ABLTY[4:4]	1
0xBB000F08	MAC_EEE_ABLTY [5].EEE_ABLTY[5:5]	1
0xBB000F08	MAC_EEE_ABLTY [6].EEE_ABLTY[6:6]	1
0xBB000F08	MAC_EEE_ABLTY [7].EEE_ABLTY[7:7]	1
0xBB000F08	MAC_EEE_ABLTY [8].EEE_ABLTY[8:8]	1
0xBB000F08	MAC_EEE_ABLTY [9].EEE_ABLTY[9:9]	1
0xBB000F08	MAC_EEE_ABLTY [10].EEE_ABLTY[10:10]	1
0xBB000F08	MAC_EEE_ABLTY [11].EEE_ABLTY[11:11]	1
0xBB000F08	MAC_EEE_ABLTY [12].EEE_ABLTY[12:12]	1
0xBB000F08	MAC_EEE_ABLTY [13].EEE_ABLTY[13:13]	1
0xBB000F08	MAC_EEE_ABLTY [14].EEE_ABLTY[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F08	MAC_EEE_ABLTY [15].EEE_ABLTY[15:15]	1
0xBB000F08	MAC_EEE_ABLTY [16].EEE_ABLTY[16:16]	1
0xBB000F08	MAC_EEE_ABLTY [17].EEE_ABLTY[17:17]	1
0xBB000F08	MAC_EEE_ABLTY [18].EEE_ABLTY[18:18]	1
0xBB000F08	MAC_EEE_ABLTY [19].EEE_ABLTY[19:19]	1
0xBB000F08	MAC_EEE_ABLTY [20].EEE_ABLTY[20:20]	1
0xBB000F08	MAC_EEE_ABLTY [21].EEE_ABLTY[21:21]	1
0xBB000F08	MAC_EEE_ABLTY [22].EEE_ABLTY[22:22]	1
0xBB000F08	MAC_EEE_ABLTY [23].EEE_ABLTY[23:23]	1
0xBB000F08	MAC_EEE_ABLTY [24].EEE_ABLTY[24:24]	1
0xBB000F08	MAC_EEE_ABLTY [25].EEE_ABLTY[25:25]	1
0xBB000F08	MAC_EEE_ABLTY [26].EEE_ABLTY[26:26]	1
0xBB000F08	MAC_EEE_ABLTY [27].EEE_ABLTY[27:27]	1
0xBB000F08	MAC_EEE_ABLTY [28].EEE_ABLTY[28:28]	1
0xBB000F08	MAC_EEE_ABLTY [29].EEE_ABLTY[29:29]	1
0xBB000F08	MAC_EEE_ABLTY [30].EEE_ABLTY[30:30]	1
0xBB000F08	MAC_EEE_ABLTY [31].EEE_ABLTY[31:31]	1
0xBB000F0C	MAC_EEE_ABLTY [32].EEE_ABLTY[0:0]	1
0xBB000F0C	MAC_EEE_ABLTY [33].EEE_ABLTY[1:1]	1
0xBB000F0C	MAC_EEE_ABLTY [34].EEE_ABLTY[2:2]	1
0xBB000F0C	MAC_EEE_ABLTY [35].EEE_ABLTY[3:3]	1
0xBB000F0C	MAC_EEE_ABLTY [36].EEE_ABLTY[4:4]	1
0xBB000F0C	MAC_EEE_ABLTY [37].EEE_ABLTY[5:5]	1
0xBB000F0C	MAC_EEE_ABLTY [38].EEE_ABLTY[6:6]	1
0xBB000F0C	MAC_EEE_ABLTY [39].EEE_ABLTY[7:7]	1
0xBB000F0C	MAC_EEE_ABLTY [40].EEE_ABLTY[8:8]	1
0xBB000F0C	MAC_EEE_ABLTY [41].EEE_ABLTY[9:9]	1
0xBB000F0C	MAC_EEE_ABLTY [42].EEE_ABLTY[10:10]	1
0xBB000F0C	MAC_EEE_ABLTY [43].EEE_ABLTY[11:11]	1
0xBB000F0C	MAC_EEE_ABLTY [44].EEE_ABLTY[12:12]	1
0xBB000F0C	MAC_EEE_ABLTY [45].EEE_ABLTY[13:13]	1
0xBB000F0C	MAC_EEE_ABLTY [46].EEE_ABLTY[14:14]	1
0xBB000F0C	MAC_EEE_ABLTY [47].EEE_ABLTY[15:15]	1
0xBB000F0C	MAC_EEE_ABLTY [48].EEE_ABLTY[16:16]	1
0xBB000F0C	MAC_EEE_ABLTY [49].EEE_ABLTY[17:17]	1
0xBB000F0C	MAC_EEE_ABLTY [50].EEE_ABLTY[18:18]	1
0xBB000F0C	MAC_EEE_ABLTY [51].EEE_ABLTY[19:19]	1
0xBB000F0C	MAC_EEE_ABLTY [52].EEE_ABLTY[20:20]	1
0xBB000F0C	MAC_EEE_ABLTY [53].EEE_ABLTY[21:21]	1
0xBB000F0C	MAC_EEE_ABLTY [54].EEE_ABLTY[22:22]	1
0xBB000F0C	MAC_EEE_ABLTY [55].EEE_ABLTY[23:23]	1
0xBB000F0C	MAC_EEE_ABLTY [56].EEE_ABLTY[24:24]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].RESERVED[31:26]	6
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FEFI_SEL[25:25]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].FORCE_MSTR_SLV[24:24]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].EEE_10G_EN[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].EEE_5G_EN[22:22]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].EEE_2P5G_EN[21:21]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].EEE_1000M_EN[20:20]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].RESERVED[19:19]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].EEE_100M_EN[18:18]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_SPD_SEL[15:12]	4
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_DUP_SEL[11:11]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_MEDIA[10:10]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_LINK[9:9]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].RESERVED[8:8]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_FC_EN[4:4]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DCC	MAC_FORCE_MODE_CTRL [0].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].RESERVED[31:26]	6
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FEFL_SEL[25:25]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].FORCE_MSTR_SLV[24:24]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].EEE_10G_EN[23:23]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].EEE_5G_EN[22:22]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].EEE_2P5G_EN[21:21]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].EEE_1000M_EN[20:20]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].RESERVED[19:19]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].EEE_100M_EN[18:18]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_SPD_SEL[15:12]	4
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_DUP_SEL[11:11]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_MEDIA[10:10]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_LINK[9:9]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].RESERVED[8:8]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_FC_EN[4:4]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DD0	MAC_FORCE_MODE_CTRL [1].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].RESERVED[31:26]	6
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FEFL_SEL[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].FORCE_MSTR_SLV[24:24]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].EEE_10G_EN[23:23]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].EEE_5G_EN[22:22]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].EEE_2P5G_EN[21:21]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].EEE_1000M_EN[20:20]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].RESERVED[19:19]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].EEE_100M_EN[18:18]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_SPD_SEL[15:12]	4
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_DUP_SEL[11:11]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_MEDIA[10:10]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_LINK[9:9]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].RESERVED[8:8]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_FC_EN[4:4]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DD4	MAC_FORCE_MODE_CTRL [2].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].RESERVED[31:26]	6
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FEFI_SEL[25:25]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].FORCE_MSTR_SLV[24:24]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].EEE_10G_EN[23:23]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].EEE_5G_EN[22:22]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].EEE_2P5G_EN[21:21]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].EEE_1000M_EN[20:20]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].RESERVED[19:19]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].EEE_100M_EN[18:18]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_SPD_SEL[15:12]	4
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_DUP_SEL[11:11]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_MEDIA[10:10]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_LINK[9:9]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].RESERVED[8:8]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_FC_EN[4:4]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DD8	MAC_FORCE_MODE_CTRL [3].SMI_FORCE_LINK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].RESERVED[31:26]	6
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FEFL_SEL[25:25]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].FORCE_MSTR_SLV[24:24]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].EEE_10G_EN[23:23]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].EEE_5G_EN[22:22]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].EEE_2P5G_EN[21:21]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].EEE_1000M_EN[20:20]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].RESERVED[19:19]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].EEE_100M_EN[18:18]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_SPD_SEL[15:12]	4
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_DUP_SEL[11:11]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_MEDIA[10:10]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_LINK[9:9]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].RESERVED[8:8]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_FC_EN[4:4]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DDC	MAC_FORCE_MODE_CTRL [4].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].RESERVED[31:26]	6
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FEFL_SEL[25:25]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].FORCE_MSTR_SLV[24:24]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].EEE_10G_EN[23:23]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].EEE_5G_EN[22:22]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].EEE_2P5G_EN[21:21]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].EEE_1000M_EN[20:20]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].RESERVED[19:19]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].EEE_100M_EN[18:18]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_SPD_SEL[15:12]	4
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_DUP_SEL[11:11]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_MEDIA[10:10]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_LINK[9:9]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].RESERVED[8:8]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_FC_EN[4:4]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_DUP_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DE0	MAC_FORCE_MODE_CTRL [5].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].RESERVED[31:26]	6
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FEFI_SEL[25:25]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].FORCE_MSTR_SLV[24:24]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].EEE_10G_EN[23:23]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].EEE_5G_EN[22:22]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].EEE_2P5G_EN[21:21]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].EEE_1000M_EN[20:20]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].RESERVED[19:19]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].EEE_100M_EN[18:18]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_SPD_SEL[15:12]	4
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_DUP_SEL[11:11]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_MEDIA[10:10]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_LINK[9:9]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].RESERVED[8:8]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_FC_EN[4:4]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DE4	MAC_FORCE_MODE_CTRL [6].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].RESERVED[31:26]	6
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FEFI_SEL[25:25]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].FORCE_MSTR_SLV[24:24]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].EEE_10G_EN[23:23]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].EEE_5G_EN[22:22]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].EEE_2P5G_EN[21:21]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].EEE_1000M_EN[20:20]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].RESERVED[19:19]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].EEE_100M_EN[18:18]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_SPD_SEL[15:12]	4
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_DUP_SEL[11:11]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_MEDIA[10:10]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_LINK[9:9]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].RESERVED[8:8]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_FC_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DE8	MAC_FORCE_MODE_CTRL [7].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].RESERVED[31:26]	6
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FEFI_SEL[25:25]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].FORCE_MSTR_SLV[24:24]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].EEE_10G_EN[23:23]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].EEE_5G_EN[22:22]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].EEE_2P5G_EN[21:21]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].EEE_1000M_EN[20:20]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].RESERVED[19:19]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].EEE_100M_EN[18:18]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_SPD_SEL[15:12]	4
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_DUP_SEL[11:11]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_MEDIA[10:10]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_LINK[9:9]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].RESERVED[8:8]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_FC_EN[4:4]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DEC	MAC_FORCE_MODE_CTRL [8].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].RESERVED[31:26]	6
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FEFI_SEL[25:25]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].FORCE_MSTR_SLV[24:24]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].EEE_10G_EN[23:23]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].EEE_5G_EN[22:22]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].EEE_2P5G_EN[21:21]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].EEE_1000M_EN[20:20]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].RESERVED[19:19]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].EEE_100M_EN[18:18]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_SPD_SEL[15:12]	4
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_DUP_SEL[11:11]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_MEDIA[10:10]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_LINK[9:9]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].RESERVED[8:8]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_MSTR_SLV_EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_FC_EN[4:4]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DF0	MAC_FORCE_MODE_CTRL [9].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].RESERVED[31:26]	6
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FEFI_SEL[25:25]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].FORCE_MSTR_SLV[24:24]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].EEE_10G_EN[23:23]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].EEE_5G_EN[22:22]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].EEE_2P5G_EN[21:21]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].EEE_1000M_EN[20:20]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].RESERVED[19:19]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].EEE_100M_EN[18:18]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_SPD_SEL[15:12]	4
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_DUP_SEL[11:11]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_MEDIA[10:10]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_LINK[9:9]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].RESERVED[8:8]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_FEE_EN[5:5]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_FC_EN[4:4]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DF4	MAC_FORCE_MODE_CTRL [10].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].RESERVED[31:26]	6
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FEFI_SEL[25:25]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].FORCE_MSTR_SLV[24:24]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].EEE_10G_EN[23:23]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].EEE_5G_EN[22:22]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].EEE_2P5G_EN[21:21]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].EEE_1000M_EN[20:20]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].RESERVED[19:19]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].EEE_100M_EN[18:18]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_SPD_SEL[15:12]	4
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_DUP_SEL[11:11]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_MEDIA[10:10]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_LINK[9:9]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].RESERVED[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_FC_EN[4:4]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DF8	MAC_FORCE_MODE_CTRL [11].SMI_FORCE_LINK_EN[0:0]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].RESERVED[31:26]	6
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FEFL_SEL[25:25]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].FORCE_MSTR_SLV[24:24]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].EEE_10G_EN[23:23]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].EEE_5G_EN[22:22]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].EEE_2P5G_EN[21:21]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].EEE_1000M_EN[20:20]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].RESERVED[19:19]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].EEE_100M_EN[18:18]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_RX_PAUSE_EN[17:17]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_TX_PAUSE_EN[16:16]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_SPD_SEL[15:12]	4
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_DUP_SEL[11:11]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_MEDIA[10:10]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_LINK[9:9]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].RESERVED[8:8]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_EEE_EN[5:5]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_FC_EN[4:4]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_SPD_EN[3:3]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_DUP_EN[2:2]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000DFC	MAC_FORCE_MODE_CTRL [12].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].RESERVED[31:26]	6
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FEFL_SEL[25:25]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].FORCE_MSTR_SLV[24:24]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].EEE_10G_EN[23:23]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].EEE_5G_EN[22:22]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].EEE_2P5G_EN[21:21]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].EEE_1000M_EN[20:20]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].RESERVED[19:19]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].EEE_100M_EN[18:18]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_SPD_SEL[15:12]	4
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_DUP_SEL[11:11]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_MEDIA[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_LINK[9:9]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].RESERVED[8:8]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_FC_EN[4:4]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E00	MAC_FORCE_MODE_CTRL [13].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].RESERVED[31:26]	6
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FIFI_SEL[25:25]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].FORCE_MSTR_SLV[24:24]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].EEE_10G_EN[23:23]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].EEE_5G_EN[22:22]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].EEE_2P5G_EN[21:21]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].EEE_1000M_EN[20:20]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].RESERVED[19:19]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].EEE_100M_EN[18:18]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_SPD_SEL[15:12]	4
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_DUP_SEL[11:11]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_MEDIA[10:10]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_LINK[9:9]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].RESERVED[8:8]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_FC_EN[4:4]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E04	MAC_FORCE_MODE_CTRL [14].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].RESERVED[31:26]	6
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FIFI_SEL[25:25]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].FORCE_MSTR_SLV[24:24]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].EEE_10G_EN[23:23]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].EEE_5G_EN[22:22]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].EEE_2P5G_EN[21:21]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].EEE_1000M_EN[20:20]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].RESERVED[19:19]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].EEE_100M_EN[18:18]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_SPD_SEL[15:12]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_DUP_SEL[11:11]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_MEDIA[10:10]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_LINK[9:9]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].RESERVED[8:8]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_FC_EN[4:4]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E08	MAC_FORCE_MODE_CTRL [15].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].RESERVED[31:26]	6
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FEFI_SEL[25:25]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].FORCE_MSTR_SLV[24:24]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].EEE_10G_EN[23:23]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].EEE_5G_EN[22:22]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].EEE_2P5G_EN[21:21]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].EEE_1000M_EN[20:20]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].RESERVED[19:19]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].EEE_100M_EN[18:18]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_SPD_SEL[15:12]	4
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_DUP_SEL[11:11]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_MEDIA[10:10]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_LINK[9:9]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].RESERVED[8:8]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_FC_EN[4:4]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E0C	MAC_FORCE_MODE_CTRL [16].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].RESERVED[31:26]	6
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FEFI_SEL[25:25]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].FORCE_MSTR_SLV[24:24]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].EEE_10G_EN[23:23]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].EEE_5G_EN[22:22]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].EEE_2P5G_EN[21:21]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].EEE_1000M_EN[20:20]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].RESERVED[19:19]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].EEE_100M_EN[18:18]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_RX_PAUSE_EN[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_SPD_SEL[15:12]	4
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_DUP_SEL[11:11]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_MEDIA[10:10]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_LINK[9:9]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].RESERVED[8:8]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FEFI_EN[7:7]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_FC_EN[4:4]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E10	MAC_FORCE_MODE_CTRL [17].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].RESERVED[31:26]	6
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FEFI_SEL[25:25]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].FORCE_MSTR_SLV[24:24]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].EEE_10G_EN[23:23]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].EEE_5G_EN[22:22]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].EEE_2P5G_EN[21:21]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].EEE_1000M_EN[20:20]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].RESERVED[19:19]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].EEE_100M_EN[18:18]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_SPD_SEL[15:12]	4
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_DUP_SEL[11:11]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_MEDIA[10:10]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_LINK[9:9]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].RESERVED[8:8]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FEFI_EN[7:7]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_FC_EN[4:4]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E14	MAC_FORCE_MODE_CTRL [18].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].RESERVED[31:26]	6
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FEFI_SEL[25:25]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].FORCE_MSTR_SLV[24:24]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].EEE_10G_EN[23:23]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].EEE_5G_EN[22:22]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].EEE_2P5G_EN[21:21]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].EEE_1000M_EN[20:20]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].RESERVED[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E18	MAC_FORCE_MODE_CTRL [19].EEE_100M_EN[18:18]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_SPD_SEL[15:12]	4
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_DUP_SEL[11:11]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_MEDIA[10:10]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_LINK[9:9]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].RESERVED[8:8]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_FC_EN[4:4]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E18	MAC_FORCE_MODE_CTRL [19].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].RESERVED[31:26]	6
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FEFI_SEL[25:25]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].FORCE_MSTR_SLV[24:24]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].EEE_10G_EN[23:23]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].EEE_5G_EN[22:22]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].EEE_2P5G_EN[21:21]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].EEE_1000M_EN[20:20]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].RESERVED[19:19]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].EEE_100M_EN[18:18]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_SPD_SEL[15:12]	4
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_DUP_SEL[11:11]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_MEDIA[10:10]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_LINK[9:9]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].RESERVED[8:8]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_FC_EN[4:4]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E1C	MAC_FORCE_MODE_CTRL [20].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].RESERVED[31:26]	6
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FEFI_SEL[25:25]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].FORCE_MSTR_SLV[24:24]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].EEE_10G_EN[23:23]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].EEE_5G_EN[22:22]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].EEE_2P5G_EN[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E20	MAC_FORCE_MODE_CTRL [21].EEE_1000M_EN[20:20]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].RESERVED[19:19]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].EEE_100M_EN[18:18]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_SPD_SEL[15:12]	4
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_DUP_SEL[11:11]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_MEDIA[10:10]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_LINK[9:9]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].RESERVED[8:8]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FEFI_EN[7:7]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_FC_EN[4:4]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E20	MAC_FORCE_MODE_CTRL [21].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].RESERVED[31:26]	6
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FEFI_SEL[25:25]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].FORCE_MSTR_SLV[24:24]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].EEE_10G_EN[23:23]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].EEE_5G_EN[22:22]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].EEE_2P5G_EN[21:21]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].EEE_1000M_EN[20:20]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].RESERVED[19:19]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].EEE_100M_EN[18:18]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_SPD_SEL[15:12]	4
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_DUP_SEL[11:11]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_MEDIA[10:10]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_LINK[9:9]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].RESERVED[8:8]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FEFI_EN[7:7]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_FC_EN[4:4]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E24	MAC_FORCE_MODE_CTRL [22].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].RESERVED[31:26]	6
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FEFI_SEL[25:25]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].FORCE_MSTR_SLV[24:24]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].EEE_10G_EN[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E28	MAC_FORCE_MODE_CTRL [23].EEE_5G_EN[22:22]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].EEE_2P5G_EN[21:21]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].EEE_1000M_EN[20:20]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].RESERVED[19:19]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].EEE_100M_EN[18:18]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_SPD_SEL[15:12]	4
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_DUP_SEL[11:11]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_MEDIA[10:10]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_LINK[9:9]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].RESERVED[8:8]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_FC_EN[4:4]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E28	MAC_FORCE_MODE_CTRL [23].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].RESERVED[31:26]	6
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FEFI_SEL[25:25]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].FORCE_MSTR_SLV[24:24]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].EEE_10G_EN[23:23]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].EEE_5G_EN[22:22]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].EEE_2P5G_EN[21:21]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].EEE_1000M_EN[20:20]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].RESERVED[19:19]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].EEE_100M_EN[18:18]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_SPD_SEL[15:12]	4
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_DUP_SEL[11:11]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_MEDIA[10:10]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_LINK[9:9]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].RESERVED[8:8]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_FC_EN[4:4]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E2C	MAC_FORCE_MODE_CTRL [24].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].RESERVED[31:26]	6
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FEFI_SEL[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E30	MAC_FORCE_MODE_CTRL [25].FORCE_MSTR_SLV[24:24]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].EEE_10G_EN[23:23]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].EEE_5G_EN[22:22]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].EEE_2P5G_EN[21:21]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].EEE_1000M_EN[20:20]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].RESERVED[19:19]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].EEE_100M_EN[18:18]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_SPD_SEL[15:12]	4
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_DUP_SEL[11:11]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_MEDIA[10:10]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_LINK[9:9]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].RESERVED[8:8]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_FC_EN[4:4]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E30	MAC_FORCE_MODE_CTRL [25].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].RESERVED[31:26]	6
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FEFI_SEL[25:25]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].FORCE_MSTR_SLV[24:24]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].EEE_10G_EN[23:23]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].EEE_5G_EN[22:22]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].EEE_2P5G_EN[21:21]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].EEE_1000M_EN[20:20]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].RESERVED[19:19]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].EEE_100M_EN[18:18]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_SPD_SEL[15:12]	4
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_DUP_SEL[11:11]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_MEDIA[10:10]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_LINK[9:9]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].RESERVED[8:8]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_FC_EN[4:4]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E34	MAC_FORCE_MODE_CTRL [26].SMI_FORCE_LINK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E38	MAC_FORCE_MODE_CTRL [27].RESERVED[31:26]	6
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FEFI_SEL[25:25]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].FORCE_MSTR_SLV[24:24]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].EEE_10G_EN[23:23]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].EEE_5G_EN[22:22]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].EEE_2P5G_EN[21:21]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].EEE_1000M_EN[20:20]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].RESERVED[19:19]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].EEE_100M_EN[18:18]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_SPD_SEL[15:12]	4
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_DUP_SEL[11:11]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_MEDIA[10:10]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_LINK[9:9]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].RESERVED[8:8]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_FC_EN[4:4]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E38	MAC_FORCE_MODE_CTRL [27].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].RESERVED[31:26]	6
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FEFI_SEL[25:25]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].FORCE_MSTR_SLV[24:24]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].EEE_10G_EN[23:23]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].EEE_5G_EN[22:22]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].EEE_2P5G_EN[21:21]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].EEE_1000M_EN[20:20]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].RESERVED[19:19]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].EEE_100M_EN[18:18]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_SPD_SEL[15:12]	4
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_DUP_SEL[11:11]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_MEDIA[10:10]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_LINK[9:9]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].RESERVED[8:8]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_FC_EN[4:4]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_DUP_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E3C	MAC_FORCE_MODE_CTRL [28].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].RESERVED[31:26]	6
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FEFI_SEL[25:25]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].FORCE_MSTR_SLV[24:24]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].EEE_10G_EN[23:23]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].EEE_5G_EN[22:22]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].EEE_2P5G_EN[21:21]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].EEE_1000M_EN[20:20]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].RESERVED[19:19]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].EEE_100M_EN[18:18]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_SPD_SEL[15:12]	4
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_DUP_SEL[11:11]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_MEDIA[10:10]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_LINK[9:9]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].RESERVED[8:8]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_FC_EN[4:4]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_SPD_EN[3:31]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E40	MAC_FORCE_MODE_CTRL [29].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].RESERVED[31:26]	6
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FEFI_SEL[25:25]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].FORCE_MSTR_SLV[24:24]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].EEE_10G_EN[23:23]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].EEE_5G_EN[22:22]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].EEE_2P5G_EN[21:21]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].EEE_1000M_EN[20:20]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].RESERVED[19:19]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].EEE_100M_EN[18:18]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_SPD_SEL[15:12]	4
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_DUP_SEL[11:11]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_MEDIA[10:10]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_LINK[9:9]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].RESERVED[8:8]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_FC_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E44	MAC_FORCE_MODE_CTRL [30].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].RESERVED[31:26]	6
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FEFI_SEL[25:25]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].FORCE_MSTR_SLV[24:24]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].EEE_10G_EN[23:23]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].EEE_5G_EN[22:22]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].EEE_2P5G_EN[21:21]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].EEE_1000M_EN[20:20]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].RESERVED[19:19]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].EEE_100M_EN[18:18]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_SPD_SEL[15:12]	4
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_DUP_SEL[11:11]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_MEDIA[10:10]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_LINK[9:9]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].RESERVED[8:8]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_FC_EN[4:4]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E48	MAC_FORCE_MODE_CTRL [31].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].RESERVED[31:26]	6
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FEFI_SEL[25:25]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].FORCE_MSTR_SLV[24:24]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].EEE_10G_EN[23:23]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].EEE_5G_EN[22:22]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].EEE_2P5G_EN[21:21]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].EEE_1000M_EN[20:20]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].RESERVED[19:19]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].EEE_100M_EN[18:18]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_SPD_SEL[15:12]	4
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_DUP_SEL[11:11]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_MEDIA[10:10]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_LINK[9:9]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].RESERVED[8:8]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_MSTR_SLV_EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_FC_EN[4:4]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E4C	MAC_FORCE_MODE_CTRL [32].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].RESERVED[31:26]	6
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FEFI_SEL[25:25]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].FORCE_MSTR_SLV[24:24]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].EEE_10G_EN[23:23]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].EEE_5G_EN[22:22]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].EEE_2P5G_EN[21:21]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].EEE_1000M_EN[20:20]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].RESERVED[19:19]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].EEE_100M_EN[18:18]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_SPD_SEL[15:12]	4
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_DUP_SEL[11:11]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_MEDIA[10:10]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_LINK[9:9]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].RESERVED[8:8]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_FEE_EN[5:5]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_FC_EN[4:4]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E50	MAC_FORCE_MODE_CTRL [33].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].RESERVED[31:26]	6
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FEFI_SEL[25:25]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].FORCE_MSTR_SLV[24:24]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].EEE_10G_EN[23:23]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].EEE_5G_EN[22:22]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].EEE_2P5G_EN[21:21]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].EEE_1000M_EN[20:20]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].RESERVED[19:19]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].EEE_100M_EN[18:18]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_SPD_SEL[15:12]	4
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_DUP_SEL[11:11]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_MEDIA[10:10]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_LINK[9:9]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].RESERVED[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_FC_EN[4:4]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E54	MAC_FORCE_MODE_CTRL [34].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].RESERVED[31:26]	6
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FEFL_SEL[25:25]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].FORCE_MSTR_SLV[24:24]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].EEE_10G_EN[23:23]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].EEE_5G_EN[22:22]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].EEE_2P5G_EN[21:21]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].EEE_1000M_EN[20:20]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].RESERVED[19:19]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].EEE_100M_EN[18:18]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_SPD_SEL[15:12]	4
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_DUP_SEL[11:11]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_MEDIA[10:10]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_LINK[9:9]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].RESERVED[8:8]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_FC_EN[4:4]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E58	MAC_FORCE_MODE_CTRL [35].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].RESERVED[31:26]	6
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FEFL_SEL[25:25]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].FORCE_MSTR_SLV[24:24]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].EEE_10G_EN[23:23]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].EEE_5G_EN[22:22]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].EEE_2P5G_EN[21:21]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].EEE_1000M_EN[20:20]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].RESERVED[19:19]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].EEE_100M_EN[18:18]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_SPD_SEL[15:12]	4
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_DUP_SEL[11:11]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_MEDIA[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_LINK[9:9]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].RESERVED[8:8]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_FC_EN[4:4]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E5C	MAC_FORCE_MODE_CTRL [36].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].RESERVED[31:26]	6
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FIFI_SEL[25:25]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].FORCE_MSTR_SLV[24:24]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].EEE_10G_EN[23:23]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].EEE_5G_EN[22:22]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].EEE_2P5G_EN[21:21]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].EEE_1000M_EN[20:20]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].RESERVED[19:19]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].EEE_100M_EN[18:18]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_SPD_SEL[15:12]	4
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_DUP_SEL[11:11]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_MEDIA[10:10]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_LINK[9:9]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].RESERVED[8:8]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_FC_EN[4:4]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E60	MAC_FORCE_MODE_CTRL [37].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].RESERVED[31:26]	6
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FIFI_SEL[25:25]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].FORCE_MSTR_SLV[24:24]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].EEE_10G_EN[23:23]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].EEE_5G_EN[22:22]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].EEE_2P5G_EN[21:21]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].EEE_1000M_EN[20:20]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].RESERVED[19:19]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].EEE_100M_EN[18:18]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_SPD_SEL[15:12]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_DUP_SEL[11:11]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_MEDIA[10:10]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_LINK[9:9]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].RESERVED[8:8]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_FC_EN[4:4]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E64	MAC_FORCE_MODE_CTRL [38].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].RESERVED[31:26]	6
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FEFI_SEL[25:25]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].FORCE_MSTR_SLV[24:24]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].EEE_10G_EN[23:23]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].EEE_5G_EN[22:22]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].EEE_2P5G_EN[21:21]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].EEE_1000M_EN[20:20]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].RESERVED[19:19]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].EEE_100M_EN[18:18]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_SPD_SEL[15:12]	4
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_DUP_SEL[11:11]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_MEDIA[10:10]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_LINK[9:9]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].RESERVED[8:8]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_FC_EN[4:4]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E68	MAC_FORCE_MODE_CTRL [39].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].RESERVED[31:26]	6
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FEFI_SEL[25:25]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].FORCE_MSTR_SLV[24:24]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].EEE_10G_EN[23:23]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].EEE_5G_EN[22:22]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].EEE_2P5G_EN[21:21]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].EEE_1000M_EN[20:20]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].RESERVED[19:19]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].EEE_100M_EN[18:18]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_RX_PAUSE_EN[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_SPD_SEL[15:12]	4
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_DUP_SEL[11:11]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_MEDIA[10:10]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_LINK[9:9]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].RESERVED[8:8]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FEFI_EN[7:7]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_FC_EN[4:4]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E6C	MAC_FORCE_MODE_CTRL [40].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].RESERVED[31:26]	6
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FEFI_SEL[25:25]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].FORCE_MSTR_SLV[24:24]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].EEE_10G_EN[23:23]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].EEE_5G_EN[22:22]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].EEE_2P5G_EN[21:21]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].EEE_1000M_EN[20:20]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].RESERVED[19:19]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].EEE_100M_EN[18:18]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_SPD_SEL[15:12]	4
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_DUP_SEL[11:11]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_MEDIA[10:10]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_LINK[9:9]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].RESERVED[8:8]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FEFI_EN[7:7]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_FC_EN[4:4]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E70	MAC_FORCE_MODE_CTRL [41].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].RESERVED[31:26]	6
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FEFI_SEL[25:25]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].FORCE_MSTR_SLV[24:24]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].EEE_10G_EN[23:23]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].EEE_5G_EN[22:22]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].EEE_2P5G_EN[21:21]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].EEE_1000M_EN[20:20]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].RESERVED[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E74	MAC_FORCE_MODE_CTRL [42].EEE_100M_EN[18:18]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_SPD_SEL[15:12]	4
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_DUP_SEL[11:11]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_MEDIA[10:10]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_LINK[9:9]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].RESERVED[8:8]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_FC_EN[4:4]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E74	MAC_FORCE_MODE_CTRL [42].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].RESERVED[31:26]	6
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FEFI_SEL[25:25]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].FORCE_MSTR_SLV[24:24]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].EEE_10G_EN[23:23]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].EEE_5G_EN[22:22]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].EEE_2P5G_EN[21:21]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].EEE_1000M_EN[20:20]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].RESERVED[19:19]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].EEE_100M_EN[18:18]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_SPD_SEL[15:12]	4
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_DUP_SEL[11:11]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_MEDIA[10:10]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_LINK[9:9]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].RESERVED[8:8]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_FC_EN[4:4]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E78	MAC_FORCE_MODE_CTRL [43].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].RESERVED[31:26]	6
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FEFI_SEL[25:25]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].FORCE_MSTR_SLV[24:24]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].EEE_10G_EN[23:23]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].EEE_5G_EN[22:22]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].EEE_2P5G_EN[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].EEE_1000M_EN[20:20]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].RESERVED[19:19]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].EEE_100M_EN[18:18]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_SPD_SEL[15:12]	4
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_DUP_SEL[11:11]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_MEDIA[10:10]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_LINK[9:9]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].RESERVED[8:8]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FEFI_EN[7:7]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_FC_EN[4:4]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E7C	MAC_FORCE_MODE_CTRL [44].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].RESERVED[31:26]	6
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FEFI_SEL[25:25]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].FORCE_MSTR_SLV[24:24]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].EEE_10G_EN[23:23]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].EEE_5G_EN[22:22]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].EEE_2P5G_EN[21:21]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].EEE_1000M_EN[20:20]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].RESERVED[19:19]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].EEE_100M_EN[18:18]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_SPD_SEL[15:12]	4
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_DUP_SEL[11:11]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_MEDIA[10:10]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_LINK[9:9]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].RESERVED[8:8]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FEFI_EN[7:7]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_FC_EN[4:4]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E80	MAC_FORCE_MODE_CTRL [45].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].RESERVED[31:26]	6
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FEFI_SEL[25:25]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].FORCE_MSTR_SLV[24:24]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].EEE_10G_EN[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E84	MAC_FORCE_MODE_CTRL [46].EEE_5G_EN[22:22]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].EEE_2P5G_EN[21:21]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].EEE_1000M_EN[20:20]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].RESERVED[19:19]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].EEE_100M_EN[18:18]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_SPD_SEL[15:12]	4
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_DUP_SEL[11:11]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_MEDIA[10:10]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_LINK[9:9]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].RESERVED[8:8]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_FC_EN[4:4]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E84	MAC_FORCE_MODE_CTRL [46].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].RESERVED[31:26]	6
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FEFI_SEL[25:25]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].FORCE_MSTR_SLV[24:24]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].EEE_10G_EN[23:23]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].EEE_5G_EN[22:22]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].EEE_2P5G_EN[21:21]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].EEE_1000M_EN[20:20]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].RESERVED[19:19]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].EEE_100M_EN[18:18]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_SPD_SEL[15:12]	4
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_DUP_SEL[11:11]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_MEDIA[10:10]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_LINK[9:9]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].RESERVED[8:8]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_FC_EN[4:4]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E88	MAC_FORCE_MODE_CTRL [47].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].RESERVED[31:26]	6
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FEFI_SEL[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].FORCE_MSTR_SLV[24:24]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].EEE_10G_EN[23:23]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].EEE_5G_EN[22:22]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].EEE_2P5G_EN[21:21]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].EEE_1000M_EN[20:20]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].RESERVED[19:19]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].EEE_100M_EN[18:18]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_SPD_SEL[15:12]	4
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_DUP_SEL[11:11]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_MEDIA[10:10]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_LINK[9:9]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].RESERVED[8:8]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_FC_EN[4:4]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E8C	MAC_FORCE_MODE_CTRL [48].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].RESERVED[31:26]	6
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FEFI_SEL[25:25]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].FORCE_MSTR_SLV[24:24]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].EEE_10G_EN[23:23]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].EEE_5G_EN[22:22]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].EEE_2P5G_EN[21:21]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].EEE_1000M_EN[20:20]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].RESERVED[19:19]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].EEE_100M_EN[18:18]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_SPD_SEL[15:12]	4
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_DUP_SEL[11:11]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_MEDIA[10:10]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_LINK[9:9]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].RESERVED[8:8]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_FC_EN[4:4]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E90	MAC_FORCE_MODE_CTRL [49].SMI_FORCE_LINK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E94	MAC_FORCE_MODE_CTRL [50].RESERVED[31:26]	6
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FEFL_SEL[25:25]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].FORCE_MSTR_SLV[24:24]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].EEE_10G_EN[23:23]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].EEE_5G_EN[22:22]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].EEE_2P5G_EN[21:21]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].EEE_1000M_EN[20:20]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].RESERVED[19:19]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].EEE_100M_EN[18:18]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_SPD_SEL[15:12]	4
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_DUP_SEL[11:11]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_MEDIA[10:10]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_LINK[9:9]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].RESERVED[8:8]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_FC_EN[4:4]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E94	MAC_FORCE_MODE_CTRL [50].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].RESERVED[31:26]	6
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FEFL_SEL[25:25]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].FORCE_MSTR_SLV[24:24]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].EEE_10G_EN[23:23]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].EEE_5G_EN[22:22]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].EEE_2P5G_EN[21:21]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].EEE_1000M_EN[20:20]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].RESERVED[19:19]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].EEE_100M_EN[18:18]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_SPD_SEL[15:12]	4
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_DUP_SEL[11:11]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_MEDIA[10:10]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_LINK[9:9]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].RESERVED[8:8]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_FEFL_EN[7:7]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_FC_EN[4:4]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_SPD_EN[3:3]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_DUP_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E98	MAC_FORCE_MODE_CTRL [51].SMI_FORCE_LINK_EN[0:0]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].RESERVED[31:26]	6
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FEFI_SEL[25:25]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].FORCE_MSTR_SLV[24:24]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].EEE_10G_EN[23:23]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].EEE_5G_EN[22:22]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].EEE_2P5G_EN[21:21]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].EEE_1000M_EN[20:20]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].RESERVED[19:19]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].EEE_100M_EN[18:18]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_RX_PAUSE_EN[17:17]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_TX_PAUSE_EN[16:16]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_SPD_SEL[15:12]	4
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_DUP_SEL[11:11]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_MEDIA[10:10]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_LINK[9:9]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].RESERVED[8:8]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_EEE_EN[5:5]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_FC_EN[4:4]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_SPD_EN[3:31]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_DUP_EN[2:2]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000E9C	MAC_FORCE_MODE_CTRL [52].SMI_FORCE_LINK_EN[0:0]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].RESERVED[31:26]	6
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FEFI_SEL[25:25]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].FORCE_MSTR_SLV[24:24]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].EEE_10G_EN[23:23]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].EEE_5G_EN[22:22]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].EEE_2P5G_EN[21:21]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].EEE_1000M_EN[20:20]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].RESERVED[19:19]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].EEE_100M_EN[18:18]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_RX_PAUSE_EN[17:17]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_TX_PAUSE_EN[16:16]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_SPD_SEL[15:12]	4
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_DUP_SEL[11:11]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_MEDIA[10:10]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_LINK[9:9]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].RESERVED[8:8]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_EEE_EN[5:5]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_FC_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_SPD_EN[3:3]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_DUP_EN[2:2]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000EA0	MAC_FORCE_MODE_CTRL [53].SMI_FORCE_LINK_EN[0:0]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].RESERVED[31:26]	6
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FEFI_SEL[25:25]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].FORCE_MSTR_SLV[24:24]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].EEE_10G_EN[23:23]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].EEE_5G_EN[22:22]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].EEE_2P5G_EN[21:21]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].EEE_1000M_EN[20:20]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].RESERVED[19:19]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].EEE_100M_EN[18:18]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_RX_PAUSE_EN[17:17]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_TX_PAUSE_EN[16:16]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_SPD_SEL[15:12]	4
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_DUP_SEL[11:11]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_MEDIA[10:10]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_LINK[9:9]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].RESERVED[8:8]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_EEE_EN[5:5]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_FC_EN[4:4]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_SPD_EN[3:3]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_DUP_EN[2:2]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000EA4	MAC_FORCE_MODE_CTRL [54].SMI_FORCE_LINK_EN[0:0]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].RESERVED[31:26]	6
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FEFI_SEL[25:25]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].FORCE_MSTR_SLV[24:24]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].EEE_10G_EN[23:23]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].EEE_5G_EN[22:22]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].EEE_2P5G_EN[21:21]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].EEE_1000M_EN[20:20]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].RESERVED[19:19]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].EEE_100M_EN[18:18]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_RX_PAUSE_EN[17:17]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_TX_PAUSE_EN[16:16]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_SPD_SEL[15:12]	4
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_DUP_SEL[11:11]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_MEDIA[10:10]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_LINK[9:9]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].RESERVED[8:8]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_MSTR_SLV_EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_EEE_EN[5:5]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_FC_EN[4:4]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_SPD_EN[3:3]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_DUP_EN[2:2]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000EA8	MAC_FORCE_MODE_CTRL [55].SMI_FORCE_LINK_EN[0:0]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].RESERVED[31:26]	6
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FEFI_SEL[25:25]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].FORCE_MSTR_SLV[24:24]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].EEE_10G_EN[23:23]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].EEE_5G_EN[22:22]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].EEE_2P5G_EN[21:21]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].EEE_1000M_EN[20:20]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].RESERVED[19:19]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].EEE_100M_EN[18:18]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_RX_PAUSE_EN[17:17]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_TX_PAUSE_EN[16:16]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_SPD_SEL[15:12]	4
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_DUP_SEL[11:11]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_MEDIA[10:10]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_LINK[9:9]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].RESERVED[8:8]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_FEFI_EN[7:7]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_MSTR_SLV_EN[6:6]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_FEE_EN[5:5]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_FC_EN[4:4]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_SPD_EN[3:3]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_DUP_EN[2:2]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_MEDIA_EN[1:1]	1
0xBB000EAC	MAC_FORCE_MODE_CTRL [56].SMI_FORCE_LINK_EN[0:0]	1
0xBB005550	MAC_GLB_CTRL.RESERVED[31:14]	18
0xBB005550	MAC_GLB_CTRL.MAC_48PASS1_DROP_EN[13:13]	1
0xBB005550	MAC_GLB_CTRL.ORIGINAL_CRS[12:12]	1
0xBB005550	MAC_GLB_CTRL.ORIGINAL_COL[11:11]	1
0xBB005550	MAC_GLB_CTRL.DEFER_PKT_CONT_SEL[10:10]	1
0xBB005550	MAC_GLB_CTRL.MAX_RETX_SEL[9:9]	1
0xBB005550	MAC_GLB_CTRL.LATE_COLI_DROP_EN[8:8]	1
0xBB005550	MAC_GLB_CTRL.IOL_MAX_RETRY_EN[7:7]	1
0xBB005550	MAC_GLB_CTRL.BKOFF_SPDUP[6:6]	1
0xBB005550	MAC_GLB_CTRL.BKOFF_SEL[5:4]	2
0xBB005550	MAC_GLB_CTRL.HALF_48PASS1_EN[3:3]	1
0xBB005550	MAC_GLB_CTRL.BKPRES_MTHD_SEL[2:2]	1
0xBB005550	MAC_GLB_CTRL.DEFER_IPG_SEL[1:0]	2
0xBB00135C	MAC_L2_ADDR_CTRL.RESERVED[31:16]	16
0xBB00135C	MAC_L2_ADDR_CTRL.SW_MAC_ADDR_47_32[15:0]	16
0xBB001360	MAC_L2_ADDR_CTRL.SW_MAC_ADDR_31_0[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00485C	MAC_L2_CPU_CRC.RESERVED[31:2]	30
0xBB00485C	MAC_L2_CPU_CRC.CRC_CPU_RC_EN[1:0]	2
0xBB001368	MAC_L2_CPU_MAX_LEN_CTRL.RESERVED[31:28]	4
0xBB001368	MAC_L2_CPU_MAX_LEN_CTRL.CPU_PORT_RX_MAX_LEN[27:14]	14
0xBB001368	MAC_L2_CPU_MAX_LEN_CTRL.CPU_PORT_TX_MAX_LEN[13:0]	14
0xBB005548	MAC_L2_GLOBAL_CTRL1.FIB_UNIDIR_WIN_LOW[31:30]	2
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_5G_4NBYTE_COMPS_EN[29:29]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.FWD_PAUSE_TAG_EN[28:28]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.LINK_INTR_EN[27:27]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.FIB_UNIDIR_ONLY_CPUTX_EN[26:26]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_10G_4NBYTE_COMPS_EN[25:25]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_5G_COMPS_EN[24:24]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_5G_SEL[23:23]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_2P5G_COMPS_EN[22:22]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_2P5G_SEL[21:21]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.FWD_PAUSE_EN[20:20]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.FWD_INVLD_MAC_CTRL_EN[19:19]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.FWD_UNKN_OPCODE_EN[18:18]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.LIMIT_PAUSE_EN[17:16]	2
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_1G_100M_10M_COMPS_EN[15:15]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_1G_100M_10M_SEL[14:14]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_10G_COMPS_EN[13:13]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IPG_10G_SEL[12:12]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IOL_LEN_ERR_EN[11:11]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.IOL_MAX_LEN_EN[10:10]	1
0xBB005548	MAC_L2_GLOBAL_CTRL1.LIMIT_IPG_CFG_1G_2P5G[9:5]	5
0xBB005548	MAC_L2_GLOBAL_CTRL1.LIMIT_IPG_CFG_10M_100M[4:0]	5
0xBB001358	MAC_L2_GLOBAL_CTRL2.RESERVED[31:27]	5
0xBB001358	MAC_L2_GLOBAL_CTRL2.IFU_PATCH_EN[26:26]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.HW_RESET_EN[25:25]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.DDR_DATA_BUS_WIDTH_SEL[24:24]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.BYPASS_OC0_PLL[23:23]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.BYPASS_INITIAL[22:22]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.RST_CMD_DIS[21:21]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.RESERVED[20:19]	2
0xBB001358	MAC_L2_GLOBAL_CTRL2.OUTPUT_125M_SEL[18:18]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.SMI_SET3_SEL[17:17]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.LED_SYNC_SEL[16:16]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.JTAG_SEL[15:15]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.SYS_CLK_SEL[14:13]	2
0xBB001358	MAC_L2_GLOBAL_CTRL2.SET3_MDC_EN[12:12]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.SET2_MDC_EN[11:11]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.SET1_MDC_EN[10:10]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.SET0_MDC_EN[9:9]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.SYS_LED_EN[8:8]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.RESERVED[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001358	MAC_L2_GLOBAL_CTRL2.BOOT_SEL[6:6]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.DDR_TYPE_SEL[5:4]	2
0xBB001358	MAC_L2_GLOBAL_CTRL2.POR_SEL[3:3]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.EXT_CPU_EN[2:2]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.REG_IF_SEL[1:1]	1
0xBB001358	MAC_L2_GLOBAL_CTRL2.RESERVED[0:0]	1
0xBB00554C	MAC_L2_PADDING_SEL.RESERVED[31:8]	24
0xBB00554C	MAC_L2_PADDING_SEL.PADDING_SEL[7:0]	8
0xBB006000	MAC_L2_PORT_CTRL [0].PER_PORT_MAC_ECO[31:22]	10
0xBB006000	MAC_L2_PORT_CTRL [0].STK_1G_PKT_FMT[21:21]	1
0xBB006000	MAC_L2_PORT_CTRL [0].TX_IPG[20:5]	16
0xBB006000	MAC_L2_PORT_CTRL [0].PADDING_UND_SIZE_EN[4:4]	1
0xBB006000	MAC_L2_PORT_CTRL [0].PASS_ALL_MODE_EN[3:3]	1
0xBB006000	MAC_L2_PORT_CTRL [0].BYP_TX_CRC[2:2]	1
0xBB006000	MAC_L2_PORT_CTRL [0].TX_EN[1:1]	1
0xBB006000	MAC_L2_PORT_CTRL [0].RX_EN[0:0]	1
0xBB006080	MAC_L2_PORT_CTRL [1].PER_PORT_MAC_ECO[31:22]	10
0xBB006080	MAC_L2_PORT_CTRL [1].STK_1G_PKT_FMT[21:21]	1
0xBB006080	MAC_L2_PORT_CTRL [1].TX_IPG[20:5]	16
0xBB006080	MAC_L2_PORT_CTRL [1].PADDING_UND_SIZE_EN[4:4]	1
0xBB006080	MAC_L2_PORT_CTRL [1].PASS_ALL_MODE_EN[3:3]	1
0xBB006080	MAC_L2_PORT_CTRL [1].BYP_TX_CRC[2:2]	1
0xBB006080	MAC_L2_PORT_CTRL [1].TX_EN[1:1]	1
0xBB006080	MAC_L2_PORT_CTRL [1].RX_EN[0:0]	1
0xBB006100	MAC_L2_PORT_CTRL [2].PER_PORT_MAC_ECO[31:22]	10
0xBB006100	MAC_L2_PORT_CTRL [2].STK_1G_PKT_FMT[21:21]	1
0xBB006100	MAC_L2_PORT_CTRL [2].TX_IPG[20:5]	16
0xBB006100	MAC_L2_PORT_CTRL [2].PADDING_UND_SIZE_EN[4:4]	1
0xBB006100	MAC_L2_PORT_CTRL [2].PASS_ALL_MODE_EN[3:3]	1
0xBB006100	MAC_L2_PORT_CTRL [2].BYP_TX_CRC[2:2]	1
0xBB006100	MAC_L2_PORT_CTRL [2].TX_EN[1:1]	1
0xBB006100	MAC_L2_PORT_CTRL [2].RX_EN[0:0]	1
0xBB006180	MAC_L2_PORT_CTRL [3].PER_PORT_MAC_ECO[31:22]	10
0xBB006180	MAC_L2_PORT_CTRL [3].STK_1G_PKT_FMT[21:21]	1
0xBB006180	MAC_L2_PORT_CTRL [3].TX_IPG[20:5]	16
0xBB006180	MAC_L2_PORT_CTRL [3].PADDING_UND_SIZE_EN[4:4]	1
0xBB006180	MAC_L2_PORT_CTRL [3].PASS_ALL_MODE_EN[3:3]	1
0xBB006180	MAC_L2_PORT_CTRL [3].BYP_TX_CRC[2:2]	1
0xBB006180	MAC_L2_PORT_CTRL [3].TX_EN[1:1]	1
0xBB006180	MAC_L2_PORT_CTRL [3].RX_EN[0:0]	1
0xBB006200	MAC_L2_PORT_CTRL [4].PER_PORT_MAC_ECO[31:22]	10
0xBB006200	MAC_L2_PORT_CTRL [4].STK_1G_PKT_FMT[21:21]	1
0xBB006200	MAC_L2_PORT_CTRL [4].TX_IPG[20:5]	16
0xBB006200	MAC_L2_PORT_CTRL [4].PADDING_UND_SIZE_EN[4:4]	1
0xBB006200	MAC_L2_PORT_CTRL [4].PASS_ALL_MODE_EN[3:3]	1
0xBB006200	MAC_L2_PORT_CTRL [4].BYP_TX_CRC[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006200	MAC_L2_PORT_CTRL [4].TX_EN[1:1]	1
0xBB006200	MAC_L2_PORT_CTRL [4].RX_EN[0:0]	1
0xBB006280	MAC_L2_PORT_CTRL [5].PER_PORT_MAC_ECO[31:22]	10
0xBB006280	MAC_L2_PORT_CTRL [5].STK_1G_PKT_FMT[21:21]	1
0xBB006280	MAC_L2_PORT_CTRL [5].TX_IPG[20:5]	16
0xBB006280	MAC_L2_PORT_CTRL [5].PADDING_UND_SIZE_EN[4:4]	1
0xBB006280	MAC_L2_PORT_CTRL [5].PASS_ALL_MODE_EN[3:3]	1
0xBB006280	MAC_L2_PORT_CTRL [5].BYP_TX_CRC[2:2]	1
0xBB006280	MAC_L2_PORT_CTRL [5].TX_EN[1:1]	1
0xBB006280	MAC_L2_PORT_CTRL [5].RX_EN[0:0]	1
0xBB006300	MAC_L2_PORT_CTRL [6].PER_PORT_MAC_ECO[31:22]	10
0xBB006300	MAC_L2_PORT_CTRL [6].STK_1G_PKT_FMT[21:21]	1
0xBB006300	MAC_L2_PORT_CTRL [6].TX_IPG[20:5]	16
0xBB006300	MAC_L2_PORT_CTRL [6].PADDING_UND_SIZE_EN[4:4]	1
0xBB006300	MAC_L2_PORT_CTRL [6].PASS_ALL_MODE_EN[3:3]	1
0xBB006300	MAC_L2_PORT_CTRL [6].BYP_TX_CRC[2:2]	1
0xBB006300	MAC_L2_PORT_CTRL [6].TX_EN[1:1]	1
0xBB006300	MAC_L2_PORT_CTRL [6].RX_EN[0:0]	1
0xBB006380	MAC_L2_PORT_CTRL [7].PER_PORT_MAC_ECO[31:22]	10
0xBB006380	MAC_L2_PORT_CTRL [7].STK_1G_PKT_FMT[21:21]	1
0xBB006380	MAC_L2_PORT_CTRL [7].TX_IPG[20:5]	16
0xBB006380	MAC_L2_PORT_CTRL [7].PADDING_UND_SIZE_EN[4:4]	1
0xBB006380	MAC_L2_PORT_CTRL [7].PASS_ALL_MODE_EN[3:3]	1
0xBB006380	MAC_L2_PORT_CTRL [7].BYP_TX_CRC[2:2]	1
0xBB006380	MAC_L2_PORT_CTRL [7].TX_EN[1:1]	1
0xBB006380	MAC_L2_PORT_CTRL [7].RX_EN[0:0]	1
0xBB006400	MAC_L2_PORT_CTRL [8].PER_PORT_MAC_ECO[31:22]	10
0xBB006400	MAC_L2_PORT_CTRL [8].STK_1G_PKT_FMT[21:21]	1
0xBB006400	MAC_L2_PORT_CTRL [8].TX_IPG[20:5]	16
0xBB006400	MAC_L2_PORT_CTRL [8].PADDING_UND_SIZE_EN[4:4]	1
0xBB006400	MAC_L2_PORT_CTRL [8].PASS_ALL_MODE_EN[3:3]	1
0xBB006400	MAC_L2_PORT_CTRL [8].BYP_TX_CRC[2:2]	1
0xBB006400	MAC_L2_PORT_CTRL [8].TX_EN[1:1]	1
0xBB006400	MAC_L2_PORT_CTRL [8].RX_EN[0:0]	1
0xBB006480	MAC_L2_PORT_CTRL [9].PER_PORT_MAC_ECO[31:22]	10
0xBB006480	MAC_L2_PORT_CTRL [9].STK_1G_PKT_FMT[21:21]	1
0xBB006480	MAC_L2_PORT_CTRL [9].TX_IPG[20:5]	16
0xBB006480	MAC_L2_PORT_CTRL [9].PADDING_UND_SIZE_EN[4:4]	1
0xBB006480	MAC_L2_PORT_CTRL [9].PASS_ALL_MODE_EN[3:3]	1
0xBB006480	MAC_L2_PORT_CTRL [9].BYP_TX_CRC[2:2]	1
0xBB006480	MAC_L2_PORT_CTRL [9].TX_EN[1:1]	1
0xBB006480	MAC_L2_PORT_CTRL [9].RX_EN[0:0]	1
0xBB006500	MAC_L2_PORT_CTRL [10].PER_PORT_MAC_ECO[31:22]	10
0xBB006500	MAC_L2_PORT_CTRL [10].STK_1G_PKT_FMT[21:21]	1
0xBB006500	MAC_L2_PORT_CTRL [10].TX_IPG[20:5]	16
0xBB006500	MAC_L2_PORT_CTRL [10].PADDING_UND_SIZE_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006500	MAC_L2_PORT_CTRL [10].PASS_ALL_MODE_EN[3:3]	1
0xBB006500	MAC_L2_PORT_CTRL [10].BYP_TX_CRC[2:2]	1
0xBB006500	MAC_L2_PORT_CTRL [10].TX_EN[1:1]	1
0xBB006500	MAC_L2_PORT_CTRL [10].RX_EN[0:0]	1
0xBB006580	MAC_L2_PORT_CTRL [11].PER_PORT_MAC_ECO[31:22]	10
0xBB006580	MAC_L2_PORT_CTRL [11].STK_1G_PKT_FMT[21:21]	1
0xBB006580	MAC_L2_PORT_CTRL [11].TX_IPG[20:5]	16
0xBB006580	MAC_L2_PORT_CTRL [11].PADDING_UND_SIZE_EN[4:4]	1
0xBB006580	MAC_L2_PORT_CTRL [11].PASS_ALL_MODE_EN[3:3]	1
0xBB006580	MAC_L2_PORT_CTRL [11].BYP_TX_CRC[2:2]	1
0xBB006580	MAC_L2_PORT_CTRL [11].TX_EN[1:1]	1
0xBB006580	MAC_L2_PORT_CTRL [11].RX_EN[0:0]	1
0xBB006600	MAC_L2_PORT_CTRL [12].PER_PORT_MAC_ECO[31:22]	10
0xBB006600	MAC_L2_PORT_CTRL [12].STK_1G_PKT_FMT[21:21]	1
0xBB006600	MAC_L2_PORT_CTRL [12].TX_IPG[20:5]	16
0xBB006600	MAC_L2_PORT_CTRL [12].PADDING_UND_SIZE_EN[4:4]	1
0xBB006600	MAC_L2_PORT_CTRL [12].PASS_ALL_MODE_EN[3:3]	1
0xBB006600	MAC_L2_PORT_CTRL [12].BYP_TX_CRC[2:2]	1
0xBB006600	MAC_L2_PORT_CTRL [12].TX_EN[1:1]	1
0xBB006600	MAC_L2_PORT_CTRL [12].RX_EN[0:0]	1
0xBB006680	MAC_L2_PORT_CTRL [13].PER_PORT_MAC_ECO[31:22]	10
0xBB006680	MAC_L2_PORT_CTRL [13].STK_1G_PKT_FMT[21:21]	1
0xBB006680	MAC_L2_PORT_CTRL [13].TX_IPG[20:5]	16
0xBB006680	MAC_L2_PORT_CTRL [13].PADDING_UND_SIZE_EN[4:4]	1
0xBB006680	MAC_L2_PORT_CTRL [13].PASS_ALL_MODE_EN[3:3]	1
0xBB006680	MAC_L2_PORT_CTRL [13].BYP_TX_CRC[2:2]	1
0xBB006680	MAC_L2_PORT_CTRL [13].TX_EN[1:1]	1
0xBB006680	MAC_L2_PORT_CTRL [13].RX_EN[0:0]	1
0xBB006700	MAC_L2_PORT_CTRL [14].PER_PORT_MAC_ECO[31:22]	10
0xBB006700	MAC_L2_PORT_CTRL [14].STK_1G_PKT_FMT[21:21]	1
0xBB006700	MAC_L2_PORT_CTRL [14].TX_IPG[20:5]	16
0xBB006700	MAC_L2_PORT_CTRL [14].PADDING_UND_SIZE_EN[4:4]	1
0xBB006700	MAC_L2_PORT_CTRL [14].PASS_ALL_MODE_EN[3:3]	1
0xBB006700	MAC_L2_PORT_CTRL [14].BYP_TX_CRC[2:2]	1
0xBB006700	MAC_L2_PORT_CTRL [14].TX_EN[1:1]	1
0xBB006700	MAC_L2_PORT_CTRL [14].RX_EN[0:0]	1
0xBB006780	MAC_L2_PORT_CTRL [15].PER_PORT_MAC_ECO[31:22]	10
0xBB006780	MAC_L2_PORT_CTRL [15].STK_1G_PKT_FMT[21:21]	1
0xBB006780	MAC_L2_PORT_CTRL [15].TX_IPG[20:5]	16
0xBB006780	MAC_L2_PORT_CTRL [15].PADDING_UND_SIZE_EN[4:4]	1
0xBB006780	MAC_L2_PORT_CTRL [15].PASS_ALL_MODE_EN[3:3]	1
0xBB006780	MAC_L2_PORT_CTRL [15].BYP_TX_CRC[2:2]	1
0xBB006780	MAC_L2_PORT_CTRL [15].TX_EN[1:1]	1
0xBB006780	MAC_L2_PORT_CTRL [15].RX_EN[0:0]	1
0xBB006800	MAC_L2_PORT_CTRL [16].PER_PORT_MAC_ECO[31:22]	10
0xBB006800	MAC_L2_PORT_CTRL [16].STK_1G_PKT_FMT[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006800	MAC_L2_PORT_CTRL [16].TX_IPG[20:5]	16
0xBB006800	MAC_L2_PORT_CTRL [16].PADDING_UND_SIZE_EN[4:4]	1
0xBB006800	MAC_L2_PORT_CTRL [16].PASS_ALL_MODE_EN[3:3]	1
0xBB006800	MAC_L2_PORT_CTRL [16].BYP_TX_CRC[2:2]	1
0xBB006800	MAC_L2_PORT_CTRL [16].TX_EN[1:1]	1
0xBB006800	MAC_L2_PORT_CTRL [16].RX_EN[0:0]	1
0xBB006880	MAC_L2_PORT_CTRL [17].PER_PORT_MAC_ECO[31:22]	10
0xBB006880	MAC_L2_PORT_CTRL [17].STK_1G_PKT_FMT[21:21]	1
0xBB006880	MAC_L2_PORT_CTRL [17].TX_IPG[20:5]	16
0xBB006880	MAC_L2_PORT_CTRL [17].PADDING_UND_SIZE_EN[4:4]	1
0xBB006880	MAC_L2_PORT_CTRL [17].PASS_ALL_MODE_EN[3:3]	1
0xBB006880	MAC_L2_PORT_CTRL [17].BYP_TX_CRC[2:2]	1
0xBB006880	MAC_L2_PORT_CTRL [17].TX_EN[1:1]	1
0xBB006880	MAC_L2_PORT_CTRL [17].RX_EN[0:0]	1
0xBB006900	MAC_L2_PORT_CTRL [18].PER_PORT_MAC_ECO[31:22]	10
0xBB006900	MAC_L2_PORT_CTRL [18].STK_1G_PKT_FMT[21:21]	1
0xBB006900	MAC_L2_PORT_CTRL [18].TX_IPG[20:5]	16
0xBB006900	MAC_L2_PORT_CTRL [18].PADDING_UND_SIZE_EN[4:4]	1
0xBB006900	MAC_L2_PORT_CTRL [18].PASS_ALL_MODE_EN[3:3]	1
0xBB006900	MAC_L2_PORT_CTRL [18].BYP_TX_CRC[2:2]	1
0xBB006900	MAC_L2_PORT_CTRL [18].TX_EN[1:1]	1
0xBB006900	MAC_L2_PORT_CTRL [18].RX_EN[0:0]	1
0xBB006980	MAC_L2_PORT_CTRL [19].PER_PORT_MAC_ECO[31:22]	10
0xBB006980	MAC_L2_PORT_CTRL [19].STK_1G_PKT_FMT[21:21]	1
0xBB006980	MAC_L2_PORT_CTRL [19].TX_IPG[20:5]	16
0xBB006980	MAC_L2_PORT_CTRL [19].PADDING_UND_SIZE_EN[4:4]	1
0xBB006980	MAC_L2_PORT_CTRL [19].PASS_ALL_MODE_EN[3:3]	1
0xBB006980	MAC_L2_PORT_CTRL [19].BYP_TX_CRC[2:2]	1
0xBB006980	MAC_L2_PORT_CTRL [19].TX_EN[1:1]	1
0xBB006980	MAC_L2_PORT_CTRL [19].RX_EN[0:0]	1
0xBB006A00	MAC_L2_PORT_CTRL [20].PER_PORT_MAC_ECO[31:22]	10
0xBB006A00	MAC_L2_PORT_CTRL [20].STK_1G_PKT_FMT[21:21]	1
0xBB006A00	MAC_L2_PORT_CTRL [20].TX_IPG[20:5]	16
0xBB006A00	MAC_L2_PORT_CTRL [20].PADDING_UND_SIZE_EN[4:4]	1
0xBB006A00	MAC_L2_PORT_CTRL [20].PASS_ALL_MODE_EN[3:3]	1
0xBB006A00	MAC_L2_PORT_CTRL [20].BYP_TX_CRC[2:2]	1
0xBB006A00	MAC_L2_PORT_CTRL [20].TX_EN[1:1]	1
0xBB006A00	MAC_L2_PORT_CTRL [20].RX_EN[0:0]	1
0xBB006A80	MAC_L2_PORT_CTRL [21].PER_PORT_MAC_ECO[31:22]	10
0xBB006A80	MAC_L2_PORT_CTRL [21].STK_1G_PKT_FMT[21:21]	1
0xBB006A80	MAC_L2_PORT_CTRL [21].TX_IPG[20:5]	16
0xBB006A80	MAC_L2_PORT_CTRL [21].PADDING_UND_SIZE_EN[4:4]	1
0xBB006A80	MAC_L2_PORT_CTRL [21].PASS_ALL_MODE_EN[3:3]	1
0xBB006A80	MAC_L2_PORT_CTRL [21].BYP_TX_CRC[2:2]	1
0xBB006A80	MAC_L2_PORT_CTRL [21].TX_EN[1:1]	1
0xBB006A80	MAC_L2_PORT_CTRL [21].RX_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006B00	MAC_L2_PORT_CTRL [22].PER_PORT_MAC_ECO[31:22]	10
0xBB006B00	MAC_L2_PORT_CTRL [22].STK_1G_PKT_FMT[21:21]	1
0xBB006B00	MAC_L2_PORT_CTRL [22].TX_IPG[20:5]	16
0xBB006B00	MAC_L2_PORT_CTRL [22].PADDING_UND_SIZE_EN[4:4]	1
0xBB006B00	MAC_L2_PORT_CTRL [22].PASS_ALL_MODE_EN[3:3]	1
0xBB006B00	MAC_L2_PORT_CTRL [22].BYP_TX_CRC[2:2]	1
0xBB006B00	MAC_L2_PORT_CTRL [22].TX_EN[1:1]	1
0xBB006B00	MAC_L2_PORT_CTRL [22].RX_EN[0:0]	1
0xBB006B80	MAC_L2_PORT_CTRL [23].PER_PORT_MAC_ECO[31:22]	10
0xBB006B80	MAC_L2_PORT_CTRL [23].STK_1G_PKT_FMT[21:21]	1
0xBB006B80	MAC_L2_PORT_CTRL [23].TX_IPG[20:5]	16
0xBB006B80	MAC_L2_PORT_CTRL [23].PADDING_UND_SIZE_EN[4:4]	1
0xBB006B80	MAC_L2_PORT_CTRL [23].PASS_ALL_MODE_EN[3:3]	1
0xBB006B80	MAC_L2_PORT_CTRL [23].BYP_TX_CRC[2:2]	1
0xBB006B80	MAC_L2_PORT_CTRL [23].TX_EN[1:1]	1
0xBB006B80	MAC_L2_PORT_CTRL [23].RX_EN[0:0]	1
0xBB006C00	MAC_L2_PORT_CTRL [24].PER_PORT_MAC_ECO[31:22]	10
0xBB006C00	MAC_L2_PORT_CTRL [24].STK_1G_PKT_FMT[21:21]	1
0xBB006C00	MAC_L2_PORT_CTRL [24].TX_IPG[20:5]	16
0xBB006C00	MAC_L2_PORT_CTRL [24].PADDING_UND_SIZE_EN[4:4]	1
0xBB006C00	MAC_L2_PORT_CTRL [24].PASS_ALL_MODE_EN[3:3]	1
0xBB006C00	MAC_L2_PORT_CTRL [24].BYP_TX_CRC[2:2]	1
0xBB006C00	MAC_L2_PORT_CTRL [24].TX_EN[1:1]	1
0xBB006C00	MAC_L2_PORT_CTRL [24].RX_EN[0:0]	1
0xBB006C80	MAC_L2_PORT_CTRL [25].PER_PORT_MAC_ECO[31:22]	10
0xBB006C80	MAC_L2_PORT_CTRL [25].STK_1G_PKT_FMT[21:21]	1
0xBB006C80	MAC_L2_PORT_CTRL [25].TX_IPG[20:5]	16
0xBB006C80	MAC_L2_PORT_CTRL [25].PADDING_UND_SIZE_EN[4:4]	1
0xBB006C80	MAC_L2_PORT_CTRL [25].PASS_ALL_MODE_EN[3:3]	1
0xBB006C80	MAC_L2_PORT_CTRL [25].BYP_TX_CRC[2:2]	1
0xBB006C80	MAC_L2_PORT_CTRL [25].TX_EN[1:1]	1
0xBB006C80	MAC_L2_PORT_CTRL [25].RX_EN[0:0]	1
0xBB006D00	MAC_L2_PORT_CTRL [26].PER_PORT_MAC_ECO[31:22]	10
0xBB006D00	MAC_L2_PORT_CTRL [26].STK_1G_PKT_FMT[21:21]	1
0xBB006D00	MAC_L2_PORT_CTRL [26].TX_IPG[20:5]	16
0xBB006D00	MAC_L2_PORT_CTRL [26].PADDING_UND_SIZE_EN[4:4]	1
0xBB006D00	MAC_L2_PORT_CTRL [26].PASS_ALL_MODE_EN[3:3]	1
0xBB006D00	MAC_L2_PORT_CTRL [26].BYP_TX_CRC[2:2]	1
0xBB006D00	MAC_L2_PORT_CTRL [26].TX_EN[1:1]	1
0xBB006D00	MAC_L2_PORT_CTRL [26].RX_EN[0:0]	1
0xBB006D80	MAC_L2_PORT_CTRL [27].PER_PORT_MAC_ECO[31:22]	10
0xBB006D80	MAC_L2_PORT_CTRL [27].STK_1G_PKT_FMT[21:21]	1
0xBB006D80	MAC_L2_PORT_CTRL [27].TX_IPG[20:5]	16
0xBB006D80	MAC_L2_PORT_CTRL [27].PADDING_UND_SIZE_EN[4:4]	1
0xBB006D80	MAC_L2_PORT_CTRL [27].PASS_ALL_MODE_EN[3:3]	1
0xBB006D80	MAC_L2_PORT_CTRL [27].BYP_TX_CRC[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006D80	MAC_L2_PORT_CTRL [27].TX_EN[1:1]	1
0xBB006D80	MAC_L2_PORT_CTRL [27].RX_EN[0:0]	1
0xBB006E00	MAC_L2_PORT_CTRL [28].PER_PORT_MAC_ECO[31:22]	10
0xBB006E00	MAC_L2_PORT_CTRL [28].STK_1G_PKT_FMT[21:21]	1
0xBB006E00	MAC_L2_PORT_CTRL [28].TX_IPG[20:5]	16
0xBB006E00	MAC_L2_PORT_CTRL [28].PADDING_UND_SIZE_EN[4:4]	1
0xBB006E00	MAC_L2_PORT_CTRL [28].PASS_ALL_MODE_EN[3:3]	1
0xBB006E00	MAC_L2_PORT_CTRL [28].BYP_TX_CRC[2:2]	1
0xBB006E00	MAC_L2_PORT_CTRL [28].TX_EN[1:1]	1
0xBB006E00	MAC_L2_PORT_CTRL [28].RX_EN[0:0]	1
0xBB006E80	MAC_L2_PORT_CTRL [29].PER_PORT_MAC_ECO[31:22]	10
0xBB006E80	MAC_L2_PORT_CTRL [29].STK_1G_PKT_FMT[21:21]	1
0xBB006E80	MAC_L2_PORT_CTRL [29].TX_IPG[20:5]	16
0xBB006E80	MAC_L2_PORT_CTRL [29].PADDING_UND_SIZE_EN[4:4]	1
0xBB006E80	MAC_L2_PORT_CTRL [29].PASS_ALL_MODE_EN[3:3]	1
0xBB006E80	MAC_L2_PORT_CTRL [29].BYP_TX_CRC[2:2]	1
0xBB006E80	MAC_L2_PORT_CTRL [29].TX_EN[1:1]	1
0xBB006E80	MAC_L2_PORT_CTRL [29].RX_EN[0:0]	1
0xBB006F00	MAC_L2_PORT_CTRL [30].PER_PORT_MAC_ECO[31:22]	10
0xBB006F00	MAC_L2_PORT_CTRL [30].STK_1G_PKT_FMT[21:21]	1
0xBB006F00	MAC_L2_PORT_CTRL [30].TX_IPG[20:5]	16
0xBB006F00	MAC_L2_PORT_CTRL [30].PADDING_UND_SIZE_EN[4:4]	1
0xBB006F00	MAC_L2_PORT_CTRL [30].PASS_ALL_MODE_EN[3:3]	1
0xBB006F00	MAC_L2_PORT_CTRL [30].BYP_TX_CRC[2:2]	1
0xBB006F00	MAC_L2_PORT_CTRL [30].TX_EN[1:1]	1
0xBB006F00	MAC_L2_PORT_CTRL [30].RX_EN[0:0]	1
0xBB006F80	MAC_L2_PORT_CTRL [31].PER_PORT_MAC_ECO[31:22]	10
0xBB006F80	MAC_L2_PORT_CTRL [31].STK_1G_PKT_FMT[21:21]	1
0xBB006F80	MAC_L2_PORT_CTRL [31].TX_IPG[20:5]	16
0xBB006F80	MAC_L2_PORT_CTRL [31].PADDING_UND_SIZE_EN[4:4]	1
0xBB006F80	MAC_L2_PORT_CTRL [31].PASS_ALL_MODE_EN[3:3]	1
0xBB006F80	MAC_L2_PORT_CTRL [31].BYP_TX_CRC[2:2]	1
0xBB006F80	MAC_L2_PORT_CTRL [31].TX_EN[1:1]	1
0xBB006F80	MAC_L2_PORT_CTRL [31].RX_EN[0:0]	1
0xBB007000	MAC_L2_PORT_CTRL [32].PER_PORT_MAC_ECO[31:22]	10
0xBB007000	MAC_L2_PORT_CTRL [32].STK_1G_PKT_FMT[21:21]	1
0xBB007000	MAC_L2_PORT_CTRL [32].TX_IPG[20:5]	16
0xBB007000	MAC_L2_PORT_CTRL [32].PADDING_UND_SIZE_EN[4:4]	1
0xBB007000	MAC_L2_PORT_CTRL [32].PASS_ALL_MODE_EN[3:3]	1
0xBB007000	MAC_L2_PORT_CTRL [32].BYP_TX_CRC[2:2]	1
0xBB007000	MAC_L2_PORT_CTRL [32].TX_EN[1:1]	1
0xBB007000	MAC_L2_PORT_CTRL [32].RX_EN[0:0]	1
0xBB007080	MAC_L2_PORT_CTRL [33].PER_PORT_MAC_ECO[31:22]	10
0xBB007080	MAC_L2_PORT_CTRL [33].STK_1G_PKT_FMT[21:21]	1
0xBB007080	MAC_L2_PORT_CTRL [33].TX_IPG[20:5]	16
0xBB007080	MAC_L2_PORT_CTRL [33].PADDING_UND_SIZE_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007080	MAC_L2_PORT_CTRL [33].PASS_ALL_MODE_EN[3:3]	1
0xBB007080	MAC_L2_PORT_CTRL [33].BYP_TX_CRC[2:2]	1
0xBB007080	MAC_L2_PORT_CTRL [33].TX_EN[1:1]	1
0xBB007080	MAC_L2_PORT_CTRL [33].RX_EN[0:0]	1
0xBB007100	MAC_L2_PORT_CTRL [34].PER_PORT_MAC_ECO[31:22]	10
0xBB007100	MAC_L2_PORT_CTRL [34].STK_1G_PKT_FMT[21:21]	1
0xBB007100	MAC_L2_PORT_CTRL [34].TX_IPG[20:5]	16
0xBB007100	MAC_L2_PORT_CTRL [34].PADDING_UND_SIZE_EN[4:4]	1
0xBB007100	MAC_L2_PORT_CTRL [34].PASS_ALL_MODE_EN[3:3]	1
0xBB007100	MAC_L2_PORT_CTRL [34].BYP_TX_CRC[2:2]	1
0xBB007100	MAC_L2_PORT_CTRL [34].TX_EN[1:1]	1
0xBB007100	MAC_L2_PORT_CTRL [34].RX_EN[0:0]	1
0xBB007180	MAC_L2_PORT_CTRL [35].PER_PORT_MAC_ECO[31:22]	10
0xBB007180	MAC_L2_PORT_CTRL [35].STK_1G_PKT_FMT[21:21]	1
0xBB007180	MAC_L2_PORT_CTRL [35].TX_IPG[20:5]	16
0xBB007180	MAC_L2_PORT_CTRL [35].PADDING_UND_SIZE_EN[4:4]	1
0xBB007180	MAC_L2_PORT_CTRL [35].PASS_ALL_MODE_EN[3:3]	1
0xBB007180	MAC_L2_PORT_CTRL [35].BYP_TX_CRC[2:2]	1
0xBB007180	MAC_L2_PORT_CTRL [35].TX_EN[1:1]	1
0xBB007180	MAC_L2_PORT_CTRL [35].RX_EN[0:0]	1
0xBB007200	MAC_L2_PORT_CTRL [36].PER_PORT_MAC_ECO[31:22]	10
0xBB007200	MAC_L2_PORT_CTRL [36].STK_1G_PKT_FMT[21:21]	1
0xBB007200	MAC_L2_PORT_CTRL [36].TX_IPG[20:5]	16
0xBB007200	MAC_L2_PORT_CTRL [36].PADDING_UND_SIZE_EN[4:4]	1
0xBB007200	MAC_L2_PORT_CTRL [36].PASS_ALL_MODE_EN[3:3]	1
0xBB007200	MAC_L2_PORT_CTRL [36].BYP_TX_CRC[2:2]	1
0xBB007200	MAC_L2_PORT_CTRL [36].TX_EN[1:1]	1
0xBB007200	MAC_L2_PORT_CTRL [36].RX_EN[0:0]	1
0xBB007280	MAC_L2_PORT_CTRL [37].PER_PORT_MAC_ECO[31:22]	10
0xBB007280	MAC_L2_PORT_CTRL [37].STK_1G_PKT_FMT[21:21]	1
0xBB007280	MAC_L2_PORT_CTRL [37].TX_IPG[20:5]	16
0xBB007280	MAC_L2_PORT_CTRL [37].PADDING_UND_SIZE_EN[4:4]	1
0xBB007280	MAC_L2_PORT_CTRL [37].PASS_ALL_MODE_EN[3:3]	1
0xBB007280	MAC_L2_PORT_CTRL [37].BYP_TX_CRC[2:2]	1
0xBB007280	MAC_L2_PORT_CTRL [37].TX_EN[1:1]	1
0xBB007280	MAC_L2_PORT_CTRL [37].RX_EN[0:0]	1
0xBB007300	MAC_L2_PORT_CTRL [38].PER_PORT_MAC_ECO[31:22]	10
0xBB007300	MAC_L2_PORT_CTRL [38].STK_1G_PKT_FMT[21:21]	1
0xBB007300	MAC_L2_PORT_CTRL [38].TX_IPG[20:5]	16
0xBB007300	MAC_L2_PORT_CTRL [38].PADDING_UND_SIZE_EN[4:4]	1
0xBB007300	MAC_L2_PORT_CTRL [38].PASS_ALL_MODE_EN[3:3]	1
0xBB007300	MAC_L2_PORT_CTRL [38].BYP_TX_CRC[2:2]	1
0xBB007300	MAC_L2_PORT_CTRL [38].TX_EN[1:1]	1
0xBB007300	MAC_L2_PORT_CTRL [38].RX_EN[0:0]	1
0xBB007380	MAC_L2_PORT_CTRL [39].PER_PORT_MAC_ECO[31:22]	10
0xBB007380	MAC_L2_PORT_CTRL [39].STK_1G_PKT_FMT[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007380	MAC_L2_PORT_CTRL [39].TX_IPG[20:5]	16
0xBB007380	MAC_L2_PORT_CTRL [39].PADDING_UND_SIZE_EN[4:4]	1
0xBB007380	MAC_L2_PORT_CTRL [39].PASS_ALL_MODE_EN[3:3]	1
0xBB007380	MAC_L2_PORT_CTRL [39].BYP_TX_CRC[2:2]	1
0xBB007380	MAC_L2_PORT_CTRL [39].TX_EN[1:1]	1
0xBB007380	MAC_L2_PORT_CTRL [39].RX_EN[0:0]	1
0xBB007400	MAC_L2_PORT_CTRL [40].PER_PORT_MAC_ECO[31:22]	10
0xBB007400	MAC_L2_PORT_CTRL [40].STK_1G_PKT_FMT[21:21]	1
0xBB007400	MAC_L2_PORT_CTRL [40].TX_IPG[20:5]	16
0xBB007400	MAC_L2_PORT_CTRL [40].PADDING_UND_SIZE_EN[4:4]	1
0xBB007400	MAC_L2_PORT_CTRL [40].PASS_ALL_MODE_EN[3:3]	1
0xBB007400	MAC_L2_PORT_CTRL [40].BYP_TX_CRC[2:2]	1
0xBB007400	MAC_L2_PORT_CTRL [40].TX_EN[1:1]	1
0xBB007400	MAC_L2_PORT_CTRL [40].RX_EN[0:0]	1
0xBB007480	MAC_L2_PORT_CTRL [41].PER_PORT_MAC_ECO[31:22]	10
0xBB007480	MAC_L2_PORT_CTRL [41].STK_1G_PKT_FMT[21:21]	1
0xBB007480	MAC_L2_PORT_CTRL [41].TX_IPG[20:5]	16
0xBB007480	MAC_L2_PORT_CTRL [41].PADDING_UND_SIZE_EN[4:4]	1
0xBB007480	MAC_L2_PORT_CTRL [41].PASS_ALL_MODE_EN[3:3]	1
0xBB007480	MAC_L2_PORT_CTRL [41].BYP_TX_CRC[2:2]	1
0xBB007480	MAC_L2_PORT_CTRL [41].TX_EN[1:1]	1
0xBB007480	MAC_L2_PORT_CTRL [41].RX_EN[0:0]	1
0xBB007500	MAC_L2_PORT_CTRL [42].PER_PORT_MAC_ECO[31:22]	10
0xBB007500	MAC_L2_PORT_CTRL [42].STK_1G_PKT_FMT[21:21]	1
0xBB007500	MAC_L2_PORT_CTRL [42].TX_IPG[20:5]	16
0xBB007500	MAC_L2_PORT_CTRL [42].PADDING_UND_SIZE_EN[4:4]	1
0xBB007500	MAC_L2_PORT_CTRL [42].PASS_ALL_MODE_EN[3:3]	1
0xBB007500	MAC_L2_PORT_CTRL [42].BYP_TX_CRC[2:2]	1
0xBB007500	MAC_L2_PORT_CTRL [42].TX_EN[1:1]	1
0xBB007500	MAC_L2_PORT_CTRL [42].RX_EN[0:0]	1
0xBB007580	MAC_L2_PORT_CTRL [43].PER_PORT_MAC_ECO[31:22]	10
0xBB007580	MAC_L2_PORT_CTRL [43].STK_1G_PKT_FMT[21:21]	1
0xBB007580	MAC_L2_PORT_CTRL [43].TX_IPG[20:5]	16
0xBB007580	MAC_L2_PORT_CTRL [43].PADDING_UND_SIZE_EN[4:4]	1
0xBB007580	MAC_L2_PORT_CTRL [43].PASS_ALL_MODE_EN[3:3]	1
0xBB007580	MAC_L2_PORT_CTRL [43].BYP_TX_CRC[2:2]	1
0xBB007580	MAC_L2_PORT_CTRL [43].TX_EN[1:1]	1
0xBB007580	MAC_L2_PORT_CTRL [43].RX_EN[0:0]	1
0xBB007600	MAC_L2_PORT_CTRL [44].PER_PORT_MAC_ECO[31:22]	10
0xBB007600	MAC_L2_PORT_CTRL [44].STK_1G_PKT_FMT[21:21]	1
0xBB007600	MAC_L2_PORT_CTRL [44].TX_IPG[20:5]	16
0xBB007600	MAC_L2_PORT_CTRL [44].PADDING_UND_SIZE_EN[4:4]	1
0xBB007600	MAC_L2_PORT_CTRL [44].PASS_ALL_MODE_EN[3:3]	1
0xBB007600	MAC_L2_PORT_CTRL [44].BYP_TX_CRC[2:2]	1
0xBB007600	MAC_L2_PORT_CTRL [44].TX_EN[1:1]	1
0xBB007600	MAC_L2_PORT_CTRL [44].RX_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007680	MAC_L2_PORT_CTRL [45].PER_PORT_MAC_ECO[31:22]	10
0xBB007680	MAC_L2_PORT_CTRL [45].STK_1G_PKT_FMT[21:21]	1
0xBB007680	MAC_L2_PORT_CTRL [45].TX_IPG[20:5]	16
0xBB007680	MAC_L2_PORT_CTRL [45].PADDING_UND_SIZE_EN[4:4]	1
0xBB007680	MAC_L2_PORT_CTRL [45].PASS_ALL_MODE_EN[3:3]	1
0xBB007680	MAC_L2_PORT_CTRL [45].BYP_TX_CRC[2:2]	1
0xBB007680	MAC_L2_PORT_CTRL [45].TX_EN[1:1]	1
0xBB007680	MAC_L2_PORT_CTRL [45].RX_EN[0:0]	1
0xBB007700	MAC_L2_PORT_CTRL [46].PER_PORT_MAC_ECO[31:22]	10
0xBB007700	MAC_L2_PORT_CTRL [46].STK_1G_PKT_FMT[21:21]	1
0xBB007700	MAC_L2_PORT_CTRL [46].TX_IPG[20:5]	16
0xBB007700	MAC_L2_PORT_CTRL [46].PADDING_UND_SIZE_EN[4:4]	1
0xBB007700	MAC_L2_PORT_CTRL [46].PASS_ALL_MODE_EN[3:3]	1
0xBB007700	MAC_L2_PORT_CTRL [46].BYP_TX_CRC[2:2]	1
0xBB007700	MAC_L2_PORT_CTRL [46].TX_EN[1:1]	1
0xBB007700	MAC_L2_PORT_CTRL [46].RX_EN[0:0]	1
0xBB007780	MAC_L2_PORT_CTRL [47].PER_PORT_MAC_ECO[31:22]	10
0xBB007780	MAC_L2_PORT_CTRL [47].STK_1G_PKT_FMT[21:21]	1
0xBB007780	MAC_L2_PORT_CTRL [47].TX_IPG[20:5]	16
0xBB007780	MAC_L2_PORT_CTRL [47].PADDING_UND_SIZE_EN[4:4]	1
0xBB007780	MAC_L2_PORT_CTRL [47].PASS_ALL_MODE_EN[3:3]	1
0xBB007780	MAC_L2_PORT_CTRL [47].BYP_TX_CRC[2:2]	1
0xBB007780	MAC_L2_PORT_CTRL [47].TX_EN[1:1]	1
0xBB007780	MAC_L2_PORT_CTRL [47].RX_EN[0:0]	1
0xBB007800	MAC_L2_PORT_CTRL [48].PER_PORT_MAC_ECO[31:22]	10
0xBB007800	MAC_L2_PORT_CTRL [48].STK_1G_PKT_FMT[21:21]	1
0xBB007800	MAC_L2_PORT_CTRL [48].TX_IPG[20:5]	16
0xBB007800	MAC_L2_PORT_CTRL [48].PADDING_UND_SIZE_EN[4:4]	1
0xBB007800	MAC_L2_PORT_CTRL [48].PASS_ALL_MODE_EN[3:3]	1
0xBB007800	MAC_L2_PORT_CTRL [48].BYP_TX_CRC[2:2]	1
0xBB007800	MAC_L2_PORT_CTRL [48].TX_EN[1:1]	1
0xBB007800	MAC_L2_PORT_CTRL [48].RX_EN[0:0]	1
0xBB007880	MAC_L2_PORT_CTRL [49].PER_PORT_MAC_ECO[31:22]	10
0xBB007880	MAC_L2_PORT_CTRL [49].STK_1G_PKT_FMT[21:21]	1
0xBB007880	MAC_L2_PORT_CTRL [49].TX_IPG[20:5]	16
0xBB007880	MAC_L2_PORT_CTRL [49].PADDING_UND_SIZE_EN[4:4]	1
0xBB007880	MAC_L2_PORT_CTRL [49].PASS_ALL_MODE_EN[3:3]	1
0xBB007880	MAC_L2_PORT_CTRL [49].BYP_TX_CRC[2:2]	1
0xBB007880	MAC_L2_PORT_CTRL [49].TX_EN[1:1]	1
0xBB007880	MAC_L2_PORT_CTRL [49].RX_EN[0:0]	1
0xBB007900	MAC_L2_PORT_CTRL [50].PER_PORT_MAC_ECO[31:22]	10
0xBB007900	MAC_L2_PORT_CTRL [50].STK_1G_PKT_FMT[21:21]	1
0xBB007900	MAC_L2_PORT_CTRL [50].TX_IPG[20:5]	16
0xBB007900	MAC_L2_PORT_CTRL [50].PADDING_UND_SIZE_EN[4:4]	1
0xBB007900	MAC_L2_PORT_CTRL [50].PASS_ALL_MODE_EN[3:3]	1
0xBB007900	MAC_L2_PORT_CTRL [50].BYP_TX_CRC[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007900	MAC_L2_PORT_CTRL [50].TX_EN[1:1]	1
0xBB007900	MAC_L2_PORT_CTRL [50].RX_EN[0:0]	1
0xBB007980	MAC_L2_PORT_CTRL [51].PER_PORT_MAC_ECO[31:22]	10
0xBB007980	MAC_L2_PORT_CTRL [51].STK_1G_PKT_FMT[21:21]	1
0xBB007980	MAC_L2_PORT_CTRL [51].TX_IPG[20:5]	16
0xBB007980	MAC_L2_PORT_CTRL [51].PADDING_UND_SIZE_EN[4:4]	1
0xBB007980	MAC_L2_PORT_CTRL [51].PASS_ALL_MODE_EN[3:3]	1
0xBB007980	MAC_L2_PORT_CTRL [51].BYP_TX_CRC[2:2]	1
0xBB007980	MAC_L2_PORT_CTRL [51].TX_EN[1:1]	1
0xBB007980	MAC_L2_PORT_CTRL [51].RX_EN[0:0]	1
0xBB007A00	MAC_L2_PORT_CTRL [52].PER_PORT_MAC_ECO[31:22]	10
0xBB007A00	MAC_L2_PORT_CTRL [52].STK_1G_PKT_FMT[21:21]	1
0xBB007A00	MAC_L2_PORT_CTRL [52].TX_IPG[20:5]	16
0xBB007A00	MAC_L2_PORT_CTRL [52].PADDING_UND_SIZE_EN[4:4]	1
0xBB007A00	MAC_L2_PORT_CTRL [52].PASS_ALL_MODE_EN[3:3]	1
0xBB007A00	MAC_L2_PORT_CTRL [52].BYP_TX_CRC[2:2]	1
0xBB007A00	MAC_L2_PORT_CTRL [52].TX_EN[1:1]	1
0xBB007A00	MAC_L2_PORT_CTRL [52].RX_EN[0:0]	1
0xBB007A80	MAC_L2_PORT_CTRL [53].PER_PORT_MAC_ECO[31:22]	10
0xBB007A80	MAC_L2_PORT_CTRL [53].STK_1G_PKT_FMT[21:21]	1
0xBB007A80	MAC_L2_PORT_CTRL [53].TX_IPG[20:5]	16
0xBB007A80	MAC_L2_PORT_CTRL [53].PADDING_UND_SIZE_EN[4:4]	1
0xBB007A80	MAC_L2_PORT_CTRL [53].PASS_ALL_MODE_EN[3:3]	1
0xBB007A80	MAC_L2_PORT_CTRL [53].BYP_TX_CRC[2:2]	1
0xBB007A80	MAC_L2_PORT_CTRL [53].TX_EN[1:1]	1
0xBB007A80	MAC_L2_PORT_CTRL [53].RX_EN[0:0]	1
0xBB007B00	MAC_L2_PORT_CTRL [54].PER_PORT_MAC_ECO[31:22]	10
0xBB007B00	MAC_L2_PORT_CTRL [54].STK_1G_PKT_FMT[21:21]	1
0xBB007B00	MAC_L2_PORT_CTRL [54].TX_IPG[20:5]	16
0xBB007B00	MAC_L2_PORT_CTRL [54].PADDING_UND_SIZE_EN[4:4]	1
0xBB007B00	MAC_L2_PORT_CTRL [54].PASS_ALL_MODE_EN[3:3]	1
0xBB007B00	MAC_L2_PORT_CTRL [54].BYP_TX_CRC[2:2]	1
0xBB007B00	MAC_L2_PORT_CTRL [54].TX_EN[1:1]	1
0xBB007B00	MAC_L2_PORT_CTRL [54].RX_EN[0:0]	1
0xBB007B80	MAC_L2_PORT_CTRL [55].PER_PORT_MAC_ECO[31:22]	10
0xBB007B80	MAC_L2_PORT_CTRL [55].STK_1G_PKT_FMT[21:21]	1
0xBB007B80	MAC_L2_PORT_CTRL [55].TX_IPG[20:5]	16
0xBB007B80	MAC_L2_PORT_CTRL [55].PADDING_UND_SIZE_EN[4:4]	1
0xBB007B80	MAC_L2_PORT_CTRL [55].PASS_ALL_MODE_EN[3:3]	1
0xBB007B80	MAC_L2_PORT_CTRL [55].BYP_TX_CRC[2:2]	1
0xBB007B80	MAC_L2_PORT_CTRL [55].TX_EN[1:1]	1
0xBB007B80	MAC_L2_PORT_CTRL [55].RX_EN[0:0]	1
0xBB007C00	MAC_L2_PORT_CTRL [56].PER_PORT_MAC_ECO[31:22]	10
0xBB007C00	MAC_L2_PORT_CTRL [56].STK_1G_PKT_FMT[21:21]	1
0xBB007C00	MAC_L2_PORT_CTRL [56].TX_IPG[20:5]	16
0xBB007C00	MAC_L2_PORT_CTRL [56].PADDING_UND_SIZE_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007C00	MAC_L2_PORT_CTRL [56].PASS_ALL_MODE_EN[3:3]	1
0xBB007C00	MAC_L2_PORT_CTRL [56].BYP_TX_CRC[2:2]	1
0xBB007C00	MAC_L2_PORT_CTRL [56].TX_EN[1:1]	1
0xBB007C00	MAC_L2_PORT_CTRL [56].RX_EN[0:0]	1
0xBB005554	MAC_L2_PORT_MAX_LEN_CTRL [0].RESERVED[31:29]	3
0xBB005554	MAC_L2_PORT_MAX_LEN_CTRL [0].MAX_LEN_TAG_INC[28:28]	1
0xBB005554	MAC_L2_PORT_MAX_LEN_CTRL [0].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005554	MAC_L2_PORT_MAX_LEN_CTRL [0].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005558	MAC_L2_PORT_MAX_LEN_CTRL [1].RESERVED[31:29]	3
0xBB005558	MAC_L2_PORT_MAX_LEN_CTRL [1].MAX_LEN_TAG_INC[28:28]	1
0xBB005558	MAC_L2_PORT_MAX_LEN_CTRL [1].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005558	MAC_L2_PORT_MAX_LEN_CTRL [1].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00555C	MAC_L2_PORT_MAX_LEN_CTRL [2].RESERVED[31:29]	3
0xBB00555C	MAC_L2_PORT_MAX_LEN_CTRL [2].MAX_LEN_TAG_INC[28:28]	1
0xBB00555C	MAC_L2_PORT_MAX_LEN_CTRL [2].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00555C	MAC_L2_PORT_MAX_LEN_CTRL [2].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005560	MAC_L2_PORT_MAX_LEN_CTRL [3].RESERVED[31:29]	3
0xBB005560	MAC_L2_PORT_MAX_LEN_CTRL [3].MAX_LEN_TAG_INC[28:28]	1
0xBB005560	MAC_L2_PORT_MAX_LEN_CTRL [3].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005560	MAC_L2_PORT_MAX_LEN_CTRL [3].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005564	MAC_L2_PORT_MAX_LEN_CTRL [4].RESERVED[31:29]	3
0xBB005564	MAC_L2_PORT_MAX_LEN_CTRL [4].MAX_LEN_TAG_INC[28:28]	1
0xBB005564	MAC_L2_PORT_MAX_LEN_CTRL [4].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005564	MAC_L2_PORT_MAX_LEN_CTRL [4].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005568	MAC_L2_PORT_MAX_LEN_CTRL [5].RESERVED[31:29]	3
0xBB005568	MAC_L2_PORT_MAX_LEN_CTRL [5].MAX_LEN_TAG_INC[28:28]	1
0xBB005568	MAC_L2_PORT_MAX_LEN_CTRL [5].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005568	MAC_L2_PORT_MAX_LEN_CTRL [5].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00556C	MAC_L2_PORT_MAX_LEN_CTRL [6].RESERVED[31:29]	3
0xBB00556C	MAC_L2_PORT_MAX_LEN_CTRL [6].MAX_LEN_TAG_INC[28:28]	1
0xBB00556C	MAC_L2_PORT_MAX_LEN_CTRL [6].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00556C	MAC_L2_PORT_MAX_LEN_CTRL [6].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005570	MAC_L2_PORT_MAX_LEN_CTRL [7].RESERVED[31:29]	3
0xBB005570	MAC_L2_PORT_MAX_LEN_CTRL [7].MAX_LEN_TAG_INC[28:28]	1
0xBB005570	MAC_L2_PORT_MAX_LEN_CTRL [7].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005570	MAC_L2_PORT_MAX_LEN_CTRL [7].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005574	MAC_L2_PORT_MAX_LEN_CTRL [8].RESERVED[31:29]	3
0xBB005574	MAC_L2_PORT_MAX_LEN_CTRL [8].MAX_LEN_TAG_INC[28:28]	1
0xBB005574	MAC_L2_PORT_MAX_LEN_CTRL [8].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005574	MAC_L2_PORT_MAX_LEN_CTRL [8].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005578	MAC_L2_PORT_MAX_LEN_CTRL [9].RESERVED[31:29]	3
0xBB005578	MAC_L2_PORT_MAX_LEN_CTRL [9].MAX_LEN_TAG_INC[28:28]	1
0xBB005578	MAC_L2_PORT_MAX_LEN_CTRL [9].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005578	MAC_L2_PORT_MAX_LEN_CTRL [9].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00557C	MAC_L2_PORT_MAX_LEN_CTRL [10].RESERVED[31:29]	3
0xBB00557C	MAC_L2_PORT_MAX_LEN_CTRL [10].MAX_LEN_TAG_INC[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00557C	MAC_L2_PORT_MAX_LEN_CTRL [10].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00557C	MAC_L2_PORT_MAX_LEN_CTRL [10].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005580	MAC_L2_PORT_MAX_LEN_CTRL [11].RESERVED[31:29]	3
0xBB005580	MAC_L2_PORT_MAX_LEN_CTRL [11].MAX_LEN_TAG_INC[28:28]	1
0xBB005580	MAC_L2_PORT_MAX_LEN_CTRL [11].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005580	MAC_L2_PORT_MAX_LEN_CTRL [11].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005584	MAC_L2_PORT_MAX_LEN_CTRL [12].RESERVED[31:29]	3
0xBB005584	MAC_L2_PORT_MAX_LEN_CTRL [12].MAX_LEN_TAG_INC[28:28]	1
0xBB005584	MAC_L2_PORT_MAX_LEN_CTRL [12].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005584	MAC_L2_PORT_MAX_LEN_CTRL [12].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005588	MAC_L2_PORT_MAX_LEN_CTRL [13].RESERVED[31:29]	3
0xBB005588	MAC_L2_PORT_MAX_LEN_CTRL [13].MAX_LEN_TAG_INC[28:28]	1
0xBB005588	MAC_L2_PORT_MAX_LEN_CTRL [13].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005588	MAC_L2_PORT_MAX_LEN_CTRL [13].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00558C	MAC_L2_PORT_MAX_LEN_CTRL [14].RESERVED[31:29]	3
0xBB00558C	MAC_L2_PORT_MAX_LEN_CTRL [14].MAX_LEN_TAG_INC[28:28]	1
0xBB00558C	MAC_L2_PORT_MAX_LEN_CTRL [14].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00558C	MAC_L2_PORT_MAX_LEN_CTRL [14].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005590	MAC_L2_PORT_MAX_LEN_CTRL [15].RESERVED[31:29]	3
0xBB005590	MAC_L2_PORT_MAX_LEN_CTRL [15].MAX_LEN_TAG_INC[28:28]	1
0xBB005590	MAC_L2_PORT_MAX_LEN_CTRL [15].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005590	MAC_L2_PORT_MAX_LEN_CTRL [15].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005594	MAC_L2_PORT_MAX_LEN_CTRL [16].RESERVED[31:29]	3
0xBB005594	MAC_L2_PORT_MAX_LEN_CTRL [16].MAX_LEN_TAG_INC[28:28]	1
0xBB005594	MAC_L2_PORT_MAX_LEN_CTRL [16].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005594	MAC_L2_PORT_MAX_LEN_CTRL [16].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005598	MAC_L2_PORT_MAX_LEN_CTRL [17].RESERVED[31:29]	3
0xBB005598	MAC_L2_PORT_MAX_LEN_CTRL [17].MAX_LEN_TAG_INC[28:28]	1
0xBB005598	MAC_L2_PORT_MAX_LEN_CTRL [17].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005598	MAC_L2_PORT_MAX_LEN_CTRL [17].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00559C	MAC_L2_PORT_MAX_LEN_CTRL [18].RESERVED[31:29]	3
0xBB00559C	MAC_L2_PORT_MAX_LEN_CTRL [18].MAX_LEN_TAG_INC[28:28]	1
0xBB00559C	MAC_L2_PORT_MAX_LEN_CTRL [18].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00559C	MAC_L2_PORT_MAX_LEN_CTRL [18].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055A0	MAC_L2_PORT_MAX_LEN_CTRL [19].RESERVED[31:29]	3
0xBB0055A0	MAC_L2_PORT_MAX_LEN_CTRL [19].MAX_LEN_TAG_INC[28:28]	1
0xBB0055A0	MAC_L2_PORT_MAX_LEN_CTRL [19].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055A0	MAC_L2_PORT_MAX_LEN_CTRL [19].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055A4	MAC_L2_PORT_MAX_LEN_CTRL [20].RESERVED[31:29]	3
0xBB0055A4	MAC_L2_PORT_MAX_LEN_CTRL [20].MAX_LEN_TAG_INC[28:28]	1
0xBB0055A4	MAC_L2_PORT_MAX_LEN_CTRL [20].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055A4	MAC_L2_PORT_MAX_LEN_CTRL [20].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055A8	MAC_L2_PORT_MAX_LEN_CTRL [21].RESERVED[31:29]	3
0xBB0055A8	MAC_L2_PORT_MAX_LEN_CTRL [21].MAX_LEN_TAG_INC[28:28]	1
0xBB0055A8	MAC_L2_PORT_MAX_LEN_CTRL [21].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055A8	MAC_L2_PORT_MAX_LEN_CTRL [21].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0055AC	MAC_L2_PORT_MAX_LEN_CTRL [22].RESERVED[31:29]	3
0xBB0055AC	MAC_L2_PORT_MAX_LEN_CTRL [22].MAX_LEN_TAG_INC[28:28]	1
0xBB0055AC	MAC_L2_PORT_MAX_LEN_CTRL [22].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055AC	MAC_L2_PORT_MAX_LEN_CTRL [22].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055B0	MAC_L2_PORT_MAX_LEN_CTRL [23].RESERVED[31:29]	3
0xBB0055B0	MAC_L2_PORT_MAX_LEN_CTRL [23].MAX_LEN_TAG_INC[28:28]	1
0xBB0055B0	MAC_L2_PORT_MAX_LEN_CTRL [23].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055B0	MAC_L2_PORT_MAX_LEN_CTRL [23].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055B4	MAC_L2_PORT_MAX_LEN_CTRL [24].RESERVED[31:29]	3
0xBB0055B4	MAC_L2_PORT_MAX_LEN_CTRL [24].MAX_LEN_TAG_INC[28:28]	1
0xBB0055B4	MAC_L2_PORT_MAX_LEN_CTRL [24].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055B4	MAC_L2_PORT_MAX_LEN_CTRL [24].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055B8	MAC_L2_PORT_MAX_LEN_CTRL [25].RESERVED[31:29]	3
0xBB0055B8	MAC_L2_PORT_MAX_LEN_CTRL [25].MAX_LEN_TAG_INC[28:28]	1
0xBB0055B8	MAC_L2_PORT_MAX_LEN_CTRL [25].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055B8	MAC_L2_PORT_MAX_LEN_CTRL [25].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055BC	MAC_L2_PORT_MAX_LEN_CTRL [26].RESERVED[31:29]	3
0xBB0055BC	MAC_L2_PORT_MAX_LEN_CTRL [26].MAX_LEN_TAG_INC[28:28]	1
0xBB0055BC	MAC_L2_PORT_MAX_LEN_CTRL [26].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055BC	MAC_L2_PORT_MAX_LEN_CTRL [26].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055C0	MAC_L2_PORT_MAX_LEN_CTRL [27].RESERVED[31:29]	3
0xBB0055C0	MAC_L2_PORT_MAX_LEN_CTRL [27].MAX_LEN_TAG_INC[28:28]	1
0xBB0055C0	MAC_L2_PORT_MAX_LEN_CTRL [27].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055C0	MAC_L2_PORT_MAX_LEN_CTRL [27].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055C4	MAC_L2_PORT_MAX_LEN_CTRL [28].RESERVED[31:29]	3
0xBB0055C4	MAC_L2_PORT_MAX_LEN_CTRL [28].MAX_LEN_TAG_INC[28:28]	1
0xBB0055C4	MAC_L2_PORT_MAX_LEN_CTRL [28].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055C4	MAC_L2_PORT_MAX_LEN_CTRL [28].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055C8	MAC_L2_PORT_MAX_LEN_CTRL [29].RESERVED[31:29]	3
0xBB0055C8	MAC_L2_PORT_MAX_LEN_CTRL [29].MAX_LEN_TAG_INC[28:28]	1
0xBB0055C8	MAC_L2_PORT_MAX_LEN_CTRL [29].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055C8	MAC_L2_PORT_MAX_LEN_CTRL [29].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055CC	MAC_L2_PORT_MAX_LEN_CTRL [30].RESERVED[31:29]	3
0xBB0055CC	MAC_L2_PORT_MAX_LEN_CTRL [30].MAX_LEN_TAG_INC[28:28]	1
0xBB0055CC	MAC_L2_PORT_MAX_LEN_CTRL [30].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055CC	MAC_L2_PORT_MAX_LEN_CTRL [30].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055D0	MAC_L2_PORT_MAX_LEN_CTRL [31].RESERVED[31:29]	3
0xBB0055D0	MAC_L2_PORT_MAX_LEN_CTRL [31].MAX_LEN_TAG_INC[28:28]	1
0xBB0055D0	MAC_L2_PORT_MAX_LEN_CTRL [31].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055D0	MAC_L2_PORT_MAX_LEN_CTRL [31].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055D4	MAC_L2_PORT_MAX_LEN_CTRL [32].RESERVED[31:29]	3
0xBB0055D4	MAC_L2_PORT_MAX_LEN_CTRL [32].MAX_LEN_TAG_INC[28:28]	1
0xBB0055D4	MAC_L2_PORT_MAX_LEN_CTRL [32].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055D4	MAC_L2_PORT_MAX_LEN_CTRL [32].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055D8	MAC_L2_PORT_MAX_LEN_CTRL [33].RESERVED[31:29]	3
0xBB0055D8	MAC_L2_PORT_MAX_LEN_CTRL [33].MAX_LEN_TAG_INC[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0055D8	MAC_L2_PORT_MAX_LEN_CTRL [33].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055D8	MAC_L2_PORT_MAX_LEN_CTRL [33].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055DC	MAC_L2_PORT_MAX_LEN_CTRL [34].RESERVED[31:29]	3
0xBB0055DC	MAC_L2_PORT_MAX_LEN_CTRL [34].MAX_LEN_TAG_INC[28:28]	1
0xBB0055DC	MAC_L2_PORT_MAX_LEN_CTRL [34].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055DC	MAC_L2_PORT_MAX_LEN_CTRL [34].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055E0	MAC_L2_PORT_MAX_LEN_CTRL [35].RESERVED[31:29]	3
0xBB0055E0	MAC_L2_PORT_MAX_LEN_CTRL [35].MAX_LEN_TAG_INC[28:28]	1
0xBB0055E0	MAC_L2_PORT_MAX_LEN_CTRL [35].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055E0	MAC_L2_PORT_MAX_LEN_CTRL [35].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055E4	MAC_L2_PORT_MAX_LEN_CTRL [36].RESERVED[31:29]	3
0xBB0055E4	MAC_L2_PORT_MAX_LEN_CTRL [36].MAX_LEN_TAG_INC[28:28]	1
0xBB0055E4	MAC_L2_PORT_MAX_LEN_CTRL [36].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055E4	MAC_L2_PORT_MAX_LEN_CTRL [36].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055E8	MAC_L2_PORT_MAX_LEN_CTRL [37].RESERVED[31:29]	3
0xBB0055E8	MAC_L2_PORT_MAX_LEN_CTRL [37].MAX_LEN_TAG_INC[28:28]	1
0xBB0055E8	MAC_L2_PORT_MAX_LEN_CTRL [37].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055E8	MAC_L2_PORT_MAX_LEN_CTRL [37].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055EC	MAC_L2_PORT_MAX_LEN_CTRL [38].RESERVED[31:29]	3
0xBB0055EC	MAC_L2_PORT_MAX_LEN_CTRL [38].MAX_LEN_TAG_INC[28:28]	1
0xBB0055EC	MAC_L2_PORT_MAX_LEN_CTRL [38].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055EC	MAC_L2_PORT_MAX_LEN_CTRL [38].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055F0	MAC_L2_PORT_MAX_LEN_CTRL [39].RESERVED[31:29]	3
0xBB0055F0	MAC_L2_PORT_MAX_LEN_CTRL [39].MAX_LEN_TAG_INC[28:28]	1
0xBB0055F0	MAC_L2_PORT_MAX_LEN_CTRL [39].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055F0	MAC_L2_PORT_MAX_LEN_CTRL [39].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055F4	MAC_L2_PORT_MAX_LEN_CTRL [40].RESERVED[31:29]	3
0xBB0055F4	MAC_L2_PORT_MAX_LEN_CTRL [40].MAX_LEN_TAG_INC[28:28]	1
0xBB0055F4	MAC_L2_PORT_MAX_LEN_CTRL [40].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055F4	MAC_L2_PORT_MAX_LEN_CTRL [40].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055F8	MAC_L2_PORT_MAX_LEN_CTRL [41].RESERVED[31:29]	3
0xBB0055F8	MAC_L2_PORT_MAX_LEN_CTRL [41].MAX_LEN_TAG_INC[28:28]	1
0xBB0055F8	MAC_L2_PORT_MAX_LEN_CTRL [41].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055F8	MAC_L2_PORT_MAX_LEN_CTRL [41].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB0055FC	MAC_L2_PORT_MAX_LEN_CTRL [42].RESERVED[31:29]	3
0xBB0055FC	MAC_L2_PORT_MAX_LEN_CTRL [42].MAX_LEN_TAG_INC[28:28]	1
0xBB0055FC	MAC_L2_PORT_MAX_LEN_CTRL [42].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB0055FC	MAC_L2_PORT_MAX_LEN_CTRL [42].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005600	MAC_L2_PORT_MAX_LEN_CTRL [43].RESERVED[31:29]	3
0xBB005600	MAC_L2_PORT_MAX_LEN_CTRL [43].MAX_LEN_TAG_INC[28:28]	1
0xBB005600	MAC_L2_PORT_MAX_LEN_CTRL [43].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005600	MAC_L2_PORT_MAX_LEN_CTRL [43].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005604	MAC_L2_PORT_MAX_LEN_CTRL [44].RESERVED[31:29]	3
0xBB005604	MAC_L2_PORT_MAX_LEN_CTRL [44].MAX_LEN_TAG_INC[28:28]	1
0xBB005604	MAC_L2_PORT_MAX_LEN_CTRL [44].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005604	MAC_L2_PORT_MAX_LEN_CTRL [44].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB005608	MAC_L2_PORT_MAX_LEN_CTRL [45].RESERVED[31:29]	3
0xBB005608	MAC_L2_PORT_MAX_LEN_CTRL [45].MAX_LEN_TAG_INC[28:28]	1
0xBB005608	MAC_L2_PORT_MAX_LEN_CTRL [45].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005608	MAC_L2_PORT_MAX_LEN_CTRL [45].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00560C	MAC_L2_PORT_MAX_LEN_CTRL [46].RESERVED[31:29]	3
0xBB00560C	MAC_L2_PORT_MAX_LEN_CTRL [46].MAX_LEN_TAG_INC[28:28]	1
0xBB00560C	MAC_L2_PORT_MAX_LEN_CTRL [46].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00560C	MAC_L2_PORT_MAX_LEN_CTRL [46].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005610	MAC_L2_PORT_MAX_LEN_CTRL [47].RESERVED[31:29]	3
0xBB005610	MAC_L2_PORT_MAX_LEN_CTRL [47].MAX_LEN_TAG_INC[28:28]	1
0xBB005610	MAC_L2_PORT_MAX_LEN_CTRL [47].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005610	MAC_L2_PORT_MAX_LEN_CTRL [47].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005614	MAC_L2_PORT_MAX_LEN_CTRL [48].RESERVED[31:29]	3
0xBB005614	MAC_L2_PORT_MAX_LEN_CTRL [48].MAX_LEN_TAG_INC[28:28]	1
0xBB005614	MAC_L2_PORT_MAX_LEN_CTRL [48].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005614	MAC_L2_PORT_MAX_LEN_CTRL [48].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005618	MAC_L2_PORT_MAX_LEN_CTRL [49].RESERVED[31:29]	3
0xBB005618	MAC_L2_PORT_MAX_LEN_CTRL [49].MAX_LEN_TAG_INC[28:28]	1
0xBB005618	MAC_L2_PORT_MAX_LEN_CTRL [49].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005618	MAC_L2_PORT_MAX_LEN_CTRL [49].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00561C	MAC_L2_PORT_MAX_LEN_CTRL [50].RESERVED[31:29]	3
0xBB00561C	MAC_L2_PORT_MAX_LEN_CTRL [50].MAX_LEN_TAG_INC[28:28]	1
0xBB00561C	MAC_L2_PORT_MAX_LEN_CTRL [50].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00561C	MAC_L2_PORT_MAX_LEN_CTRL [50].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005620	MAC_L2_PORT_MAX_LEN_CTRL [51].RESERVED[31:29]	3
0xBB005620	MAC_L2_PORT_MAX_LEN_CTRL [51].MAX_LEN_TAG_INC[28:28]	1
0xBB005620	MAC_L2_PORT_MAX_LEN_CTRL [51].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005620	MAC_L2_PORT_MAX_LEN_CTRL [51].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005624	MAC_L2_PORT_MAX_LEN_CTRL [52].RESERVED[31:29]	3
0xBB005624	MAC_L2_PORT_MAX_LEN_CTRL [52].MAX_LEN_TAG_INC[28:28]	1
0xBB005624	MAC_L2_PORT_MAX_LEN_CTRL [52].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005624	MAC_L2_PORT_MAX_LEN_CTRL [52].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005628	MAC_L2_PORT_MAX_LEN_CTRL [53].RESERVED[31:29]	3
0xBB005628	MAC_L2_PORT_MAX_LEN_CTRL [53].MAX_LEN_TAG_INC[28:28]	1
0xBB005628	MAC_L2_PORT_MAX_LEN_CTRL [53].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005628	MAC_L2_PORT_MAX_LEN_CTRL [53].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB00562C	MAC_L2_PORT_MAX_LEN_CTRL [54].RESERVED[31:29]	3
0xBB00562C	MAC_L2_PORT_MAX_LEN_CTRL [54].MAX_LEN_TAG_INC[28:28]	1
0xBB00562C	MAC_L2_PORT_MAX_LEN_CTRL [54].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB00562C	MAC_L2_PORT_MAX_LEN_CTRL [54].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB005630	MAC_L2_PORT_MAX_LEN_CTRL [55].RESERVED[31:29]	3
0xBB005630	MAC_L2_PORT_MAX_LEN_CTRL [55].MAX_LEN_TAG_INC[28:28]	1
0xBB005630	MAC_L2_PORT_MAX_LEN_CTRL [55].MAX_LEN_100M_10M_SEL[27:14]	14
0xBB005630	MAC_L2_PORT_MAX_LEN_CTRL [55].MAX_LEN_1G_2P5G_5G_10G_SEL[13:0]	14
0xBB000EF0	MAC_LINK_DUP_STS [0].DUP_STS[0:0]	1
0xBB000EF0	MAC_LINK_DUP_STS [1].DUP_STS[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EF0	MAC_LINK_DUP_STS [2].DUP_STS[2:2]	1
0xBB000EF0	MAC_LINK_DUP_STS [3].DUP_STS[3:3]	1
0xBB000EF0	MAC_LINK_DUP_STS [4].DUP_STS[4:4]	1
0xBB000EF0	MAC_LINK_DUP_STS [5].DUP_STS[5:5]	1
0xBB000EF0	MAC_LINK_DUP_STS [6].DUP_STS[6:6]	1
0xBB000EF0	MAC_LINK_DUP_STS [7].DUP_STS[7:7]	1
0xBB000EF0	MAC_LINK_DUP_STS [8].DUP_STS[8:8]	1
0xBB000EF0	MAC_LINK_DUP_STS [9].DUP_STS[9:9]	1
0xBB000EF0	MAC_LINK_DUP_STS [10].DUP_STS[10:10]	1
0xBB000EF0	MAC_LINK_DUP_STS [11].DUP_STS[11:11]	1
0xBB000EF0	MAC_LINK_DUP_STS [12].DUP_STS[12:12]	1
0xBB000EF0	MAC_LINK_DUP_STS [13].DUP_STS[13:13]	1
0xBB000EF0	MAC_LINK_DUP_STS [14].DUP_STS[14:14]	1
0xBB000EF0	MAC_LINK_DUP_STS [15].DUP_STS[15:15]	1
0xBB000EF0	MAC_LINK_DUP_STS [16].DUP_STS[16:16]	1
0xBB000EF0	MAC_LINK_DUP_STS [17].DUP_STS[17:17]	1
0xBB000EF0	MAC_LINK_DUP_STS [18].DUP_STS[18:18]	1
0xBB000EF0	MAC_LINK_DUP_STS [19].DUP_STS[19:19]	1
0xBB000EF0	MAC_LINK_DUP_STS [20].DUP_STS[20:20]	1
0xBB000EF0	MAC_LINK_DUP_STS [21].DUP_STS[21:21]	1
0xBB000EF0	MAC_LINK_DUP_STS [22].DUP_STS[22:22]	1
0xBB000EF0	MAC_LINK_DUP_STS [23].DUP_STS[23:23]	1
0xBB000EF0	MAC_LINK_DUP_STS [24].DUP_STS[24:24]	1
0xBB000EF0	MAC_LINK_DUP_STS [25].DUP_STS[25:25]	1
0xBB000EF0	MAC_LINK_DUP_STS [26].DUP_STS[26:26]	1
0xBB000EF0	MAC_LINK_DUP_STS [27].DUP_STS[27:27]	1
0xBB000EF0	MAC_LINK_DUP_STS [28].DUP_STS[28:28]	1
0xBB000EF0	MAC_LINK_DUP_STS [29].DUP_STS[29:29]	1
0xBB000EF0	MAC_LINK_DUP_STS [30].DUP_STS[30:30]	1
0xBB000EF0	MAC_LINK_DUP_STS [31].DUP_STS[31:31]	1
0xBB000EF4	MAC_LINK_DUP_STS [32].DUP_STS[0:0]	1
0xBB000EF4	MAC_LINK_DUP_STS [33].DUP_STS[1:1]	1
0xBB000EF4	MAC_LINK_DUP_STS [34].DUP_STS[2:2]	1
0xBB000EF4	MAC_LINK_DUP_STS [35].DUP_STS[3:3]	1
0xBB000EF4	MAC_LINK_DUP_STS [36].DUP_STS[4:4]	1
0xBB000EF4	MAC_LINK_DUP_STS [37].DUP_STS[5:5]	1
0xBB000EF4	MAC_LINK_DUP_STS [38].DUP_STS[6:6]	1
0xBB000EF4	MAC_LINK_DUP_STS [39].DUP_STS[7:7]	1
0xBB000EF4	MAC_LINK_DUP_STS [40].DUP_STS[8:8]	1
0xBB000EF4	MAC_LINK_DUP_STS [41].DUP_STS[9:9]	1
0xBB000EF4	MAC_LINK_DUP_STS [42].DUP_STS[10:10]	1
0xBB000EF4	MAC_LINK_DUP_STS [43].DUP_STS[11:11]	1
0xBB000EF4	MAC_LINK_DUP_STS [44].DUP_STS[12:12]	1
0xBB000EF4	MAC_LINK_DUP_STS [45].DUP_STS[13:13]	1
0xBB000EF4	MAC_LINK_DUP_STS [46].DUP_STS[14:14]	1
0xBB000EF4	MAC_LINK_DUP_STS [47].DUP_STS[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EF4	MAC_LINK_DUP_STS [48].DUP_STS[16:16]	1
0xBB000EF4	MAC_LINK_DUP_STS [49].DUP_STS[17:17]	1
0xBB000EF4	MAC_LINK_DUP_STS [50].DUP_STS[18:18]	1
0xBB000EF4	MAC_LINK_DUP_STS [51].DUP_STS[19:19]	1
0xBB000EF4	MAC_LINK_DUP_STS [52].DUP_STS[20:20]	1
0xBB000EF4	MAC_LINK_DUP_STS [53].DUP_STS[21:21]	1
0xBB000EF4	MAC_LINK_DUP_STS [54].DUP_STS[22:22]	1
0xBB000EF4	MAC_LINK_DUP_STS [55].DUP_STS[23:23]	1
0xBB000EF4	MAC_LINK_DUP_STS [56].DUP_STS[24:24]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [0].MEDIA_STS[0:0]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [1].MEDIA_STS[1:1]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [2].MEDIA_STS[2:2]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [3].MEDIA_STS[3:3]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [4].MEDIA_STS[4:4]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [5].MEDIA_STS[5:5]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [6].MEDIA_STS[6:6]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [7].MEDIA_STS[7:7]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [8].MEDIA_STS[8:8]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [9].MEDIA_STS[9:9]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [10].MEDIA_STS[10:10]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [11].MEDIA_STS[11:11]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [12].MEDIA_STS[12:12]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [13].MEDIA_STS[13:13]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [14].MEDIA_STS[14:14]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [15].MEDIA_STS[15:15]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [16].MEDIA_STS[16:16]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [17].MEDIA_STS[17:17]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [18].MEDIA_STS[18:18]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [19].MEDIA_STS[19:19]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [20].MEDIA_STS[20:20]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [21].MEDIA_STS[21:21]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [22].MEDIA_STS[22:22]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [23].MEDIA_STS[23:23]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [24].MEDIA_STS[24:24]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [25].MEDIA_STS[25:25]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [26].MEDIA_STS[26:26]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [27].MEDIA_STS[27:27]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [28].MEDIA_STS[28:28]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [29].MEDIA_STS[29:29]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [30].MEDIA_STS[30:30]	1
0xBB000EC8	MAC_LINK_MEDIA_STS [31].MEDIA_STS[31:31]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [32].MEDIA_STS[0:0]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [33].MEDIA_STS[1:1]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [34].MEDIA_STS[2:2]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [35].MEDIA_STS[3:3]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [36].MEDIA_STS[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000ECC	MAC_LINK_MEDIA_STS [37].MEDIA_STS[5:5]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [38].MEDIA_STS[6:6]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [39].MEDIA_STS[7:7]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [40].MEDIA_STS[8:8]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [41].MEDIA_STS[9:9]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [42].MEDIA_STS[10:10]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [43].MEDIA_STS[11:11]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [44].MEDIA_STS[12:12]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [45].MEDIA_STS[13:13]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [46].MEDIA_STS[14:14]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [47].MEDIA_STS[15:15]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [48].MEDIA_STS[16:16]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [49].MEDIA_STS[17:17]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [50].MEDIA_STS[18:18]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [51].MEDIA_STS[19:19]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [52].MEDIA_STS[20:20]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [53].MEDIA_STS[21:21]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [54].MEDIA_STS[22:22]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [55].MEDIA_STS[23:23]	1
0xBB000ECC	MAC_LINK_MEDIA_STS [56].MEDIA_STS[24:24]	1
0xBB000ED0	MAC_LINK_SPD_STS [0].SPD_STS[3:0]	4
0xBB000ED0	MAC_LINK_SPD_STS [1].SPD_STS[7:4]	4
0xBB000ED0	MAC_LINK_SPD_STS [2].SPD_STS[11:8]	4
0xBB000ED0	MAC_LINK_SPD_STS [3].SPD_STS[15:12]	4
0xBB000ED0	MAC_LINK_SPD_STS [4].SPD_STS[19:16]	4
0xBB000ED0	MAC_LINK_SPD_STS [5].SPD_STS[23:20]	4
0xBB000ED0	MAC_LINK_SPD_STS [6].SPD_STS[27:24]	4
0xBB000ED0	MAC_LINK_SPD_STS [7].SPD_STS[31:28]	4
0xBB000ED4	MAC_LINK_SPD_STS [8].SPD_STS[3:0]	4
0xBB000ED4	MAC_LINK_SPD_STS [9].SPD_STS[7:4]	4
0xBB000ED4	MAC_LINK_SPD_STS [10].SPD_STS[11:8]	4
0xBB000ED4	MAC_LINK_SPD_STS [11].SPD_STS[15:12]	4
0xBB000ED4	MAC_LINK_SPD_STS [12].SPD_STS[19:16]	4
0xBB000ED4	MAC_LINK_SPD_STS [13].SPD_STS[23:20]	4
0xBB000ED4	MAC_LINK_SPD_STS [14].SPD_STS[27:24]	4
0xBB000ED4	MAC_LINK_SPD_STS [15].SPD_STS[31:28]	4
0xBB000ED8	MAC_LINK_SPD_STS [16].SPD_STS[3:0]	4
0xBB000ED8	MAC_LINK_SPD_STS [17].SPD_STS[7:4]	4
0xBB000ED8	MAC_LINK_SPD_STS [18].SPD_STS[11:8]	4
0xBB000ED8	MAC_LINK_SPD_STS [19].SPD_STS[15:12]	4
0xBB000ED8	MAC_LINK_SPD_STS [20].SPD_STS[19:16]	4
0xBB000ED8	MAC_LINK_SPD_STS [21].SPD_STS[23:20]	4
0xBB000ED8	MAC_LINK_SPD_STS [22].SPD_STS[27:24]	4
0xBB000ED8	MAC_LINK_SPD_STS [23].SPD_STS[31:28]	4
0xBB000EDC	MAC_LINK_SPD_STS [24].SPD_STS[3:0]	4
0xBB000EDC	MAC_LINK_SPD_STS [25].SPD_STS[7:4]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EDC	MAC_LINK_SPD_STS [26].SPD_STS[11:8]	4
0xBB000EDC	MAC_LINK_SPD_STS [27].SPD_STS[15:12]	4
0xBB000EDC	MAC_LINK_SPD_STS [28].SPD_STS[19:16]	4
0xBB000EDC	MAC_LINK_SPD_STS [29].SPD_STS[23:20]	4
0xBB000EDC	MAC_LINK_SPD_STS [30].SPD_STS[27:24]	4
0xBB000EDC	MAC_LINK_SPD_STS [31].SPD_STS[31:28]	4
0xBB000EE0	MAC_LINK_SPD_STS [32].SPD_STS[3:0]	4
0xBB000EE0	MAC_LINK_SPD_STS [33].SPD_STS[7:4]	4
0xBB000EE0	MAC_LINK_SPD_STS [34].SPD_STS[11:8]	4
0xBB000EE0	MAC_LINK_SPD_STS [35].SPD_STS[15:12]	4
0xBB000EE0	MAC_LINK_SPD_STS [36].SPD_STS[19:16]	4
0xBB000EE0	MAC_LINK_SPD_STS [37].SPD_STS[23:20]	4
0xBB000EE0	MAC_LINK_SPD_STS [38].SPD_STS[27:24]	4
0xBB000EE0	MAC_LINK_SPD_STS [39].SPD_STS[31:28]	4
0xBB000EE4	MAC_LINK_SPD_STS [40].SPD_STS[3:0]	4
0xBB000EE4	MAC_LINK_SPD_STS [41].SPD_STS[7:4]	4
0xBB000EE4	MAC_LINK_SPD_STS [42].SPD_STS[11:8]	4
0xBB000EE4	MAC_LINK_SPD_STS [43].SPD_STS[15:12]	4
0xBB000EE4	MAC_LINK_SPD_STS [44].SPD_STS[19:16]	4
0xBB000EE4	MAC_LINK_SPD_STS [45].SPD_STS[23:20]	4
0xBB000EE4	MAC_LINK_SPD_STS [46].SPD_STS[27:24]	4
0xBB000EE4	MAC_LINK_SPD_STS [47].SPD_STS[31:28]	4
0xBB000EE8	MAC_LINK_SPD_STS [48].SPD_STS[3:0]	4
0xBB000EE8	MAC_LINK_SPD_STS [49].SPD_STS[7:4]	4
0xBB000EE8	MAC_LINK_SPD_STS [50].SPD_STS[11:8]	4
0xBB000EE8	MAC_LINK_SPD_STS [51].SPD_STS[15:12]	4
0xBB000EE8	MAC_LINK_SPD_STS [52].SPD_STS[19:16]	4
0xBB000EE8	MAC_LINK_SPD_STS [53].SPD_STS[23:20]	4
0xBB000EE8	MAC_LINK_SPD_STS [54].SPD_STS[27:24]	4
0xBB000EE8	MAC_LINK_SPD_STS [55].SPD_STS[31:28]	4
0xBB000EEC	MAC_LINK_SPD_STS [56].SPD_STS[3:0]	4
0xBB000EC0	MAC_LINK_STS [0].LINK_STS[0:0]	1
0xBB000EC0	MAC_LINK_STS [1].LINK_STS[1:1]	1
0xBB000EC0	MAC_LINK_STS [2].LINK_STS[2:2]	1
0xBB000EC0	MAC_LINK_STS [3].LINK_STS[3:3]	1
0xBB000EC0	MAC_LINK_STS [4].LINK_STS[4:4]	1
0xBB000EC0	MAC_LINK_STS [5].LINK_STS[5:5]	1
0xBB000EC0	MAC_LINK_STS [6].LINK_STS[6:6]	1
0xBB000EC0	MAC_LINK_STS [7].LINK_STS[7:7]	1
0xBB000EC0	MAC_LINK_STS [8].LINK_STS[8:8]	1
0xBB000EC0	MAC_LINK_STS [9].LINK_STS[9:9]	1
0xBB000EC0	MAC_LINK_STS [10].LINK_STS[10:10]	1
0xBB000EC0	MAC_LINK_STS [11].LINK_STS[11:11]	1
0xBB000EC0	MAC_LINK_STS [12].LINK_STS[12:12]	1
0xBB000EC0	MAC_LINK_STS [13].LINK_STS[13:13]	1
0xBB000EC0	MAC_LINK_STS [14].LINK_STS[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EC0	MAC_LINK_STS [15].LINK_STS[15:15]	1
0xBB000EC0	MAC_LINK_STS [16].LINK_STS[16:16]	1
0xBB000EC0	MAC_LINK_STS [17].LINK_STS[17:17]	1
0xBB000EC0	MAC_LINK_STS [18].LINK_STS[18:18]	1
0xBB000EC0	MAC_LINK_STS [19].LINK_STS[19:19]	1
0xBB000EC0	MAC_LINK_STS [20].LINK_STS[20:20]	1
0xBB000EC0	MAC_LINK_STS [21].LINK_STS[21:21]	1
0xBB000EC0	MAC_LINK_STS [22].LINK_STS[22:22]	1
0xBB000EC0	MAC_LINK_STS [23].LINK_STS[23:23]	1
0xBB000EC0	MAC_LINK_STS [24].LINK_STS[24:24]	1
0xBB000EC0	MAC_LINK_STS [25].LINK_STS[25:25]	1
0xBB000EC0	MAC_LINK_STS [26].LINK_STS[26:26]	1
0xBB000EC0	MAC_LINK_STS [27].LINK_STS[27:27]	1
0xBB000EC0	MAC_LINK_STS [28].LINK_STS[28:28]	1
0xBB000EC0	MAC_LINK_STS [29].LINK_STS[29:29]	1
0xBB000EC0	MAC_LINK_STS [30].LINK_STS[30:30]	1
0xBB000EC0	MAC_LINK_STS [31].LINK_STS[31:31]	1
0xBB000EC4	MAC_LINK_STS [32].LINK_STS[0:0]	1
0xBB000EC4	MAC_LINK_STS [33].LINK_STS[1:1]	1
0xBB000EC4	MAC_LINK_STS [34].LINK_STS[2:2]	1
0xBB000EC4	MAC_LINK_STS [35].LINK_STS[3:3]	1
0xBB000EC4	MAC_LINK_STS [36].LINK_STS[4:4]	1
0xBB000EC4	MAC_LINK_STS [37].LINK_STS[5:5]	1
0xBB000EC4	MAC_LINK_STS [38].LINK_STS[6:6]	1
0xBB000EC4	MAC_LINK_STS [39].LINK_STS[7:7]	1
0xBB000EC4	MAC_LINK_STS [40].LINK_STS[8:8]	1
0xBB000EC4	MAC_LINK_STS [41].LINK_STS[9:9]	1
0xBB000EC4	MAC_LINK_STS [42].LINK_STS[10:10]	1
0xBB000EC4	MAC_LINK_STS [43].LINK_STS[11:11]	1
0xBB000EC4	MAC_LINK_STS [44].LINK_STS[12:12]	1
0xBB000EC4	MAC_LINK_STS [45].LINK_STS[13:13]	1
0xBB000EC4	MAC_LINK_STS [46].LINK_STS[14:14]	1
0xBB000EC4	MAC_LINK_STS [47].LINK_STS[15:15]	1
0xBB000EC4	MAC_LINK_STS [48].LINK_STS[16:16]	1
0xBB000EC4	MAC_LINK_STS [49].LINK_STS[17:17]	1
0xBB000EC4	MAC_LINK_STS [50].LINK_STS[18:18]	1
0xBB000EC4	MAC_LINK_STS [51].LINK_STS[19:19]	1
0xBB000EC4	MAC_LINK_STS [52].LINK_STS[20:20]	1
0xBB000EC4	MAC_LINK_STS [53].LINK_STS[21:21]	1
0xBB000EC4	MAC_LINK_STS [54].LINK_STS[22:22]	1
0xBB000EC4	MAC_LINK_STS [55].LINK_STS[23:23]	1
0xBB000EC4	MAC_LINK_STS [56].LINK_STS[24:24]	1
0xBB000F10	MAC_MSTR_SLV_STS [0].MSTR_SLV_STS[0:0]	1
0xBB000F10	MAC_MSTR_SLV_STS [1].MSTR_SLV_STS[1:1]	1
0xBB000F10	MAC_MSTR_SLV_STS [2].MSTR_SLV_STS[2:2]	1
0xBB000F10	MAC_MSTR_SLV_STS [3].MSTR_SLV_STS[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F10	MAC_MSTR_SLV_STS [4].MSTR_SLV_STS[4:4]	1
0xBB000F10	MAC_MSTR_SLV_STS [5].MSTR_SLV_STS[5:5]	1
0xBB000F10	MAC_MSTR_SLV_STS [6].MSTR_SLV_STS[6:6]	1
0xBB000F10	MAC_MSTR_SLV_STS [7].MSTR_SLV_STS[7:7]	1
0xBB000F10	MAC_MSTR_SLV_STS [8].MSTR_SLV_STS[8:8]	1
0xBB000F10	MAC_MSTR_SLV_STS [9].MSTR_SLV_STS[9:9]	1
0xBB000F10	MAC_MSTR_SLV_STS [10].MSTR_SLV_STS[10:10]	1
0xBB000F10	MAC_MSTR_SLV_STS [11].MSTR_SLV_STS[11:11]	1
0xBB000F10	MAC_MSTR_SLV_STS [12].MSTR_SLV_STS[12:12]	1
0xBB000F10	MAC_MSTR_SLV_STS [13].MSTR_SLV_STS[13:13]	1
0xBB000F10	MAC_MSTR_SLV_STS [14].MSTR_SLV_STS[14:14]	1
0xBB000F10	MAC_MSTR_SLV_STS [15].MSTR_SLV_STS[15:15]	1
0xBB000F10	MAC_MSTR_SLV_STS [16].MSTR_SLV_STS[16:16]	1
0xBB000F10	MAC_MSTR_SLV_STS [17].MSTR_SLV_STS[17:17]	1
0xBB000F10	MAC_MSTR_SLV_STS [18].MSTR_SLV_STS[18:18]	1
0xBB000F10	MAC_MSTR_SLV_STS [19].MSTR_SLV_STS[19:19]	1
0xBB000F10	MAC_MSTR_SLV_STS [20].MSTR_SLV_STS[20:20]	1
0xBB000F10	MAC_MSTR_SLV_STS [21].MSTR_SLV_STS[21:21]	1
0xBB000F10	MAC_MSTR_SLV_STS [22].MSTR_SLV_STS[22:22]	1
0xBB000F10	MAC_MSTR_SLV_STS [23].MSTR_SLV_STS[23:23]	1
0xBB000F10	MAC_MSTR_SLV_STS [24].MSTR_SLV_STS[24:24]	1
0xBB000F10	MAC_MSTR_SLV_STS [25].MSTR_SLV_STS[25:25]	1
0xBB000F10	MAC_MSTR_SLV_STS [26].MSTR_SLV_STS[26:26]	1
0xBB000F10	MAC_MSTR_SLV_STS [27].MSTR_SLV_STS[27:27]	1
0xBB000F10	MAC_MSTR_SLV_STS [28].MSTR_SLV_STS[28:28]	1
0xBB000F10	MAC_MSTR_SLV_STS [29].MSTR_SLV_STS[29:29]	1
0xBB000F10	MAC_MSTR_SLV_STS [30].MSTR_SLV_STS[30:30]	1
0xBB000F10	MAC_MSTR_SLV_STS [31].MSTR_SLV_STS[31:31]	1
0xBB000F14	MAC_MSTR_SLV_STS [32].MSTR_SLV_STS[0:0]	1
0xBB000F14	MAC_MSTR_SLV_STS [33].MSTR_SLV_STS[1:1]	1
0xBB000F14	MAC_MSTR_SLV_STS [34].MSTR_SLV_STS[2:2]	1
0xBB000F14	MAC_MSTR_SLV_STS [35].MSTR_SLV_STS[3:3]	1
0xBB000F14	MAC_MSTR_SLV_STS [36].MSTR_SLV_STS[4:4]	1
0xBB000F14	MAC_MSTR_SLV_STS [37].MSTR_SLV_STS[5:5]	1
0xBB000F14	MAC_MSTR_SLV_STS [38].MSTR_SLV_STS[6:6]	1
0xBB000F14	MAC_MSTR_SLV_STS [39].MSTR_SLV_STS[7:7]	1
0xBB000F14	MAC_MSTR_SLV_STS [40].MSTR_SLV_STS[8:8]	1
0xBB000F14	MAC_MSTR_SLV_STS [41].MSTR_SLV_STS[9:9]	1
0xBB000F14	MAC_MSTR_SLV_STS [42].MSTR_SLV_STS[10:10]	1
0xBB000F14	MAC_MSTR_SLV_STS [43].MSTR_SLV_STS[11:11]	1
0xBB000F14	MAC_MSTR_SLV_STS [44].MSTR_SLV_STS[12:12]	1
0xBB000F14	MAC_MSTR_SLV_STS [45].MSTR_SLV_STS[13:13]	1
0xBB000F14	MAC_MSTR_SLV_STS [46].MSTR_SLV_STS[14:14]	1
0xBB000F14	MAC_MSTR_SLV_STS [47].MSTR_SLV_STS[15:15]	1
0xBB000F14	MAC_MSTR_SLV_STS [48].MSTR_SLV_STS[16:16]	1
0xBB000F14	MAC_MSTR_SLV_STS [49].MSTR_SLV_STS[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F14	MAC_MSTR_SLV_STS [50].MSTR_SLV_STS[18:18]	1
0xBB000F14	MAC_MSTR_SLV_STS [51].MSTR_SLV_STS[19:19]	1
0xBB000F14	MAC_MSTR_SLV_STS [52].MSTR_SLV_STS[20:20]	1
0xBB000F14	MAC_MSTR_SLV_STS [53].MSTR_SLV_STS[21:21]	1
0xBB000F14	MAC_MSTR_SLV_STS [54].MSTR_SLV_STS[22:22]	1
0xBB000F14	MAC_MSTR_SLV_STS [55].MSTR_SLV_STS[23:23]	1
0xBB000F14	MAC_MSTR_SLV_STS [56].MSTR_SLV_STS[24:24]	1
0xBB006004	MAC_PORT_CTRL [0].RESERVED[31:5]	27
0xBB006004	MAC_PORT_CTRL [0].PRECOLLAT_SEL[4:3]	2
0xBB006004	MAC_PORT_CTRL [0].LATE_COLI_THR[2:1]	2
0xBB006004	MAC_PORT_CTRL [0].BKPRES_EN[0:0]	1
0xBB006084	MAC_PORT_CTRL [1].RESERVED[31:5]	27
0xBB006084	MAC_PORT_CTRL [1].PRECOLLAT_SEL[4:3]	2
0xBB006084	MAC_PORT_CTRL [1].LATE_COLI_THR[2:1]	2
0xBB006084	MAC_PORT_CTRL [1].BKPRES_EN[0:0]	1
0xBB006104	MAC_PORT_CTRL [2].RESERVED[31:5]	27
0xBB006104	MAC_PORT_CTRL [2].PRECOLLAT_SEL[4:3]	2
0xBB006104	MAC_PORT_CTRL [2].LATE_COLI_THR[2:1]	2
0xBB006104	MAC_PORT_CTRL [2].BKPRES_EN[0:0]	1
0xBB006184	MAC_PORT_CTRL [3].RESERVED[31:5]	27
0xBB006184	MAC_PORT_CTRL [3].PRECOLLAT_SEL[4:3]	2
0xBB006184	MAC_PORT_CTRL [3].LATE_COLI_THR[2:1]	2
0xBB006184	MAC_PORT_CTRL [3].BKPRES_EN[0:0]	1
0xBB006204	MAC_PORT_CTRL [4].RESERVED[31:5]	27
0xBB006204	MAC_PORT_CTRL [4].PRECOLLAT_SEL[4:3]	2
0xBB006204	MAC_PORT_CTRL [4].LATE_COLI_THR[2:1]	2
0xBB006204	MAC_PORT_CTRL [4].BKPRES_EN[0:0]	1
0xBB006284	MAC_PORT_CTRL [5].RESERVED[31:5]	27
0xBB006284	MAC_PORT_CTRL [5].PRECOLLAT_SEL[4:3]	2
0xBB006284	MAC_PORT_CTRL [5].LATE_COLI_THR[2:1]	2
0xBB006284	MAC_PORT_CTRL [5].BKPRES_EN[0:0]	1
0xBB006304	MAC_PORT_CTRL [6].RESERVED[31:5]	27
0xBB006304	MAC_PORT_CTRL [6].PRECOLLAT_SEL[4:3]	2
0xBB006304	MAC_PORT_CTRL [6].LATE_COLI_THR[2:1]	2
0xBB006304	MAC_PORT_CTRL [6].BKPRES_EN[0:0]	1
0xBB006384	MAC_PORT_CTRL [7].RESERVED[31:5]	27
0xBB006384	MAC_PORT_CTRL [7].PRECOLLAT_SEL[4:3]	2
0xBB006384	MAC_PORT_CTRL [7].LATE_COLI_THR[2:1]	2
0xBB006384	MAC_PORT_CTRL [7].BKPRES_EN[0:0]	1
0xBB006404	MAC_PORT_CTRL [8].RESERVED[31:5]	27
0xBB006404	MAC_PORT_CTRL [8].PRECOLLAT_SEL[4:3]	2
0xBB006404	MAC_PORT_CTRL [8].LATE_COLI_THR[2:1]	2
0xBB006404	MAC_PORT_CTRL [8].BKPRES_EN[0:0]	1
0xBB006484	MAC_PORT_CTRL [9].RESERVED[31:5]	27
0xBB006484	MAC_PORT_CTRL [9].PRECOLLAT_SEL[4:3]	2
0xBB006484	MAC_PORT_CTRL [9].LATE_COLI_THR[2:1]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006484	MAC_PORT_CTRL [9].BKPRES_EN[0:0]	1
0xBB006504	MAC_PORT_CTRL [10].RESERVED[31:5]	27
0xBB006504	MAC_PORT_CTRL [10].PRECOLLAT_SEL[4:3]	2
0xBB006504	MAC_PORT_CTRL [10].LATE_COLI_THR[2:1]	2
0xBB006504	MAC_PORT_CTRL [10].BKPRES_EN[0:0]	1
0xBB006584	MAC_PORT_CTRL [11].RESERVED[31:5]	27
0xBB006584	MAC_PORT_CTRL [11].PRECOLLAT_SEL[4:3]	2
0xBB006584	MAC_PORT_CTRL [11].LATE_COLI_THR[2:1]	2
0xBB006584	MAC_PORT_CTRL [11].BKPRES_EN[0:0]	1
0xBB006604	MAC_PORT_CTRL [12].RESERVED[31:5]	27
0xBB006604	MAC_PORT_CTRL [12].PRECOLLAT_SEL[4:3]	2
0xBB006604	MAC_PORT_CTRL [12].LATE_COLI_THR[2:1]	2
0xBB006604	MAC_PORT_CTRL [12].BKPRES_EN[0:0]	1
0xBB006684	MAC_PORT_CTRL [13].RESERVED[31:5]	27
0xBB006684	MAC_PORT_CTRL [13].PRECOLLAT_SEL[4:3]	2
0xBB006684	MAC_PORT_CTRL [13].LATE_COLI_THR[2:1]	2
0xBB006684	MAC_PORT_CTRL [13].BKPRES_EN[0:0]	1
0xBB006704	MAC_PORT_CTRL [14].RESERVED[31:5]	27
0xBB006704	MAC_PORT_CTRL [14].PRECOLLAT_SEL[4:3]	2
0xBB006704	MAC_PORT_CTRL [14].LATE_COLI_THR[2:1]	2
0xBB006704	MAC_PORT_CTRL [14].BKPRES_EN[0:0]	1
0xBB006784	MAC_PORT_CTRL [15].RESERVED[31:5]	27
0xBB006784	MAC_PORT_CTRL [15].PRECOLLAT_SEL[4:3]	2
0xBB006784	MAC_PORT_CTRL [15].LATE_COLI_THR[2:1]	2
0xBB006784	MAC_PORT_CTRL [15].BKPRES_EN[0:0]	1
0xBB006804	MAC_PORT_CTRL [16].RESERVED[31:5]	27
0xBB006804	MAC_PORT_CTRL [16].PRECOLLAT_SEL[4:3]	2
0xBB006804	MAC_PORT_CTRL [16].LATE_COLI_THR[2:1]	2
0xBB006804	MAC_PORT_CTRL [16].BKPRES_EN[0:0]	1
0xBB006884	MAC_PORT_CTRL [17].RESERVED[31:5]	27
0xBB006884	MAC_PORT_CTRL [17].PRECOLLAT_SEL[4:3]	2
0xBB006884	MAC_PORT_CTRL [17].LATE_COLI_THR[2:1]	2
0xBB006884	MAC_PORT_CTRL [17].BKPRES_EN[0:0]	1
0xBB006904	MAC_PORT_CTRL [18].RESERVED[31:5]	27
0xBB006904	MAC_PORT_CTRL [18].PRECOLLAT_SEL[4:3]	2
0xBB006904	MAC_PORT_CTRL [18].LATE_COLI_THR[2:1]	2
0xBB006904	MAC_PORT_CTRL [18].BKPRES_EN[0:0]	1
0xBB006984	MAC_PORT_CTRL [19].RESERVED[31:5]	27
0xBB006984	MAC_PORT_CTRL [19].PRECOLLAT_SEL[4:3]	2
0xBB006984	MAC_PORT_CTRL [19].LATE_COLI_THR[2:1]	2
0xBB006984	MAC_PORT_CTRL [19].BKPRES_EN[0:0]	1
0xBB006A04	MAC_PORT_CTRL [20].RESERVED[31:5]	27
0xBB006A04	MAC_PORT_CTRL [20].PRECOLLAT_SEL[4:3]	2
0xBB006A04	MAC_PORT_CTRL [20].LATE_COLI_THR[2:1]	2
0xBB006A04	MAC_PORT_CTRL [20].BKPRES_EN[0:0]	1
0xBB006A84	MAC_PORT_CTRL [21].RESERVED[31:5]	27

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006A84	MAC_PORT_CTRL [21].PRECOLLAT_SEL[4:3]	2
0xBB006A84	MAC_PORT_CTRL [21].LATE_COLI_THR[2:1]	2
0xBB006A84	MAC_PORT_CTRL [21].BKPRES_EN[0:0]	1
0xBB006B04	MAC_PORT_CTRL [22].RESERVED[31:5]	27
0xBB006B04	MAC_PORT_CTRL [22].PRECOLLAT_SEL[4:3]	2
0xBB006B04	MAC_PORT_CTRL [22].LATE_COLI_THR[2:1]	2
0xBB006B04	MAC_PORT_CTRL [22].BKPRES_EN[0:0]	1
0xBB006B84	MAC_PORT_CTRL [23].RESERVED[31:5]	27
0xBB006B84	MAC_PORT_CTRL [23].PRECOLLAT_SEL[4:3]	2
0xBB006B84	MAC_PORT_CTRL [23].LATE_COLI_THR[2:1]	2
0xBB006B84	MAC_PORT_CTRL [23].BKPRES_EN[0:0]	1
0xBB006C04	MAC_PORT_CTRL [24].RESERVED[31:5]	27
0xBB006C04	MAC_PORT_CTRL [24].PRECOLLAT_SEL[4:3]	2
0xBB006C04	MAC_PORT_CTRL [24].LATE_COLI_THR[2:1]	2
0xBB006C04	MAC_PORT_CTRL [24].BKPRES_EN[0:0]	1
0xBB006C84	MAC_PORT_CTRL [25].RESERVED[31:5]	27
0xBB006C84	MAC_PORT_CTRL [25].PRECOLLAT_SEL[4:3]	2
0xBB006C84	MAC_PORT_CTRL [25].LATE_COLI_THR[2:1]	2
0xBB006C84	MAC_PORT_CTRL [25].BKPRES_EN[0:0]	1
0xBB006D04	MAC_PORT_CTRL [26].RESERVED[31:5]	27
0xBB006D04	MAC_PORT_CTRL [26].PRECOLLAT_SEL[4:3]	2
0xBB006D04	MAC_PORT_CTRL [26].LATE_COLI_THR[2:1]	2
0xBB006D04	MAC_PORT_CTRL [26].BKPRES_EN[0:0]	1
0xBB006D84	MAC_PORT_CTRL [27].RESERVED[31:5]	27
0xBB006D84	MAC_PORT_CTRL [27].PRECOLLAT_SEL[4:3]	2
0xBB006D84	MAC_PORT_CTRL [27].LATE_COLI_THR[2:1]	2
0xBB006D84	MAC_PORT_CTRL [27].BKPRES_EN[0:0]	1
0xBB006E04	MAC_PORT_CTRL [28].RESERVED[31:5]	27
0xBB006E04	MAC_PORT_CTRL [28].PRECOLLAT_SEL[4:3]	2
0xBB006E04	MAC_PORT_CTRL [28].LATE_COLI_THR[2:1]	2
0xBB006E04	MAC_PORT_CTRL [28].BKPRES_EN[0:0]	1
0xBB006E84	MAC_PORT_CTRL [29].RESERVED[31:5]	27
0xBB006E84	MAC_PORT_CTRL [29].PRECOLLAT_SEL[4:3]	2
0xBB006E84	MAC_PORT_CTRL [29].LATE_COLI_THR[2:1]	2
0xBB006E84	MAC_PORT_CTRL [29].BKPRES_EN[0:0]	1
0xBB006F04	MAC_PORT_CTRL [30].RESERVED[31:5]	27
0xBB006F04	MAC_PORT_CTRL [30].PRECOLLAT_SEL[4:3]	2
0xBB006F04	MAC_PORT_CTRL [30].LATE_COLI_THR[2:1]	2
0xBB006F04	MAC_PORT_CTRL [30].BKPRES_EN[0:0]	1
0xBB006F84	MAC_PORT_CTRL [31].RESERVED[31:5]	27
0xBB006F84	MAC_PORT_CTRL [31].PRECOLLAT_SEL[4:3]	2
0xBB006F84	MAC_PORT_CTRL [31].LATE_COLI_THR[2:1]	2
0xBB006F84	MAC_PORT_CTRL [31].BKPRES_EN[0:0]	1
0xBB007004	MAC_PORT_CTRL [32].RESERVED[31:5]	27
0xBB007004	MAC_PORT_CTRL [32].PRECOLLAT_SEL[4:3]	2
0xBB007004	MAC_PORT_CTRL [32].LATE_COLI_THR[2:1]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007004	MAC_PORT_CTRL [32].BKPRES_EN[0:0]	1
0xBB007084	MAC_PORT_CTRL [33].RESERVED[31:5]	27
0xBB007084	MAC_PORT_CTRL [33].PRECOLLAT_SEL[4:3]	2
0xBB007084	MAC_PORT_CTRL [33].LATE_COLI_THR[2:1]	2
0xBB007084	MAC_PORT_CTRL [33].BKPRES_EN[0:0]	1
0xBB007104	MAC_PORT_CTRL [34].RESERVED[31:5]	27
0xBB007104	MAC_PORT_CTRL [34].PRECOLLAT_SEL[4:3]	2
0xBB007104	MAC_PORT_CTRL [34].LATE_COLI_THR[2:1]	2
0xBB007104	MAC_PORT_CTRL [34].BKPRES_EN[0:0]	1
0xBB007184	MAC_PORT_CTRL [35].RESERVED[31:5]	27
0xBB007184	MAC_PORT_CTRL [35].PRECOLLAT_SEL[4:3]	2
0xBB007184	MAC_PORT_CTRL [35].LATE_COLI_THR[2:1]	2
0xBB007184	MAC_PORT_CTRL [35].BKPRES_EN[0:0]	1
0xBB007204	MAC_PORT_CTRL [36].RESERVED[31:5]	27
0xBB007204	MAC_PORT_CTRL [36].PRECOLLAT_SEL[4:3]	2
0xBB007204	MAC_PORT_CTRL [36].LATE_COLI_THR[2:1]	2
0xBB007204	MAC_PORT_CTRL [36].BKPRES_EN[0:0]	1
0xBB007284	MAC_PORT_CTRL [37].RESERVED[31:5]	27
0xBB007284	MAC_PORT_CTRL [37].PRECOLLAT_SEL[4:3]	2
0xBB007284	MAC_PORT_CTRL [37].LATE_COLI_THR[2:1]	2
0xBB007284	MAC_PORT_CTRL [37].BKPRES_EN[0:0]	1
0xBB007304	MAC_PORT_CTRL [38].RESERVED[31:5]	27
0xBB007304	MAC_PORT_CTRL [38].PRECOLLAT_SEL[4:3]	2
0xBB007304	MAC_PORT_CTRL [38].LATE_COLI_THR[2:1]	2
0xBB007304	MAC_PORT_CTRL [38].BKPRES_EN[0:0]	1
0xBB007384	MAC_PORT_CTRL [39].RESERVED[31:5]	27
0xBB007384	MAC_PORT_CTRL [39].PRECOLLAT_SEL[4:3]	2
0xBB007384	MAC_PORT_CTRL [39].LATE_COLI_THR[2:1]	2
0xBB007384	MAC_PORT_CTRL [39].BKPRES_EN[0:0]	1
0xBB007404	MAC_PORT_CTRL [40].RESERVED[31:5]	27
0xBB007404	MAC_PORT_CTRL [40].PRECOLLAT_SEL[4:3]	2
0xBB007404	MAC_PORT_CTRL [40].LATE_COLI_THR[2:1]	2
0xBB007404	MAC_PORT_CTRL [40].BKPRES_EN[0:0]	1
0xBB007484	MAC_PORT_CTRL [41].RESERVED[31:5]	27
0xBB007484	MAC_PORT_CTRL [41].PRECOLLAT_SEL[4:3]	2
0xBB007484	MAC_PORT_CTRL [41].LATE_COLI_THR[2:1]	2
0xBB007484	MAC_PORT_CTRL [41].BKPRES_EN[0:0]	1
0xBB007504	MAC_PORT_CTRL [42].RESERVED[31:5]	27
0xBB007504	MAC_PORT_CTRL [42].PRECOLLAT_SEL[4:3]	2
0xBB007504	MAC_PORT_CTRL [42].LATE_COLI_THR[2:1]	2
0xBB007504	MAC_PORT_CTRL [42].BKPRES_EN[0:0]	1
0xBB007584	MAC_PORT_CTRL [43].RESERVED[31:5]	27
0xBB007584	MAC_PORT_CTRL [43].PRECOLLAT_SEL[4:3]	2
0xBB007584	MAC_PORT_CTRL [43].LATE_COLI_THR[2:1]	2
0xBB007584	MAC_PORT_CTRL [43].BKPRES_EN[0:0]	1
0xBB007604	MAC_PORT_CTRL [44].RESERVED[31:5]	27

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007604	MAC_PORT_CTRL [44].PRECOLLAT_SEL[4:3]	2
0xBB007604	MAC_PORT_CTRL [44].LATE_COLI_THR[2:1]	2
0xBB007604	MAC_PORT_CTRL [44].BKPRES_EN[0:0]	1
0xBB007684	MAC_PORT_CTRL [45].RESERVED[31:5]	27
0xBB007684	MAC_PORT_CTRL [45].PRECOLLAT_SEL[4:3]	2
0xBB007684	MAC_PORT_CTRL [45].LATE_COLI_THR[2:1]	2
0xBB007684	MAC_PORT_CTRL [45].BKPRES_EN[0:0]	1
0xBB007704	MAC_PORT_CTRL [46].RESERVED[31:5]	27
0xBB007704	MAC_PORT_CTRL [46].PRECOLLAT_SEL[4:3]	2
0xBB007704	MAC_PORT_CTRL [46].LATE_COLI_THR[2:1]	2
0xBB007704	MAC_PORT_CTRL [46].BKPRES_EN[0:0]	1
0xBB007784	MAC_PORT_CTRL [47].RESERVED[31:5]	27
0xBB007784	MAC_PORT_CTRL [47].PRECOLLAT_SEL[4:3]	2
0xBB007784	MAC_PORT_CTRL [47].LATE_COLI_THR[2:1]	2
0xBB007784	MAC_PORT_CTRL [47].BKPRES_EN[0:0]	1
0xBB007804	MAC_PORT_CTRL [48].RESERVED[31:5]	27
0xBB007804	MAC_PORT_CTRL [48].PRECOLLAT_SEL[4:3]	2
0xBB007804	MAC_PORT_CTRL [48].LATE_COLI_THR[2:1]	2
0xBB007804	MAC_PORT_CTRL [48].BKPRES_EN[0:0]	1
0xBB007884	MAC_PORT_CTRL [49].RESERVED[31:5]	27
0xBB007884	MAC_PORT_CTRL [49].PRECOLLAT_SEL[4:3]	2
0xBB007884	MAC_PORT_CTRL [49].LATE_COLI_THR[2:1]	2
0xBB007884	MAC_PORT_CTRL [49].BKPRES_EN[0:0]	1
0xBB007904	MAC_PORT_CTRL [50].RESERVED[31:5]	27
0xBB007904	MAC_PORT_CTRL [50].PRECOLLAT_SEL[4:3]	2
0xBB007904	MAC_PORT_CTRL [50].LATE_COLI_THR[2:1]	2
0xBB007904	MAC_PORT_CTRL [50].BKPRES_EN[0:0]	1
0xBB007984	MAC_PORT_CTRL [51].RESERVED[31:5]	27
0xBB007984	MAC_PORT_CTRL [51].PRECOLLAT_SEL[4:3]	2
0xBB007984	MAC_PORT_CTRL [51].LATE_COLI_THR[2:1]	2
0xBB007984	MAC_PORT_CTRL [51].BKPRES_EN[0:0]	1
0xBB007A04	MAC_PORT_CTRL [52].RESERVED[31:5]	27
0xBB007A04	MAC_PORT_CTRL [52].PRECOLLAT_SEL[4:3]	2
0xBB007A04	MAC_PORT_CTRL [52].LATE_COLI_THR[2:1]	2
0xBB007A04	MAC_PORT_CTRL [52].BKPRES_EN[0:0]	1
0xBB007A84	MAC_PORT_CTRL [53].RESERVED[31:5]	27
0xBB007A84	MAC_PORT_CTRL [53].PRECOLLAT_SEL[4:3]	2
0xBB007A84	MAC_PORT_CTRL [53].LATE_COLI_THR[2:1]	2
0xBB007A84	MAC_PORT_CTRL [53].BKPRES_EN[0:0]	1
0xBB007B04	MAC_PORT_CTRL [54].RESERVED[31:5]	27
0xBB007B04	MAC_PORT_CTRL [54].PRECOLLAT_SEL[4:3]	2
0xBB007B04	MAC_PORT_CTRL [54].LATE_COLI_THR[2:1]	2
0xBB007B04	MAC_PORT_CTRL [54].BKPRES_EN[0:0]	1
0xBB007B84	MAC_PORT_CTRL [55].RESERVED[31:5]	27
0xBB007B84	MAC_PORT_CTRL [55].PRECOLLAT_SEL[4:3]	2
0xBB007B84	MAC_PORT_CTRL [55].LATE_COLI_THR[2:1]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007B84	MAC_PORT_CTRL [55].BKPRESET_EN[0:0]	1
0xBB007C04	MAC_PORT_CTRL [56].RESERVED[31:5]	27
0xBB007C04	MAC_PORT_CTRL [56].PRECOLLAT_SEL[4:3]	2
0xBB007C04	MAC_PORT_CTRL [56].LATE_COLI_THR[2:1]	2
0xBB007C04	MAC_PORT_CTRL [56].BKPRESET_EN[0:0]	1
0xBB000EB0	MAC_RLFDEN [0].RLFDEN[0:0]	1
0xBB000EB0	MAC_RLFDEN [1].RLFDEN[1:1]	1
0xBB000EB0	MAC_RLFDEN [2].RLFDEN[2:2]	1
0xBB000EB0	MAC_RLFDEN [3].RLFDEN[3:3]	1
0xBB000EB0	MAC_RLFDEN [4].RLFDEN[4:4]	1
0xBB000EB0	MAC_RLFDEN [5].RLFDEN[5:5]	1
0xBB000EB0	MAC_RLFDEN [6].RLFDEN[6:6]	1
0xBB000EB0	MAC_RLFDEN [7].RLFDEN[7:7]	1
0xBB000EB0	MAC_RLFDEN [8].RLFDEN[8:8]	1
0xBB000EB0	MAC_RLFDEN [9].RLFDEN[9:9]	1
0xBB000EB0	MAC_RLFDEN [10].RLFDEN[10:10]	1
0xBB000EB0	MAC_RLFDEN [11].RLFDEN[11:11]	1
0xBB000EB0	MAC_RLFDEN [12].RLFDEN[12:12]	1
0xBB000EB0	MAC_RLFDEN [13].RLFDEN[13:13]	1
0xBB000EB0	MAC_RLFDEN [14].RLFDEN[14:14]	1
0xBB000EB0	MAC_RLFDEN [15].RLFDEN[15:15]	1
0xBB000EB0	MAC_RLFDEN [16].RLFDEN[16:16]	1
0xBB000EB0	MAC_RLFDEN [17].RLFDEN[17:17]	1
0xBB000EB0	MAC_RLFDEN [18].RLFDEN[18:18]	1
0xBB000EB0	MAC_RLFDEN [19].RLFDEN[19:19]	1
0xBB000EB0	MAC_RLFDEN [20].RLFDEN[20:20]	1
0xBB000EB0	MAC_RLFDEN [21].RLFDEN[21:21]	1
0xBB000EB0	MAC_RLFDEN [22].RLFDEN[22:22]	1
0xBB000EB0	MAC_RLFDEN [23].RLFDEN[23:23]	1
0xBB000EB0	MAC_RLFDEN [24].RLFDEN[24:24]	1
0xBB000EB0	MAC_RLFDEN [25].RLFDEN[25:25]	1
0xBB000EB0	MAC_RLFDEN [26].RLFDEN[26:26]	1
0xBB000EB0	MAC_RLFDEN [27].RLFDEN[27:27]	1
0xBB000EB0	MAC_RLFDEN [28].RLFDEN[28:28]	1
0xBB000EB0	MAC_RLFDEN [29].RLFDEN[29:29]	1
0xBB000EB0	MAC_RLFDEN [30].RLFDEN[30:30]	1
0xBB000EB0	MAC_RLFDEN [31].RLFDEN[31:31]	1
0xBB000EB4	MAC_RLFDEN [32].RLFDEN[0:0]	1
0xBB000EB4	MAC_RLFDEN [33].RLFDEN[1:1]	1
0xBB000EB4	MAC_RLFDEN [34].RLFDEN[2:2]	1
0xBB000EB4	MAC_RLFDEN [35].RLFDEN[3:3]	1
0xBB000EB4	MAC_RLFDEN [36].RLFDEN[4:4]	1
0xBB000EB4	MAC_RLFDEN [37].RLFDEN[5:5]	1
0xBB000EB4	MAC_RLFDEN [38].RLFDEN[6:6]	1
0xBB000EB4	MAC_RLFDEN [39].RLFDEN[7:7]	1
0xBB000EB4	MAC_RLFDEN [40].RLFDEN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EB4	MAC_RLFD_EN [41].RLFD_EN[9:9]	1
0xBB000EB4	MAC_RLFD_EN [42].RLFD_EN[10:10]	1
0xBB000EB4	MAC_RLFD_EN [43].RLFD_EN[11:11]	1
0xBB000EB4	MAC_RLFD_EN [44].RLFD_EN[12:12]	1
0xBB000EB4	MAC_RLFD_EN [45].RLFD_EN[13:13]	1
0xBB000EB4	MAC_RLFD_EN [46].RLFD_EN[14:14]	1
0xBB000EB4	MAC_RLFD_EN [47].RLFD_EN[15:15]	1
0xBB000EB4	MAC_RLFD_EN [48].RLFD_EN[16:16]	1
0xBB000EB4	MAC_RLFD_EN [49].RLFD_EN[17:17]	1
0xBB000EB4	MAC_RLFD_EN [50].RLFD_EN[18:18]	1
0xBB000EB4	MAC_RLFD_EN [51].RLFD_EN[19:19]	1
0xBB000EB4	MAC_RLFD_EN [52].RLFD_EN[20:20]	1
0xBB000EB4	MAC_RLFD_EN [53].RLFD_EN[21:21]	1
0xBB000EB4	MAC_RLFD_EN [54].RLFD_EN[22:22]	1
0xBB000EB4	MAC_RLFD_EN [55].RLFD_EN[23:23]	1
0xBB000EB8	MAC_RLFD_STS [0].RLFD_STS[0:0]	1
0xBB000EB8	MAC_RLFD_STS [1].RLFD_STS[1:1]	1
0xBB000EB8	MAC_RLFD_STS [2].RLFD_STS[2:2]	1
0xBB000EB8	MAC_RLFD_STS [3].RLFD_STS[3:3]	1
0xBB000EB8	MAC_RLFD_STS [4].RLFD_STS[4:4]	1
0xBB000EB8	MAC_RLFD_STS [5].RLFD_STS[5:5]	1
0xBB000EB8	MAC_RLFD_STS [6].RLFD_STS[6:6]	1
0xBB000EB8	MAC_RLFD_STS [7].RLFD_STS[7:7]	1
0xBB000EB8	MAC_RLFD_STS [8].RLFD_STS[8:8]	1
0xBB000EB8	MAC_RLFD_STS [9].RLFD_STS[9:9]	1
0xBB000EB8	MAC_RLFD_STS [10].RLFD_STS[10:10]	1
0xBB000EB8	MAC_RLFD_STS [11].RLFD_STS[11:11]	1
0xBB000EB8	MAC_RLFD_STS [12].RLFD_STS[12:12]	1
0xBB000EB8	MAC_RLFD_STS [13].RLFD_STS[13:13]	1
0xBB000EB8	MAC_RLFD_STS [14].RLFD_STS[14:14]	1
0xBB000EB8	MAC_RLFD_STS [15].RLFD_STS[15:15]	1
0xBB000EB8	MAC_RLFD_STS [16].RLFD_STS[16:16]	1
0xBB000EB8	MAC_RLFD_STS [17].RLFD_STS[17:17]	1
0xBB000EB8	MAC_RLFD_STS [18].RLFD_STS[18:18]	1
0xBB000EB8	MAC_RLFD_STS [19].RLFD_STS[19:19]	1
0xBB000EB8	MAC_RLFD_STS [20].RLFD_STS[20:20]	1
0xBB000EB8	MAC_RLFD_STS [21].RLFD_STS[21:21]	1
0xBB000EB8	MAC_RLFD_STS [22].RLFD_STS[22:22]	1
0xBB000EB8	MAC_RLFD_STS [23].RLFD_STS[23:23]	1
0xBB000EB8	MAC_RLFD_STS [24].RLFD_STS[24:24]	1
0xBB000EB8	MAC_RLFD_STS [25].RLFD_STS[25:25]	1
0xBB000EB8	MAC_RLFD_STS [26].RLFD_STS[26:26]	1
0xBB000EB8	MAC_RLFD_STS [27].RLFD_STS[27:27]	1
0xBB000EB8	MAC_RLFD_STS [28].RLFD_STS[28:28]	1
0xBB000EB8	MAC_RLFD_STS [29].RLFD_STS[29:29]	1
0xBB000EB8	MAC_RLFD_STS [30].RLFD_STS[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EB8	MAC_RLFD_STS [31].RLFD_STS[31:31]	1
0xBB000EBC	MAC_RLFD_STS [32].RLFD_STS[0:0]	1
0xBB000EBC	MAC_RLFD_STS [33].RLFD_STS[1:1]	1
0xBB000EBC	MAC_RLFD_STS [34].RLFD_STS[2:2]	1
0xBB000EBC	MAC_RLFD_STS [35].RLFD_STS[3:3]	1
0xBB000EBC	MAC_RLFD_STS [36].RLFD_STS[4:4]	1
0xBB000EBC	MAC_RLFD_STS [37].RLFD_STS[5:5]	1
0xBB000EBC	MAC_RLFD_STS [38].RLFD_STS[6:6]	1
0xBB000EBC	MAC_RLFD_STS [39].RLFD_STS[7:7]	1
0xBB000EBC	MAC_RLFD_STS [40].RLFD_STS[8:8]	1
0xBB000EBC	MAC_RLFD_STS [41].RLFD_STS[9:9]	1
0xBB000EBC	MAC_RLFD_STS [42].RLFD_STS[10:10]	1
0xBB000EBC	MAC_RLFD_STS [43].RLFD_STS[11:11]	1
0xBB000EBC	MAC_RLFD_STS [44].RLFD_STS[12:12]	1
0xBB000EBC	MAC_RLFD_STS [45].RLFD_STS[13:13]	1
0xBB000EBC	MAC_RLFD_STS [46].RLFD_STS[14:14]	1
0xBB000EBC	MAC_RLFD_STS [47].RLFD_STS[15:15]	1
0xBB000EBC	MAC_RLFD_STS [48].RLFD_STS[16:16]	1
0xBB000EBC	MAC_RLFD_STS [49].RLFD_STS[17:17]	1
0xBB000EBC	MAC_RLFD_STS [50].RLFD_STS[18:18]	1
0xBB000EBC	MAC_RLFD_STS [51].RLFD_STS[19:19]	1
0xBB000EBC	MAC_RLFD_STS [52].RLFD_STS[20:20]	1
0xBB000EBC	MAC_RLFD_STS [53].RLFD_STS[21:21]	1
0xBB000EBC	MAC_RLFD_STS [54].RLFD_STS[22:22]	1
0xBB000EBC	MAC_RLFD_STS [55].RLFD_STS[23:23]	1
0xBB000EBC	MAC_RLFD_STS [56].RLFD_STS[24:24]	1
0xBB000F00	MAC_RX_PAUSE_STS [0].RX_PAUSE_STS[0:0]	1
0xBB000F00	MAC_RX_PAUSE_STS [1].RX_PAUSE_STS[1:1]	1
0xBB000F00	MAC_RX_PAUSE_STS [2].RX_PAUSE_STS[2:2]	1
0xBB000F00	MAC_RX_PAUSE_STS [3].RX_PAUSE_STS[3:3]	1
0xBB000F00	MAC_RX_PAUSE_STS [4].RX_PAUSE_STS[4:4]	1
0xBB000F00	MAC_RX_PAUSE_STS [5].RX_PAUSE_STS[5:5]	1
0xBB000F00	MAC_RX_PAUSE_STS [6].RX_PAUSE_STS[6:6]	1
0xBB000F00	MAC_RX_PAUSE_STS [7].RX_PAUSE_STS[7:7]	1
0xBB000F00	MAC_RX_PAUSE_STS [8].RX_PAUSE_STS[8:8]	1
0xBB000F00	MAC_RX_PAUSE_STS [9].RX_PAUSE_STS[9:9]	1
0xBB000F00	MAC_RX_PAUSE_STS [10].RX_PAUSE_STS[10:10]	1
0xBB000F00	MAC_RX_PAUSE_STS [11].RX_PAUSE_STS[11:11]	1
0xBB000F00	MAC_RX_PAUSE_STS [12].RX_PAUSE_STS[12:12]	1
0xBB000F00	MAC_RX_PAUSE_STS [13].RX_PAUSE_STS[13:13]	1
0xBB000F00	MAC_RX_PAUSE_STS [14].RX_PAUSE_STS[14:14]	1
0xBB000F00	MAC_RX_PAUSE_STS [15].RX_PAUSE_STS[15:15]	1
0xBB000F00	MAC_RX_PAUSE_STS [16].RX_PAUSE_STS[16:16]	1
0xBB000F00	MAC_RX_PAUSE_STS [17].RX_PAUSE_STS[17:17]	1
0xBB000F00	MAC_RX_PAUSE_STS [18].RX_PAUSE_STS[18:18]	1
0xBB000F00	MAC_RX_PAUSE_STS [19].RX_PAUSE_STS[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F00	MAC_RX_PAUSE_STS [20].RX_PAUSE_STS[20:20]	1
0xBB000F00	MAC_RX_PAUSE_STS [21].RX_PAUSE_STS[21:21]	1
0xBB000F00	MAC_RX_PAUSE_STS [22].RX_PAUSE_STS[22:22]	1
0xBB000F00	MAC_RX_PAUSE_STS [23].RX_PAUSE_STS[23:23]	1
0xBB000F00	MAC_RX_PAUSE_STS [24].RX_PAUSE_STS[24:24]	1
0xBB000F00	MAC_RX_PAUSE_STS [25].RX_PAUSE_STS[25:25]	1
0xBB000F00	MAC_RX_PAUSE_STS [26].RX_PAUSE_STS[26:26]	1
0xBB000F00	MAC_RX_PAUSE_STS [27].RX_PAUSE_STS[27:27]	1
0xBB000F00	MAC_RX_PAUSE_STS [28].RX_PAUSE_STS[28:28]	1
0xBB000F00	MAC_RX_PAUSE_STS [29].RX_PAUSE_STS[29:29]	1
0xBB000F00	MAC_RX_PAUSE_STS [30].RX_PAUSE_STS[30:30]	1
0xBB000F00	MAC_RX_PAUSE_STS [31].RX_PAUSE_STS[31:31]	1
0xBB000F04	MAC_RX_PAUSE_STS [32].RX_PAUSE_STS[0:0]	1
0xBB000F04	MAC_RX_PAUSE_STS [33].RX_PAUSE_STS[1:1]	1
0xBB000F04	MAC_RX_PAUSE_STS [34].RX_PAUSE_STS[2:2]	1
0xBB000F04	MAC_RX_PAUSE_STS [35].RX_PAUSE_STS[3:3]	1
0xBB000F04	MAC_RX_PAUSE_STS [36].RX_PAUSE_STS[4:4]	1
0xBB000F04	MAC_RX_PAUSE_STS [37].RX_PAUSE_STS[5:5]	1
0xBB000F04	MAC_RX_PAUSE_STS [38].RX_PAUSE_STS[6:6]	1
0xBB000F04	MAC_RX_PAUSE_STS [39].RX_PAUSE_STS[7:7]	1
0xBB000F04	MAC_RX_PAUSE_STS [40].RX_PAUSE_STS[8:8]	1
0xBB000F04	MAC_RX_PAUSE_STS [41].RX_PAUSE_STS[9:9]	1
0xBB000F04	MAC_RX_PAUSE_STS [42].RX_PAUSE_STS[10:10]	1
0xBB000F04	MAC_RX_PAUSE_STS [43].RX_PAUSE_STS[11:11]	1
0xBB000F04	MAC_RX_PAUSE_STS [44].RX_PAUSE_STS[12:12]	1
0xBB000F04	MAC_RX_PAUSE_STS [45].RX_PAUSE_STS[13:13]	1
0xBB000F04	MAC_RX_PAUSE_STS [46].RX_PAUSE_STS[14:14]	1
0xBB000F04	MAC_RX_PAUSE_STS [47].RX_PAUSE_STS[15:15]	1
0xBB000F04	MAC_RX_PAUSE_STS [48].RX_PAUSE_STS[16:16]	1
0xBB000F04	MAC_RX_PAUSE_STS [49].RX_PAUSE_STS[17:17]	1
0xBB000F04	MAC_RX_PAUSE_STS [50].RX_PAUSE_STS[18:18]	1
0xBB000F04	MAC_RX_PAUSE_STS [51].RX_PAUSE_STS[19:19]	1
0xBB000F04	MAC_RX_PAUSE_STS [52].RX_PAUSE_STS[20:20]	1
0xBB000F04	MAC_RX_PAUSE_STS [53].RX_PAUSE_STS[21:21]	1
0xBB000F04	MAC_RX_PAUSE_STS [54].RX_PAUSE_STS[22:22]	1
0xBB000F04	MAC_RX_PAUSE_STS [55].RX_PAUSE_STS[23:23]	1
0xBB000F04	MAC_RX_PAUSE_STS [56].RX_PAUSE_STS[24:24]	1
0xBB00136C	MAC_SERDES_MODE_CTRL [0].RESERVED[31:12]	20
0xBB00136C	MAC_SERDES_MODE_CTRL [0].MAC10G_SERDES_MODE[11:9]	3
0xBB00136C	MAC_SERDES_MODE_CTRL [0].MAC5G_SERDES_MODE[8:6]	3
0xBB00136C	MAC_SERDES_MODE_CTRL [0].MAC2P5G_SERDES_MODE[5:3]	3
0xBB00136C	MAC_SERDES_MODE_CTRL [0].MAC1G_SERDES_MODE[2:0]	3
0xBB001370	MAC_SERDES_MODE_CTRL [1].RESERVED[31:12]	20
0xBB001370	MAC_SERDES_MODE_CTRL [1].MAC10G_SERDES_MODE[11:9]	3
0xBB001370	MAC_SERDES_MODE_CTRL [1].MAC5G_SERDES_MODE[8:6]	3
0xBB001370	MAC_SERDES_MODE_CTRL [1].MAC2P5G_SERDES_MODE[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001370	MAC_SERDES_MODE_CTRL [1].MAC1G_SERDES_MODE[2:0]	3
0xBB001374	MAC_SERDES_MODE_CTRL [2].RESERVED[31:12]	20
0xBB001374	MAC_SERDES_MODE_CTRL [2].MAC10G_SERDES_MODE[11:9]	3
0xBB001374	MAC_SERDES_MODE_CTRL [2].MAC5G_SERDES_MODE[8:6]	3
0xBB001374	MAC_SERDES_MODE_CTRL [2].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001374	MAC_SERDES_MODE_CTRL [2].MAC1G_SERDES_MODE[2:0]	3
0xBB001378	MAC_SERDES_MODE_CTRL [3].RESERVED[31:12]	20
0xBB001378	MAC_SERDES_MODE_CTRL [3].MAC10G_SERDES_MODE[11:9]	3
0xBB001378	MAC_SERDES_MODE_CTRL [3].MAC5G_SERDES_MODE[8:6]	3
0xBB001378	MAC_SERDES_MODE_CTRL [3].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001378	MAC_SERDES_MODE_CTRL [3].MAC1G_SERDES_MODE[2:0]	3
0xBB00137C	MAC_SERDES_MODE_CTRL [4].RESERVED[31:12]	20
0xBB00137C	MAC_SERDES_MODE_CTRL [4].MAC10G_SERDES_MODE[11:9]	3
0xBB00137C	MAC_SERDES_MODE_CTRL [4].MAC5G_SERDES_MODE[8:6]	3
0xBB00137C	MAC_SERDES_MODE_CTRL [4].MAC2P5G_SERDES_MODE[5:3]	3
0xBB00137C	MAC_SERDES_MODE_CTRL [4].MAC1G_SERDES_MODE[2:0]	3
0xBB001380	MAC_SERDES_MODE_CTRL [5].RESERVED[31:12]	20
0xBB001380	MAC_SERDES_MODE_CTRL [5].MAC10G_SERDES_MODE[11:9]	3
0xBB001380	MAC_SERDES_MODE_CTRL [5].MAC5G_SERDES_MODE[8:6]	3
0xBB001380	MAC_SERDES_MODE_CTRL [5].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001380	MAC_SERDES_MODE_CTRL [5].MAC1G_SERDES_MODE[2:0]	3
0xBB001384	MAC_SERDES_MODE_CTRL [6].RESERVED[31:12]	20
0xBB001384	MAC_SERDES_MODE_CTRL [6].MAC10G_SERDES_MODE[11:9]	3
0xBB001384	MAC_SERDES_MODE_CTRL [6].MAC5G_SERDES_MODE[8:6]	3
0xBB001384	MAC_SERDES_MODE_CTRL [6].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001384	MAC_SERDES_MODE_CTRL [6].MAC1G_SERDES_MODE[2:0]	3
0xBB001388	MAC_SERDES_MODE_CTRL [7].RESERVED[31:12]	20
0xBB001388	MAC_SERDES_MODE_CTRL [7].MAC10G_SERDES_MODE[11:9]	3
0xBB001388	MAC_SERDES_MODE_CTRL [7].MAC5G_SERDES_MODE[8:6]	3
0xBB001388	MAC_SERDES_MODE_CTRL [7].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001388	MAC_SERDES_MODE_CTRL [7].MAC1G_SERDES_MODE[2:0]	3
0xBB00138C	MAC_SERDES_MODE_CTRL [8].RESERVED[31:12]	20
0xBB00138C	MAC_SERDES_MODE_CTRL [8].MAC10G_SERDES_MODE[11:9]	3
0xBB00138C	MAC_SERDES_MODE_CTRL [8].MAC5G_SERDES_MODE[8:6]	3
0xBB00138C	MAC_SERDES_MODE_CTRL [8].MAC2P5G_SERDES_MODE[5:3]	3
0xBB00138C	MAC_SERDES_MODE_CTRL [8].MAC1G_SERDES_MODE[2:0]	3
0xBB001390	MAC_SERDES_MODE_CTRL [9].RESERVED[31:12]	20
0xBB001390	MAC_SERDES_MODE_CTRL [9].MAC10G_SERDES_MODE[11:9]	3
0xBB001390	MAC_SERDES_MODE_CTRL [9].MAC5G_SERDES_MODE[8:6]	3
0xBB001390	MAC_SERDES_MODE_CTRL [9].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001390	MAC_SERDES_MODE_CTRL [9].MAC1G_SERDES_MODE[2:0]	3
0xBB001394	MAC_SERDES_MODE_CTRL [10].RESERVED[31:12]	20
0xBB001394	MAC_SERDES_MODE_CTRL [10].MAC10G_SERDES_MODE[11:9]	3
0xBB001394	MAC_SERDES_MODE_CTRL [10].MAC5G_SERDES_MODE[8:6]	3
0xBB001394	MAC_SERDES_MODE_CTRL [10].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001394	MAC_SERDES_MODE_CTRL [10].MAC1G_SERDES_MODE[2:0]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001398	MAC_SERDES_MODE_CTRL [11].RESERVED[31:12]	20
0xBB001398	MAC_SERDES_MODE_CTRL [11].MAC10G_SERDES_MODE[11:9]	3
0xBB001398	MAC_SERDES_MODE_CTRL [11].MAC5G_SERDES_MODE[8:6]	3
0xBB001398	MAC_SERDES_MODE_CTRL [11].MAC2P5G_SERDES_MODE[5:3]	3
0xBB001398	MAC_SERDES_MODE_CTRL [11].MAC1G_SERDES_MODE[2:0]	3
0xBB00139C	MAC_SERDES_MODE_CTRL [12].RESERVED[31:12]	20
0xBB00139C	MAC_SERDES_MODE_CTRL [12].MAC10G_SERDES_MODE[11:9]	3
0xBB00139C	MAC_SERDES_MODE_CTRL [12].MAC5G_SERDES_MODE[8:6]	3
0xBB00139C	MAC_SERDES_MODE_CTRL [12].MAC2P5G_SERDES_MODE[5:3]	3
0xBB00139C	MAC_SERDES_MODE_CTRL [12].MAC1G_SERDES_MODE[2:0]	3
0xBB0013A0	MAC_SERDES_MODE_CTRL [13].RESERVED[31:12]	20
0xBB0013A0	MAC_SERDES_MODE_CTRL [13].MAC10G_SERDES_MODE[11:9]	3
0xBB0013A0	MAC_SERDES_MODE_CTRL [13].MAC5G_SERDES_MODE[8:6]	3
0xBB0013A0	MAC_SERDES_MODE_CTRL [13].MAC2P5G_SERDES_MODE[5:3]	3
0xBB0013A0	MAC_SERDES_MODE_CTRL [13].MAC1G_SERDES_MODE[2:0]	3
0xBB00124C	MAC_SLV_TIMEOUT.RESERVED[31:15]	17
0xBB00124C	MAC_SLV_TIMEOUT.SPI_TIMEOUT_SET[14:1]	14
0xBB00124C	MAC_SLV_TIMEOUT.SPI_TIMEOUT_FLAG[0:0]	1
0xBB000EF8	MAC_TX_PAUSE_STS [0].TX_PAUSE_STS[0:0]	1
0xBB000EF8	MAC_TX_PAUSE_STS [1].TX_PAUSE_STS[1:1]	1
0xBB000EF8	MAC_TX_PAUSE_STS [2].TX_PAUSE_STS[2:2]	1
0xBB000EF8	MAC_TX_PAUSE_STS [3].TX_PAUSE_STS[3:3]	1
0xBB000EF8	MAC_TX_PAUSE_STS [4].TX_PAUSE_STS[4:4]	1
0xBB000EF8	MAC_TX_PAUSE_STS [5].TX_PAUSE_STS[5:5]	1
0xBB000EF8	MAC_TX_PAUSE_STS [6].TX_PAUSE_STS[6:6]	1
0xBB000EF8	MAC_TX_PAUSE_STS [7].TX_PAUSE_STS[7:7]	1
0xBB000EF8	MAC_TX_PAUSE_STS [8].TX_PAUSE_STS[8:8]	1
0xBB000EF8	MAC_TX_PAUSE_STS [9].TX_PAUSE_STS[9:9]	1
0xBB000EF8	MAC_TX_PAUSE_STS [10].TX_PAUSE_STS[10:10]	1
0xBB000EF8	MAC_TX_PAUSE_STS [11].TX_PAUSE_STS[11:11]	1
0xBB000EF8	MAC_TX_PAUSE_STS [12].TX_PAUSE_STS[12:12]	1
0xBB000EF8	MAC_TX_PAUSE_STS [13].TX_PAUSE_STS[13:13]	1
0xBB000EF8	MAC_TX_PAUSE_STS [14].TX_PAUSE_STS[14:14]	1
0xBB000EF8	MAC_TX_PAUSE_STS [15].TX_PAUSE_STS[15:15]	1
0xBB000EF8	MAC_TX_PAUSE_STS [16].TX_PAUSE_STS[16:16]	1
0xBB000EF8	MAC_TX_PAUSE_STS [17].TX_PAUSE_STS[17:17]	1
0xBB000EF8	MAC_TX_PAUSE_STS [18].TX_PAUSE_STS[18:18]	1
0xBB000EF8	MAC_TX_PAUSE_STS [19].TX_PAUSE_STS[19:19]	1
0xBB000EF8	MAC_TX_PAUSE_STS [20].TX_PAUSE_STS[20:20]	1
0xBB000EF8	MAC_TX_PAUSE_STS [21].TX_PAUSE_STS[21:21]	1
0xBB000EF8	MAC_TX_PAUSE_STS [22].TX_PAUSE_STS[22:22]	1
0xBB000EF8	MAC_TX_PAUSE_STS [23].TX_PAUSE_STS[23:23]	1
0xBB000EF8	MAC_TX_PAUSE_STS [24].TX_PAUSE_STS[24:24]	1
0xBB000EF8	MAC_TX_PAUSE_STS [25].TX_PAUSE_STS[25:25]	1
0xBB000EF8	MAC_TX_PAUSE_STS [26].TX_PAUSE_STS[26:26]	1
0xBB000EF8	MAC_TX_PAUSE_STS [27].TX_PAUSE_STS[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000EF8	MAC_TX_PAUSE_STS [28].TX_PAUSE_STS[28:28]	1
0xBB000EF8	MAC_TX_PAUSE_STS [29].TX_PAUSE_STS[29:29]	1
0xBB000EF8	MAC_TX_PAUSE_STS [30].TX_PAUSE_STS[30:30]	1
0xBB000EF8	MAC_TX_PAUSE_STS [31].TX_PAUSE_STS[31:31]	1
0xBB000EFC	MAC_TX_PAUSE_STS [32].TX_PAUSE_STS[0:0]	1
0xBB000EFC	MAC_TX_PAUSE_STS [33].TX_PAUSE_STS[1:1]	1
0xBB000EFC	MAC_TX_PAUSE_STS [34].TX_PAUSE_STS[2:2]	1
0xBB000EFC	MAC_TX_PAUSE_STS [35].TX_PAUSE_STS[3:3]	1
0xBB000EFC	MAC_TX_PAUSE_STS [36].TX_PAUSE_STS[4:4]	1
0xBB000EFC	MAC_TX_PAUSE_STS [37].TX_PAUSE_STS[5:5]	1
0xBB000EFC	MAC_TX_PAUSE_STS [38].TX_PAUSE_STS[6:6]	1
0xBB000EFC	MAC_TX_PAUSE_STS [39].TX_PAUSE_STS[7:7]	1
0xBB000EFC	MAC_TX_PAUSE_STS [40].TX_PAUSE_STS[8:8]	1
0xBB000EFC	MAC_TX_PAUSE_STS [41].TX_PAUSE_STS[9:9]	1
0xBB000EFC	MAC_TX_PAUSE_STS [42].TX_PAUSE_STS[10:10]	1
0xBB000EFC	MAC_TX_PAUSE_STS [43].TX_PAUSE_STS[11:11]	1
0xBB000EFC	MAC_TX_PAUSE_STS [44].TX_PAUSE_STS[12:12]	1
0xBB000EFC	MAC_TX_PAUSE_STS [45].TX_PAUSE_STS[13:13]	1
0xBB000EFC	MAC_TX_PAUSE_STS [46].TX_PAUSE_STS[14:14]	1
0xBB000EFC	MAC_TX_PAUSE_STS [47].TX_PAUSE_STS[15:15]	1
0xBB000EFC	MAC_TX_PAUSE_STS [48].TX_PAUSE_STS[16:16]	1
0xBB000EFC	MAC_TX_PAUSE_STS [49].TX_PAUSE_STS[17:17]	1
0xBB000EFC	MAC_TX_PAUSE_STS [50].TX_PAUSE_STS[18:18]	1
0xBB000EFC	MAC_TX_PAUSE_STS [51].TX_PAUSE_STS[19:19]	1
0xBB000EFC	MAC_TX_PAUSE_STS [52].TX_PAUSE_STS[20:20]	1
0xBB000EFC	MAC_TX_PAUSE_STS [53].TX_PAUSE_STS[21:21]	1
0xBB000EFC	MAC_TX_PAUSE_STS [54].TX_PAUSE_STS[22:22]	1
0xBB000EFC	MAC_TX_PAUSE_STS [55].TX_PAUSE_STS[23:23]	1
0xBB000EFC	MAC_TX_PAUSE_STS [56].TX_PAUSE_STS[24:24]	1
0xBB000934	MBIST_NIC_R.RESERVED[31:15]	17
0xBB000934	MBIST_NIC_R.NIC_DRF_START_PAUSE_02[14:14]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_BIST_FAIL_02[13:13]	1
0xBB000934	MBIST_NIC_R.NIC_BIST_FAIL_02[12:12]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_BIST_DONE_02[11:11]	1
0xBB000934	MBIST_NIC_R.NIC_BIST_DONE_02[10:10]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_START_PAUSE_01[9:9]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_BIST_FAIL_01[8:8]	1
0xBB000934	MBIST_NIC_R.NIC_BIST_FAIL_01[7:7]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_BIST_DONE_01[6:6]	1
0xBB000934	MBIST_NIC_R.NIC_BIST_DONE_01[5:5]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_START_PAUSE[4:4]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_BIST_FAIL[3:3]	1
0xBB000934	MBIST_NIC_R.NIC_BIST_FAIL[2:2]	1
0xBB000934	MBIST_NIC_R.NIC_DRF_BIST_DONE[1:1]	1
0xBB000934	MBIST_NIC_R.NIC_BIST_DONE[0:0]	1
0xBB000930	MBIST_NIC_W.RESERVED[31:4]	28

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000930	MBIST_NIC_W.CFG_DRF_TEST_RESUME_02[3:3]	1
0xBB000930	MBIST_NIC_W.CFG_DRF_TEST_RESUME_01[2:2]	1
0xBB000930	MBIST_NIC_W.CFG_BIST_RSTN_02[1:1]	1
0xBB000930	MBIST_NIC_W.CFG_BIST_RSTN_01[0:0]	1
0xBB000CDC	MDIO_FREE_CNT_CTRL.RESERVED[31:14]	18
0xBB000CDC	MDIO_FREE_CNT_CTRL.MDIO_FREE_CNT_RDY[13:13]	1
0xBB000CDC	MDIO_FREE_CNT_CTRL.MDIO_FREE_CNT_SEL[12:1]	12
0xBB000CDC	MDIO_FREE_CNT_CTRL.MDIO_FREE_CNT_EN[0:0]	1
0xBB00411C	METER_GLB_CTRL.RESERVED[31:3]	29
0xBB00411C	METER_GLB_CTRL.TRTCM_TYPE[2:2]	1
0xBB00411C	METER_GLB_CTRL.SRTCM_SMART_TKN[1:1]	1
0xBB00411C	METER_GLB_CTRL.INCL_PREIFG[0:0]	1
0xBB004120	METER_LB_EXCEED_STS [0][0].LB_EXCEED[0:0]	1
0xBB004120	METER_LB_EXCEED_STS [0][1].LB_EXCEED[1:1]	1
0xBB004120	METER_LB_EXCEED_STS [0][2].LB_EXCEED[2:2]	1
0xBB004120	METER_LB_EXCEED_STS [0][3].LB_EXCEED[3:3]	1
0xBB004120	METER_LB_EXCEED_STS [0][4].LB_EXCEED[4:4]	1
0xBB004120	METER_LB_EXCEED_STS [0][5].LB_EXCEED[5:5]	1
0xBB004120	METER_LB_EXCEED_STS [0][6].LB_EXCEED[6:6]	1
0xBB004120	METER_LB_EXCEED_STS [0][7].LB_EXCEED[7:7]	1
0xBB004120	METER_LB_EXCEED_STS [0][8].LB_EXCEED[8:8]	1
0xBB004120	METER_LB_EXCEED_STS [0][9].LB_EXCEED[9:9]	1
0xBB004120	METER_LB_EXCEED_STS [0][10].LB_EXCEED[10:10]	1
0xBB004120	METER_LB_EXCEED_STS [0][11].LB_EXCEED[11:11]	1
0xBB004120	METER_LB_EXCEED_STS [0][12].LB_EXCEED[12:12]	1
0xBB004120	METER_LB_EXCEED_STS [0][13].LB_EXCEED[13:13]	1
0xBB004120	METER_LB_EXCEED_STS [0][14].LB_EXCEED[14:14]	1
0xBB004120	METER_LB_EXCEED_STS [0][15].LB_EXCEED[15:15]	1
0xBB004120	METER_LB_EXCEED_STS [0][16].LB_EXCEED[16:16]	1
0xBB004120	METER_LB_EXCEED_STS [0][17].LB_EXCEED[17:17]	1
0xBB004120	METER_LB_EXCEED_STS [0][18].LB_EXCEED[18:18]	1
0xBB004120	METER_LB_EXCEED_STS [0][19].LB_EXCEED[19:19]	1
0xBB004120	METER_LB_EXCEED_STS [0][20].LB_EXCEED[20:20]	1
0xBB004120	METER_LB_EXCEED_STS [0][21].LB_EXCEED[21:21]	1
0xBB004120	METER_LB_EXCEED_STS [0][22].LB_EXCEED[22:22]	1
0xBB004120	METER_LB_EXCEED_STS [0][23].LB_EXCEED[23:23]	1
0xBB004120	METER_LB_EXCEED_STS [0][24].LB_EXCEED[24:24]	1
0xBB004120	METER_LB_EXCEED_STS [0][25].LB_EXCEED[25:25]	1
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0xBB004158	METER_LB_EXCEED_STS [14][29].LB_EXCEED[29:29]	1
0xBB004158	METER_LB_EXCEED_STS [14][30].LB_EXCEED[30:30]	1
0xBB004158	METER_LB_EXCEED_STS [14][31].LB_EXCEED[31:31]	1
0xBB00415C	METER_LB_EXCEED_STS [15][0].LB_EXCEED[0:0]	1
0xBB00415C	METER_LB_EXCEED_STS [15][1].LB_EXCEED[1:1]	1
0xBB00415C	METER_LB_EXCEED_STS [15][2].LB_EXCEED[2:2]	1
0xBB00415C	METER_LB_EXCEED_STS [15][3].LB_EXCEED[3:3]	1
0xBB00415C	METER_LB_EXCEED_STS [15][4].LB_EXCEED[4:4]	1
0xBB00415C	METER_LB_EXCEED_STS [15][5].LB_EXCEED[5:5]	1
0xBB00415C	METER_LB_EXCEED_STS [15][6].LB_EXCEED[6:6]	1
0xBB00415C	METER_LB_EXCEED_STS [15][7].LB_EXCEED[7:7]	1
0xBB00415C	METER_LB_EXCEED_STS [15][8].LB_EXCEED[8:8]	1
0xBB00415C	METER_LB_EXCEED_STS [15][9].LB_EXCEED[9:9]	1
0xBB00415C	METER_LB_EXCEED_STS [15][10].LB_EXCEED[10:10]	1
0xBB00415C	METER_LB_EXCEED_STS [15][11].LB_EXCEED[11:11]	1
0xBB00415C	METER_LB_EXCEED_STS [15][12].LB_EXCEED[12:12]	1
0xBB00415C	METER_LB_EXCEED_STS [15][13].LB_EXCEED[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00415C	METER_LB_EXCEED_STS [15][14].LB_EXCEED[14:14]	1
0xBB00415C	METER_LB_EXCEED_STS [15][15].LB_EXCEED[15:15]	1
0xBB00415C	METER_LB_EXCEED_STS [15][16].LB_EXCEED[16:16]	1
0xBB00415C	METER_LB_EXCEED_STS [15][17].LB_EXCEED[17:17]	1
0xBB00415C	METER_LB_EXCEED_STS [15][18].LB_EXCEED[18:18]	1
0xBB00415C	METER_LB_EXCEED_STS [15][19].LB_EXCEED[19:19]	1
0xBB00415C	METER_LB_EXCEED_STS [15][20].LB_EXCEED[20:20]	1
0xBB00415C	METER_LB_EXCEED_STS [15][21].LB_EXCEED[21:21]	1
0xBB00415C	METER_LB_EXCEED_STS [15][22].LB_EXCEED[22:22]	1
0xBB00415C	METER_LB_EXCEED_STS [15][23].LB_EXCEED[23:23]	1
0xBB00415C	METER_LB_EXCEED_STS [15][24].LB_EXCEED[24:24]	1
0xBB00415C	METER_LB_EXCEED_STS [15][25].LB_EXCEED[25:25]	1
0xBB00415C	METER_LB_EXCEED_STS [15][26].LB_EXCEED[26:26]	1
0xBB00415C	METER_LB_EXCEED_STS [15][27].LB_EXCEED[27:27]	1
0xBB00415C	METER_LB_EXCEED_STS [15][28].LB_EXCEED[28:28]	1
0xBB00415C	METER_LB_EXCEED_STS [15][29].LB_EXCEED[29:29]	1
0xBB00415C	METER_LB_EXCEED_STS [15][30].LB_EXCEED[30:30]	1
0xBB00415C	METER_LB_EXCEED_STS [15][31].LB_EXCEED[31:31]	1
0xBB004160	METER_LB_GLB_EXCEED_STS.LB_EXCEED[31:0]	32
0xBB00AF00	MIR_CTRL [0].RESERVED[31:19]	13
0xBB00AF00	MIR_CTRL [0].MTP_DEV[18:15]	4
0xBB00AF00	MIR_CTRL [0].MTP_PORT[14:9]	6
0xBB00AF00	MIR_CTRL [0].MTP_IS_TRK[8:8]	1
0xBB00AF00	MIR_CTRL [0].TRAFFIC_SEL[7:7]	1
0xBB00AF00	MIR_CTRL [0].MTP_TX_ISO[6:6]	1
0xBB00AF00	MIR_CTRL [0].MIR_MODE[5:5]	1
0xBB00AF00	MIR_CTRL [0].MIR_RX_TX_SEL[4:4]	1
0xBB00AF00	MIR_CTRL [0].MTP_SELF_FILTER[3:3]	1
0xBB00AF00	MIR_CTRL [0].MIR_OP[2:2]	1
0xBB00AF00	MIR_CTRL [0].MIR_TYPE[1:0]	2
0xBB00AF04	MIR_CTRL [1].RESERVED[31:19]	13
0xBB00AF04	MIR_CTRL [1].MTP_DEV[18:15]	4
0xBB00AF04	MIR_CTRL [1].MTP_PORT[14:9]	6
0xBB00AF04	MIR_CTRL [1].MTP_IS_TRK[8:8]	1
0xBB00AF04	MIR_CTRL [1].TRAFFIC_SEL[7:7]	1
0xBB00AF04	MIR_CTRL [1].MTP_TX_ISO[6:6]	1
0xBB00AF04	MIR_CTRL [1].MIR_MODE[5:5]	1
0xBB00AF04	MIR_CTRL [1].MIR_RX_TX_SEL[4:4]	1
0xBB00AF04	MIR_CTRL [1].MTP_SELF_FILTER[3:3]	1
0xBB00AF04	MIR_CTRL [1].MIR_OP[2:2]	1
0xBB00AF04	MIR_CTRL [1].MIR_TYPE[1:0]	2
0xBB00AF08	MIR_CTRL [2].RESERVED[31:19]	13
0xBB00AF08	MIR_CTRL [2].MTP_DEV[18:15]	4
0xBB00AF08	MIR_CTRL [2].MTP_PORT[14:9]	6
0xBB00AF08	MIR_CTRL [2].MTP_IS_TRK[8:8]	1
0xBB00AF08	MIR_CTRL [2].TRAFFIC_SEL[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00AF08	MIR_CTRL [2].MTP_TX_ISO[6:6]	1
0xBB00AF08	MIR_CTRL [2].MIR_MODE[5:5]	1
0xBB00AF08	MIR_CTRL [2].MIR_RX_TX_SEL[4:4]	1
0xBB00AF08	MIR_CTRL [2].MTP_SELF_FILTER[3:3]	1
0xBB00AF08	MIR_CTRL [2].MIR_OP[2:2]	1
0xBB00AF08	MIR_CTRL [2].MIR_TYPE[1:0]	2
0xBB00AF0C	MIR_CTRL [3].RESERVED[31:19]	13
0xBB00AF0C	MIR_CTRL [3].MTP_DEV[18:15]	4
0xBB00AF0C	MIR_CTRL [3].MTP_PORT[14:9]	6
0xBB00AF0C	MIR_CTRL [3].MTP_IS_TRK[8:8]	1
0xBB00AF0C	MIR_CTRL [3].TRAFFIC_SEL[7:7]	1
0xBB00AF0C	MIR_CTRL [3].MTP_TX_ISO[6:6]	1
0xBB00AF0C	MIR_CTRL [3].MIR_MODE[5:5]	1
0xBB00AF0C	MIR_CTRL [3].MIR_RX_TX_SEL[4:4]	1
0xBB00AF0C	MIR_CTRL [3].MTP_SELF_FILTER[3:3]	1
0xBB00AF0C	MIR_CTRL [3].MIR_OP[2:2]	1
0xBB00AF0C	MIR_CTRL [3].MIR_TYPE[1:0]	2
0xBB00AF30	MIR_DPM_CTRL [0].RESERVED[31:25]	7
0xBB00AF30	MIR_DPM_CTRL [0].DPM_56_32[24:0]	25
0xBB00AF34	MIR_DPM_CTRL [0].DPM_31_0[31:0]	32
0xBB00AF38	MIR_DPM_CTRL [1].RESERVED[31:25]	7
0xBB00AF38	MIR_DPM_CTRL [1].DPM_56_32[24:0]	25
0xBB00AF3C	MIR_DPM_CTRL [1].DPM_31_0[31:0]	32
0xBB00AF40	MIR_DPM_CTRL [2].RESERVED[31:25]	7
0xBB00AF40	MIR_DPM_CTRL [2].DPM_56_32[24:0]	25
0xBB00AF44	MIR_DPM_CTRL [2].DPM_31_0[31:0]	32
0xBB00AF48	MIR_DPM_CTRL [3].RESERVED[31:25]	7
0xBB00AF48	MIR_DPM_CTRL [3].DPM_56_32[24:0]	25
0xBB00AF4C	MIR_DPM_CTRL [3].DPM_31_0[31:0]	32
0xBB00AF58	MIR_QID_CTRL.RESERVED[31:4]	28
0xBB00AF58	MIR_QID_CTRL.MIR_QID[3:1]	3
0xBB00AF58	MIR_QID_CTRL.MIR_QID_EN[0:0]	1
0xBB00AF5C	MIR_RSPAN_RX_TAG_RM_CTRL [0].RSPAN_RX_TAG_RM[0:0]	1
0xBB00AF5C	MIR_RSPAN_RX_TAG_RM_CTRL [1].RSPAN_RX_TAG_RM[1:1]	1
0xBB00AF5C	MIR_RSPAN_RX_TAG_RM_CTRL [2].RSPAN_RX_TAG_RM[2:2]	1
0xBB00AF5C	MIR_RSPAN_RX_TAG_RM_CTRL [3].RSPAN_RX_TAG_RM[3:3]	1
0xBB001420	MIR_RSPAN_TX_CTRL [0].RSPAN_TX_TAG_ADD[0:0]	1
0xBB001420	MIR_RSPAN_TX_CTRL [1].RSPAN_TX_TAG_ADD[1:1]	1
0xBB001420	MIR_RSPAN_TX_CTRL [2].RSPAN_TX_TAG_ADD[2:2]	1
0xBB001420	MIR_RSPAN_TX_CTRL [3].RSPAN_TX_TAG_ADD[3:3]	1
0xBB001410	MIR_RSPAN_VLAN_CTRL [0].RSPAN_TAG_TPID[31:16]	16
0xBB001410	MIR_RSPAN_VLAN_CTRL [0].RSPAN_TAG_PRI[15:13]	3
0xBB001410	MIR_RSPAN_VLAN_CTRL [0].RSPAN_TAG_CFI[12:12]	1
0xBB001410	MIR_RSPAN_VLAN_CTRL [0].RSPAN_TAG_VID[11:0]	12
0xBB001414	MIR_RSPAN_VLAN_CTRL [1].RSPAN_TAG_TPID[31:16]	16
0xBB001414	MIR_RSPAN_VLAN_CTRL [1].RSPAN_TAG_PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001414	MIR_RSPAN_VLAN_CTRL [1].RSPAN_TAG_CFI[12:12]	1
0xBB001414	MIR_RSPAN_VLAN_CTRL [1].RSPAN_TAG_VID[11:0]	12
0xBB001418	MIR_RSPAN_VLAN_CTRL [2].RSPAN_TAG_TPID[31:16]	16
0xBB001418	MIR_RSPAN_VLAN_CTRL [2].RSPAN_TAG_PRI[15:13]	3
0xBB001418	MIR_RSPAN_VLAN_CTRL [2].RSPAN_TAG_CFI[12:12]	1
0xBB001418	MIR_RSPAN_VLAN_CTRL [2].RSPAN_TAG_VID[11:0]	12
0xBB00141C	MIR_RSPAN_VLAN_CTRL [3].RSPAN_TAG_TPID[31:16]	16
0xBB00141C	MIR_RSPAN_VLAN_CTRL [3].RSPAN_TAG_PRI[15:13]	3
0xBB00141C	MIR_RSPAN_VLAN_CTRL [3].RSPAN_TAG_CFI[12:12]	1
0xBB00141C	MIR_RSPAN_VLAN_CTRL [3].RSPAN_TAG_VID[11:0]	12
0xBB00AF50	MIR_SAMPLE_RATE_CTRL [0].SAMPLE_RATE[15:0]	16
0xBB00AF50	MIR_SAMPLE_RATE_CTRL [1].SAMPLE_RATE[31:16]	16
0xBB00AF54	MIR_SAMPLE_RATE_CTRL [2].SAMPLE_RATE[15:0]	16
0xBB00AF54	MIR_SAMPLE_RATE_CTRL [3].SAMPLE_RATE[31:16]	16
0xBB00AF10	MIR_SPM_CTRL [0].RESERVED[31:25]	7
0xBB00AF10	MIR_SPM_CTRL [0].SPM_56_32[24:0]	25
0xBB00AF14	MIR_SPM_CTRL [0].SPM_31_0[31:0]	32
0xBB00AF18	MIR_SPM_CTRL [1].RESERVED[31:25]	7
0xBB00AF18	MIR_SPM_CTRL [1].SPM_56_32[24:0]	25
0xBB00AF1C	MIR_SPM_CTRL [1].SPM_31_0[31:0]	32
0xBB00AF20	MIR_SPM_CTRL [2].RESERVED[31:25]	7
0xBB00AF20	MIR_SPM_CTRL [2].SPM_56_32[24:0]	25
0xBB00AF24	MIR_SPM_CTRL [2].SPM_31_0[31:0]	32
0xBB00AF28	MIR_SPM_CTRL [3].RESERVED[31:25]	7
0xBB00AF28	MIR_SPM_CTRL [3].SPM_56_32[24:0]	25
0xBB00AF2C	MIR_SPM_CTRL [3].SPM_31_0[31:0]	32
0xBB000004	MODEL_NAME_INFO.RTL_ID[31:16]	16
0xBB000004	MODEL_NAME_INFO.MODEL_CHAR_1ST[15:11]	5
0xBB000004	MODEL_NAME_INFO.MODEL_CHAR_2ND[10:6]	5
0xBB000004	MODEL_NAME_INFO.MODEL_CHAR_3RD[5:4]	2
0xBB000004	MODEL_NAME_INFO.RTL_VID[3:0]	4
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][0].TC[2:0]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][1].TC[5:3]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][2].TC[8:6]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][3].TC[11:9]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][4].TC[14:12]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][5].TC[17:15]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][6].TC[20:18]	3
0xBB004F50	MPLS_DPINTPRI2TC_CTRL [0][7].TC[23:21]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][0].TC[2:0]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][1].TC[5:3]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][2].TC[8:6]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][3].TC[11:9]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][4].TC[14:12]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][5].TC[17:15]	3
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][6].TC[20:18]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004F54	MPLS_DPINTPRI2TC_CTRL [1][7].TC[23:21]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][0].TC[2:0]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][1].TC[5:3]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][2].TC[8:6]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][3].TC[11:9]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][4].TC[14:12]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][5].TC[17:15]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][6].TC[20:18]	3
0xBB004F58	MPLS_DPINTPRI2TC_CTRL [2][7].TC[23:21]	3
0xBB004F4C	MPLS_ENCAP_CTRL.RESERVED[31:1]	31
0xBB004F4C	MPLS_ENCAP_CTRL CTRL_WORD_ADD[0:0]	1
0xBB00F2E4	MPLS_GLB_CTRL.RESERVED[31:6]	26
0xBB00F2E4	MPLS_GLB_CTRL.LABEL_0_15_ACT[5:5]	1
0xBB00F2E4	MPLS_GLB_CTRL.TTL_FAIL[4:4]	1
0xBB00F2E4	MPLS_GLB_CTRL.HASH_SEL[3:3]	1
0xBB00F2E4	MPLS_GLB_CTRL.CPU_SEL[2:2]	1
0xBB00F2E4	MPLS_GLB_CTRL.LABEL_NUM_EXCEED[1:1]	1
0xBB00F2E4	MPLS_GLB_CTRL.LABEL_UNKW[0:0]	1
0xBB00052C	MPLS_PARSE_CTRL.RESERVED[31:1]	31
0xBB00052C	MPLS_PARSE_CTRL CTRL_WORD_EN[0:0]	1
0xBB008D40	OAM_CTRL.RESERVED[31:5]	27
0xBB008D40	OAM_CTRL.LRN[4:4]	1
0xBB008D40	OAM_CTRL.CPU_SEL[3:3]	1
0xBB008D40	OAM_CTRL.MAC_SWAP[2:2]	1
0xBB008D40	OAM_CTRL.DIS_ACT[1:1]	1
0xBB008D40	OAM_CTRL.EN[0:0]	1
0xBB001424	OAM_GLB_DYING_GASP_CTRL.RESERVED[31:22]	10
0xBB001424	OAM_GLB_DYING_GASP_CTRL.DYINC_GASP_VTH[21:20]	2
0xBB001424	OAM_GLB_DYING_GASP_CTRL.DYING_GASP_PKTCNT[19:17]	3
0xBB001424	OAM_GLB_DYING_GASP_CTRL.DYTNG_GASP_TRIG[16:16]	1
0xBB001424	OAM_GLB_DYING_GASP_CTRL.TBP_VAL[15:0]	16
0xBB008D44	OAM_PORT_ACT_CTRL [0].RESERVED[31:3]	29
0xBB008D44	OAM_PORT_ACT_CTRL [0].PAR_ACT[2:1]	2
0xBB008D44	OAM_PORT_ACT_CTRL [0].MUX_ACT[0:0]	1
0xBB008D48	OAM_PORT_ACT_CTRL [1].RESERVED[31:3]	29
0xBB008D48	OAM_PORT_ACT_CTRL [1].PAR_ACT[2:1]	2
0xBB008D48	OAM_PORT_ACT_CTRL [1].MUX_ACT[0:0]	1
0xBB008D4C	OAM_PORT_ACT_CTRL [2].RESERVED[31:3]	29
0xBB008D4C	OAM_PORT_ACT_CTRL [2].PAR_ACT[2:1]	2
0xBB008D4C	OAM_PORT_ACT_CTRL [2].MUX_ACT[0:0]	1
0xBB008D50	OAM_PORT_ACT_CTRL [3].RESERVED[31:3]	29
0xBB008D50	OAM_PORT_ACT_CTRL [3].PAR_ACT[2:1]	2
0xBB008D50	OAM_PORT_ACT_CTRL [3].MUX_ACT[0:0]	1
0xBB008D54	OAM_PORT_ACT_CTRL [4].RESERVED[31:3]	29
0xBB008D54	OAM_PORT_ACT_CTRL [4].PAR_ACT[2:1]	2
0xBB008D54	OAM_PORT_ACT_CTRL [4].MUX_ACT[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008D58	OAM_PORT_ACT_CTRL [5].RESERVED[31:3]	29
0xBB008D58	OAM_PORT_ACT_CTRL [5].PAR_ACT[2:1]	2
0xBB008D58	OAM_PORT_ACT_CTRL [5].MUX_ACT[0:0]	1
0xBB008D5C	OAM_PORT_ACT_CTRL [6].RESERVED[31:3]	29
0xBB008D5C	OAM_PORT_ACT_CTRL [6].PAR_ACT[2:1]	2
0xBB008D5C	OAM_PORT_ACT_CTRL [6].MUX_ACT[0:0]	1
0xBB008D60	OAM_PORT_ACT_CTRL [7].RESERVED[31:3]	29
0xBB008D60	OAM_PORT_ACT_CTRL [7].PAR_ACT[2:1]	2
0xBB008D60	OAM_PORT_ACT_CTRL [7].MUX_ACT[0:0]	1
0xBB008D64	OAM_PORT_ACT_CTRL [8].RESERVED[31:3]	29
0xBB008D64	OAM_PORT_ACT_CTRL [8].PAR_ACT[2:1]	2
0xBB008D64	OAM_PORT_ACT_CTRL [8].MUX_ACT[0:0]	1
0xBB008D68	OAM_PORT_ACT_CTRL [9].RESERVED[31:3]	29
0xBB008D68	OAM_PORT_ACT_CTRL [9].PAR_ACT[2:1]	2
0xBB008D68	OAM_PORT_ACT_CTRL [9].MUX_ACT[0:0]	1
0xBB008D6C	OAM_PORT_ACT_CTRL [10].RESERVED[31:3]	29
0xBB008D6C	OAM_PORT_ACT_CTRL [10].PAR_ACT[2:1]	2
0xBB008D6C	OAM_PORT_ACT_CTRL [10].MUX_ACT[0:0]	1
0xBB008D70	OAM_PORT_ACT_CTRL [11].RESERVED[31:3]	29
0xBB008D70	OAM_PORT_ACT_CTRL [11].PAR_ACT[2:1]	2
0xBB008D70	OAM_PORT_ACT_CTRL [11].MUX_ACT[0:0]	1
0xBB008D74	OAM_PORT_ACT_CTRL [12].RESERVED[31:3]	29
0xBB008D74	OAM_PORT_ACT_CTRL [12].PAR_ACT[2:1]	2
0xBB008D74	OAM_PORT_ACT_CTRL [12].MUX_ACT[0:0]	1
0xBB008D78	OAM_PORT_ACT_CTRL [13].RESERVED[31:3]	29
0xBB008D78	OAM_PORT_ACT_CTRL [13].PAR_ACT[2:1]	2
0xBB008D78	OAM_PORT_ACT_CTRL [13].MUX_ACT[0:0]	1
0xBB008D7C	OAM_PORT_ACT_CTRL [14].RESERVED[31:3]	29
0xBB008D7C	OAM_PORT_ACT_CTRL [14].PAR_ACT[2:1]	2
0xBB008D7C	OAM_PORT_ACT_CTRL [14].MUX_ACT[0:0]	1
0xBB008D80	OAM_PORT_ACT_CTRL [15].RESERVED[31:3]	29
0xBB008D80	OAM_PORT_ACT_CTRL [15].PAR_ACT[2:1]	2
0xBB008D80	OAM_PORT_ACT_CTRL [15].MUX_ACT[0:0]	1
0xBB008D84	OAM_PORT_ACT_CTRL [16].RESERVED[31:3]	29
0xBB008D84	OAM_PORT_ACT_CTRL [16].PAR_ACT[2:1]	2
0xBB008D84	OAM_PORT_ACT_CTRL [16].MUX_ACT[0:0]	1
0xBB008D88	OAM_PORT_ACT_CTRL [17].RESERVED[31:3]	29
0xBB008D88	OAM_PORT_ACT_CTRL [17].PAR_ACT[2:1]	2
0xBB008D88	OAM_PORT_ACT_CTRL [17].MUX_ACT[0:0]	1
0xBB008D8C	OAM_PORT_ACT_CTRL [18].RESERVED[31:3]	29
0xBB008D8C	OAM_PORT_ACT_CTRL [18].PAR_ACT[2:1]	2
0xBB008D8C	OAM_PORT_ACT_CTRL [18].MUX_ACT[0:0]	1
0xBB008D90	OAM_PORT_ACT_CTRL [19].RESERVED[31:3]	29
0xBB008D90	OAM_PORT_ACT_CTRL [19].PAR_ACT[2:1]	2
0xBB008D90	OAM_PORT_ACT_CTRL [19].MUX_ACT[0:0]	1
0xBB008D94	OAM_PORT_ACT_CTRL [20].RESERVED[31:3]	29

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008D94	OAM_PORT_ACT_CTRL [20].PAR_ACT[2:1]	2
0xBB008D94	OAM_PORT_ACT_CTRL [20].MUX_ACT[0:0]	1
0xBB008D98	OAM_PORT_ACT_CTRL [21].RESERVED[31:3]	29
0xBB008D98	OAM_PORT_ACT_CTRL [21].PAR_ACT[2:1]	2
0xBB008D98	OAM_PORT_ACT_CTRL [21].MUX_ACT[0:0]	1
0xBB008D9C	OAM_PORT_ACT_CTRL [22].RESERVED[31:3]	29
0xBB008D9C	OAM_PORT_ACT_CTRL [22].PAR_ACT[2:1]	2
0xBB008D9C	OAM_PORT_ACT_CTRL [22].MUX_ACT[0:0]	1
0xBB008DA0	OAM_PORT_ACT_CTRL [23].RESERVED[31:3]	29
0xBB008DA0	OAM_PORT_ACT_CTRL [23].PAR_ACT[2:1]	2
0xBB008DA0	OAM_PORT_ACT_CTRL [23].MUX_ACT[0:0]	1
0xBB008DA4	OAM_PORT_ACT_CTRL [24].RESERVED[31:3]	29
0xBB008DA4	OAM_PORT_ACT_CTRL [24].PAR_ACT[2:1]	2
0xBB008DA4	OAM_PORT_ACT_CTRL [24].MUX_ACT[0:0]	1
0xBB008DA8	OAM_PORT_ACT_CTRL [25].RESERVED[31:3]	29
0xBB008DA8	OAM_PORT_ACT_CTRL [25].PAR_ACT[2:1]	2
0xBB008DA8	OAM_PORT_ACT_CTRL [25].MUX_ACT[0:0]	1
0xBB008DAC	OAM_PORT_ACT_CTRL [26].RESERVED[31:3]	29
0xBB008DAC	OAM_PORT_ACT_CTRL [26].PAR_ACT[2:1]	2
0xBB008DAC	OAM_PORT_ACT_CTRL [26].MUX_ACT[0:0]	1
0xBB008DB0	OAM_PORT_ACT_CTRL [27].RESERVED[31:3]	29
0xBB008DB0	OAM_PORT_ACT_CTRL [27].PAR_ACT[2:1]	2
0xBB008DB0	OAM_PORT_ACT_CTRL [27].MUX_ACT[0:0]	1
0xBB008DB4	OAM_PORT_ACT_CTRL [28].RESERVED[31:3]	29
0xBB008DB4	OAM_PORT_ACT_CTRL [28].PAR_ACT[2:1]	2
0xBB008DB4	OAM_PORT_ACT_CTRL [28].MUX_ACT[0:0]	1
0xBB008DB8	OAM_PORT_ACT_CTRL [29].RESERVED[31:3]	29
0xBB008DB8	OAM_PORT_ACT_CTRL [29].PAR_ACT[2:1]	2
0xBB008DB8	OAM_PORT_ACT_CTRL [29].MUX_ACT[0:0]	1
0xBB008DBC	OAM_PORT_ACT_CTRL [30].RESERVED[31:3]	29
0xBB008DBC	OAM_PORT_ACT_CTRL [30].PAR_ACT[2:1]	2
0xBB008DBC	OAM_PORT_ACT_CTRL [30].MUX_ACT[0:0]	1
0xBB008DC0	OAM_PORT_ACT_CTRL [31].RESERVED[31:3]	29
0xBB008DC0	OAM_PORT_ACT_CTRL [31].PAR_ACT[2:1]	2
0xBB008DC0	OAM_PORT_ACT_CTRL [31].MUX_ACT[0:0]	1
0xBB008DC4	OAM_PORT_ACT_CTRL [32].RESERVED[31:3]	29
0xBB008DC4	OAM_PORT_ACT_CTRL [32].PAR_ACT[2:1]	2
0xBB008DC4	OAM_PORT_ACT_CTRL [32].MUX_ACT[0:0]	1
0xBB008DC8	OAM_PORT_ACT_CTRL [33].RESERVED[31:3]	29
0xBB008DC8	OAM_PORT_ACT_CTRL [33].PAR_ACT[2:1]	2
0xBB008DC8	OAM_PORT_ACT_CTRL [33].MUX_ACT[0:0]	1
0xBB008DCC	OAM_PORT_ACT_CTRL [34].RESERVED[31:3]	29
0xBB008DCC	OAM_PORT_ACT_CTRL [34].PAR_ACT[2:1]	2
0xBB008DCC	OAM_PORT_ACT_CTRL [34].MUX_ACT[0:0]	1
0xBB008DD0	OAM_PORT_ACT_CTRL [35].RESERVED[31:3]	29
0xBB008DD0	OAM_PORT_ACT_CTRL [35].PAR_ACT[2:1]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008DD0	OAM_PORT_ACT_CTRL [35].MUX_ACT[0:0]	1
0xBB008DD4	OAM_PORT_ACT_CTRL [36].RESERVED[31:3]	29
0xBB008DD4	OAM_PORT_ACT_CTRL [36].PAR_ACT[2:1]	2
0xBB008DD4	OAM_PORT_ACT_CTRL [36].MUX_ACT[0:0]	1
0xBB008DD8	OAM_PORT_ACT_CTRL [37].RESERVED[31:3]	29
0xBB008DD8	OAM_PORT_ACT_CTRL [37].PAR_ACT[2:1]	2
0xBB008DD8	OAM_PORT_ACT_CTRL [37].MUX_ACT[0:0]	1
0xBB008DDC	OAM_PORT_ACT_CTRL [38].RESERVED[31:3]	29
0xBB008DDC	OAM_PORT_ACT_CTRL [38].PAR_ACT[2:1]	2
0xBB008DDC	OAM_PORT_ACT_CTRL [38].MUX_ACT[0:0]	1
0xBB008DE0	OAM_PORT_ACT_CTRL [39].RESERVED[31:3]	29
0xBB008DE0	OAM_PORT_ACT_CTRL [39].PAR_ACT[2:1]	2
0xBB008DE0	OAM_PORT_ACT_CTRL [39].MUX_ACT[0:0]	1
0xBB008DE4	OAM_PORT_ACT_CTRL [40].RESERVED[31:3]	29
0xBB008DE4	OAM_PORT_ACT_CTRL [40].PAR_ACT[2:1]	2
0xBB008DE4	OAM_PORT_ACT_CTRL [40].MUX_ACT[0:0]	1
0xBB008DE8	OAM_PORT_ACT_CTRL [41].RESERVED[31:3]	29
0xBB008DE8	OAM_PORT_ACT_CTRL [41].PAR_ACT[2:1]	2
0xBB008DE8	OAM_PORT_ACT_CTRL [41].MUX_ACT[0:0]	1
0xBB008DEC	OAM_PORT_ACT_CTRL [42].RESERVED[31:3]	29
0xBB008DEC	OAM_PORT_ACT_CTRL [42].PAR_ACT[2:1]	2
0xBB008DEC	OAM_PORT_ACT_CTRL [42].MUX_ACT[0:0]	1
0xBB008DF0	OAM_PORT_ACT_CTRL [43].RESERVED[31:3]	29
0xBB008DF0	OAM_PORT_ACT_CTRL [43].PAR_ACT[2:1]	2
0xBB008DF0	OAM_PORT_ACT_CTRL [43].MUX_ACT[0:0]	1
0xBB008DF4	OAM_PORT_ACT_CTRL [44].RESERVED[31:3]	29
0xBB008DF4	OAM_PORT_ACT_CTRL [44].PAR_ACT[2:1]	2
0xBB008DF4	OAM_PORT_ACT_CTRL [44].MUX_ACT[0:0]	1
0xBB008DF8	OAM_PORT_ACT_CTRL [45].RESERVED[31:3]	29
0xBB008DF8	OAM_PORT_ACT_CTRL [45].PAR_ACT[2:1]	2
0xBB008DF8	OAM_PORT_ACT_CTRL [45].MUX_ACT[0:0]	1
0xBB008DFC	OAM_PORT_ACT_CTRL [46].RESERVED[31:3]	29
0xBB008DFC	OAM_PORT_ACT_CTRL [46].PAR_ACT[2:1]	2
0xBB008DFC	OAM_PORT_ACT_CTRL [46].MUX_ACT[0:0]	1
0xBB008E00	OAM_PORT_ACT_CTRL [47].RESERVED[31:3]	29
0xBB008E00	OAM_PORT_ACT_CTRL [47].PAR_ACT[2:1]	2
0xBB008E00	OAM_PORT_ACT_CTRL [47].MUX_ACT[0:0]	1
0xBB008E04	OAM_PORT_ACT_CTRL [48].RESERVED[31:3]	29
0xBB008E04	OAM_PORT_ACT_CTRL [48].PAR_ACT[2:1]	2
0xBB008E04	OAM_PORT_ACT_CTRL [48].MUX_ACT[0:0]	1
0xBB008E08	OAM_PORT_ACT_CTRL [49].RESERVED[31:3]	29
0xBB008E08	OAM_PORT_ACT_CTRL [49].PAR_ACT[2:1]	2
0xBB008E08	OAM_PORT_ACT_CTRL [49].MUX_ACT[0:0]	1
0xBB008E0C	OAM_PORT_ACT_CTRL [50].RESERVED[31:3]	29
0xBB008E0C	OAM_PORT_ACT_CTRL [50].PAR_ACT[2:1]	2
0xBB008E0C	OAM_PORT_ACT_CTRL [50].MUX_ACT[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008E10	OAM_PORT_ACT_CTRL [51].RESERVED[31:3]	29
0xBB008E10	OAM_PORT_ACT_CTRL [51].PAR_ACT[2:1]	2
0xBB008E10	OAM_PORT_ACT_CTRL [51].MUX_ACT[0:0]	1
0xBB008E14	OAM_PORT_ACT_CTRL [52].RESERVED[31:3]	29
0xBB008E14	OAM_PORT_ACT_CTRL [52].PAR_ACT[2:1]	2
0xBB008E14	OAM_PORT_ACT_CTRL [52].MUX_ACT[0:0]	1
0xBB008E18	OAM_PORT_ACT_CTRL [53].RESERVED[31:3]	29
0xBB008E18	OAM_PORT_ACT_CTRL [53].PAR_ACT[2:1]	2
0xBB008E18	OAM_PORT_ACT_CTRL [53].MUX_ACT[0:0]	1
0xBB008E1C	OAM_PORT_ACT_CTRL [54].RESERVED[31:3]	29
0xBB008E1C	OAM_PORT_ACT_CTRL [54].PAR_ACT[2:1]	2
0xBB008E1C	OAM_PORT_ACT_CTRL [54].MUX_ACT[0:0]	1
0xBB008E20	OAM_PORT_ACT_CTRL [55].RESERVED[31:3]	29
0xBB008E20	OAM_PORT_ACT_CTRL [55].PAR_ACT[2:1]	2
0xBB008E20	OAM_PORT_ACT_CTRL [55].MUX_ACT[0:0]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [0].PORT_OAM_DYING_GASP_EN[0:0]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [1].PORT_OAM_DYING_GASP_EN[1:1]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [2].PORT_OAM_DYING_GASP_EN[2:2]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [3].PORT_OAM_DYING_GASP_EN[3:3]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [4].PORT_OAM_DYING_GASP_EN[4:4]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [5].PORT_OAM_DYING_GASP_EN[5:5]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [6].PORT_OAM_DYING_GASP_EN[6:6]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [7].PORT_OAM_DYING_GASP_EN[7:7]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [8].PORT_OAM_DYING_GASP_EN[8:8]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [9].PORT_OAM_DYING_GASP_EN[9:9]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [10].PORT_OAM_DYING_GASP_EN[10:10]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [11].PORT_OAM_DYING_GASP_EN[11:11]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [12].PORT_OAM_DYING_GASP_EN[12:12]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [13].PORT_OAM_DYING_GASP_EN[13:13]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [14].PORT_OAM_DYING_GASP_EN[14:14]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [15].PORT_OAM_DYING_GASP_EN[15:15]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [16].PORT_OAM_DYING_GASP_EN[16:16]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [17].PORT_OAM_DYING_GASP_EN[17:17]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [18].PORT_OAM_DYING_GASP_EN[18:18]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [19].PORT_OAM_DYING_GASP_EN[19:19]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [20].PORT_OAM_DYING_GASP_EN[20:20]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [21].PORT_OAM_DYING_GASP_EN[21:21]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [22].PORT_OAM_DYING_GASP_EN[22:22]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [23].PORT_OAM_DYING_GASP_EN[23:23]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [24].PORT_OAM_DYING_GASP_EN[24:24]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [25].PORT_OAM_DYING_GASP_EN[25:25]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [26].PORT_OAM_DYING_GASP_EN[26:26]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [27].PORT_OAM_DYING_GASP_EN[27:27]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [28].PORT_OAM_DYING_GASP_EN[28:28]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [29].PORT_OAM_DYING_GASP_EN[29:29]	1
0xBB003058	OAM_PORT_DYING_GASP_CTRL [30].PORT_OAM_DYING_GASP_EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB003058	OAM_PORT_DYING_GASP_CTRL [31].PORT_OAM_DYING_GASP_EN[31:31]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [32].PORT_OAM_DYING_GASP_EN[0:0]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [33].PORT_OAM_DYING_GASP_EN[1:1]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [34].PORT_OAM_DYING_GASP_EN[2:2]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [35].PORT_OAM_DYING_GASP_EN[3:3]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [36].PORT_OAM_DYING_GASP_EN[4:4]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [37].PORT_OAM_DYING_GASP_EN[5:5]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [38].PORT_OAM_DYING_GASP_EN[6:6]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [39].PORT_OAM_DYING_GASP_EN[7:7]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [40].PORT_OAM_DYING_GASP_EN[8:8]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [41].PORT_OAM_DYING_GASP_EN[9:9]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [42].PORT_OAM_DYING_GASP_EN[10:10]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [43].PORT_OAM_DYING_GASP_EN[11:11]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [44].PORT_OAM_DYING_GASP_EN[12:12]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [45].PORT_OAM_DYING_GASP_EN[13:13]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [46].PORT_OAM_DYING_GASP_EN[14:14]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [47].PORT_OAM_DYING_GASP_EN[15:15]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [48].PORT_OAM_DYING_GASP_EN[16:16]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [49].PORT_OAM_DYING_GASP_EN[17:17]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [50].PORT_OAM_DYING_GASP_EN[18:18]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [51].PORT_OAM_DYING_GASP_EN[19:19]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [52].PORT_OAM_DYING_GASP_EN[20:20]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [53].PORT_OAM_DYING_GASP_EN[21:21]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [54].PORT_OAM_DYING_GASP_EN[22:22]	1
0xBB00305C	OAM_PORT_DYING_GASP_CTRL [55].PORT_OAM_DYING_GASP_EN[23:23]	1
0xBB00D408	OF_ACT_CTRL.RESERVED[31:7]	25
0xBB00D408	OF_ACT_CTRL.TRAP_TARGET[6:6]	1
0xBB00D408	OF_ACT_CTRL.LB_LIMIT[5:0]	6
0xBB004F68	OF_EGR_TBL_CNTR.LOOKUP[31:0]	32
0xBB004F6C	OF_EGR_TBL_CNTR.MATCH[31:0]	32
0xBB004F64	OF_EGR_TBL_MIS.RESERVED[31:1]	31
0xBB004F64	OF_EGR_TBL_MIS.ACT[0:0]	1
0xBB004F70	OF_EXCPT_CTRL.RESERVED[31:1]	31
0xBB004F70	OF_EXCPT_CTRL.TTL_ACT[0:0]	1
0xBB00D414	OF_GRP_HASH_CTRL.RESERVED[31:7]	25
0xBB00D414	OF_GRP_HASH_CTRL.L4_DPORT_INC[6:6]	1
0xBB00D414	OF_GRP_HASH_CTRL.L4_SPORT_INC[5:5]	1
0xBB00D414	OF_GRP_HASH_CTRL.DIP_INC[4:4]	1
0xBB00D414	OF_GRP_HASH_CTRL.SIP_INC[3:3]	1
0xBB00D414	OF_GRP_HASH_CTRL.DMAC_INC[2:2]	1
0xBB00D414	OF_GRP_HASH_CTRL.SMAC_INC[1:1]	1
0xBB00D414	OF_GRP_HASH_CTRL.IGR_PORT_INC[0:0]	1
0xBB00D41C	OF_IGR_TBL_CNTR [0].LOOKUP[31:0]	32
0xBB00D420	OF_IGR_TBL_CNTR [0].MATCH[31:0]	32
0xBB00D424	OF_IGR_TBL_CNTR [1].LOOKUP[31:0]	32
0xBB00D428	OF_IGR_TBL_CNTR [1].MATCH[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D42C	OF_IGR_TBL_CNTR [2].LOOKUP[31:0]	32
0xBB00D430	OF_IGR_TBL_CNTR [2].MATCH[31:0]	32
0xBB00D434	OF_IGR_TBL_CNTR [3].LOOKUP[31:0]	32
0xBB00D438	OF_IGR_TBL_CNTR [3].MATCH[31:0]	32
0xBB00D418	OF_IGR_TBL_MIS [0].ACT[1:0]	2
0xBB00D418	OF_IGR_TBL_MIS [1].ACT[3:2]	2
0xBB00D418	OF_IGR_TBL_MIS [2].ACT[5:4]	2
0xBB00D418	OF_IGR_TBL_MIS [3].ACT[7:6]	2
0xBB00D40C	OF_L2_FLOW_TBL_CTRL.RESERVED[31:6]	26
0xBB00D40C	OF_L2_FLOW_TBL_CTRL.BLK1_ALGO[5:5]	1
0xBB00D40C	OF_L2_FLOW_TBL_CTRL.BLK0_ALGO[4:4]	1
0xBB00D40C	OF_L2_FLOW_TBL_CTRL.LU_SEL[3:0]	4
0xBB00D410	OF_L3_FLOW_TBL_CTRL.RESERVED[31:7]	25
0xBB00D410	OF_L3_FLOW_TBL_CTRL.BLK1_ALGO[6:6]	1
0xBB00D410	OF_L3_FLOW_TBL_CTRL.BLK0_ALGO[5:5]	1
0xBB00D410	OF_L3_FLOW_TBL_CTRL.LU_PRI_SEL[4:4]	1
0xBB00D410	OF_L3_FLOW_TBL_CTRL.HASH_LU_SEL[3:2]	2
0xBB00D410	OF_L3_FLOW_TBL_CTRL.CAM_LU_SEL[1:0]	2
0xBB00D000	OF_PORT_CTRL [0].EN[0:0]	1
0xBB00D000	OF_PORT_CTRL [1].EN[1:1]	1
0xBB00D000	OF_PORT_CTRL [2].EN[2:2]	1
0xBB00D000	OF_PORT_CTRL [3].EN[3:3]	1
0xBB00D000	OF_PORT_CTRL [4].EN[4:4]	1
0xBB00D000	OF_PORT_CTRL [5].EN[5:5]	1
0xBB00D000	OF_PORT_CTRL [6].EN[6:6]	1
0xBB00D000	OF_PORT_CTRL [7].EN[7:7]	1
0xBB00D000	OF_PORT_CTRL [8].EN[8:8]	1
0xBB00D000	OF_PORT_CTRL [9].EN[9:9]	1
0xBB00D000	OF_PORT_CTRL [10].EN[10:10]	1
0xBB00D000	OF_PORT_CTRL [11].EN[11:11]	1
0xBB00D000	OF_PORT_CTRL [12].EN[12:12]	1
0xBB00D000	OF_PORT_CTRL [13].EN[13:13]	1
0xBB00D000	OF_PORT_CTRL [14].EN[14:14]	1
0xBB00D000	OF_PORT_CTRL [15].EN[15:15]	1
0xBB00D000	OF_PORT_CTRL [16].EN[16:16]	1
0xBB00D000	OF_PORT_CTRL [17].EN[17:17]	1
0xBB00D000	OF_PORT_CTRL [18].EN[18:18]	1
0xBB00D000	OF_PORT_CTRL [19].EN[19:19]	1
0xBB00D000	OF_PORT_CTRL [20].EN[20:20]	1
0xBB00D000	OF_PORT_CTRL [21].EN[21:21]	1
0xBB00D000	OF_PORT_CTRL [22].EN[22:22]	1
0xBB00D000	OF_PORT_CTRL [23].EN[23:23]	1
0xBB00D000	OF_PORT_CTRL [24].EN[24:24]	1
0xBB00D000	OF_PORT_CTRL [25].EN[25:25]	1
0xBB00D000	OF_PORT_CTRL [26].EN[26:26]	1
0xBB00D000	OF_PORT_CTRL [27].EN[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D000	OF_PORT_CTRL [28].EN[28:28]	1
0xBB00D000	OF_PORT_CTRL [29].EN[29:29]	1
0xBB00D000	OF_PORT_CTRL [30].EN[30:30]	1
0xBB00D000	OF_PORT_CTRL [31].EN[31:31]	1
0xBB00D004	OF_PORT_CTRL [32].EN[0:0]	1
0xBB00D004	OF_PORT_CTRL [33].EN[1:1]	1
0xBB00D004	OF_PORT_CTRL [34].EN[2:2]	1
0xBB00D004	OF_PORT_CTRL [35].EN[3:3]	1
0xBB00D004	OF_PORT_CTRL [36].EN[4:4]	1
0xBB00D004	OF_PORT_CTRL [37].EN[5:5]	1
0xBB00D004	OF_PORT_CTRL [38].EN[6:6]	1
0xBB00D004	OF_PORT_CTRL [39].EN[7:7]	1
0xBB00D004	OF_PORT_CTRL [40].EN[8:8]	1
0xBB00D004	OF_PORT_CTRL [41].EN[9:9]	1
0xBB00D004	OF_PORT_CTRL [42].EN[10:10]	1
0xBB00D004	OF_PORT_CTRL [43].EN[11:11]	1
0xBB00D004	OF_PORT_CTRL [44].EN[12:12]	1
0xBB00D004	OF_PORT_CTRL [45].EN[13:13]	1
0xBB00D004	OF_PORT_CTRL [46].EN[14:14]	1
0xBB00D004	OF_PORT_CTRL [47].EN[15:15]	1
0xBB00D004	OF_PORT_CTRL [48].EN[16:16]	1
0xBB00D004	OF_PORT_CTRL [49].EN[17:17]	1
0xBB00D004	OF_PORT_CTRL [50].EN[18:18]	1
0xBB00D004	OF_PORT_CTRL [51].EN[19:19]	1
0xBB00D004	OF_PORT_CTRL [52].EN[20:20]	1
0xBB00D004	OF_PORT_CTRL [53].EN[21:21]	1
0xBB00D004	OF_PORT_CTRL [54].EN[22:22]	1
0xBB00D004	OF_PORT_CTRL [55].EN[23:23]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [0].EN[0:0]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [1].EN[1:1]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [2].EN[2:2]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [3].EN[3:3]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [4].EN[4:4]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [5].EN[5:5]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [6].EN[6:6]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [7].EN[7:7]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [8].EN[8:8]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [9].EN[9:9]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [10].EN[10:10]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [11].EN[11:11]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [12].EN[12:12]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [13].EN[13:13]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [14].EN[14:14]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [15].EN[15:15]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [16].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D208	OF_VLAN_AND_PORT_EN [17].EN[17:17]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [18].EN[18:18]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [19].EN[19:19]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [20].EN[20:20]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [21].EN[21:21]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [22].EN[22:22]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [23].EN[23:23]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [24].EN[24:24]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [25].EN[25:25]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [26].EN[26:26]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [27].EN[27:27]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [28].EN[28:28]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [29].EN[29:29]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [30].EN[30:30]	1
0xBB00D208	OF_VLAN_AND_PORT_EN [31].EN[31:31]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [32].EN[0:0]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [33].EN[1:1]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [34].EN[2:2]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [35].EN[3:3]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [36].EN[4:4]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [37].EN[5:5]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [38].EN[6:6]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [39].EN[7:7]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [40].EN[8:8]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [41].EN[9:9]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [42].EN[10:10]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [43].EN[11:11]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [44].EN[12:12]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [45].EN[13:13]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [46].EN[14:14]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [47].EN[15:15]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [48].EN[16:16]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [49].EN[17:17]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [50].EN[18:18]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [51].EN[19:19]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [52].EN[20:20]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [53].EN[21:21]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [54].EN[22:22]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [55].EN[23:23]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [56].EN[24:24]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [57].EN[25:25]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [58].EN[26:26]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [59].EN[27:27]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [60].EN[28:28]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [61].EN[29:29]	1
0xBB00D20C	OF_VLAN_AND_PORT_EN [62].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D20C	OF_VLAN_AND_PORT_EN [63].EN[31:31]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [64].EN[0:0]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [65].EN[1:1]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [66].EN[2:2]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [67].EN[3:3]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [68].EN[4:4]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [69].EN[5:5]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [70].EN[6:6]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [71].EN[7:7]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [72].EN[8:8]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [73].EN[9:9]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [74].EN[10:10]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [75].EN[11:11]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [76].EN[12:12]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [77].EN[13:13]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [78].EN[14:14]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [79].EN[15:15]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [80].EN[16:16]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [81].EN[17:17]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [82].EN[18:18]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [83].EN[19:19]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [84].EN[20:20]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [85].EN[21:21]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [86].EN[22:22]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [87].EN[23:23]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [88].EN[24:24]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [89].EN[25:25]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [90].EN[26:26]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [91].EN[27:27]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [92].EN[28:28]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [93].EN[29:29]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [94].EN[30:30]	1
0xBB00D210	OF_VLAN_AND_PORT_EN [95].EN[31:31]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [96].EN[0:0]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [97].EN[1:1]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [98].EN[2:2]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [99].EN[3:3]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [100].EN[4:4]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [101].EN[5:5]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [102].EN[6:6]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [103].EN[7:7]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [104].EN[8:8]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [105].EN[9:9]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [106].EN[10:10]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [107].EN[11:11]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [108].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D214	OF_VLAN_AND_PORT_EN [109].EN[13:13]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [110].EN[14:14]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [111].EN[15:15]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [112].EN[16:16]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [113].EN[17:17]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [114].EN[18:18]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [115].EN[19:19]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [116].EN[20:20]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [117].EN[21:21]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [118].EN[22:22]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [119].EN[23:23]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [120].EN[24:24]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [121].EN[25:25]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [122].EN[26:26]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [123].EN[27:27]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [124].EN[28:28]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [125].EN[29:29]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [126].EN[30:30]	1
0xBB00D214	OF_VLAN_AND_PORT_EN [127].EN[31:31]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [128].EN[0:0]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [129].EN[1:1]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [130].EN[2:2]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [131].EN[3:3]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [132].EN[4:4]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [133].EN[5:5]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [134].EN[6:6]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [135].EN[7:7]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [136].EN[8:8]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [137].EN[9:9]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [138].EN[10:10]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [139].EN[11:11]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [140].EN[12:12]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [141].EN[13:13]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [142].EN[14:14]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [143].EN[15:15]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [144].EN[16:16]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [145].EN[17:17]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [146].EN[18:18]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [147].EN[19:19]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [148].EN[20:20]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [149].EN[21:21]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [150].EN[22:22]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [151].EN[23:23]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [152].EN[24:24]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [153].EN[25:25]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [154].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D218	OF_VLAN_AND_PORT_EN [155].EN[27:27]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [156].EN[28:28]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [157].EN[29:29]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [158].EN[30:30]	1
0xBB00D218	OF_VLAN_AND_PORT_EN [159].EN[31:31]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [160].EN[0:0]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [161].EN[1:1]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [162].EN[2:2]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [163].EN[3:3]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [164].EN[4:4]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [165].EN[5:5]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [166].EN[6:6]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [167].EN[7:7]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [168].EN[8:8]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [169].EN[9:9]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [170].EN[10:10]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [171].EN[11:11]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [172].EN[12:12]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [173].EN[13:13]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [174].EN[14:14]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [175].EN[15:15]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [176].EN[16:16]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [177].EN[17:17]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [178].EN[18:18]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [179].EN[19:19]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [180].EN[20:20]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [181].EN[21:21]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [182].EN[22:22]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [183].EN[23:23]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [184].EN[24:24]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [185].EN[25:25]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [186].EN[26:26]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [187].EN[27:27]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [188].EN[28:28]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [189].EN[29:29]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [190].EN[30:30]	1
0xBB00D21C	OF_VLAN_AND_PORT_EN [191].EN[31:31]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [192].EN[0:0]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [193].EN[1:1]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [194].EN[2:2]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [195].EN[3:3]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [196].EN[4:4]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [197].EN[5:5]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [198].EN[6:6]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [199].EN[7:7]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [200].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D220	OF_VLAN_AND_PORT_EN [201].EN[9:9]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [202].EN[10:10]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [203].EN[11:11]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [204].EN[12:12]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [205].EN[13:13]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [206].EN[14:14]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [207].EN[15:15]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [208].EN[16:16]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [209].EN[17:17]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [210].EN[18:18]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [211].EN[19:19]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [212].EN[20:20]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [213].EN[21:21]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [214].EN[22:22]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [215].EN[23:23]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [216].EN[24:24]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [217].EN[25:25]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [218].EN[26:26]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [219].EN[27:27]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [220].EN[28:28]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [221].EN[29:29]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [222].EN[30:30]	1
0xBB00D220	OF_VLAN_AND_PORT_EN [223].EN[31:31]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [224].EN[0:0]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [225].EN[1:1]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [226].EN[2:2]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [227].EN[3:3]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [228].EN[4:4]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [229].EN[5:5]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [230].EN[6:6]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [231].EN[7:7]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [232].EN[8:8]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [233].EN[9:9]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [234].EN[10:10]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [235].EN[11:11]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [236].EN[12:12]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [237].EN[13:13]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [238].EN[14:14]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [239].EN[15:15]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [240].EN[16:16]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [241].EN[17:17]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [242].EN[18:18]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [243].EN[19:19]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [244].EN[20:20]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [245].EN[21:21]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [246].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D224	OF_VLAN_AND_PORT_EN [247].EN[23:23]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [248].EN[24:24]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [249].EN[25:25]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [250].EN[26:26]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [251].EN[27:27]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [252].EN[28:28]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [253].EN[29:29]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [254].EN[30:30]	1
0xBB00D224	OF_VLAN_AND_PORT_EN [255].EN[31:31]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [256].EN[0:0]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [257].EN[1:1]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [258].EN[2:2]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [259].EN[3:3]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [260].EN[4:4]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [261].EN[5:5]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [262].EN[6:6]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [263].EN[7:7]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [264].EN[8:8]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [265].EN[9:9]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [266].EN[10:10]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [267].EN[11:11]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [268].EN[12:12]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [269].EN[13:13]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [270].EN[14:14]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [271].EN[15:15]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [272].EN[16:16]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [273].EN[17:17]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [274].EN[18:18]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [275].EN[19:19]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [276].EN[20:20]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [277].EN[21:21]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [278].EN[22:22]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [279].EN[23:23]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [280].EN[24:24]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [281].EN[25:25]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [282].EN[26:26]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [283].EN[27:27]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [284].EN[28:28]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [285].EN[29:29]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [286].EN[30:30]	1
0xBB00D228	OF_VLAN_AND_PORT_EN [287].EN[31:31]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [288].EN[0:0]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [289].EN[1:1]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [290].EN[2:2]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [291].EN[3:3]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [292].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D22C	OF_VLAN_AND_PORT_EN [293].EN[5:5]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [294].EN[6:6]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [295].EN[7:7]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [296].EN[8:8]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [297].EN[9:9]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [298].EN[10:10]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [299].EN[11:11]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [300].EN[12:12]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [301].EN[13:13]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [302].EN[14:14]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [303].EN[15:15]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [304].EN[16:16]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [305].EN[17:17]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [306].EN[18:18]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [307].EN[19:19]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [308].EN[20:20]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [309].EN[21:21]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [310].EN[22:22]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [311].EN[23:23]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [312].EN[24:24]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [313].EN[25:25]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [314].EN[26:26]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [315].EN[27:27]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [316].EN[28:28]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [317].EN[29:29]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [318].EN[30:30]	1
0xBB00D22C	OF_VLAN_AND_PORT_EN [319].EN[31:31]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [320].EN[0:0]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [321].EN[1:1]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [322].EN[2:2]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [323].EN[3:3]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [324].EN[4:4]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [325].EN[5:5]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [326].EN[6:6]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [327].EN[7:7]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [328].EN[8:8]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [329].EN[9:9]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [330].EN[10:10]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [331].EN[11:11]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [332].EN[12:12]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [333].EN[13:13]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [334].EN[14:14]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [335].EN[15:15]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [336].EN[16:16]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [337].EN[17:17]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [338].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D230	OF_VLAN_AND_PORT_EN [339].EN[19:19]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [340].EN[20:20]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [341].EN[21:21]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [342].EN[22:22]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [343].EN[23:23]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [344].EN[24:24]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [345].EN[25:25]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [346].EN[26:26]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [347].EN[27:27]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [348].EN[28:28]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [349].EN[29:29]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [350].EN[30:30]	1
0xBB00D230	OF_VLAN_AND_PORT_EN [351].EN[31:31]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [352].EN[0:0]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [353].EN[1:1]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [354].EN[2:2]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [355].EN[3:3]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [356].EN[4:4]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [357].EN[5:5]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [358].EN[6:6]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [359].EN[7:7]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [360].EN[8:8]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [361].EN[9:9]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [362].EN[10:10]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [363].EN[11:11]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [364].EN[12:12]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [365].EN[13:13]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [366].EN[14:14]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [367].EN[15:15]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [368].EN[16:16]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [369].EN[17:17]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [370].EN[18:18]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [371].EN[19:19]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [372].EN[20:20]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [373].EN[21:21]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [374].EN[22:22]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [375].EN[23:23]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [376].EN[24:24]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [377].EN[25:25]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [378].EN[26:26]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [379].EN[27:27]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [380].EN[28:28]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [381].EN[29:29]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [382].EN[30:30]	1
0xBB00D234	OF_VLAN_AND_PORT_EN [383].EN[31:31]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [384].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D238	OF_VLAN_AND_PORT_EN [385].EN[1:1]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [386].EN[2:2]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [387].EN[3:3]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [388].EN[4:4]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [389].EN[5:5]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [390].EN[6:6]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [391].EN[7:7]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [392].EN[8:8]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [393].EN[9:9]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [394].EN[10:10]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [395].EN[11:11]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [396].EN[12:12]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [397].EN[13:13]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [398].EN[14:14]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [399].EN[15:15]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [400].EN[16:16]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [401].EN[17:17]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [402].EN[18:18]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [403].EN[19:19]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [404].EN[20:20]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [405].EN[21:21]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [406].EN[22:22]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [407].EN[23:23]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [408].EN[24:24]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [409].EN[25:25]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [410].EN[26:26]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [411].EN[27:27]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [412].EN[28:28]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [413].EN[29:29]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [414].EN[30:30]	1
0xBB00D238	OF_VLAN_AND_PORT_EN [415].EN[31:31]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [416].EN[0:0]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [417].EN[1:1]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [418].EN[2:2]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [419].EN[3:3]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [420].EN[4:4]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [421].EN[5:5]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [422].EN[6:6]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [423].EN[7:7]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [424].EN[8:8]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [425].EN[9:9]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [426].EN[10:10]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [427].EN[11:11]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [428].EN[12:12]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [429].EN[13:13]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [430].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D23C	OF_VLAN_AND_PORT_EN [431].EN[15:15]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [432].EN[16:16]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [433].EN[17:17]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [434].EN[18:18]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [435].EN[19:19]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [436].EN[20:20]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [437].EN[21:21]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [438].EN[22:22]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [439].EN[23:23]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [440].EN[24:24]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [441].EN[25:25]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [442].EN[26:26]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [443].EN[27:27]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [444].EN[28:28]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [445].EN[29:29]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [446].EN[30:30]	1
0xBB00D23C	OF_VLAN_AND_PORT_EN [447].EN[31:31]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [448].EN[0:0]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [449].EN[1:1]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [450].EN[2:2]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [451].EN[3:3]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [452].EN[4:4]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [453].EN[5:5]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [454].EN[6:6]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [455].EN[7:7]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [456].EN[8:8]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [457].EN[9:9]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [458].EN[10:10]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [459].EN[11:11]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [460].EN[12:12]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [461].EN[13:13]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [462].EN[14:14]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [463].EN[15:15]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [464].EN[16:16]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [465].EN[17:17]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [466].EN[18:18]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [467].EN[19:19]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [468].EN[20:20]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [469].EN[21:21]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [470].EN[22:22]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [471].EN[23:23]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [472].EN[24:24]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [473].EN[25:25]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [474].EN[26:26]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [475].EN[27:27]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [476].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D240	OF_VLAN_AND_PORT_EN [477].EN[29:29]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [478].EN[30:30]	1
0xBB00D240	OF_VLAN_AND_PORT_EN [479].EN[31:31]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [480].EN[0:0]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [481].EN[1:1]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [482].EN[2:2]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [483].EN[3:3]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [484].EN[4:4]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [485].EN[5:5]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [486].EN[6:6]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [487].EN[7:7]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [488].EN[8:8]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [489].EN[9:9]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [490].EN[10:10]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [491].EN[11:11]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [492].EN[12:12]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [493].EN[13:13]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [494].EN[14:14]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [495].EN[15:15]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [496].EN[16:16]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [497].EN[17:17]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [498].EN[18:18]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [499].EN[19:19]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [500].EN[20:20]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [501].EN[21:21]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [502].EN[22:22]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [503].EN[23:23]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [504].EN[24:24]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [505].EN[25:25]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [506].EN[26:26]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [507].EN[27:27]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [508].EN[28:28]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [509].EN[29:29]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [510].EN[30:30]	1
0xBB00D244	OF_VLAN_AND_PORT_EN [511].EN[31:31]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [512].EN[0:0]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [513].EN[1:1]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [514].EN[2:2]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [515].EN[3:3]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [516].EN[4:4]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [517].EN[5:5]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [518].EN[6:6]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [519].EN[7:7]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [520].EN[8:8]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [521].EN[9:9]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [522].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D248	OF_VLAN_AND_PORT_EN [523].EN[11:11]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [524].EN[12:12]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [525].EN[13:13]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [526].EN[14:14]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [527].EN[15:15]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [528].EN[16:16]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [529].EN[17:17]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [530].EN[18:18]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [531].EN[19:19]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [532].EN[20:20]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [533].EN[21:21]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [534].EN[22:22]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [535].EN[23:23]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [536].EN[24:24]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [537].EN[25:25]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [538].EN[26:26]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [539].EN[27:27]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [540].EN[28:28]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [541].EN[29:29]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [542].EN[30:30]	1
0xBB00D248	OF_VLAN_AND_PORT_EN [543].EN[31:31]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [544].EN[0:0]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [545].EN[1:1]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [546].EN[2:2]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [547].EN[3:3]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [548].EN[4:4]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [549].EN[5:5]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [550].EN[6:6]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [551].EN[7:7]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [552].EN[8:8]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [553].EN[9:9]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [554].EN[10:10]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [555].EN[11:11]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [556].EN[12:12]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [557].EN[13:13]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [558].EN[14:14]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [559].EN[15:15]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [560].EN[16:16]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [561].EN[17:17]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [562].EN[18:18]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [563].EN[19:19]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [564].EN[20:20]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [565].EN[21:21]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [566].EN[22:22]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [567].EN[23:23]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [568].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D24C	OF_VLAN_AND_PORT_EN [569].EN[25:25]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [570].EN[26:26]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [571].EN[27:27]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [572].EN[28:28]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [573].EN[29:29]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [574].EN[30:30]	1
0xBB00D24C	OF_VLAN_AND_PORT_EN [575].EN[31:31]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [576].EN[0:0]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [577].EN[1:1]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [578].EN[2:2]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [579].EN[3:3]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [580].EN[4:4]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [581].EN[5:5]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [582].EN[6:6]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [583].EN[7:7]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [584].EN[8:8]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [585].EN[9:9]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [586].EN[10:10]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [587].EN[11:11]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [588].EN[12:12]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [589].EN[13:13]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [590].EN[14:14]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [591].EN[15:15]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [592].EN[16:16]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [593].EN[17:17]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [594].EN[18:18]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [595].EN[19:19]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [596].EN[20:20]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [597].EN[21:21]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [598].EN[22:22]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [599].EN[23:23]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [600].EN[24:24]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [601].EN[25:25]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [602].EN[26:26]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [603].EN[27:27]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [604].EN[28:28]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [605].EN[29:29]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [606].EN[30:30]	1
0xBB00D250	OF_VLAN_AND_PORT_EN [607].EN[31:31]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [608].EN[0:0]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [609].EN[1:1]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [610].EN[2:2]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [611].EN[3:3]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [612].EN[4:4]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [613].EN[5:5]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [614].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D254	OF_VLAN_AND_PORT_EN [615].EN[7:7]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [616].EN[8:8]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [617].EN[9:9]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [618].EN[10:10]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [619].EN[11:11]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [620].EN[12:12]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [621].EN[13:13]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [622].EN[14:14]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [623].EN[15:15]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [624].EN[16:16]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [625].EN[17:17]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [626].EN[18:18]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [627].EN[19:19]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [628].EN[20:20]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [629].EN[21:21]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [630].EN[22:22]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [631].EN[23:23]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [632].EN[24:24]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [633].EN[25:25]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [634].EN[26:26]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [635].EN[27:27]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [636].EN[28:28]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [637].EN[29:29]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [638].EN[30:30]	1
0xBB00D254	OF_VLAN_AND_PORT_EN [639].EN[31:31]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [640].EN[0:0]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [641].EN[1:1]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [642].EN[2:2]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [643].EN[3:3]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [644].EN[4:4]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [645].EN[5:5]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [646].EN[6:6]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [647].EN[7:7]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [648].EN[8:8]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [649].EN[9:9]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [650].EN[10:10]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [651].EN[11:11]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [652].EN[12:12]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [653].EN[13:13]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [654].EN[14:14]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [655].EN[15:15]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [656].EN[16:16]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [657].EN[17:17]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [658].EN[18:18]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [659].EN[19:19]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [660].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D258	OF_VLAN_AND_PORT_EN [661].EN[21:21]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [662].EN[22:22]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [663].EN[23:23]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [664].EN[24:24]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [665].EN[25:25]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [666].EN[26:26]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [667].EN[27:27]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [668].EN[28:28]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [669].EN[29:29]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [670].EN[30:30]	1
0xBB00D258	OF_VLAN_AND_PORT_EN [671].EN[31:31]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [672].EN[0:0]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [673].EN[1:1]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [674].EN[2:2]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [675].EN[3:3]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [676].EN[4:4]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [677].EN[5:5]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [678].EN[6:6]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [679].EN[7:7]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [680].EN[8:8]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [681].EN[9:9]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [682].EN[10:10]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [683].EN[11:11]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [684].EN[12:12]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [685].EN[13:13]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [686].EN[14:14]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [687].EN[15:15]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [688].EN[16:16]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [689].EN[17:17]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [690].EN[18:18]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [691].EN[19:19]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [692].EN[20:20]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [693].EN[21:21]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [694].EN[22:22]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [695].EN[23:23]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [696].EN[24:24]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [697].EN[25:25]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [698].EN[26:26]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [699].EN[27:27]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [700].EN[28:28]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [701].EN[29:29]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [702].EN[30:30]	1
0xBB00D25C	OF_VLAN_AND_PORT_EN [703].EN[31:31]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [704].EN[0:0]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [705].EN[1:1]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [706].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D260	OF_VLAN_AND_PORT_EN [707].EN[3:3]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [708].EN[4:4]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [709].EN[5:5]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [710].EN[6:6]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [711].EN[7:7]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [712].EN[8:8]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [713].EN[9:9]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [714].EN[10:10]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [715].EN[11:11]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [716].EN[12:12]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [717].EN[13:13]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [718].EN[14:14]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [719].EN[15:15]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [720].EN[16:16]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [721].EN[17:17]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [722].EN[18:18]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [723].EN[19:19]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [724].EN[20:20]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [725].EN[21:21]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [726].EN[22:22]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [727].EN[23:23]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [728].EN[24:24]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [729].EN[25:25]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [730].EN[26:26]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [731].EN[27:27]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [732].EN[28:28]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [733].EN[29:29]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [734].EN[30:30]	1
0xBB00D260	OF_VLAN_AND_PORT_EN [735].EN[31:31]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [736].EN[0:0]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [737].EN[1:1]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [738].EN[2:2]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [739].EN[3:3]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [740].EN[4:4]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [741].EN[5:5]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [742].EN[6:6]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [743].EN[7:7]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [744].EN[8:8]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [745].EN[9:9]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [746].EN[10:10]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [747].EN[11:11]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [748].EN[12:12]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [749].EN[13:13]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [750].EN[14:14]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [751].EN[15:15]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [752].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D264	OF_VLAN_AND_PORT_EN [753].EN[17:17]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [754].EN[18:18]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [755].EN[19:19]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [756].EN[20:20]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [757].EN[21:21]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [758].EN[22:22]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [759].EN[23:23]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [760].EN[24:24]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [761].EN[25:25]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [762].EN[26:26]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [763].EN[27:27]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [764].EN[28:28]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [765].EN[29:29]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [766].EN[30:30]	1
0xBB00D264	OF_VLAN_AND_PORT_EN [767].EN[31:31]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [768].EN[0:0]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [769].EN[1:1]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [770].EN[2:2]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [771].EN[3:3]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [772].EN[4:4]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [773].EN[5:5]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [774].EN[6:6]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [775].EN[7:7]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [776].EN[8:8]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [777].EN[9:9]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [778].EN[10:10]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [779].EN[11:11]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [780].EN[12:12]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [781].EN[13:13]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [782].EN[14:14]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [783].EN[15:15]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [784].EN[16:16]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [785].EN[17:17]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [786].EN[18:18]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [787].EN[19:19]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [788].EN[20:20]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [789].EN[21:21]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [790].EN[22:22]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [791].EN[23:23]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [792].EN[24:24]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [793].EN[25:25]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [794].EN[26:26]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [795].EN[27:27]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [796].EN[28:28]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [797].EN[29:29]	1
0xBB00D268	OF_VLAN_AND_PORT_EN [798].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D268	OF_VLAN_AND_PORT_EN [799].EN[31:31]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [800].EN[0:0]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [801].EN[1:1]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [802].EN[2:2]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [803].EN[3:3]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [804].EN[4:4]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [805].EN[5:5]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [806].EN[6:6]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [807].EN[7:7]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [808].EN[8:8]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [809].EN[9:9]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [810].EN[10:10]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [811].EN[11:11]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [812].EN[12:12]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [813].EN[13:13]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [814].EN[14:14]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [815].EN[15:15]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [816].EN[16:16]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [817].EN[17:17]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [818].EN[18:18]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [819].EN[19:19]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [820].EN[20:20]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [821].EN[21:21]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [822].EN[22:22]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [823].EN[23:23]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [824].EN[24:24]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [825].EN[25:25]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [826].EN[26:26]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [827].EN[27:27]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [828].EN[28:28]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [829].EN[29:29]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [830].EN[30:30]	1
0xBB00D26C	OF_VLAN_AND_PORT_EN [831].EN[31:31]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [832].EN[0:0]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [833].EN[1:1]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [834].EN[2:2]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [835].EN[3:3]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [836].EN[4:4]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [837].EN[5:5]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [838].EN[6:6]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [839].EN[7:7]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [840].EN[8:8]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [841].EN[9:9]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [842].EN[10:10]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [843].EN[11:11]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [844].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D270	OF_VLAN_AND_PORT_EN [845].EN[13:13]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [846].EN[14:14]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [847].EN[15:15]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [848].EN[16:16]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [849].EN[17:17]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [850].EN[18:18]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [851].EN[19:19]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [852].EN[20:20]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [853].EN[21:21]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [854].EN[22:22]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [855].EN[23:23]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [856].EN[24:24]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [857].EN[25:25]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [858].EN[26:26]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [859].EN[27:27]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [860].EN[28:28]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [861].EN[29:29]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [862].EN[30:30]	1
0xBB00D270	OF_VLAN_AND_PORT_EN [863].EN[31:31]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [864].EN[0:0]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [865].EN[1:1]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [866].EN[2:2]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [867].EN[3:3]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [868].EN[4:4]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [869].EN[5:5]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [870].EN[6:6]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [871].EN[7:7]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [872].EN[8:8]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [873].EN[9:9]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [874].EN[10:10]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [875].EN[11:11]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [876].EN[12:12]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [877].EN[13:13]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [878].EN[14:14]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [879].EN[15:15]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [880].EN[16:16]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [881].EN[17:17]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [882].EN[18:18]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [883].EN[19:19]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [884].EN[20:20]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [885].EN[21:21]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [886].EN[22:22]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [887].EN[23:23]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [888].EN[24:24]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [889].EN[25:25]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [890].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D274	OF_VLAN_AND_PORT_EN [891].EN[27:27]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [892].EN[28:28]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [893].EN[29:29]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [894].EN[30:30]	1
0xBB00D274	OF_VLAN_AND_PORT_EN [895].EN[31:31]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [896].EN[0:0]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [897].EN[1:1]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [898].EN[2:2]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [899].EN[3:3]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [900].EN[4:4]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [901].EN[5:5]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [902].EN[6:6]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [903].EN[7:7]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [904].EN[8:8]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [905].EN[9:9]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [906].EN[10:10]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [907].EN[11:11]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [908].EN[12:12]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [909].EN[13:13]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [910].EN[14:14]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [911].EN[15:15]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [912].EN[16:16]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [913].EN[17:17]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [914].EN[18:18]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [915].EN[19:19]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [916].EN[20:20]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [917].EN[21:21]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [918].EN[22:22]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [919].EN[23:23]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [920].EN[24:24]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [921].EN[25:25]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [922].EN[26:26]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [923].EN[27:27]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [924].EN[28:28]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [925].EN[29:29]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [926].EN[30:30]	1
0xBB00D278	OF_VLAN_AND_PORT_EN [927].EN[31:31]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [928].EN[0:0]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [929].EN[1:1]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [930].EN[2:2]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [931].EN[3:3]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [932].EN[4:4]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [933].EN[5:5]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [934].EN[6:6]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [935].EN[7:7]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [936].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D27C	OF_VLAN_AND_PORT_EN [937].EN[9:9]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [938].EN[10:10]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [939].EN[11:11]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [940].EN[12:12]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [941].EN[13:13]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [942].EN[14:14]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [943].EN[15:15]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [944].EN[16:16]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [945].EN[17:17]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [946].EN[18:18]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [947].EN[19:19]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [948].EN[20:20]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [949].EN[21:21]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [950].EN[22:22]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [951].EN[23:23]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [952].EN[24:24]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [953].EN[25:25]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [954].EN[26:26]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [955].EN[27:27]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [956].EN[28:28]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [957].EN[29:29]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [958].EN[30:30]	1
0xBB00D27C	OF_VLAN_AND_PORT_EN [959].EN[31:31]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [960].EN[0:0]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [961].EN[1:1]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [962].EN[2:2]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [963].EN[3:3]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [964].EN[4:4]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [965].EN[5:5]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [966].EN[6:6]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [967].EN[7:7]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [968].EN[8:8]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [969].EN[9:9]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [970].EN[10:10]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [971].EN[11:11]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [972].EN[12:12]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [973].EN[13:13]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [974].EN[14:14]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [975].EN[15:15]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [976].EN[16:16]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [977].EN[17:17]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [978].EN[18:18]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [979].EN[19:19]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [980].EN[20:20]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [981].EN[21:21]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [982].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D280	OF_VLAN_AND_PORT_EN [983].EN[23:23]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [984].EN[24:24]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [985].EN[25:25]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [986].EN[26:26]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [987].EN[27:27]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [988].EN[28:28]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [989].EN[29:29]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [990].EN[30:30]	1
0xBB00D280	OF_VLAN_AND_PORT_EN [991].EN[31:31]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [992].EN[0:0]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [993].EN[1:1]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [994].EN[2:2]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [995].EN[3:3]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [996].EN[4:4]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [997].EN[5:5]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [998].EN[6:6]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [999].EN[7:7]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1000].EN[8:8]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1001].EN[9:9]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1002].EN[10:10]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1003].EN[11:11]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1004].EN[12:12]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1005].EN[13:13]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1006].EN[14:14]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1007].EN[15:15]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1008].EN[16:16]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1009].EN[17:17]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1010].EN[18:18]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1011].EN[19:19]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1012].EN[20:20]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1013].EN[21:21]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1014].EN[22:22]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1015].EN[23:23]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1016].EN[24:24]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1017].EN[25:25]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1018].EN[26:26]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1019].EN[27:27]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1020].EN[28:28]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1021].EN[29:29]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1022].EN[30:30]	1
0xBB00D284	OF_VLAN_AND_PORT_EN [1023].EN[31:31]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1024].EN[0:0]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1025].EN[1:1]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1026].EN[2:2]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1027].EN[3:3]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1028].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D288	OF_VLAN_AND_PORT_EN [1029].EN[5:5]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1030].EN[6:6]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1031].EN[7:7]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1032].EN[8:8]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1033].EN[9:9]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1034].EN[10:10]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1035].EN[11:11]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1036].EN[12:12]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1037].EN[13:13]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1038].EN[14:14]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1039].EN[15:15]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1040].EN[16:16]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1041].EN[17:17]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1042].EN[18:18]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1043].EN[19:19]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1044].EN[20:20]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1045].EN[21:21]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1046].EN[22:22]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1047].EN[23:23]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1048].EN[24:24]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1049].EN[25:25]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1050].EN[26:26]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1051].EN[27:27]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1052].EN[28:28]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1053].EN[29:29]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1054].EN[30:30]	1
0xBB00D288	OF_VLAN_AND_PORT_EN [1055].EN[31:31]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1056].EN[0:0]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1057].EN[1:1]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1058].EN[2:2]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1059].EN[3:3]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1060].EN[4:4]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1061].EN[5:5]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1062].EN[6:6]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1063].EN[7:7]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1064].EN[8:8]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1065].EN[9:9]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1066].EN[10:10]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1067].EN[11:11]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1068].EN[12:12]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1069].EN[13:13]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1070].EN[14:14]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1071].EN[15:15]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1072].EN[16:16]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1073].EN[17:17]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1074].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D28C	OF_VLAN_AND_PORT_EN [1075].EN[19:19]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1076].EN[20:20]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1077].EN[21:21]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1078].EN[22:22]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1079].EN[23:23]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1080].EN[24:24]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1081].EN[25:25]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1082].EN[26:26]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1083].EN[27:27]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1084].EN[28:28]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1085].EN[29:29]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1086].EN[30:30]	1
0xBB00D28C	OF_VLAN_AND_PORT_EN [1087].EN[31:31]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1088].EN[0:0]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1089].EN[1:1]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1090].EN[2:2]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1091].EN[3:3]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1092].EN[4:4]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1093].EN[5:5]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1094].EN[6:6]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1095].EN[7:7]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1096].EN[8:8]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1097].EN[9:9]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1098].EN[10:10]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1099].EN[11:11]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1100].EN[12:12]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1101].EN[13:13]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1102].EN[14:14]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1103].EN[15:15]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1104].EN[16:16]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1105].EN[17:17]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1106].EN[18:18]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1107].EN[19:19]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1108].EN[20:20]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1109].EN[21:21]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1110].EN[22:22]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1111].EN[23:23]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1112].EN[24:24]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1113].EN[25:25]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1114].EN[26:26]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1115].EN[27:27]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1116].EN[28:28]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1117].EN[29:29]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1118].EN[30:30]	1
0xBB00D290	OF_VLAN_AND_PORT_EN [1119].EN[31:31]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1120].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D294	OF_VLAN_AND_PORT_EN [1121].EN[1:1]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1122].EN[2:2]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1123].EN[3:3]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1124].EN[4:4]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1125].EN[5:5]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1126].EN[6:6]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1127].EN[7:7]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1128].EN[8:8]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1129].EN[9:9]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1130].EN[10:10]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1131].EN[11:11]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1132].EN[12:12]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1133].EN[13:13]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1134].EN[14:14]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1135].EN[15:15]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1136].EN[16:16]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1137].EN[17:17]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1138].EN[18:18]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1139].EN[19:19]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1140].EN[20:20]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1141].EN[21:21]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1142].EN[22:22]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1143].EN[23:23]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1144].EN[24:24]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1145].EN[25:25]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1146].EN[26:26]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1147].EN[27:27]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1148].EN[28:28]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1149].EN[29:29]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1150].EN[30:30]	1
0xBB00D294	OF_VLAN_AND_PORT_EN [1151].EN[31:31]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1152].EN[0:0]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1153].EN[1:1]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1154].EN[2:2]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1155].EN[3:3]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1156].EN[4:4]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1157].EN[5:5]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1158].EN[6:6]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1159].EN[7:7]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1160].EN[8:8]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1161].EN[9:9]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1162].EN[10:10]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1163].EN[11:11]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1164].EN[12:12]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1165].EN[13:13]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1166].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D298	OF_VLAN_AND_PORT_EN [1167].EN[15:15]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1168].EN[16:16]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1169].EN[17:17]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1170].EN[18:18]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1171].EN[19:19]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1172].EN[20:20]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1173].EN[21:21]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1174].EN[22:22]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1175].EN[23:23]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1176].EN[24:24]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1177].EN[25:25]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1178].EN[26:26]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1179].EN[27:27]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1180].EN[28:28]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1181].EN[29:29]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1182].EN[30:30]	1
0xBB00D298	OF_VLAN_AND_PORT_EN [1183].EN[31:31]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1184].EN[0:0]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1185].EN[1:1]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1186].EN[2:2]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1187].EN[3:3]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1188].EN[4:4]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1189].EN[5:5]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1190].EN[6:6]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1191].EN[7:7]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1192].EN[8:8]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1193].EN[9:9]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1194].EN[10:10]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1195].EN[11:11]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1196].EN[12:12]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1197].EN[13:13]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1198].EN[14:14]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1199].EN[15:15]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1200].EN[16:16]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1201].EN[17:17]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1202].EN[18:18]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1203].EN[19:19]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1204].EN[20:20]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1205].EN[21:21]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1206].EN[22:22]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1207].EN[23:23]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1208].EN[24:24]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1209].EN[25:25]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1210].EN[26:26]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1211].EN[27:27]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1212].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D29C	OF_VLAN_AND_PORT_EN [1213].EN[29:29]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1214].EN[30:30]	1
0xBB00D29C	OF_VLAN_AND_PORT_EN [1215].EN[31:31]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1216].EN[0:0]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1217].EN[1:1]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1218].EN[2:2]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1219].EN[3:3]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1220].EN[4:4]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1221].EN[5:5]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1222].EN[6:6]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1223].EN[7:7]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1224].EN[8:8]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1225].EN[9:9]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1226].EN[10:10]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1227].EN[11:11]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1228].EN[12:12]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1229].EN[13:13]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1230].EN[14:14]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1231].EN[15:15]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1232].EN[16:16]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1233].EN[17:17]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1234].EN[18:18]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1235].EN[19:19]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1236].EN[20:20]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1237].EN[21:21]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1238].EN[22:22]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1239].EN[23:23]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1240].EN[24:24]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1241].EN[25:25]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1242].EN[26:26]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1243].EN[27:27]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1244].EN[28:28]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1245].EN[29:29]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1246].EN[30:30]	1
0xBB00D2A0	OF_VLAN_AND_PORT_EN [1247].EN[31:31]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1248].EN[0:0]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1249].EN[1:1]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1250].EN[2:2]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1251].EN[3:3]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1252].EN[4:4]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1253].EN[5:5]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1254].EN[6:6]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1255].EN[7:7]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1256].EN[8:8]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1257].EN[9:9]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1258].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1259].EN[11:11]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1260].EN[12:12]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1261].EN[13:13]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1262].EN[14:14]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1263].EN[15:15]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1264].EN[16:16]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1265].EN[17:17]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1266].EN[18:18]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1267].EN[19:19]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1268].EN[20:20]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1269].EN[21:21]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1270].EN[22:22]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1271].EN[23:23]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1272].EN[24:24]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1273].EN[25:25]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1274].EN[26:26]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1275].EN[27:27]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1276].EN[28:28]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1277].EN[29:29]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1278].EN[30:30]	1
0xBB00D2A4	OF_VLAN_AND_PORT_EN [1279].EN[31:31]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1280].EN[0:0]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1281].EN[1:1]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1282].EN[2:2]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1283].EN[3:3]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1284].EN[4:4]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1285].EN[5:5]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1286].EN[6:6]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1287].EN[7:7]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1288].EN[8:8]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1289].EN[9:9]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1290].EN[10:10]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1291].EN[11:11]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1292].EN[12:12]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1293].EN[13:13]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1294].EN[14:14]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1295].EN[15:15]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1296].EN[16:16]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1297].EN[17:17]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1298].EN[18:18]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1299].EN[19:19]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1300].EN[20:20]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1301].EN[21:21]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1302].EN[22:22]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1303].EN[23:23]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1304].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1305].EN[25:25]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1306].EN[26:26]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1307].EN[27:27]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1308].EN[28:28]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1309].EN[29:29]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1310].EN[30:30]	1
0xBB00D2A8	OF_VLAN_AND_PORT_EN [1311].EN[31:31]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1312].EN[0:0]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1313].EN[1:1]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1314].EN[2:2]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1315].EN[3:3]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1316].EN[4:4]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1317].EN[5:5]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1318].EN[6:6]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1319].EN[7:7]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1320].EN[8:8]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1321].EN[9:9]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1322].EN[10:10]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1323].EN[11:11]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1324].EN[12:12]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1325].EN[13:13]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1326].EN[14:14]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1327].EN[15:15]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1328].EN[16:16]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1329].EN[17:17]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1330].EN[18:18]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1331].EN[19:19]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1332].EN[20:20]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1333].EN[21:21]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1334].EN[22:22]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1335].EN[23:23]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1336].EN[24:24]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1337].EN[25:25]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1338].EN[26:26]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1339].EN[27:27]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1340].EN[28:28]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1341].EN[29:29]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1342].EN[30:30]	1
0xBB00D2AC	OF_VLAN_AND_PORT_EN [1343].EN[31:31]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1344].EN[0:0]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1345].EN[1:1]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1346].EN[2:2]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1347].EN[3:3]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1348].EN[4:4]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1349].EN[5:5]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1350].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1351].EN[7:7]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1352].EN[8:8]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1353].EN[9:9]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1354].EN[10:10]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1355].EN[11:11]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1356].EN[12:12]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1357].EN[13:13]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1358].EN[14:14]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1359].EN[15:15]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1360].EN[16:16]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1361].EN[17:17]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1362].EN[18:18]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1363].EN[19:19]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1364].EN[20:20]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1365].EN[21:21]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1366].EN[22:22]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1367].EN[23:23]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1368].EN[24:24]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1369].EN[25:25]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1370].EN[26:26]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1371].EN[27:27]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1372].EN[28:28]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1373].EN[29:29]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1374].EN[30:30]	1
0xBB00D2B0	OF_VLAN_AND_PORT_EN [1375].EN[31:31]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1376].EN[0:0]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1377].EN[1:1]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1378].EN[2:2]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1379].EN[3:3]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1380].EN[4:4]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1381].EN[5:5]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1382].EN[6:6]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1383].EN[7:7]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1384].EN[8:8]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1385].EN[9:9]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1386].EN[10:10]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1387].EN[11:11]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1388].EN[12:12]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1389].EN[13:13]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1390].EN[14:14]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1391].EN[15:15]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1392].EN[16:16]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1393].EN[17:17]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1394].EN[18:18]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1395].EN[19:19]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1396].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1397].EN[21:21]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1398].EN[22:22]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1399].EN[23:23]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1400].EN[24:24]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1401].EN[25:25]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1402].EN[26:26]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1403].EN[27:27]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1404].EN[28:28]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1405].EN[29:29]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1406].EN[30:30]	1
0xBB00D2B4	OF_VLAN_AND_PORT_EN [1407].EN[31:31]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1408].EN[0:0]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1409].EN[1:1]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1410].EN[2:2]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1411].EN[3:3]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1412].EN[4:4]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1413].EN[5:5]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1414].EN[6:6]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1415].EN[7:7]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1416].EN[8:8]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1417].EN[9:9]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1418].EN[10:10]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1419].EN[11:11]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1420].EN[12:12]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1421].EN[13:13]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1422].EN[14:14]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1423].EN[15:15]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1424].EN[16:16]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1425].EN[17:17]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1426].EN[18:18]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1427].EN[19:19]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1428].EN[20:20]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1429].EN[21:21]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1430].EN[22:22]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1431].EN[23:23]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1432].EN[24:24]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1433].EN[25:25]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1434].EN[26:26]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1435].EN[27:27]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1436].EN[28:28]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1437].EN[29:29]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1438].EN[30:30]	1
0xBB00D2B8	OF_VLAN_AND_PORT_EN [1439].EN[31:31]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1440].EN[0:0]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1441].EN[1:1]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1442].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1443].EN[3:3]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1444].EN[4:4]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1445].EN[5:5]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1446].EN[6:6]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1447].EN[7:7]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1448].EN[8:8]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1449].EN[9:9]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1450].EN[10:10]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1451].EN[11:11]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1452].EN[12:12]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1453].EN[13:13]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1454].EN[14:14]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1455].EN[15:15]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1456].EN[16:16]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1457].EN[17:17]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1458].EN[18:18]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1459].EN[19:19]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1460].EN[20:20]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1461].EN[21:21]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1462].EN[22:22]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1463].EN[23:23]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1464].EN[24:24]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1465].EN[25:25]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1466].EN[26:26]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1467].EN[27:27]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1468].EN[28:28]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1469].EN[29:29]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1470].EN[30:30]	1
0xBB00D2BC	OF_VLAN_AND_PORT_EN [1471].EN[31:31]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1472].EN[0:0]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1473].EN[1:1]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1474].EN[2:2]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1475].EN[3:3]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1476].EN[4:4]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1477].EN[5:5]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1478].EN[6:6]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1479].EN[7:7]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1480].EN[8:8]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1481].EN[9:9]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1482].EN[10:10]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1483].EN[11:11]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1484].EN[12:12]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1485].EN[13:13]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1486].EN[14:14]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1487].EN[15:15]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1488].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1489].EN[17:17]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1490].EN[18:18]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1491].EN[19:19]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1492].EN[20:20]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1493].EN[21:21]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1494].EN[22:22]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1495].EN[23:23]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1496].EN[24:24]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1497].EN[25:25]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1498].EN[26:26]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1499].EN[27:27]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1500].EN[28:28]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1501].EN[29:29]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1502].EN[30:30]	1
0xBB00D2C0	OF_VLAN_AND_PORT_EN [1503].EN[31:31]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1504].EN[0:0]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1505].EN[1:1]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1506].EN[2:2]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1507].EN[3:3]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1508].EN[4:4]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1509].EN[5:5]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1510].EN[6:6]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1511].EN[7:7]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1512].EN[8:8]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1513].EN[9:9]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1514].EN[10:10]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1515].EN[11:11]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1516].EN[12:12]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1517].EN[13:13]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1518].EN[14:14]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1519].EN[15:15]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1520].EN[16:16]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1521].EN[17:17]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1522].EN[18:18]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1523].EN[19:19]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1524].EN[20:20]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1525].EN[21:21]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1526].EN[22:22]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1527].EN[23:23]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1528].EN[24:24]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1529].EN[25:25]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1530].EN[26:26]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1531].EN[27:27]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1532].EN[28:28]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1533].EN[29:29]	1
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1534].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2C4	OF_VLAN_AND_PORT_EN [1535].EN[31:31]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1536].EN[0:0]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1537].EN[1:1]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1538].EN[2:2]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1539].EN[3:3]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1540].EN[4:4]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1541].EN[5:5]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1542].EN[6:6]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1543].EN[7:7]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1544].EN[8:8]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1545].EN[9:9]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1546].EN[10:10]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1547].EN[11:11]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1548].EN[12:12]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1549].EN[13:13]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1550].EN[14:14]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1551].EN[15:15]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1552].EN[16:16]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1553].EN[17:17]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1554].EN[18:18]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1555].EN[19:19]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1556].EN[20:20]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1557].EN[21:21]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1558].EN[22:22]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1559].EN[23:23]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1560].EN[24:24]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1561].EN[25:25]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1562].EN[26:26]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1563].EN[27:27]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1564].EN[28:28]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1565].EN[29:29]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1566].EN[30:30]	1
0xBB00D2C8	OF_VLAN_AND_PORT_EN [1567].EN[31:31]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1568].EN[0:0]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1569].EN[1:1]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1570].EN[2:2]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1571].EN[3:3]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1572].EN[4:4]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1573].EN[5:5]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1574].EN[6:6]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1575].EN[7:7]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1576].EN[8:8]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1577].EN[9:9]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1578].EN[10:10]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1579].EN[11:11]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1580].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1581].EN[13:13]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1582].EN[14:14]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1583].EN[15:15]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1584].EN[16:16]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1585].EN[17:17]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1586].EN[18:18]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1587].EN[19:19]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1588].EN[20:20]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1589].EN[21:21]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1590].EN[22:22]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1591].EN[23:23]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1592].EN[24:24]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1593].EN[25:25]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1594].EN[26:26]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1595].EN[27:27]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1596].EN[28:28]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1597].EN[29:29]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1598].EN[30:30]	1
0xBB00D2CC	OF_VLAN_AND_PORT_EN [1599].EN[31:31]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1600].EN[0:0]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1601].EN[1:1]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1602].EN[2:2]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1603].EN[3:3]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1604].EN[4:4]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1605].EN[5:5]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1606].EN[6:6]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1607].EN[7:7]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1608].EN[8:8]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1609].EN[9:9]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1610].EN[10:10]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1611].EN[11:11]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1612].EN[12:12]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1613].EN[13:13]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1614].EN[14:14]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1615].EN[15:15]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1616].EN[16:16]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1617].EN[17:17]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1618].EN[18:18]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1619].EN[19:19]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1620].EN[20:20]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1621].EN[21:21]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1622].EN[22:22]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1623].EN[23:23]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1624].EN[24:24]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1625].EN[25:25]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1626].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1627].EN[27:27]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1628].EN[28:28]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1629].EN[29:29]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1630].EN[30:30]	1
0xBB00D2D0	OF_VLAN_AND_PORT_EN [1631].EN[31:31]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1632].EN[0:0]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1633].EN[1:1]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1634].EN[2:2]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1635].EN[3:3]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1636].EN[4:4]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1637].EN[5:5]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1638].EN[6:6]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1639].EN[7:7]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1640].EN[8:8]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1641].EN[9:9]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1642].EN[10:10]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1643].EN[11:11]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1644].EN[12:12]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1645].EN[13:13]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1646].EN[14:14]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1647].EN[15:15]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1648].EN[16:16]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1649].EN[17:17]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1650].EN[18:18]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1651].EN[19:19]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1652].EN[20:20]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1653].EN[21:21]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1654].EN[22:22]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1655].EN[23:23]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1656].EN[24:24]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1657].EN[25:25]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1658].EN[26:26]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1659].EN[27:27]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1660].EN[28:28]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1661].EN[29:29]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1662].EN[30:30]	1
0xBB00D2D4	OF_VLAN_AND_PORT_EN [1663].EN[31:31]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1664].EN[0:0]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1665].EN[1:1]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1666].EN[2:2]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1667].EN[3:3]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1668].EN[4:4]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1669].EN[5:5]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1670].EN[6:6]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1671].EN[7:7]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1672].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1673].EN[9:9]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1674].EN[10:10]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1675].EN[11:11]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1676].EN[12:12]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1677].EN[13:13]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1678].EN[14:14]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1679].EN[15:15]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1680].EN[16:16]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1681].EN[17:17]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1682].EN[18:18]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1683].EN[19:19]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1684].EN[20:20]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1685].EN[21:21]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1686].EN[22:22]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1687].EN[23:23]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1688].EN[24:24]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1689].EN[25:25]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1690].EN[26:26]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1691].EN[27:27]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1692].EN[28:28]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1693].EN[29:29]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1694].EN[30:30]	1
0xBB00D2D8	OF_VLAN_AND_PORT_EN [1695].EN[31:31]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1696].EN[0:0]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1697].EN[1:1]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1698].EN[2:2]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1699].EN[3:3]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1700].EN[4:4]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1701].EN[5:5]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1702].EN[6:6]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1703].EN[7:7]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1704].EN[8:8]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1705].EN[9:9]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1706].EN[10:10]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1707].EN[11:11]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1708].EN[12:12]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1709].EN[13:13]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1710].EN[14:14]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1711].EN[15:15]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1712].EN[16:16]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1713].EN[17:17]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1714].EN[18:18]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1715].EN[19:19]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1716].EN[20:20]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1717].EN[21:21]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1718].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1719].EN[23:23]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1720].EN[24:24]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1721].EN[25:25]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1722].EN[26:26]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1723].EN[27:27]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1724].EN[28:28]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1725].EN[29:29]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1726].EN[30:30]	1
0xBB00D2DC	OF_VLAN_AND_PORT_EN [1727].EN[31:31]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1728].EN[0:0]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1729].EN[1:1]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1730].EN[2:2]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1731].EN[3:3]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1732].EN[4:4]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1733].EN[5:5]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1734].EN[6:6]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1735].EN[7:7]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1736].EN[8:8]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1737].EN[9:9]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1738].EN[10:10]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1739].EN[11:11]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1740].EN[12:12]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1741].EN[13:13]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1742].EN[14:14]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1743].EN[15:15]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1744].EN[16:16]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1745].EN[17:17]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1746].EN[18:18]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1747].EN[19:19]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1748].EN[20:20]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1749].EN[21:21]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1750].EN[22:22]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1751].EN[23:23]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1752].EN[24:24]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1753].EN[25:25]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1754].EN[26:26]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1755].EN[27:27]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1756].EN[28:28]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1757].EN[29:29]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1758].EN[30:30]	1
0xBB00D2E0	OF_VLAN_AND_PORT_EN [1759].EN[31:31]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1760].EN[0:0]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1761].EN[1:1]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1762].EN[2:2]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1763].EN[3:3]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1764].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1765].EN[5:5]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1766].EN[6:6]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1767].EN[7:7]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1768].EN[8:8]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1769].EN[9:9]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1770].EN[10:10]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1771].EN[11:11]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1772].EN[12:12]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1773].EN[13:13]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1774].EN[14:14]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1775].EN[15:15]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1776].EN[16:16]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1777].EN[17:17]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1778].EN[18:18]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1779].EN[19:19]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1780].EN[20:20]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1781].EN[21:21]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1782].EN[22:22]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1783].EN[23:23]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1784].EN[24:24]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1785].EN[25:25]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1786].EN[26:26]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1787].EN[27:27]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1788].EN[28:28]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1789].EN[29:29]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1790].EN[30:30]	1
0xBB00D2E4	OF_VLAN_AND_PORT_EN [1791].EN[31:31]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1792].EN[0:0]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1793].EN[1:1]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1794].EN[2:2]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1795].EN[3:3]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1796].EN[4:4]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1797].EN[5:5]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1798].EN[6:6]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1799].EN[7:7]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1800].EN[8:8]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1801].EN[9:9]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1802].EN[10:10]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1803].EN[11:11]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1804].EN[12:12]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1805].EN[13:13]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1806].EN[14:14]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1807].EN[15:15]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1808].EN[16:16]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1809].EN[17:17]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1810].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1811].EN[19:19]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1812].EN[20:20]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1813].EN[21:21]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1814].EN[22:22]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1815].EN[23:23]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1816].EN[24:24]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1817].EN[25:25]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1818].EN[26:26]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1819].EN[27:27]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1820].EN[28:28]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1821].EN[29:29]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1822].EN[30:30]	1
0xBB00D2E8	OF_VLAN_AND_PORT_EN [1823].EN[31:31]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1824].EN[0:0]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1825].EN[1:1]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1826].EN[2:2]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1827].EN[3:3]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1828].EN[4:4]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1829].EN[5:5]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1830].EN[6:6]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1831].EN[7:7]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1832].EN[8:8]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1833].EN[9:9]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1834].EN[10:10]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1835].EN[11:11]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1836].EN[12:12]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1837].EN[13:13]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1838].EN[14:14]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1839].EN[15:15]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1840].EN[16:16]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1841].EN[17:17]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1842].EN[18:18]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1843].EN[19:19]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1844].EN[20:20]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1845].EN[21:21]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1846].EN[22:22]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1847].EN[23:23]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1848].EN[24:24]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1849].EN[25:25]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1850].EN[26:26]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1851].EN[27:27]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1852].EN[28:28]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1853].EN[29:29]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1854].EN[30:30]	1
0xBB00D2EC	OF_VLAN_AND_PORT_EN [1855].EN[31:31]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1856].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1857].EN[1:1]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1858].EN[2:2]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1859].EN[3:3]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1860].EN[4:4]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1861].EN[5:5]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1862].EN[6:6]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1863].EN[7:7]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1864].EN[8:8]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1865].EN[9:9]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1866].EN[10:10]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1867].EN[11:11]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1868].EN[12:12]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1869].EN[13:13]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1870].EN[14:14]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1871].EN[15:15]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1872].EN[16:16]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1873].EN[17:17]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1874].EN[18:18]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1875].EN[19:19]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1876].EN[20:20]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1877].EN[21:21]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1878].EN[22:22]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1879].EN[23:23]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1880].EN[24:24]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1881].EN[25:25]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1882].EN[26:26]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1883].EN[27:27]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1884].EN[28:28]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1885].EN[29:29]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1886].EN[30:30]	1
0xBB00D2F0	OF_VLAN_AND_PORT_EN [1887].EN[31:31]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1888].EN[0:0]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1889].EN[1:1]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1890].EN[2:2]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1891].EN[3:3]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1892].EN[4:4]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1893].EN[5:5]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1894].EN[6:6]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1895].EN[7:7]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1896].EN[8:8]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1897].EN[9:9]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1898].EN[10:10]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1899].EN[11:11]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1900].EN[12:12]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1901].EN[13:13]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1902].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1903].EN[15:15]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1904].EN[16:16]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1905].EN[17:17]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1906].EN[18:18]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1907].EN[19:19]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1908].EN[20:20]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1909].EN[21:21]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1910].EN[22:22]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1911].EN[23:23]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1912].EN[24:24]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1913].EN[25:25]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1914].EN[26:26]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1915].EN[27:27]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1916].EN[28:28]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1917].EN[29:29]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1918].EN[30:30]	1
0xBB00D2F4	OF_VLAN_AND_PORT_EN [1919].EN[31:31]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1920].EN[0:0]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1921].EN[1:1]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1922].EN[2:2]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1923].EN[3:3]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1924].EN[4:4]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1925].EN[5:5]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1926].EN[6:6]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1927].EN[7:7]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1928].EN[8:8]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1929].EN[9:9]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1930].EN[10:10]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1931].EN[11:11]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1932].EN[12:12]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1933].EN[13:13]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1934].EN[14:14]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1935].EN[15:15]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1936].EN[16:16]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1937].EN[17:17]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1938].EN[18:18]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1939].EN[19:19]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1940].EN[20:20]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1941].EN[21:21]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1942].EN[22:22]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1943].EN[23:23]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1944].EN[24:24]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1945].EN[25:25]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1946].EN[26:26]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1947].EN[27:27]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1948].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1949].EN[29:29]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1950].EN[30:30]	1
0xBB00D2F8	OF_VLAN_AND_PORT_EN [1951].EN[31:31]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1952].EN[0:0]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1953].EN[1:1]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1954].EN[2:2]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1955].EN[3:3]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1956].EN[4:4]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1957].EN[5:5]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1958].EN[6:6]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1959].EN[7:7]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1960].EN[8:8]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1961].EN[9:9]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1962].EN[10:10]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1963].EN[11:11]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1964].EN[12:12]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1965].EN[13:13]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1966].EN[14:14]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1967].EN[15:15]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1968].EN[16:16]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1969].EN[17:17]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1970].EN[18:18]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1971].EN[19:19]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1972].EN[20:20]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1973].EN[21:21]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1974].EN[22:22]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1975].EN[23:23]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1976].EN[24:24]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1977].EN[25:25]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1978].EN[26:26]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1979].EN[27:27]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1980].EN[28:28]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1981].EN[29:29]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1982].EN[30:30]	1
0xBB00D2FC	OF_VLAN_AND_PORT_EN [1983].EN[31:31]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1984].EN[0:0]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1985].EN[1:1]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1986].EN[2:2]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1987].EN[3:3]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1988].EN[4:4]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1989].EN[5:5]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1990].EN[6:6]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1991].EN[7:7]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1992].EN[8:8]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1993].EN[9:9]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1994].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D300	OF_VLAN_AND_PORT_EN [1995].EN[11:11]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1996].EN[12:12]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1997].EN[13:13]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1998].EN[14:14]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [1999].EN[15:15]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2000].EN[16:16]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2001].EN[17:17]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2002].EN[18:18]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2003].EN[19:19]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2004].EN[20:20]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2005].EN[21:21]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2006].EN[22:22]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2007].EN[23:23]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2008].EN[24:24]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2009].EN[25:25]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2010].EN[26:26]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2011].EN[27:27]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2012].EN[28:28]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2013].EN[29:29]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2014].EN[30:30]	1
0xBB00D300	OF_VLAN_AND_PORT_EN [2015].EN[31:31]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2016].EN[0:0]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2017].EN[1:1]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2018].EN[2:2]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2019].EN[3:3]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2020].EN[4:4]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2021].EN[5:5]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2022].EN[6:6]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2023].EN[7:7]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2024].EN[8:8]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2025].EN[9:9]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2026].EN[10:10]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2027].EN[11:11]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2028].EN[12:12]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2029].EN[13:13]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2030].EN[14:14]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2031].EN[15:15]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2032].EN[16:16]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2033].EN[17:17]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2034].EN[18:18]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2035].EN[19:19]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2036].EN[20:20]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2037].EN[21:21]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2038].EN[22:22]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2039].EN[23:23]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2040].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D304	OF_VLAN_AND_PORT_EN [2041].EN[25:25]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2042].EN[26:26]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2043].EN[27:27]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2044].EN[28:28]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2045].EN[29:29]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2046].EN[30:30]	1
0xBB00D304	OF_VLAN_AND_PORT_EN [2047].EN[31:31]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2048].EN[0:0]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2049].EN[1:1]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2050].EN[2:2]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2051].EN[3:3]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2052].EN[4:4]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2053].EN[5:5]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2054].EN[6:6]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2055].EN[7:7]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2056].EN[8:8]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2057].EN[9:9]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2058].EN[10:10]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2059].EN[11:11]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2060].EN[12:12]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2061].EN[13:13]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2062].EN[14:14]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2063].EN[15:15]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2064].EN[16:16]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2065].EN[17:17]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2066].EN[18:18]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2067].EN[19:19]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2068].EN[20:20]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2069].EN[21:21]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2070].EN[22:22]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2071].EN[23:23]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2072].EN[24:24]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2073].EN[25:25]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2074].EN[26:26]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2075].EN[27:27]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2076].EN[28:28]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2077].EN[29:29]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2078].EN[30:30]	1
0xBB00D308	OF_VLAN_AND_PORT_EN [2079].EN[31:31]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2080].EN[0:0]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2081].EN[1:1]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2082].EN[2:2]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2083].EN[3:3]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2084].EN[4:4]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2085].EN[5:5]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2086].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D30C	OF_VLAN_AND_PORT_EN [2087].EN[7:7]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2088].EN[8:8]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2089].EN[9:9]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2090].EN[10:10]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2091].EN[11:11]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2092].EN[12:12]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2093].EN[13:13]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2094].EN[14:14]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2095].EN[15:15]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2096].EN[16:16]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2097].EN[17:17]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2098].EN[18:18]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2099].EN[19:19]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2100].EN[20:20]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2101].EN[21:21]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2102].EN[22:22]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2103].EN[23:23]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2104].EN[24:24]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2105].EN[25:25]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2106].EN[26:26]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2107].EN[27:27]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2108].EN[28:28]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2109].EN[29:29]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2110].EN[30:30]	1
0xBB00D30C	OF_VLAN_AND_PORT_EN [2111].EN[31:31]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2112].EN[0:0]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2113].EN[1:1]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2114].EN[2:2]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2115].EN[3:3]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2116].EN[4:4]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2117].EN[5:5]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2118].EN[6:6]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2119].EN[7:7]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2120].EN[8:8]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2121].EN[9:9]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2122].EN[10:10]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2123].EN[11:11]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2124].EN[12:12]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2125].EN[13:13]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2126].EN[14:14]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2127].EN[15:15]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2128].EN[16:16]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2129].EN[17:17]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2130].EN[18:18]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2131].EN[19:19]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2132].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D310	OF_VLAN_AND_PORT_EN [2133].EN[21:21]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2134].EN[22:22]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2135].EN[23:23]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2136].EN[24:24]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2137].EN[25:25]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2138].EN[26:26]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2139].EN[27:27]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2140].EN[28:28]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2141].EN[29:29]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2142].EN[30:30]	1
0xBB00D310	OF_VLAN_AND_PORT_EN [2143].EN[31:31]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2144].EN[0:0]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2145].EN[1:1]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2146].EN[2:2]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2147].EN[3:3]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2148].EN[4:4]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2149].EN[5:5]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2150].EN[6:6]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2151].EN[7:7]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2152].EN[8:8]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2153].EN[9:9]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2154].EN[10:10]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2155].EN[11:11]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2156].EN[12:12]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2157].EN[13:13]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2158].EN[14:14]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2159].EN[15:15]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2160].EN[16:16]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2161].EN[17:17]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2162].EN[18:18]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2163].EN[19:19]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2164].EN[20:20]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2165].EN[21:21]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2166].EN[22:22]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2167].EN[23:23]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2168].EN[24:24]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2169].EN[25:25]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2170].EN[26:26]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2171].EN[27:27]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2172].EN[28:28]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2173].EN[29:29]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2174].EN[30:30]	1
0xBB00D314	OF_VLAN_AND_PORT_EN [2175].EN[31:31]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2176].EN[0:0]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2177].EN[1:1]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2178].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D318	OF_VLAN_AND_PORT_EN [2179].EN[3:3]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2180].EN[4:4]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2181].EN[5:5]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2182].EN[6:6]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2183].EN[7:7]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2184].EN[8:8]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2185].EN[9:9]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2186].EN[10:10]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2187].EN[11:11]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2188].EN[12:12]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2189].EN[13:13]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2190].EN[14:14]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2191].EN[15:15]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2192].EN[16:16]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2193].EN[17:17]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2194].EN[18:18]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2195].EN[19:19]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2196].EN[20:20]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2197].EN[21:21]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2198].EN[22:22]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2199].EN[23:23]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2200].EN[24:24]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2201].EN[25:25]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2202].EN[26:26]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2203].EN[27:27]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2204].EN[28:28]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2205].EN[29:29]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2206].EN[30:30]	1
0xBB00D318	OF_VLAN_AND_PORT_EN [2207].EN[31:31]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2208].EN[0:0]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2209].EN[1:1]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2210].EN[2:2]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2211].EN[3:3]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2212].EN[4:4]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2213].EN[5:5]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2214].EN[6:6]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2215].EN[7:7]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2216].EN[8:8]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2217].EN[9:9]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2218].EN[10:10]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2219].EN[11:11]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2220].EN[12:12]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2221].EN[13:13]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2222].EN[14:14]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2223].EN[15:15]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2224].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D31C	OF_VLAN_AND_PORT_EN [2225].EN[17:17]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2226].EN[18:18]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2227].EN[19:19]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2228].EN[20:20]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2229].EN[21:21]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2230].EN[22:22]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2231].EN[23:23]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2232].EN[24:24]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2233].EN[25:25]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2234].EN[26:26]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2235].EN[27:27]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2236].EN[28:28]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2237].EN[29:29]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2238].EN[30:30]	1
0xBB00D31C	OF_VLAN_AND_PORT_EN [2239].EN[31:31]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2240].EN[0:0]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2241].EN[1:1]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2242].EN[2:2]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2243].EN[3:3]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2244].EN[4:4]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2245].EN[5:5]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2246].EN[6:6]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2247].EN[7:7]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2248].EN[8:8]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2249].EN[9:9]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2250].EN[10:10]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2251].EN[11:11]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2252].EN[12:12]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2253].EN[13:13]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2254].EN[14:14]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2255].EN[15:15]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2256].EN[16:16]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2257].EN[17:17]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2258].EN[18:18]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2259].EN[19:19]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2260].EN[20:20]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2261].EN[21:21]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2262].EN[22:22]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2263].EN[23:23]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2264].EN[24:24]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2265].EN[25:25]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2266].EN[26:26]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2267].EN[27:27]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2268].EN[28:28]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2269].EN[29:29]	1
0xBB00D320	OF_VLAN_AND_PORT_EN [2270].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D320	OF_VLAN_AND_PORT_EN [2271].EN[31:31]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2272].EN[0:0]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2273].EN[1:1]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2274].EN[2:2]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2275].EN[3:3]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2276].EN[4:4]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2277].EN[5:5]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2278].EN[6:6]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2279].EN[7:7]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2280].EN[8:8]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2281].EN[9:9]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2282].EN[10:10]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2283].EN[11:11]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2284].EN[12:12]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2285].EN[13:13]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2286].EN[14:14]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2287].EN[15:15]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2288].EN[16:16]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2289].EN[17:17]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2290].EN[18:18]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2291].EN[19:19]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2292].EN[20:20]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2293].EN[21:21]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2294].EN[22:22]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2295].EN[23:23]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2296].EN[24:24]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2297].EN[25:25]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2298].EN[26:26]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2299].EN[27:27]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2300].EN[28:28]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2301].EN[29:29]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2302].EN[30:30]	1
0xBB00D324	OF_VLAN_AND_PORT_EN [2303].EN[31:31]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2304].EN[0:0]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2305].EN[1:1]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2306].EN[2:2]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2307].EN[3:3]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2308].EN[4:4]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2309].EN[5:5]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2310].EN[6:6]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2311].EN[7:7]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2312].EN[8:8]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2313].EN[9:9]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2314].EN[10:10]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2315].EN[11:11]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2316].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D328	OF_VLAN_AND_PORT_EN [2317].EN[13:13]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2318].EN[14:14]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2319].EN[15:15]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2320].EN[16:16]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2321].EN[17:17]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2322].EN[18:18]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2323].EN[19:19]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2324].EN[20:20]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2325].EN[21:21]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2326].EN[22:22]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2327].EN[23:23]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2328].EN[24:24]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2329].EN[25:25]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2330].EN[26:26]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2331].EN[27:27]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2332].EN[28:28]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2333].EN[29:29]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2334].EN[30:30]	1
0xBB00D328	OF_VLAN_AND_PORT_EN [2335].EN[31:31]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2336].EN[0:0]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2337].EN[1:1]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2338].EN[2:2]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2339].EN[3:3]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2340].EN[4:4]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2341].EN[5:5]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2342].EN[6:6]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2343].EN[7:7]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2344].EN[8:8]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2345].EN[9:9]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2346].EN[10:10]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2347].EN[11:11]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2348].EN[12:12]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2349].EN[13:13]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2350].EN[14:14]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2351].EN[15:15]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2352].EN[16:16]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2353].EN[17:17]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2354].EN[18:18]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2355].EN[19:19]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2356].EN[20:20]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2357].EN[21:21]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2358].EN[22:22]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2359].EN[23:23]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2360].EN[24:24]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2361].EN[25:25]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2362].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D32C	OF_VLAN_AND_PORT_EN [2363].EN[27:27]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2364].EN[28:28]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2365].EN[29:29]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2366].EN[30:30]	1
0xBB00D32C	OF_VLAN_AND_PORT_EN [2367].EN[31:31]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2368].EN[0:0]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2369].EN[1:1]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2370].EN[2:2]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2371].EN[3:3]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2372].EN[4:4]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2373].EN[5:5]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2374].EN[6:6]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2375].EN[7:7]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2376].EN[8:8]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2377].EN[9:9]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2378].EN[10:10]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2379].EN[11:11]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2380].EN[12:12]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2381].EN[13:13]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2382].EN[14:14]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2383].EN[15:15]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2384].EN[16:16]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2385].EN[17:17]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2386].EN[18:18]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2387].EN[19:19]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2388].EN[20:20]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2389].EN[21:21]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2390].EN[22:22]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2391].EN[23:23]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2392].EN[24:24]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2393].EN[25:25]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2394].EN[26:26]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2395].EN[27:27]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2396].EN[28:28]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2397].EN[29:29]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2398].EN[30:30]	1
0xBB00D330	OF_VLAN_AND_PORT_EN [2399].EN[31:31]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2400].EN[0:0]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2401].EN[1:1]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2402].EN[2:2]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2403].EN[3:3]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2404].EN[4:4]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2405].EN[5:5]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2406].EN[6:6]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2407].EN[7:7]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2408].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D334	OF_VLAN_AND_PORT_EN [2409].EN[9:9]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2410].EN[10:10]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2411].EN[11:11]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2412].EN[12:12]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2413].EN[13:13]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2414].EN[14:14]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2415].EN[15:15]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2416].EN[16:16]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2417].EN[17:17]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2418].EN[18:18]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2419].EN[19:19]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2420].EN[20:20]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2421].EN[21:21]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2422].EN[22:22]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2423].EN[23:23]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2424].EN[24:24]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2425].EN[25:25]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2426].EN[26:26]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2427].EN[27:27]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2428].EN[28:28]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2429].EN[29:29]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2430].EN[30:30]	1
0xBB00D334	OF_VLAN_AND_PORT_EN [2431].EN[31:31]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2432].EN[0:0]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2433].EN[1:1]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2434].EN[2:2]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2435].EN[3:3]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2436].EN[4:4]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2437].EN[5:5]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2438].EN[6:6]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2439].EN[7:7]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2440].EN[8:8]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2441].EN[9:9]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2442].EN[10:10]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2443].EN[11:11]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2444].EN[12:12]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2445].EN[13:13]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2446].EN[14:14]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2447].EN[15:15]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2448].EN[16:16]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2449].EN[17:17]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2450].EN[18:18]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2451].EN[19:19]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2452].EN[20:20]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2453].EN[21:21]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2454].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D338	OF_VLAN_AND_PORT_EN [2455].EN[23:23]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2456].EN[24:24]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2457].EN[25:25]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2458].EN[26:26]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2459].EN[27:27]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2460].EN[28:28]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2461].EN[29:29]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2462].EN[30:30]	1
0xBB00D338	OF_VLAN_AND_PORT_EN [2463].EN[31:31]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2464].EN[0:0]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2465].EN[1:1]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2466].EN[2:2]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2467].EN[3:3]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2468].EN[4:4]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2469].EN[5:5]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2470].EN[6:6]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2471].EN[7:7]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2472].EN[8:8]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2473].EN[9:9]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2474].EN[10:10]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2475].EN[11:11]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2476].EN[12:12]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2477].EN[13:13]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2478].EN[14:14]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2479].EN[15:15]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2480].EN[16:16]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2481].EN[17:17]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2482].EN[18:18]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2483].EN[19:19]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2484].EN[20:20]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2485].EN[21:21]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2486].EN[22:22]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2487].EN[23:23]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2488].EN[24:24]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2489].EN[25:25]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2490].EN[26:26]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2491].EN[27:27]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2492].EN[28:28]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2493].EN[29:29]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2494].EN[30:30]	1
0xBB00D33C	OF_VLAN_AND_PORT_EN [2495].EN[31:31]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2496].EN[0:0]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2497].EN[1:1]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2498].EN[2:2]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2499].EN[3:3]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2500].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D340	OF_VLAN_AND_PORT_EN [2501].EN[5:5]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2502].EN[6:6]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2503].EN[7:7]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2504].EN[8:8]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2505].EN[9:9]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2506].EN[10:10]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2507].EN[11:11]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2508].EN[12:12]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2509].EN[13:13]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2510].EN[14:14]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2511].EN[15:15]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2512].EN[16:16]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2513].EN[17:17]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2514].EN[18:18]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2515].EN[19:19]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2516].EN[20:20]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2517].EN[21:21]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2518].EN[22:22]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2519].EN[23:23]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2520].EN[24:24]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2521].EN[25:25]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2522].EN[26:26]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2523].EN[27:27]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2524].EN[28:28]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2525].EN[29:29]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2526].EN[30:30]	1
0xBB00D340	OF_VLAN_AND_PORT_EN [2527].EN[31:31]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2528].EN[0:0]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2529].EN[1:1]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2530].EN[2:2]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2531].EN[3:3]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2532].EN[4:4]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2533].EN[5:5]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2534].EN[6:6]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2535].EN[7:7]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2536].EN[8:8]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2537].EN[9:9]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2538].EN[10:10]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2539].EN[11:11]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2540].EN[12:12]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2541].EN[13:13]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2542].EN[14:14]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2543].EN[15:15]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2544].EN[16:16]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2545].EN[17:17]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2546].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D344	OF_VLAN_AND_PORT_EN [2547].EN[19:19]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2548].EN[20:20]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2549].EN[21:21]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2550].EN[22:22]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2551].EN[23:23]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2552].EN[24:24]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2553].EN[25:25]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2554].EN[26:26]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2555].EN[27:27]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2556].EN[28:28]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2557].EN[29:29]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2558].EN[30:30]	1
0xBB00D344	OF_VLAN_AND_PORT_EN [2559].EN[31:31]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2560].EN[0:0]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2561].EN[1:1]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2562].EN[2:2]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2563].EN[3:3]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2564].EN[4:4]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2565].EN[5:5]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2566].EN[6:6]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2567].EN[7:7]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2568].EN[8:8]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2569].EN[9:9]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2570].EN[10:10]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2571].EN[11:11]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2572].EN[12:12]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2573].EN[13:13]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2574].EN[14:14]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2575].EN[15:15]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2576].EN[16:16]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2577].EN[17:17]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2578].EN[18:18]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2579].EN[19:19]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2580].EN[20:20]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2581].EN[21:21]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2582].EN[22:22]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2583].EN[23:23]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2584].EN[24:24]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2585].EN[25:25]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2586].EN[26:26]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2587].EN[27:27]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2588].EN[28:28]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2589].EN[29:29]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2590].EN[30:30]	1
0xBB00D348	OF_VLAN_AND_PORT_EN [2591].EN[31:31]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2592].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D34C	OF_VLAN_AND_PORT_EN [2593].EN[1:1]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2594].EN[2:2]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2595].EN[3:3]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2596].EN[4:4]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2597].EN[5:5]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2598].EN[6:6]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2599].EN[7:7]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2600].EN[8:8]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2601].EN[9:9]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2602].EN[10:10]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2603].EN[11:11]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2604].EN[12:12]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2605].EN[13:13]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2606].EN[14:14]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2607].EN[15:15]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2608].EN[16:16]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2609].EN[17:17]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2610].EN[18:18]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2611].EN[19:19]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2612].EN[20:20]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2613].EN[21:21]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2614].EN[22:22]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2615].EN[23:23]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2616].EN[24:24]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2617].EN[25:25]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2618].EN[26:26]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2619].EN[27:27]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2620].EN[28:28]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2621].EN[29:29]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2622].EN[30:30]	1
0xBB00D34C	OF_VLAN_AND_PORT_EN [2623].EN[31:31]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2624].EN[0:0]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2625].EN[1:1]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2626].EN[2:2]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2627].EN[3:3]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2628].EN[4:4]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2629].EN[5:5]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2630].EN[6:6]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2631].EN[7:7]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2632].EN[8:8]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2633].EN[9:9]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2634].EN[10:10]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2635].EN[11:11]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2636].EN[12:12]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2637].EN[13:13]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2638].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D350	OF_VLAN_AND_PORT_EN [2639].EN[15:15]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2640].EN[16:16]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2641].EN[17:17]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2642].EN[18:18]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2643].EN[19:19]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2644].EN[20:20]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2645].EN[21:21]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2646].EN[22:22]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2647].EN[23:23]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2648].EN[24:24]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2649].EN[25:25]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2650].EN[26:26]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2651].EN[27:27]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2652].EN[28:28]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2653].EN[29:29]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2654].EN[30:30]	1
0xBB00D350	OF_VLAN_AND_PORT_EN [2655].EN[31:31]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2656].EN[0:0]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2657].EN[1:1]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2658].EN[2:2]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2659].EN[3:3]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2660].EN[4:4]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2661].EN[5:5]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2662].EN[6:6]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2663].EN[7:7]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2664].EN[8:8]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2665].EN[9:9]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2666].EN[10:10]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2667].EN[11:11]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2668].EN[12:12]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2669].EN[13:13]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2670].EN[14:14]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2671].EN[15:15]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2672].EN[16:16]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2673].EN[17:17]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2674].EN[18:18]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2675].EN[19:19]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2676].EN[20:20]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2677].EN[21:21]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2678].EN[22:22]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2679].EN[23:23]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2680].EN[24:24]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2681].EN[25:25]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2682].EN[26:26]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2683].EN[27:27]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2684].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D354	OF_VLAN_AND_PORT_EN [2685].EN[29:29]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2686].EN[30:30]	1
0xBB00D354	OF_VLAN_AND_PORT_EN [2687].EN[31:31]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2688].EN[0:0]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2689].EN[1:1]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2690].EN[2:2]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2691].EN[3:3]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2692].EN[4:4]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2693].EN[5:5]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2694].EN[6:6]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2695].EN[7:7]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2696].EN[8:8]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2697].EN[9:9]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2698].EN[10:10]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2699].EN[11:11]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2700].EN[12:12]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2701].EN[13:13]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2702].EN[14:14]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2703].EN[15:15]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2704].EN[16:16]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2705].EN[17:17]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2706].EN[18:18]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2707].EN[19:19]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2708].EN[20:20]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2709].EN[21:21]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2710].EN[22:22]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2711].EN[23:23]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2712].EN[24:24]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2713].EN[25:25]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2714].EN[26:26]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2715].EN[27:27]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2716].EN[28:28]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2717].EN[29:29]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2718].EN[30:30]	1
0xBB00D358	OF_VLAN_AND_PORT_EN [2719].EN[31:31]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2720].EN[0:0]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2721].EN[1:1]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2722].EN[2:2]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2723].EN[3:3]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2724].EN[4:4]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2725].EN[5:5]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2726].EN[6:6]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2727].EN[7:7]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2728].EN[8:8]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2729].EN[9:9]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2730].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D35C	OF_VLAN_AND_PORT_EN [2731].EN[11:11]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2732].EN[12:12]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2733].EN[13:13]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2734].EN[14:14]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2735].EN[15:15]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2736].EN[16:16]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2737].EN[17:17]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2738].EN[18:18]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2739].EN[19:19]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2740].EN[20:20]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2741].EN[21:21]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2742].EN[22:22]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2743].EN[23:23]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2744].EN[24:24]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2745].EN[25:25]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2746].EN[26:26]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2747].EN[27:27]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2748].EN[28:28]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2749].EN[29:29]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2750].EN[30:30]	1
0xBB00D35C	OF_VLAN_AND_PORT_EN [2751].EN[31:31]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2752].EN[0:0]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2753].EN[1:1]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2754].EN[2:2]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2755].EN[3:3]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2756].EN[4:4]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2757].EN[5:5]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2758].EN[6:6]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2759].EN[7:7]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2760].EN[8:8]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2761].EN[9:9]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2762].EN[10:10]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2763].EN[11:11]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2764].EN[12:12]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2765].EN[13:13]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2766].EN[14:14]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2767].EN[15:15]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2768].EN[16:16]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2769].EN[17:17]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2770].EN[18:18]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2771].EN[19:19]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2772].EN[20:20]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2773].EN[21:21]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2774].EN[22:22]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2775].EN[23:23]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2776].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D360	OF_VLAN_AND_PORT_EN [2777].EN[25:25]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2778].EN[26:26]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2779].EN[27:27]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2780].EN[28:28]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2781].EN[29:29]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2782].EN[30:30]	1
0xBB00D360	OF_VLAN_AND_PORT_EN [2783].EN[31:31]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2784].EN[0:0]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2785].EN[1:1]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2786].EN[2:2]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2787].EN[3:3]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2788].EN[4:4]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2789].EN[5:5]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2790].EN[6:6]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2791].EN[7:7]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2792].EN[8:8]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2793].EN[9:9]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2794].EN[10:10]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2795].EN[11:11]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2796].EN[12:12]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2797].EN[13:13]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2798].EN[14:14]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2799].EN[15:15]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2800].EN[16:16]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2801].EN[17:17]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2802].EN[18:18]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2803].EN[19:19]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2804].EN[20:20]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2805].EN[21:21]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2806].EN[22:22]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2807].EN[23:23]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2808].EN[24:24]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2809].EN[25:25]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2810].EN[26:26]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2811].EN[27:27]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2812].EN[28:28]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2813].EN[29:29]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2814].EN[30:30]	1
0xBB00D364	OF_VLAN_AND_PORT_EN [2815].EN[31:31]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2816].EN[0:0]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2817].EN[1:1]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2818].EN[2:2]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2819].EN[3:3]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2820].EN[4:4]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2821].EN[5:5]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2822].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D368	OF_VLAN_AND_PORT_EN [2823].EN[7:7]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2824].EN[8:8]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2825].EN[9:9]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2826].EN[10:10]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2827].EN[11:11]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2828].EN[12:12]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2829].EN[13:13]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2830].EN[14:14]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2831].EN[15:15]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2832].EN[16:16]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2833].EN[17:17]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2834].EN[18:18]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2835].EN[19:19]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2836].EN[20:20]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2837].EN[21:21]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2838].EN[22:22]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2839].EN[23:23]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2840].EN[24:24]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2841].EN[25:25]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2842].EN[26:26]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2843].EN[27:27]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2844].EN[28:28]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2845].EN[29:29]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2846].EN[30:30]	1
0xBB00D368	OF_VLAN_AND_PORT_EN [2847].EN[31:31]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2848].EN[0:0]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2849].EN[1:1]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2850].EN[2:2]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2851].EN[3:3]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2852].EN[4:4]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2853].EN[5:5]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2854].EN[6:6]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2855].EN[7:7]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2856].EN[8:8]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2857].EN[9:9]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2858].EN[10:10]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2859].EN[11:11]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2860].EN[12:12]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2861].EN[13:13]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2862].EN[14:14]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2863].EN[15:15]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2864].EN[16:16]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2865].EN[17:17]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2866].EN[18:18]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2867].EN[19:19]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2868].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D36C	OF_VLAN_AND_PORT_EN [2869].EN[21:21]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2870].EN[22:22]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2871].EN[23:23]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2872].EN[24:24]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2873].EN[25:25]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2874].EN[26:26]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2875].EN[27:27]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2876].EN[28:28]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2877].EN[29:29]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2878].EN[30:30]	1
0xBB00D36C	OF_VLAN_AND_PORT_EN [2879].EN[31:31]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2880].EN[0:0]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2881].EN[1:1]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2882].EN[2:2]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2883].EN[3:3]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2884].EN[4:4]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2885].EN[5:5]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2886].EN[6:6]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2887].EN[7:7]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2888].EN[8:8]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2889].EN[9:9]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2890].EN[10:10]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2891].EN[11:11]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2892].EN[12:12]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2893].EN[13:13]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2894].EN[14:14]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2895].EN[15:15]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2896].EN[16:16]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2897].EN[17:17]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2898].EN[18:18]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2899].EN[19:19]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2900].EN[20:20]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2901].EN[21:21]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2902].EN[22:22]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2903].EN[23:23]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2904].EN[24:24]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2905].EN[25:25]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2906].EN[26:26]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2907].EN[27:27]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2908].EN[28:28]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2909].EN[29:29]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2910].EN[30:30]	1
0xBB00D370	OF_VLAN_AND_PORT_EN [2911].EN[31:31]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2912].EN[0:0]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2913].EN[1:1]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2914].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D374	OF_VLAN_AND_PORT_EN [2915].EN[3:3]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2916].EN[4:4]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2917].EN[5:5]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2918].EN[6:6]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2919].EN[7:7]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2920].EN[8:8]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2921].EN[9:9]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2922].EN[10:10]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2923].EN[11:11]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2924].EN[12:12]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2925].EN[13:13]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2926].EN[14:14]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2927].EN[15:15]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2928].EN[16:16]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2929].EN[17:17]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2930].EN[18:18]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2931].EN[19:19]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2932].EN[20:20]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2933].EN[21:21]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2934].EN[22:22]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2935].EN[23:23]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2936].EN[24:24]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2937].EN[25:25]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2938].EN[26:26]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2939].EN[27:27]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2940].EN[28:28]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2941].EN[29:29]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2942].EN[30:30]	1
0xBB00D374	OF_VLAN_AND_PORT_EN [2943].EN[31:31]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2944].EN[0:0]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2945].EN[1:1]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2946].EN[2:2]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2947].EN[3:3]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2948].EN[4:4]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2949].EN[5:5]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2950].EN[6:6]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2951].EN[7:7]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2952].EN[8:8]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2953].EN[9:9]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2954].EN[10:10]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2955].EN[11:11]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2956].EN[12:12]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2957].EN[13:13]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2958].EN[14:14]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2959].EN[15:15]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2960].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D378	OF_VLAN_AND_PORT_EN [2961].EN[17:17]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2962].EN[18:18]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2963].EN[19:19]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2964].EN[20:20]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2965].EN[21:21]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2966].EN[22:22]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2967].EN[23:23]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2968].EN[24:24]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2969].EN[25:25]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2970].EN[26:26]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2971].EN[27:27]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2972].EN[28:28]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2973].EN[29:29]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2974].EN[30:30]	1
0xBB00D378	OF_VLAN_AND_PORT_EN [2975].EN[31:31]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2976].EN[0:0]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2977].EN[1:1]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2978].EN[2:2]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2979].EN[3:3]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2980].EN[4:4]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2981].EN[5:5]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2982].EN[6:6]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2983].EN[7:7]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2984].EN[8:8]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2985].EN[9:9]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2986].EN[10:10]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2987].EN[11:11]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2988].EN[12:12]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2989].EN[13:13]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2990].EN[14:14]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2991].EN[15:15]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2992].EN[16:16]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2993].EN[17:17]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2994].EN[18:18]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2995].EN[19:19]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2996].EN[20:20]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2997].EN[21:21]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2998].EN[22:22]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [2999].EN[23:23]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3000].EN[24:24]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3001].EN[25:25]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3002].EN[26:26]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3003].EN[27:27]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3004].EN[28:28]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3005].EN[29:29]	1
0xBB00D37C	OF_VLAN_AND_PORT_EN [3006].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D37C	OF_VLAN_AND_PORT_EN [3007].EN[31:31]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3008].EN[0:0]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3009].EN[1:1]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3010].EN[2:2]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3011].EN[3:3]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3012].EN[4:4]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3013].EN[5:5]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3014].EN[6:6]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3015].EN[7:7]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3016].EN[8:8]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3017].EN[9:9]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3018].EN[10:10]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3019].EN[11:11]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3020].EN[12:12]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3021].EN[13:13]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3022].EN[14:14]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3023].EN[15:15]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3024].EN[16:16]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3025].EN[17:17]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3026].EN[18:18]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3027].EN[19:19]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3028].EN[20:20]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3029].EN[21:21]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3030].EN[22:22]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3031].EN[23:23]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3032].EN[24:24]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3033].EN[25:25]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3034].EN[26:26]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3035].EN[27:27]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3036].EN[28:28]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3037].EN[29:29]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3038].EN[30:30]	1
0xBB00D380	OF_VLAN_AND_PORT_EN [3039].EN[31:31]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3040].EN[0:0]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3041].EN[1:1]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3042].EN[2:2]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3043].EN[3:3]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3044].EN[4:4]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3045].EN[5:5]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3046].EN[6:6]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3047].EN[7:7]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3048].EN[8:8]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3049].EN[9:9]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3050].EN[10:10]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3051].EN[11:11]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3052].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D384	OF_VLAN_AND_PORT_EN [3053].EN[13:13]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3054].EN[14:14]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3055].EN[15:15]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3056].EN[16:16]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3057].EN[17:17]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3058].EN[18:18]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3059].EN[19:19]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3060].EN[20:20]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3061].EN[21:21]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3062].EN[22:22]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3063].EN[23:23]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3064].EN[24:24]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3065].EN[25:25]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3066].EN[26:26]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3067].EN[27:27]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3068].EN[28:28]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3069].EN[29:29]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3070].EN[30:30]	1
0xBB00D384	OF_VLAN_AND_PORT_EN [3071].EN[31:31]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3072].EN[0:0]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3073].EN[1:1]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3074].EN[2:2]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3075].EN[3:3]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3076].EN[4:4]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3077].EN[5:5]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3078].EN[6:6]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3079].EN[7:7]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3080].EN[8:8]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3081].EN[9:9]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3082].EN[10:10]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3083].EN[11:11]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3084].EN[12:12]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3085].EN[13:13]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3086].EN[14:14]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3087].EN[15:15]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3088].EN[16:16]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3089].EN[17:17]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3090].EN[18:18]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3091].EN[19:19]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3092].EN[20:20]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3093].EN[21:21]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3094].EN[22:22]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3095].EN[23:23]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3096].EN[24:24]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3097].EN[25:25]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3098].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D388	OF_VLAN_AND_PORT_EN [3099].EN[27:27]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3100].EN[28:28]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3101].EN[29:29]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3102].EN[30:30]	1
0xBB00D388	OF_VLAN_AND_PORT_EN [3103].EN[31:31]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3104].EN[0:0]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3105].EN[1:1]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3106].EN[2:2]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3107].EN[3:3]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3108].EN[4:4]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3109].EN[5:5]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3110].EN[6:6]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3111].EN[7:7]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3112].EN[8:8]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3113].EN[9:9]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3114].EN[10:10]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3115].EN[11:11]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3116].EN[12:12]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3117].EN[13:13]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3118].EN[14:14]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3119].EN[15:15]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3120].EN[16:16]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3121].EN[17:17]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3122].EN[18:18]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3123].EN[19:19]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3124].EN[20:20]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3125].EN[21:21]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3126].EN[22:22]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3127].EN[23:23]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3128].EN[24:24]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3129].EN[25:25]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3130].EN[26:26]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3131].EN[27:27]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3132].EN[28:28]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3133].EN[29:29]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3134].EN[30:30]	1
0xBB00D38C	OF_VLAN_AND_PORT_EN [3135].EN[31:31]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3136].EN[0:0]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3137].EN[1:1]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3138].EN[2:2]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3139].EN[3:3]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3140].EN[4:4]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3141].EN[5:5]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3142].EN[6:6]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3143].EN[7:7]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3144].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D390	OF_VLAN_AND_PORT_EN [3145].EN[9:9]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3146].EN[10:10]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3147].EN[11:11]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3148].EN[12:12]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3149].EN[13:13]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3150].EN[14:14]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3151].EN[15:15]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3152].EN[16:16]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3153].EN[17:17]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3154].EN[18:18]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3155].EN[19:19]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3156].EN[20:20]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3157].EN[21:21]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3158].EN[22:22]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3159].EN[23:23]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3160].EN[24:24]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3161].EN[25:25]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3162].EN[26:26]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3163].EN[27:27]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3164].EN[28:28]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3165].EN[29:29]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3166].EN[30:30]	1
0xBB00D390	OF_VLAN_AND_PORT_EN [3167].EN[31:31]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3168].EN[0:0]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3169].EN[1:1]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3170].EN[2:2]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3171].EN[3:3]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3172].EN[4:4]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3173].EN[5:5]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3174].EN[6:6]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3175].EN[7:7]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3176].EN[8:8]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3177].EN[9:9]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3178].EN[10:10]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3179].EN[11:11]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3180].EN[12:12]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3181].EN[13:13]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3182].EN[14:14]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3183].EN[15:15]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3184].EN[16:16]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3185].EN[17:17]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3186].EN[18:18]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3187].EN[19:19]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3188].EN[20:20]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3189].EN[21:21]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3190].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D394	OF_VLAN_AND_PORT_EN [3191].EN[23:23]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3192].EN[24:24]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3193].EN[25:25]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3194].EN[26:26]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3195].EN[27:27]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3196].EN[28:28]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3197].EN[29:29]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3198].EN[30:30]	1
0xBB00D394	OF_VLAN_AND_PORT_EN [3199].EN[31:31]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3200].EN[0:0]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3201].EN[1:1]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3202].EN[2:2]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3203].EN[3:3]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3204].EN[4:4]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3205].EN[5:5]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3206].EN[6:6]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3207].EN[7:7]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3208].EN[8:8]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3209].EN[9:9]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3210].EN[10:10]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3211].EN[11:11]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3212].EN[12:12]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3213].EN[13:13]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3214].EN[14:14]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3215].EN[15:15]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3216].EN[16:16]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3217].EN[17:17]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3218].EN[18:18]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3219].EN[19:19]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3220].EN[20:20]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3221].EN[21:21]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3222].EN[22:22]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3223].EN[23:23]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3224].EN[24:24]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3225].EN[25:25]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3226].EN[26:26]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3227].EN[27:27]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3228].EN[28:28]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3229].EN[29:29]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3230].EN[30:30]	1
0xBB00D398	OF_VLAN_AND_PORT_EN [3231].EN[31:31]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3232].EN[0:0]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3233].EN[1:1]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3234].EN[2:2]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3235].EN[3:3]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3236].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D39C	OF_VLAN_AND_PORT_EN [3237].EN[5:5]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3238].EN[6:6]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3239].EN[7:7]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3240].EN[8:8]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3241].EN[9:9]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3242].EN[10:10]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3243].EN[11:11]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3244].EN[12:12]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3245].EN[13:13]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3246].EN[14:14]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3247].EN[15:15]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3248].EN[16:16]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3249].EN[17:17]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3250].EN[18:18]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3251].EN[19:19]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3252].EN[20:20]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3253].EN[21:21]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3254].EN[22:22]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3255].EN[23:23]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3256].EN[24:24]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3257].EN[25:25]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3258].EN[26:26]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3259].EN[27:27]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3260].EN[28:28]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3261].EN[29:29]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3262].EN[30:30]	1
0xBB00D39C	OF_VLAN_AND_PORT_EN [3263].EN[31:31]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3264].EN[0:0]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3265].EN[1:1]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3266].EN[2:2]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3267].EN[3:3]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3268].EN[4:4]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3269].EN[5:5]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3270].EN[6:6]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3271].EN[7:7]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3272].EN[8:8]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3273].EN[9:9]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3274].EN[10:10]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3275].EN[11:11]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3276].EN[12:12]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3277].EN[13:13]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3278].EN[14:14]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3279].EN[15:15]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3280].EN[16:16]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3281].EN[17:17]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3282].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3283].EN[19:19]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3284].EN[20:20]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3285].EN[21:21]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3286].EN[22:22]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3287].EN[23:23]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3288].EN[24:24]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3289].EN[25:25]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3290].EN[26:26]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3291].EN[27:27]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3292].EN[28:28]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3293].EN[29:29]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3294].EN[30:30]	1
0xBB00D3A0	OF_VLAN_AND_PORT_EN [3295].EN[31:31]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3296].EN[0:0]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3297].EN[1:1]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3298].EN[2:2]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3299].EN[3:3]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3300].EN[4:4]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3301].EN[5:5]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3302].EN[6:6]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3303].EN[7:7]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3304].EN[8:8]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3305].EN[9:9]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3306].EN[10:10]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3307].EN[11:11]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3308].EN[12:12]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3309].EN[13:13]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3310].EN[14:14]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3311].EN[15:15]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3312].EN[16:16]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3313].EN[17:17]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3314].EN[18:18]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3315].EN[19:19]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3316].EN[20:20]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3317].EN[21:21]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3318].EN[22:22]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3319].EN[23:23]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3320].EN[24:24]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3321].EN[25:25]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3322].EN[26:26]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3323].EN[27:27]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3324].EN[28:28]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3325].EN[29:29]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3326].EN[30:30]	1
0xBB00D3A4	OF_VLAN_AND_PORT_EN [3327].EN[31:31]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3328].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3329].EN[1:1]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3330].EN[2:2]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3331].EN[3:3]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3332].EN[4:4]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3333].EN[5:5]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3334].EN[6:6]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3335].EN[7:7]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3336].EN[8:8]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3337].EN[9:9]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3338].EN[10:10]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3339].EN[11:11]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3340].EN[12:12]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3341].EN[13:13]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3342].EN[14:14]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3343].EN[15:15]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3344].EN[16:16]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3345].EN[17:17]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3346].EN[18:18]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3347].EN[19:19]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3348].EN[20:20]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3349].EN[21:21]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3350].EN[22:22]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3351].EN[23:23]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3352].EN[24:24]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3353].EN[25:25]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3354].EN[26:26]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3355].EN[27:27]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3356].EN[28:28]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3357].EN[29:29]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3358].EN[30:30]	1
0xBB00D3A8	OF_VLAN_AND_PORT_EN [3359].EN[31:31]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3360].EN[0:0]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3361].EN[1:1]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3362].EN[2:2]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3363].EN[3:3]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3364].EN[4:4]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3365].EN[5:5]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3366].EN[6:6]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3367].EN[7:7]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3368].EN[8:8]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3369].EN[9:9]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3370].EN[10:10]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3371].EN[11:11]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3372].EN[12:12]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3373].EN[13:13]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3374].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3375].EN[15:15]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3376].EN[16:16]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3377].EN[17:17]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3378].EN[18:18]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3379].EN[19:19]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3380].EN[20:20]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3381].EN[21:21]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3382].EN[22:22]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3383].EN[23:23]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3384].EN[24:24]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3385].EN[25:25]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3386].EN[26:26]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3387].EN[27:27]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3388].EN[28:28]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3389].EN[29:29]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3390].EN[30:30]	1
0xBB00D3AC	OF_VLAN_AND_PORT_EN [3391].EN[31:31]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3392].EN[0:0]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3393].EN[1:1]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3394].EN[2:2]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3395].EN[3:3]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3396].EN[4:4]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3397].EN[5:5]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3398].EN[6:6]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3399].EN[7:7]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3400].EN[8:8]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3401].EN[9:9]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3402].EN[10:10]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3403].EN[11:11]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3404].EN[12:12]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3405].EN[13:13]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3406].EN[14:14]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3407].EN[15:15]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3408].EN[16:16]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3409].EN[17:17]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3410].EN[18:18]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3411].EN[19:19]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3412].EN[20:20]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3413].EN[21:21]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3414].EN[22:22]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3415].EN[23:23]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3416].EN[24:24]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3417].EN[25:25]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3418].EN[26:26]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3419].EN[27:27]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3420].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3421].EN[29:29]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3422].EN[30:30]	1
0xBB00D3B0	OF_VLAN_AND_PORT_EN [3423].EN[31:31]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3424].EN[0:0]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3425].EN[1:1]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3426].EN[2:2]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3427].EN[3:3]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3428].EN[4:4]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3429].EN[5:5]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3430].EN[6:6]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3431].EN[7:7]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3432].EN[8:8]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3433].EN[9:9]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3434].EN[10:10]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3435].EN[11:11]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3436].EN[12:12]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3437].EN[13:13]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3438].EN[14:14]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3439].EN[15:15]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3440].EN[16:16]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3441].EN[17:17]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3442].EN[18:18]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3443].EN[19:19]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3444].EN[20:20]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3445].EN[21:21]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3446].EN[22:22]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3447].EN[23:23]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3448].EN[24:24]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3449].EN[25:25]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3450].EN[26:26]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3451].EN[27:27]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3452].EN[28:28]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3453].EN[29:29]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3454].EN[30:30]	1
0xBB00D3B4	OF_VLAN_AND_PORT_EN [3455].EN[31:31]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3456].EN[0:0]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3457].EN[1:1]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3458].EN[2:2]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3459].EN[3:3]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3460].EN[4:4]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3461].EN[5:5]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3462].EN[6:6]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3463].EN[7:7]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3464].EN[8:8]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3465].EN[9:9]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3466].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3467].EN[11:11]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3468].EN[12:12]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3469].EN[13:13]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3470].EN[14:14]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3471].EN[15:15]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3472].EN[16:16]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3473].EN[17:17]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3474].EN[18:18]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3475].EN[19:19]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3476].EN[20:20]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3477].EN[21:21]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3478].EN[22:22]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3479].EN[23:23]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3480].EN[24:24]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3481].EN[25:25]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3482].EN[26:26]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3483].EN[27:27]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3484].EN[28:28]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3485].EN[29:29]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3486].EN[30:30]	1
0xBB00D3B8	OF_VLAN_AND_PORT_EN [3487].EN[31:31]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3488].EN[0:0]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3489].EN[1:1]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3490].EN[2:2]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3491].EN[3:3]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3492].EN[4:4]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3493].EN[5:5]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3494].EN[6:6]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3495].EN[7:7]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3496].EN[8:8]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3497].EN[9:9]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3498].EN[10:10]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3499].EN[11:11]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3500].EN[12:12]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3501].EN[13:13]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3502].EN[14:14]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3503].EN[15:15]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3504].EN[16:16]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3505].EN[17:17]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3506].EN[18:18]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3507].EN[19:19]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3508].EN[20:20]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3509].EN[21:21]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3510].EN[22:22]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3511].EN[23:23]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3512].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3513].EN[25:25]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3514].EN[26:26]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3515].EN[27:27]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3516].EN[28:28]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3517].EN[29:29]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3518].EN[30:30]	1
0xBB00D3BC	OF_VLAN_AND_PORT_EN [3519].EN[31:31]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3520].EN[0:0]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3521].EN[1:1]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3522].EN[2:2]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3523].EN[3:3]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3524].EN[4:4]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3525].EN[5:5]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3526].EN[6:6]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3527].EN[7:7]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3528].EN[8:8]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3529].EN[9:9]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3530].EN[10:10]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3531].EN[11:11]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3532].EN[12:12]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3533].EN[13:13]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3534].EN[14:14]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3535].EN[15:15]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3536].EN[16:16]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3537].EN[17:17]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3538].EN[18:18]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3539].EN[19:19]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3540].EN[20:20]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3541].EN[21:21]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3542].EN[22:22]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3543].EN[23:23]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3544].EN[24:24]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3545].EN[25:25]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3546].EN[26:26]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3547].EN[27:27]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3548].EN[28:28]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3549].EN[29:29]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3550].EN[30:30]	1
0xBB00D3C0	OF_VLAN_AND_PORT_EN [3551].EN[31:31]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3552].EN[0:0]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3553].EN[1:1]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3554].EN[2:2]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3555].EN[3:3]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3556].EN[4:4]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3557].EN[5:5]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3558].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3559].EN[7:7]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3560].EN[8:8]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3561].EN[9:9]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3562].EN[10:10]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3563].EN[11:11]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3564].EN[12:12]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3565].EN[13:13]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3566].EN[14:14]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3567].EN[15:15]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3568].EN[16:16]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3569].EN[17:17]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3570].EN[18:18]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3571].EN[19:19]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3572].EN[20:20]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3573].EN[21:21]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3574].EN[22:22]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3575].EN[23:23]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3576].EN[24:24]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3577].EN[25:25]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3578].EN[26:26]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3579].EN[27:27]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3580].EN[28:28]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3581].EN[29:29]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3582].EN[30:30]	1
0xBB00D3C4	OF_VLAN_AND_PORT_EN [3583].EN[31:31]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3584].EN[0:0]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3585].EN[1:1]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3586].EN[2:2]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3587].EN[3:3]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3588].EN[4:4]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3589].EN[5:5]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3590].EN[6:6]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3591].EN[7:7]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3592].EN[8:8]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3593].EN[9:9]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3594].EN[10:10]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3595].EN[11:11]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3596].EN[12:12]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3597].EN[13:13]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3598].EN[14:14]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3599].EN[15:15]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3600].EN[16:16]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3601].EN[17:17]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3602].EN[18:18]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3603].EN[19:19]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3604].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3605].EN[21:21]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3606].EN[22:22]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3607].EN[23:23]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3608].EN[24:24]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3609].EN[25:25]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3610].EN[26:26]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3611].EN[27:27]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3612].EN[28:28]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3613].EN[29:29]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3614].EN[30:30]	1
0xBB00D3C8	OF_VLAN_AND_PORT_EN [3615].EN[31:31]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3616].EN[0:0]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3617].EN[1:1]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3618].EN[2:2]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3619].EN[3:3]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3620].EN[4:4]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3621].EN[5:5]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3622].EN[6:6]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3623].EN[7:7]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3624].EN[8:8]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3625].EN[9:9]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3626].EN[10:10]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3627].EN[11:11]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3628].EN[12:12]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3629].EN[13:13]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3630].EN[14:14]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3631].EN[15:15]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3632].EN[16:16]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3633].EN[17:17]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3634].EN[18:18]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3635].EN[19:19]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3636].EN[20:20]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3637].EN[21:21]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3638].EN[22:22]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3639].EN[23:23]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3640].EN[24:24]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3641].EN[25:25]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3642].EN[26:26]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3643].EN[27:27]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3644].EN[28:28]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3645].EN[29:29]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3646].EN[30:30]	1
0xBB00D3CC	OF_VLAN_AND_PORT_EN [3647].EN[31:31]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3648].EN[0:0]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3649].EN[1:1]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3650].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3651].EN[3:3]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3652].EN[4:4]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3653].EN[5:5]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3654].EN[6:6]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3655].EN[7:7]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3656].EN[8:8]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3657].EN[9:9]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3658].EN[10:10]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3659].EN[11:11]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3660].EN[12:12]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3661].EN[13:13]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3662].EN[14:14]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3663].EN[15:15]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3664].EN[16:16]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3665].EN[17:17]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3666].EN[18:18]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3667].EN[19:19]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3668].EN[20:20]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3669].EN[21:21]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3670].EN[22:22]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3671].EN[23:23]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3672].EN[24:24]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3673].EN[25:25]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3674].EN[26:26]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3675].EN[27:27]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3676].EN[28:28]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3677].EN[29:29]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3678].EN[30:30]	1
0xBB00D3D0	OF_VLAN_AND_PORT_EN [3679].EN[31:31]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3680].EN[0:0]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3681].EN[1:1]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3682].EN[2:2]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3683].EN[3:3]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3684].EN[4:4]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3685].EN[5:5]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3686].EN[6:6]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3687].EN[7:7]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3688].EN[8:8]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3689].EN[9:9]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3690].EN[10:10]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3691].EN[11:11]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3692].EN[12:12]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3693].EN[13:13]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3694].EN[14:14]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3695].EN[15:15]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3696].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3697].EN[17:17]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3698].EN[18:18]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3699].EN[19:19]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3700].EN[20:20]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3701].EN[21:21]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3702].EN[22:22]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3703].EN[23:23]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3704].EN[24:24]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3705].EN[25:25]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3706].EN[26:26]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3707].EN[27:27]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3708].EN[28:28]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3709].EN[29:29]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3710].EN[30:30]	1
0xBB00D3D4	OF_VLAN_AND_PORT_EN [3711].EN[31:31]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3712].EN[0:0]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3713].EN[1:1]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3714].EN[2:2]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3715].EN[3:3]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3716].EN[4:4]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3717].EN[5:5]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3718].EN[6:6]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3719].EN[7:7]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3720].EN[8:8]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3721].EN[9:9]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3722].EN[10:10]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3723].EN[11:11]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3724].EN[12:12]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3725].EN[13:13]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3726].EN[14:14]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3727].EN[15:15]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3728].EN[16:16]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3729].EN[17:17]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3730].EN[18:18]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3731].EN[19:19]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3732].EN[20:20]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3733].EN[21:21]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3734].EN[22:22]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3735].EN[23:23]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3736].EN[24:24]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3737].EN[25:25]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3738].EN[26:26]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3739].EN[27:27]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3740].EN[28:28]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3741].EN[29:29]	1
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3742].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3D8	OF_VLAN_AND_PORT_EN [3743].EN[31:31]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3744].EN[0:0]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3745].EN[1:1]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3746].EN[2:2]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3747].EN[3:3]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3748].EN[4:4]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3749].EN[5:5]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3750].EN[6:6]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3751].EN[7:7]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3752].EN[8:8]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3753].EN[9:9]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3754].EN[10:10]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3755].EN[11:11]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3756].EN[12:12]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3757].EN[13:13]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3758].EN[14:14]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3759].EN[15:15]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3760].EN[16:16]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3761].EN[17:17]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3762].EN[18:18]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3763].EN[19:19]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3764].EN[20:20]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3765].EN[21:21]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3766].EN[22:22]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3767].EN[23:23]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3768].EN[24:24]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3769].EN[25:25]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3770].EN[26:26]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3771].EN[27:27]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3772].EN[28:28]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3773].EN[29:29]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3774].EN[30:30]	1
0xBB00D3DC	OF_VLAN_AND_PORT_EN [3775].EN[31:31]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3776].EN[0:0]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3777].EN[1:1]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3778].EN[2:2]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3779].EN[3:3]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3780].EN[4:4]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3781].EN[5:5]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3782].EN[6:6]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3783].EN[7:7]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3784].EN[8:8]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3785].EN[9:9]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3786].EN[10:10]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3787].EN[11:11]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3788].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3789].EN[13:13]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3790].EN[14:14]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3791].EN[15:15]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3792].EN[16:16]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3793].EN[17:17]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3794].EN[18:18]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3795].EN[19:19]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3796].EN[20:20]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3797].EN[21:21]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3798].EN[22:22]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3799].EN[23:23]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3800].EN[24:24]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3801].EN[25:25]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3802].EN[26:26]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3803].EN[27:27]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3804].EN[28:28]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3805].EN[29:29]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3806].EN[30:30]	1
0xBB00D3E0	OF_VLAN_AND_PORT_EN [3807].EN[31:31]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3808].EN[0:0]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3809].EN[1:1]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3810].EN[2:2]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3811].EN[3:3]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3812].EN[4:4]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3813].EN[5:5]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3814].EN[6:6]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3815].EN[7:7]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3816].EN[8:8]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3817].EN[9:9]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3818].EN[10:10]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3819].EN[11:11]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3820].EN[12:12]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3821].EN[13:13]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3822].EN[14:14]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3823].EN[15:15]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3824].EN[16:16]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3825].EN[17:17]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3826].EN[18:18]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3827].EN[19:19]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3828].EN[20:20]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3829].EN[21:21]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3830].EN[22:22]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3831].EN[23:23]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3832].EN[24:24]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3833].EN[25:25]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3834].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3835].EN[27:27]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3836].EN[28:28]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3837].EN[29:29]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3838].EN[30:30]	1
0xBB00D3E4	OF_VLAN_AND_PORT_EN [3839].EN[31:31]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3840].EN[0:0]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3841].EN[1:1]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3842].EN[2:2]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3843].EN[3:3]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3844].EN[4:4]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3845].EN[5:5]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3846].EN[6:6]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3847].EN[7:7]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3848].EN[8:8]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3849].EN[9:9]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3850].EN[10:10]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3851].EN[11:11]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3852].EN[12:12]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3853].EN[13:13]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3854].EN[14:14]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3855].EN[15:15]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3856].EN[16:16]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3857].EN[17:17]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3858].EN[18:18]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3859].EN[19:19]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3860].EN[20:20]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3861].EN[21:21]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3862].EN[22:22]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3863].EN[23:23]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3864].EN[24:24]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3865].EN[25:25]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3866].EN[26:26]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3867].EN[27:27]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3868].EN[28:28]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3869].EN[29:29]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3870].EN[30:30]	1
0xBB00D3E8	OF_VLAN_AND_PORT_EN [3871].EN[31:31]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3872].EN[0:0]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3873].EN[1:1]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3874].EN[2:2]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3875].EN[3:3]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3876].EN[4:4]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3877].EN[5:5]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3878].EN[6:6]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3879].EN[7:7]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3880].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3881].EN[9:9]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3882].EN[10:10]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3883].EN[11:11]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3884].EN[12:12]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3885].EN[13:13]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3886].EN[14:14]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3887].EN[15:15]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3888].EN[16:16]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3889].EN[17:17]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3890].EN[18:18]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3891].EN[19:19]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3892].EN[20:20]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3893].EN[21:21]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3894].EN[22:22]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3895].EN[23:23]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3896].EN[24:24]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3897].EN[25:25]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3898].EN[26:26]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3899].EN[27:27]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3900].EN[28:28]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3901].EN[29:29]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3902].EN[30:30]	1
0xBB00D3EC	OF_VLAN_AND_PORT_EN [3903].EN[31:31]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3904].EN[0:0]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3905].EN[1:1]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3906].EN[2:2]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3907].EN[3:3]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3908].EN[4:4]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3909].EN[5:5]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3910].EN[6:6]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3911].EN[7:7]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3912].EN[8:8]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3913].EN[9:9]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3914].EN[10:10]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3915].EN[11:11]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3916].EN[12:12]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3917].EN[13:13]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3918].EN[14:14]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3919].EN[15:15]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3920].EN[16:16]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3921].EN[17:17]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3922].EN[18:18]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3923].EN[19:19]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3924].EN[20:20]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3925].EN[21:21]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3926].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3927].EN[23:23]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3928].EN[24:24]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3929].EN[25:25]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3930].EN[26:26]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3931].EN[27:27]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3932].EN[28:28]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3933].EN[29:29]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3934].EN[30:30]	1
0xBB00D3F0	OF_VLAN_AND_PORT_EN [3935].EN[31:31]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3936].EN[0:0]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3937].EN[1:1]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3938].EN[2:2]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3939].EN[3:3]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3940].EN[4:4]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3941].EN[5:5]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3942].EN[6:6]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3943].EN[7:7]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3944].EN[8:8]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3945].EN[9:9]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3946].EN[10:10]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3947].EN[11:11]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3948].EN[12:12]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3949].EN[13:13]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3950].EN[14:14]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3951].EN[15:15]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3952].EN[16:16]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3953].EN[17:17]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3954].EN[18:18]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3955].EN[19:19]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3956].EN[20:20]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3957].EN[21:21]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3958].EN[22:22]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3959].EN[23:23]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3960].EN[24:24]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3961].EN[25:25]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3962].EN[26:26]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3963].EN[27:27]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3964].EN[28:28]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3965].EN[29:29]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3966].EN[30:30]	1
0xBB00D3F4	OF_VLAN_AND_PORT_EN [3967].EN[31:31]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3968].EN[0:0]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3969].EN[1:1]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3970].EN[2:2]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3971].EN[3:3]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3972].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3973].EN[5:5]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3974].EN[6:6]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3975].EN[7:7]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3976].EN[8:8]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3977].EN[9:9]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3978].EN[10:10]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3979].EN[11:11]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3980].EN[12:12]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3981].EN[13:13]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3982].EN[14:14]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3983].EN[15:15]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3984].EN[16:16]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3985].EN[17:17]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3986].EN[18:18]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3987].EN[19:19]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3988].EN[20:20]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3989].EN[21:21]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3990].EN[22:22]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3991].EN[23:23]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3992].EN[24:24]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3993].EN[25:25]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3994].EN[26:26]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3995].EN[27:27]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3996].EN[28:28]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3997].EN[29:29]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3998].EN[30:30]	1
0xBB00D3F8	OF_VLAN_AND_PORT_EN [3999].EN[31:31]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4000].EN[0:0]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4001].EN[1:1]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4002].EN[2:2]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4003].EN[3:3]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4004].EN[4:4]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4005].EN[5:5]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4006].EN[6:6]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4007].EN[7:7]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4008].EN[8:8]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4009].EN[9:9]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4010].EN[10:10]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4011].EN[11:11]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4012].EN[12:12]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4013].EN[13:13]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4014].EN[14:14]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4015].EN[15:15]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4016].EN[16:16]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4017].EN[17:17]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4018].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4019].EN[19:19]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4020].EN[20:20]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4021].EN[21:21]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4022].EN[22:22]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4023].EN[23:23]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4024].EN[24:24]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4025].EN[25:25]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4026].EN[26:26]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4027].EN[27:27]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4028].EN[28:28]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4029].EN[29:29]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4030].EN[30:30]	1
0xBB00D3FC	OF_VLAN_AND_PORT_EN [4031].EN[31:31]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4032].EN[0:0]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4033].EN[1:1]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4034].EN[2:2]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4035].EN[3:3]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4036].EN[4:4]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4037].EN[5:5]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4038].EN[6:6]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4039].EN[7:7]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4040].EN[8:8]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4041].EN[9:9]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4042].EN[10:10]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4043].EN[11:11]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4044].EN[12:12]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4045].EN[13:13]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4046].EN[14:14]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4047].EN[15:15]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4048].EN[16:16]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4049].EN[17:17]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4050].EN[18:18]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4051].EN[19:19]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4052].EN[20:20]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4053].EN[21:21]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4054].EN[22:22]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4055].EN[23:23]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4056].EN[24:24]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4057].EN[25:25]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4058].EN[26:26]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4059].EN[27:27]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4060].EN[28:28]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4061].EN[29:29]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4062].EN[30:30]	1
0xBB00D400	OF_VLAN_AND_PORT_EN [4063].EN[31:31]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4064].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D404	OF_VLAN_AND_PORT_EN [4065].EN[1:1]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4066].EN[2:2]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4067].EN[3:3]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4068].EN[4:4]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4069].EN[5:5]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4070].EN[6:6]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4071].EN[7:7]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4072].EN[8:8]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4073].EN[9:9]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4074].EN[10:10]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4075].EN[11:11]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4076].EN[12:12]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4077].EN[13:13]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4078].EN[14:14]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4079].EN[15:15]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4080].EN[16:16]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4081].EN[17:17]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4082].EN[18:18]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4083].EN[19:19]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4084].EN[20:20]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4085].EN[21:21]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4086].EN[22:22]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4087].EN[23:23]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4088].EN[24:24]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4089].EN[25:25]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4090].EN[26:26]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4091].EN[27:27]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4092].EN[28:28]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4093].EN[29:29]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4094].EN[30:30]	1
0xBB00D404	OF_VLAN_AND_PORT_EN [4095].EN[31:31]	1
0xBB00D008	OF_VLAN_EN [0].EN[0:0]	1
0xBB00D008	OF_VLAN_EN [1].EN[1:1]	1
0xBB00D008	OF_VLAN_EN [2].EN[2:2]	1
0xBB00D008	OF_VLAN_EN [3].EN[3:3]	1
0xBB00D008	OF_VLAN_EN [4].EN[4:4]	1
0xBB00D008	OF_VLAN_EN [5].EN[5:5]	1
0xBB00D008	OF_VLAN_EN [6].EN[6:6]	1
0xBB00D008	OF_VLAN_EN [7].EN[7:7]	1
0xBB00D008	OF_VLAN_EN [8].EN[8:8]	1
0xBB00D008	OF_VLAN_EN [9].EN[9:9]	1
0xBB00D008	OF_VLAN_EN [10].EN[10:10]	1
0xBB00D008	OF_VLAN_EN [11].EN[11:11]	1
0xBB00D008	OF_VLAN_EN [12].EN[12:12]	1
0xBB00D008	OF_VLAN_EN [13].EN[13:13]	1
0xBB00D008	OF_VLAN_EN [14].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D008	OF_VLAN_EN [15].EN[15:15]	1
0xBB00D008	OF_VLAN_EN [16].EN[16:16]	1
0xBB00D008	OF_VLAN_EN [17].EN[17:17]	1
0xBB00D008	OF_VLAN_EN [18].EN[18:18]	1
0xBB00D008	OF_VLAN_EN [19].EN[19:19]	1
0xBB00D008	OF_VLAN_EN [20].EN[20:20]	1
0xBB00D008	OF_VLAN_EN [21].EN[21:21]	1
0xBB00D008	OF_VLAN_EN [22].EN[22:22]	1
0xBB00D008	OF_VLAN_EN [23].EN[23:23]	1
0xBB00D008	OF_VLAN_EN [24].EN[24:24]	1
0xBB00D008	OF_VLAN_EN [25].EN[25:25]	1
0xBB00D008	OF_VLAN_EN [26].EN[26:26]	1
0xBB00D008	OF_VLAN_EN [27].EN[27:27]	1
0xBB00D008	OF_VLAN_EN [28].EN[28:28]	1
0xBB00D008	OF_VLAN_EN [29].EN[29:29]	1
0xBB00D008	OF_VLAN_EN [30].EN[30:30]	1
0xBB00D008	OF_VLAN_EN [31].EN[31:31]	1
0xBB00D00C	OF_VLAN_EN [32].EN[0:0]	1
0xBB00D00C	OF_VLAN_EN [33].EN[1:1]	1
0xBB00D00C	OF_VLAN_EN [34].EN[2:2]	1
0xBB00D00C	OF_VLAN_EN [35].EN[3:3]	1
0xBB00D00C	OF_VLAN_EN [36].EN[4:4]	1
0xBB00D00C	OF_VLAN_EN [37].EN[5:5]	1
0xBB00D00C	OF_VLAN_EN [38].EN[6:6]	1
0xBB00D00C	OF_VLAN_EN [39].EN[7:7]	1
0xBB00D00C	OF_VLAN_EN [40].EN[8:8]	1
0xBB00D00C	OF_VLAN_EN [41].EN[9:9]	1
0xBB00D00C	OF_VLAN_EN [42].EN[10:10]	1
0xBB00D00C	OF_VLAN_EN [43].EN[11:11]	1
0xBB00D00C	OF_VLAN_EN [44].EN[12:12]	1
0xBB00D00C	OF_VLAN_EN [45].EN[13:13]	1
0xBB00D00C	OF_VLAN_EN [46].EN[14:14]	1
0xBB00D00C	OF_VLAN_EN [47].EN[15:15]	1
0xBB00D00C	OF_VLAN_EN [48].EN[16:16]	1
0xBB00D00C	OF_VLAN_EN [49].EN[17:17]	1
0xBB00D00C	OF_VLAN_EN [50].EN[18:18]	1
0xBB00D00C	OF_VLAN_EN [51].EN[19:19]	1
0xBB00D00C	OF_VLAN_EN [52].EN[20:20]	1
0xBB00D00C	OF_VLAN_EN [53].EN[21:21]	1
0xBB00D00C	OF_VLAN_EN [54].EN[22:22]	1
0xBB00D00C	OF_VLAN_EN [55].EN[23:23]	1
0xBB00D00C	OF_VLAN_EN [56].EN[24:24]	1
0xBB00D00C	OF_VLAN_EN [57].EN[25:25]	1
0xBB00D00C	OF_VLAN_EN [58].EN[26:26]	1
0xBB00D00C	OF_VLAN_EN [59].EN[27:27]	1
0xBB00D00C	OF_VLAN_EN [60].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D00C	OF_VLAN_EN [61].EN[29:29]	1
0xBB00D00C	OF_VLAN_EN [62].EN[30:30]	1
0xBB00D00C	OF_VLAN_EN [63].EN[31:31]	1
0xBB00D010	OF_VLAN_EN [64].EN[0:0]	1
0xBB00D010	OF_VLAN_EN [65].EN[1:1]	1
0xBB00D010	OF_VLAN_EN [66].EN[2:2]	1
0xBB00D010	OF_VLAN_EN [67].EN[3:3]	1
0xBB00D010	OF_VLAN_EN [68].EN[4:4]	1
0xBB00D010	OF_VLAN_EN [69].EN[5:5]	1
0xBB00D010	OF_VLAN_EN [70].EN[6:6]	1
0xBB00D010	OF_VLAN_EN [71].EN[7:7]	1
0xBB00D010	OF_VLAN_EN [72].EN[8:8]	1
0xBB00D010	OF_VLAN_EN [73].EN[9:9]	1
0xBB00D010	OF_VLAN_EN [74].EN[10:10]	1
0xBB00D010	OF_VLAN_EN [75].EN[11:11]	1
0xBB00D010	OF_VLAN_EN [76].EN[12:12]	1
0xBB00D010	OF_VLAN_EN [77].EN[13:13]	1
0xBB00D010	OF_VLAN_EN [78].EN[14:14]	1
0xBB00D010	OF_VLAN_EN [79].EN[15:15]	1
0xBB00D010	OF_VLAN_EN [80].EN[16:16]	1
0xBB00D010	OF_VLAN_EN [81].EN[17:17]	1
0xBB00D010	OF_VLAN_EN [82].EN[18:18]	1
0xBB00D010	OF_VLAN_EN [83].EN[19:19]	1
0xBB00D010	OF_VLAN_EN [84].EN[20:20]	1
0xBB00D010	OF_VLAN_EN [85].EN[21:21]	1
0xBB00D010	OF_VLAN_EN [86].EN[22:22]	1
0xBB00D010	OF_VLAN_EN [87].EN[23:23]	1
0xBB00D010	OF_VLAN_EN [88].EN[24:24]	1
0xBB00D010	OF_VLAN_EN [89].EN[25:25]	1
0xBB00D010	OF_VLAN_EN [90].EN[26:26]	1
0xBB00D010	OF_VLAN_EN [91].EN[27:27]	1
0xBB00D010	OF_VLAN_EN [92].EN[28:28]	1
0xBB00D010	OF_VLAN_EN [93].EN[29:29]	1
0xBB00D010	OF_VLAN_EN [94].EN[30:30]	1
0xBB00D010	OF_VLAN_EN [95].EN[31:31]	1
0xBB00D014	OF_VLAN_EN [96].EN[0:0]	1
0xBB00D014	OF_VLAN_EN [97].EN[1:1]	1
0xBB00D014	OF_VLAN_EN [98].EN[2:2]	1
0xBB00D014	OF_VLAN_EN [99].EN[3:3]	1
0xBB00D014	OF_VLAN_EN [100].EN[4:4]	1
0xBB00D014	OF_VLAN_EN [101].EN[5:5]	1
0xBB00D014	OF_VLAN_EN [102].EN[6:6]	1
0xBB00D014	OF_VLAN_EN [103].EN[7:7]	1
0xBB00D014	OF_VLAN_EN [104].EN[8:8]	1
0xBB00D014	OF_VLAN_EN [105].EN[9:9]	1
0xBB00D014	OF_VLAN_EN [106].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D014	OF_VLAN_EN [107].EN[11:11]	1
0xBB00D014	OF_VLAN_EN [108].EN[12:12]	1
0xBB00D014	OF_VLAN_EN [109].EN[13:13]	1
0xBB00D014	OF_VLAN_EN [110].EN[14:14]	1
0xBB00D014	OF_VLAN_EN [111].EN[15:15]	1
0xBB00D014	OF_VLAN_EN [112].EN[16:16]	1
0xBB00D014	OF_VLAN_EN [113].EN[17:17]	1
0xBB00D014	OF_VLAN_EN [114].EN[18:18]	1
0xBB00D014	OF_VLAN_EN [115].EN[19:19]	1
0xBB00D014	OF_VLAN_EN [116].EN[20:20]	1
0xBB00D014	OF_VLAN_EN [117].EN[21:21]	1
0xBB00D014	OF_VLAN_EN [118].EN[22:22]	1
0xBB00D014	OF_VLAN_EN [119].EN[23:23]	1
0xBB00D014	OF_VLAN_EN [120].EN[24:24]	1
0xBB00D014	OF_VLAN_EN [121].EN[25:25]	1
0xBB00D014	OF_VLAN_EN [122].EN[26:26]	1
0xBB00D014	OF_VLAN_EN [123].EN[27:27]	1
0xBB00D014	OF_VLAN_EN [124].EN[28:28]	1
0xBB00D014	OF_VLAN_EN [125].EN[29:29]	1
0xBB00D014	OF_VLAN_EN [126].EN[30:30]	1
0xBB00D014	OF_VLAN_EN [127].EN[31:31]	1
0xBB00D018	OF_VLAN_EN [128].EN[0:0]	1
0xBB00D018	OF_VLAN_EN [129].EN[1:1]	1
0xBB00D018	OF_VLAN_EN [130].EN[2:2]	1
0xBB00D018	OF_VLAN_EN [131].EN[3:3]	1
0xBB00D018	OF_VLAN_EN [132].EN[4:4]	1
0xBB00D018	OF_VLAN_EN [133].EN[5:5]	1
0xBB00D018	OF_VLAN_EN [134].EN[6:6]	1
0xBB00D018	OF_VLAN_EN [135].EN[7:7]	1
0xBB00D018	OF_VLAN_EN [136].EN[8:8]	1
0xBB00D018	OF_VLAN_EN [137].EN[9:9]	1
0xBB00D018	OF_VLAN_EN [138].EN[10:10]	1
0xBB00D018	OF_VLAN_EN [139].EN[11:11]	1
0xBB00D018	OF_VLAN_EN [140].EN[12:12]	1
0xBB00D018	OF_VLAN_EN [141].EN[13:13]	1
0xBB00D018	OF_VLAN_EN [142].EN[14:14]	1
0xBB00D018	OF_VLAN_EN [143].EN[15:15]	1
0xBB00D018	OF_VLAN_EN [144].EN[16:16]	1
0xBB00D018	OF_VLAN_EN [145].EN[17:17]	1
0xBB00D018	OF_VLAN_EN [146].EN[18:18]	1
0xBB00D018	OF_VLAN_EN [147].EN[19:19]	1
0xBB00D018	OF_VLAN_EN [148].EN[20:20]	1
0xBB00D018	OF_VLAN_EN [149].EN[21:21]	1
0xBB00D018	OF_VLAN_EN [150].EN[22:22]	1
0xBB00D018	OF_VLAN_EN [151].EN[23:23]	1
0xBB00D018	OF_VLAN_EN [152].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D018	OF_VLAN_EN [153].EN[25:25]	1
0xBB00D018	OF_VLAN_EN [154].EN[26:26]	1
0xBB00D018	OF_VLAN_EN [155].EN[27:27]	1
0xBB00D018	OF_VLAN_EN [156].EN[28:28]	1
0xBB00D018	OF_VLAN_EN [157].EN[29:29]	1
0xBB00D018	OF_VLAN_EN [158].EN[30:30]	1
0xBB00D018	OF_VLAN_EN [159].EN[31:31]	1
0xBB00D01C	OF_VLAN_EN [160].EN[0:0]	1
0xBB00D01C	OF_VLAN_EN [161].EN[1:1]	1
0xBB00D01C	OF_VLAN_EN [162].EN[2:2]	1
0xBB00D01C	OF_VLAN_EN [163].EN[3:3]	1
0xBB00D01C	OF_VLAN_EN [164].EN[4:4]	1
0xBB00D01C	OF_VLAN_EN [165].EN[5:5]	1
0xBB00D01C	OF_VLAN_EN [166].EN[6:6]	1
0xBB00D01C	OF_VLAN_EN [167].EN[7:7]	1
0xBB00D01C	OF_VLAN_EN [168].EN[8:8]	1
0xBB00D01C	OF_VLAN_EN [169].EN[9:9]	1
0xBB00D01C	OF_VLAN_EN [170].EN[10:10]	1
0xBB00D01C	OF_VLAN_EN [171].EN[11:11]	1
0xBB00D01C	OF_VLAN_EN [172].EN[12:12]	1
0xBB00D01C	OF_VLAN_EN [173].EN[13:13]	1
0xBB00D01C	OF_VLAN_EN [174].EN[14:14]	1
0xBB00D01C	OF_VLAN_EN [175].EN[15:15]	1
0xBB00D01C	OF_VLAN_EN [176].EN[16:16]	1
0xBB00D01C	OF_VLAN_EN [177].EN[17:17]	1
0xBB00D01C	OF_VLAN_EN [178].EN[18:18]	1
0xBB00D01C	OF_VLAN_EN [179].EN[19:19]	1
0xBB00D01C	OF_VLAN_EN [180].EN[20:20]	1
0xBB00D01C	OF_VLAN_EN [181].EN[21:21]	1
0xBB00D01C	OF_VLAN_EN [182].EN[22:22]	1
0xBB00D01C	OF_VLAN_EN [183].EN[23:23]	1
0xBB00D01C	OF_VLAN_EN [184].EN[24:24]	1
0xBB00D01C	OF_VLAN_EN [185].EN[25:25]	1
0xBB00D01C	OF_VLAN_EN [186].EN[26:26]	1
0xBB00D01C	OF_VLAN_EN [187].EN[27:27]	1
0xBB00D01C	OF_VLAN_EN [188].EN[28:28]	1
0xBB00D01C	OF_VLAN_EN [189].EN[29:29]	1
0xBB00D01C	OF_VLAN_EN [190].EN[30:30]	1
0xBB00D01C	OF_VLAN_EN [191].EN[31:31]	1
0xBB00D020	OF_VLAN_EN [192].EN[0:0]	1
0xBB00D020	OF_VLAN_EN [193].EN[1:1]	1
0xBB00D020	OF_VLAN_EN [194].EN[2:2]	1
0xBB00D020	OF_VLAN_EN [195].EN[3:3]	1
0xBB00D020	OF_VLAN_EN [196].EN[4:4]	1
0xBB00D020	OF_VLAN_EN [197].EN[5:5]	1
0xBB00D020	OF_VLAN_EN [198].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D020	OF_VLAN_EN [199].EN[7:7]	1
0xBB00D020	OF_VLAN_EN [200].EN[8:8]	1
0xBB00D020	OF_VLAN_EN [201].EN[9:9]	1
0xBB00D020	OF_VLAN_EN [202].EN[10:10]	1
0xBB00D020	OF_VLAN_EN [203].EN[11:11]	1
0xBB00D020	OF_VLAN_EN [204].EN[12:12]	1
0xBB00D020	OF_VLAN_EN [205].EN[13:13]	1
0xBB00D020	OF_VLAN_EN [206].EN[14:14]	1
0xBB00D020	OF_VLAN_EN [207].EN[15:15]	1
0xBB00D020	OF_VLAN_EN [208].EN[16:16]	1
0xBB00D020	OF_VLAN_EN [209].EN[17:17]	1
0xBB00D020	OF_VLAN_EN [210].EN[18:18]	1
0xBB00D020	OF_VLAN_EN [211].EN[19:19]	1
0xBB00D020	OF_VLAN_EN [212].EN[20:20]	1
0xBB00D020	OF_VLAN_EN [213].EN[21:21]	1
0xBB00D020	OF_VLAN_EN [214].EN[22:22]	1
0xBB00D020	OF_VLAN_EN [215].EN[23:23]	1
0xBB00D020	OF_VLAN_EN [216].EN[24:24]	1
0xBB00D020	OF_VLAN_EN [217].EN[25:25]	1
0xBB00D020	OF_VLAN_EN [218].EN[26:26]	1
0xBB00D020	OF_VLAN_EN [219].EN[27:27]	1
0xBB00D020	OF_VLAN_EN [220].EN[28:28]	1
0xBB00D020	OF_VLAN_EN [221].EN[29:29]	1
0xBB00D020	OF_VLAN_EN [222].EN[30:30]	1
0xBB00D020	OF_VLAN_EN [223].EN[31:31]	1
0xBB00D024	OF_VLAN_EN [224].EN[0:0]	1
0xBB00D024	OF_VLAN_EN [225].EN[1:1]	1
0xBB00D024	OF_VLAN_EN [226].EN[2:2]	1
0xBB00D024	OF_VLAN_EN [227].EN[3:3]	1
0xBB00D024	OF_VLAN_EN [228].EN[4:4]	1
0xBB00D024	OF_VLAN_EN [229].EN[5:5]	1
0xBB00D024	OF_VLAN_EN [230].EN[6:6]	1
0xBB00D024	OF_VLAN_EN [231].EN[7:7]	1
0xBB00D024	OF_VLAN_EN [232].EN[8:8]	1
0xBB00D024	OF_VLAN_EN [233].EN[9:9]	1
0xBB00D024	OF_VLAN_EN [234].EN[10:10]	1
0xBB00D024	OF_VLAN_EN [235].EN[11:11]	1
0xBB00D024	OF_VLAN_EN [236].EN[12:12]	1
0xBB00D024	OF_VLAN_EN [237].EN[13:13]	1
0xBB00D024	OF_VLAN_EN [238].EN[14:14]	1
0xBB00D024	OF_VLAN_EN [239].EN[15:15]	1
0xBB00D024	OF_VLAN_EN [240].EN[16:16]	1
0xBB00D024	OF_VLAN_EN [241].EN[17:17]	1
0xBB00D024	OF_VLAN_EN [242].EN[18:18]	1
0xBB00D024	OF_VLAN_EN [243].EN[19:19]	1
0xBB00D024	OF_VLAN_EN [244].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D024	OF_VLAN_EN [245].EN[21:21]	1
0xBB00D024	OF_VLAN_EN [246].EN[22:22]	1
0xBB00D024	OF_VLAN_EN [247].EN[23:23]	1
0xBB00D024	OF_VLAN_EN [248].EN[24:24]	1
0xBB00D024	OF_VLAN_EN [249].EN[25:25]	1
0xBB00D024	OF_VLAN_EN [250].EN[26:26]	1
0xBB00D024	OF_VLAN_EN [251].EN[27:27]	1
0xBB00D024	OF_VLAN_EN [252].EN[28:28]	1
0xBB00D024	OF_VLAN_EN [253].EN[29:29]	1
0xBB00D024	OF_VLAN_EN [254].EN[30:30]	1
0xBB00D024	OF_VLAN_EN [255].EN[31:31]	1
0xBB00D028	OF_VLAN_EN [256].EN[0:0]	1
0xBB00D028	OF_VLAN_EN [257].EN[1:1]	1
0xBB00D028	OF_VLAN_EN [258].EN[2:2]	1
0xBB00D028	OF_VLAN_EN [259].EN[3:3]	1
0xBB00D028	OF_VLAN_EN [260].EN[4:4]	1
0xBB00D028	OF_VLAN_EN [261].EN[5:5]	1
0xBB00D028	OF_VLAN_EN [262].EN[6:6]	1
0xBB00D028	OF_VLAN_EN [263].EN[7:7]	1
0xBB00D028	OF_VLAN_EN [264].EN[8:8]	1
0xBB00D028	OF_VLAN_EN [265].EN[9:9]	1
0xBB00D028	OF_VLAN_EN [266].EN[10:10]	1
0xBB00D028	OF_VLAN_EN [267].EN[11:11]	1
0xBB00D028	OF_VLAN_EN [268].EN[12:12]	1
0xBB00D028	OF_VLAN_EN [269].EN[13:13]	1
0xBB00D028	OF_VLAN_EN [270].EN[14:14]	1
0xBB00D028	OF_VLAN_EN [271].EN[15:15]	1
0xBB00D028	OF_VLAN_EN [272].EN[16:16]	1
0xBB00D028	OF_VLAN_EN [273].EN[17:17]	1
0xBB00D028	OF_VLAN_EN [274].EN[18:18]	1
0xBB00D028	OF_VLAN_EN [275].EN[19:19]	1
0xBB00D028	OF_VLAN_EN [276].EN[20:20]	1
0xBB00D028	OF_VLAN_EN [277].EN[21:21]	1
0xBB00D028	OF_VLAN_EN [278].EN[22:22]	1
0xBB00D028	OF_VLAN_EN [279].EN[23:23]	1
0xBB00D028	OF_VLAN_EN [280].EN[24:24]	1
0xBB00D028	OF_VLAN_EN [281].EN[25:25]	1
0xBB00D028	OF_VLAN_EN [282].EN[26:26]	1
0xBB00D028	OF_VLAN_EN [283].EN[27:27]	1
0xBB00D028	OF_VLAN_EN [284].EN[28:28]	1
0xBB00D028	OF_VLAN_EN [285].EN[29:29]	1
0xBB00D028	OF_VLAN_EN [286].EN[30:30]	1
0xBB00D028	OF_VLAN_EN [287].EN[31:31]	1
0xBB00D02C	OF_VLAN_EN [288].EN[0:0]	1
0xBB00D02C	OF_VLAN_EN [289].EN[1:1]	1
0xBB00D02C	OF_VLAN_EN [290].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D02C	OF_VLAN_EN [291].EN[3:3]	1
0xBB00D02C	OF_VLAN_EN [292].EN[4:4]	1
0xBB00D02C	OF_VLAN_EN [293].EN[5:5]	1
0xBB00D02C	OF_VLAN_EN [294].EN[6:6]	1
0xBB00D02C	OF_VLAN_EN [295].EN[7:7]	1
0xBB00D02C	OF_VLAN_EN [296].EN[8:8]	1
0xBB00D02C	OF_VLAN_EN [297].EN[9:9]	1
0xBB00D02C	OF_VLAN_EN [298].EN[10:10]	1
0xBB00D02C	OF_VLAN_EN [299].EN[11:11]	1
0xBB00D02C	OF_VLAN_EN [300].EN[12:12]	1
0xBB00D02C	OF_VLAN_EN [301].EN[13:13]	1
0xBB00D02C	OF_VLAN_EN [302].EN[14:14]	1
0xBB00D02C	OF_VLAN_EN [303].EN[15:15]	1
0xBB00D02C	OF_VLAN_EN [304].EN[16:16]	1
0xBB00D02C	OF_VLAN_EN [305].EN[17:17]	1
0xBB00D02C	OF_VLAN_EN [306].EN[18:18]	1
0xBB00D02C	OF_VLAN_EN [307].EN[19:19]	1
0xBB00D02C	OF_VLAN_EN [308].EN[20:20]	1
0xBB00D02C	OF_VLAN_EN [309].EN[21:21]	1
0xBB00D02C	OF_VLAN_EN [310].EN[22:22]	1
0xBB00D02C	OF_VLAN_EN [311].EN[23:23]	1
0xBB00D02C	OF_VLAN_EN [312].EN[24:24]	1
0xBB00D02C	OF_VLAN_EN [313].EN[25:25]	1
0xBB00D02C	OF_VLAN_EN [314].EN[26:26]	1
0xBB00D02C	OF_VLAN_EN [315].EN[27:27]	1
0xBB00D02C	OF_VLAN_EN [316].EN[28:28]	1
0xBB00D02C	OF_VLAN_EN [317].EN[29:29]	1
0xBB00D02C	OF_VLAN_EN [318].EN[30:30]	1
0xBB00D02C	OF_VLAN_EN [319].EN[31:31]	1
0xBB00D030	OF_VLAN_EN [320].EN[0:0]	1
0xBB00D030	OF_VLAN_EN [321].EN[1:1]	1
0xBB00D030	OF_VLAN_EN [322].EN[2:2]	1
0xBB00D030	OF_VLAN_EN [323].EN[3:3]	1
0xBB00D030	OF_VLAN_EN [324].EN[4:4]	1
0xBB00D030	OF_VLAN_EN [325].EN[5:5]	1
0xBB00D030	OF_VLAN_EN [326].EN[6:6]	1
0xBB00D030	OF_VLAN_EN [327].EN[7:7]	1
0xBB00D030	OF_VLAN_EN [328].EN[8:8]	1
0xBB00D030	OF_VLAN_EN [329].EN[9:9]	1
0xBB00D030	OF_VLAN_EN [330].EN[10:10]	1
0xBB00D030	OF_VLAN_EN [331].EN[11:11]	1
0xBB00D030	OF_VLAN_EN [332].EN[12:12]	1
0xBB00D030	OF_VLAN_EN [333].EN[13:13]	1
0xBB00D030	OF_VLAN_EN [334].EN[14:14]	1
0xBB00D030	OF_VLAN_EN [335].EN[15:15]	1
0xBB00D030	OF_VLAN_EN [336].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D030	OF_VLAN_EN [337].EN[17:17]	1
0xBB00D030	OF_VLAN_EN [338].EN[18:18]	1
0xBB00D030	OF_VLAN_EN [339].EN[19:19]	1
0xBB00D030	OF_VLAN_EN [340].EN[20:20]	1
0xBB00D030	OF_VLAN_EN [341].EN[21:21]	1
0xBB00D030	OF_VLAN_EN [342].EN[22:22]	1
0xBB00D030	OF_VLAN_EN [343].EN[23:23]	1
0xBB00D030	OF_VLAN_EN [344].EN[24:24]	1
0xBB00D030	OF_VLAN_EN [345].EN[25:25]	1
0xBB00D030	OF_VLAN_EN [346].EN[26:26]	1
0xBB00D030	OF_VLAN_EN [347].EN[27:27]	1
0xBB00D030	OF_VLAN_EN [348].EN[28:28]	1
0xBB00D030	OF_VLAN_EN [349].EN[29:29]	1
0xBB00D030	OF_VLAN_EN [350].EN[30:30]	1
0xBB00D030	OF_VLAN_EN [351].EN[31:31]	1
0xBB00D034	OF_VLAN_EN [352].EN[0:0]	1
0xBB00D034	OF_VLAN_EN [353].EN[1:1]	1
0xBB00D034	OF_VLAN_EN [354].EN[2:2]	1
0xBB00D034	OF_VLAN_EN [355].EN[3:3]	1
0xBB00D034	OF_VLAN_EN [356].EN[4:4]	1
0xBB00D034	OF_VLAN_EN [357].EN[5:5]	1
0xBB00D034	OF_VLAN_EN [358].EN[6:6]	1
0xBB00D034	OF_VLAN_EN [359].EN[7:7]	1
0xBB00D034	OF_VLAN_EN [360].EN[8:8]	1
0xBB00D034	OF_VLAN_EN [361].EN[9:9]	1
0xBB00D034	OF_VLAN_EN [362].EN[10:10]	1
0xBB00D034	OF_VLAN_EN [363].EN[11:11]	1
0xBB00D034	OF_VLAN_EN [364].EN[12:12]	1
0xBB00D034	OF_VLAN_EN [365].EN[13:13]	1
0xBB00D034	OF_VLAN_EN [366].EN[14:14]	1
0xBB00D034	OF_VLAN_EN [367].EN[15:15]	1
0xBB00D034	OF_VLAN_EN [368].EN[16:16]	1
0xBB00D034	OF_VLAN_EN [369].EN[17:17]	1
0xBB00D034	OF_VLAN_EN [370].EN[18:18]	1
0xBB00D034	OF_VLAN_EN [371].EN[19:19]	1
0xBB00D034	OF_VLAN_EN [372].EN[20:20]	1
0xBB00D034	OF_VLAN_EN [373].EN[21:21]	1
0xBB00D034	OF_VLAN_EN [374].EN[22:22]	1
0xBB00D034	OF_VLAN_EN [375].EN[23:23]	1
0xBB00D034	OF_VLAN_EN [376].EN[24:24]	1
0xBB00D034	OF_VLAN_EN [377].EN[25:25]	1
0xBB00D034	OF_VLAN_EN [378].EN[26:26]	1
0xBB00D034	OF_VLAN_EN [379].EN[27:27]	1
0xBB00D034	OF_VLAN_EN [380].EN[28:28]	1
0xBB00D034	OF_VLAN_EN [381].EN[29:29]	1
0xBB00D034	OF_VLAN_EN [382].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D034	OF_VLAN_EN [383].EN[31:31]	1
0xBB00D038	OF_VLAN_EN [384].EN[0:0]	1
0xBB00D038	OF_VLAN_EN [385].EN[1:1]	1
0xBB00D038	OF_VLAN_EN [386].EN[2:2]	1
0xBB00D038	OF_VLAN_EN [387].EN[3:3]	1
0xBB00D038	OF_VLAN_EN [388].EN[4:4]	1
0xBB00D038	OF_VLAN_EN [389].EN[5:5]	1
0xBB00D038	OF_VLAN_EN [390].EN[6:6]	1
0xBB00D038	OF_VLAN_EN [391].EN[7:7]	1
0xBB00D038	OF_VLAN_EN [392].EN[8:8]	1
0xBB00D038	OF_VLAN_EN [393].EN[9:9]	1
0xBB00D038	OF_VLAN_EN [394].EN[10:10]	1
0xBB00D038	OF_VLAN_EN [395].EN[11:11]	1
0xBB00D038	OF_VLAN_EN [396].EN[12:12]	1
0xBB00D038	OF_VLAN_EN [397].EN[13:13]	1
0xBB00D038	OF_VLAN_EN [398].EN[14:14]	1
0xBB00D038	OF_VLAN_EN [399].EN[15:15]	1
0xBB00D038	OF_VLAN_EN [400].EN[16:16]	1
0xBB00D038	OF_VLAN_EN [401].EN[17:17]	1
0xBB00D038	OF_VLAN_EN [402].EN[18:18]	1
0xBB00D038	OF_VLAN_EN [403].EN[19:19]	1
0xBB00D038	OF_VLAN_EN [404].EN[20:20]	1
0xBB00D038	OF_VLAN_EN [405].EN[21:21]	1
0xBB00D038	OF_VLAN_EN [406].EN[22:22]	1
0xBB00D038	OF_VLAN_EN [407].EN[23:23]	1
0xBB00D038	OF_VLAN_EN [408].EN[24:24]	1
0xBB00D038	OF_VLAN_EN [409].EN[25:25]	1
0xBB00D038	OF_VLAN_EN [410].EN[26:26]	1
0xBB00D038	OF_VLAN_EN [411].EN[27:27]	1
0xBB00D038	OF_VLAN_EN [412].EN[28:28]	1
0xBB00D038	OF_VLAN_EN [413].EN[29:29]	1
0xBB00D038	OF_VLAN_EN [414].EN[30:30]	1
0xBB00D038	OF_VLAN_EN [415].EN[31:31]	1
0xBB00D03C	OF_VLAN_EN [416].EN[0:0]	1
0xBB00D03C	OF_VLAN_EN [417].EN[1:1]	1
0xBB00D03C	OF_VLAN_EN [418].EN[2:2]	1
0xBB00D03C	OF_VLAN_EN [419].EN[3:3]	1
0xBB00D03C	OF_VLAN_EN [420].EN[4:4]	1
0xBB00D03C	OF_VLAN_EN [421].EN[5:5]	1
0xBB00D03C	OF_VLAN_EN [422].EN[6:6]	1
0xBB00D03C	OF_VLAN_EN [423].EN[7:7]	1
0xBB00D03C	OF_VLAN_EN [424].EN[8:8]	1
0xBB00D03C	OF_VLAN_EN [425].EN[9:9]	1
0xBB00D03C	OF_VLAN_EN [426].EN[10:10]	1
0xBB00D03C	OF_VLAN_EN [427].EN[11:11]	1
0xBB00D03C	OF_VLAN_EN [428].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D03C	OF_VLAN_EN [429].EN[13:13]	1
0xBB00D03C	OF_VLAN_EN [430].EN[14:14]	1
0xBB00D03C	OF_VLAN_EN [431].EN[15:15]	1
0xBB00D03C	OF_VLAN_EN [432].EN[16:16]	1
0xBB00D03C	OF_VLAN_EN [433].EN[17:17]	1
0xBB00D03C	OF_VLAN_EN [434].EN[18:18]	1
0xBB00D03C	OF_VLAN_EN [435].EN[19:19]	1
0xBB00D03C	OF_VLAN_EN [436].EN[20:20]	1
0xBB00D03C	OF_VLAN_EN [437].EN[21:21]	1
0xBB00D03C	OF_VLAN_EN [438].EN[22:22]	1
0xBB00D03C	OF_VLAN_EN [439].EN[23:23]	1
0xBB00D03C	OF_VLAN_EN [440].EN[24:24]	1
0xBB00D03C	OF_VLAN_EN [441].EN[25:25]	1
0xBB00D03C	OF_VLAN_EN [442].EN[26:26]	1
0xBB00D03C	OF_VLAN_EN [443].EN[27:27]	1
0xBB00D03C	OF_VLAN_EN [444].EN[28:28]	1
0xBB00D03C	OF_VLAN_EN [445].EN[29:29]	1
0xBB00D03C	OF_VLAN_EN [446].EN[30:30]	1
0xBB00D03C	OF_VLAN_EN [447].EN[31:31]	1
0xBB00D040	OF_VLAN_EN [448].EN[0:0]	1
0xBB00D040	OF_VLAN_EN [449].EN[1:1]	1
0xBB00D040	OF_VLAN_EN [450].EN[2:2]	1
0xBB00D040	OF_VLAN_EN [451].EN[3:3]	1
0xBB00D040	OF_VLAN_EN [452].EN[4:4]	1
0xBB00D040	OF_VLAN_EN [453].EN[5:5]	1
0xBB00D040	OF_VLAN_EN [454].EN[6:6]	1
0xBB00D040	OF_VLAN_EN [455].EN[7:7]	1
0xBB00D040	OF_VLAN_EN [456].EN[8:8]	1
0xBB00D040	OF_VLAN_EN [457].EN[9:9]	1
0xBB00D040	OF_VLAN_EN [458].EN[10:10]	1
0xBB00D040	OF_VLAN_EN [459].EN[11:11]	1
0xBB00D040	OF_VLAN_EN [460].EN[12:12]	1
0xBB00D040	OF_VLAN_EN [461].EN[13:13]	1
0xBB00D040	OF_VLAN_EN [462].EN[14:14]	1
0xBB00D040	OF_VLAN_EN [463].EN[15:15]	1
0xBB00D040	OF_VLAN_EN [464].EN[16:16]	1
0xBB00D040	OF_VLAN_EN [465].EN[17:17]	1
0xBB00D040	OF_VLAN_EN [466].EN[18:18]	1
0xBB00D040	OF_VLAN_EN [467].EN[19:19]	1
0xBB00D040	OF_VLAN_EN [468].EN[20:20]	1
0xBB00D040	OF_VLAN_EN [469].EN[21:21]	1
0xBB00D040	OF_VLAN_EN [470].EN[22:22]	1
0xBB00D040	OF_VLAN_EN [471].EN[23:23]	1
0xBB00D040	OF_VLAN_EN [472].EN[24:24]	1
0xBB00D040	OF_VLAN_EN [473].EN[25:25]	1
0xBB00D040	OF_VLAN_EN [474].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D040	OF_VLAN_EN [475].EN[27:27]	1
0xBB00D040	OF_VLAN_EN [476].EN[28:28]	1
0xBB00D040	OF_VLAN_EN [477].EN[29:29]	1
0xBB00D040	OF_VLAN_EN [478].EN[30:30]	1
0xBB00D040	OF_VLAN_EN [479].EN[31:31]	1
0xBB00D044	OF_VLAN_EN [480].EN[0:0]	1
0xBB00D044	OF_VLAN_EN [481].EN[1:1]	1
0xBB00D044	OF_VLAN_EN [482].EN[2:2]	1
0xBB00D044	OF_VLAN_EN [483].EN[3:3]	1
0xBB00D044	OF_VLAN_EN [484].EN[4:4]	1
0xBB00D044	OF_VLAN_EN [485].EN[5:5]	1
0xBB00D044	OF_VLAN_EN [486].EN[6:6]	1
0xBB00D044	OF_VLAN_EN [487].EN[7:7]	1
0xBB00D044	OF_VLAN_EN [488].EN[8:8]	1
0xBB00D044	OF_VLAN_EN [489].EN[9:9]	1
0xBB00D044	OF_VLAN_EN [490].EN[10:10]	1
0xBB00D044	OF_VLAN_EN [491].EN[11:11]	1
0xBB00D044	OF_VLAN_EN [492].EN[12:12]	1
0xBB00D044	OF_VLAN_EN [493].EN[13:13]	1
0xBB00D044	OF_VLAN_EN [494].EN[14:14]	1
0xBB00D044	OF_VLAN_EN [495].EN[15:15]	1
0xBB00D044	OF_VLAN_EN [496].EN[16:16]	1
0xBB00D044	OF_VLAN_EN [497].EN[17:17]	1
0xBB00D044	OF_VLAN_EN [498].EN[18:18]	1
0xBB00D044	OF_VLAN_EN [499].EN[19:19]	1
0xBB00D044	OF_VLAN_EN [500].EN[20:20]	1
0xBB00D044	OF_VLAN_EN [501].EN[21:21]	1
0xBB00D044	OF_VLAN_EN [502].EN[22:22]	1
0xBB00D044	OF_VLAN_EN [503].EN[23:23]	1
0xBB00D044	OF_VLAN_EN [504].EN[24:24]	1
0xBB00D044	OF_VLAN_EN [505].EN[25:25]	1
0xBB00D044	OF_VLAN_EN [506].EN[26:26]	1
0xBB00D044	OF_VLAN_EN [507].EN[27:27]	1
0xBB00D044	OF_VLAN_EN [508].EN[28:28]	1
0xBB00D044	OF_VLAN_EN [509].EN[29:29]	1
0xBB00D044	OF_VLAN_EN [510].EN[30:30]	1
0xBB00D044	OF_VLAN_EN [511].EN[31:31]	1
0xBB00D048	OF_VLAN_EN [512].EN[0:0]	1
0xBB00D048	OF_VLAN_EN [513].EN[1:1]	1
0xBB00D048	OF_VLAN_EN [514].EN[2:2]	1
0xBB00D048	OF_VLAN_EN [515].EN[3:3]	1
0xBB00D048	OF_VLAN_EN [516].EN[4:4]	1
0xBB00D048	OF_VLAN_EN [517].EN[5:5]	1
0xBB00D048	OF_VLAN_EN [518].EN[6:6]	1
0xBB00D048	OF_VLAN_EN [519].EN[7:7]	1
0xBB00D048	OF_VLAN_EN [520].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D048	OF_VLAN_EN [521].EN[9:9]	1
0xBB00D048	OF_VLAN_EN [522].EN[10:10]	1
0xBB00D048	OF_VLAN_EN [523].EN[11:11]	1
0xBB00D048	OF_VLAN_EN [524].EN[12:12]	1
0xBB00D048	OF_VLAN_EN [525].EN[13:13]	1
0xBB00D048	OF_VLAN_EN [526].EN[14:14]	1
0xBB00D048	OF_VLAN_EN [527].EN[15:15]	1
0xBB00D048	OF_VLAN_EN [528].EN[16:16]	1
0xBB00D048	OF_VLAN_EN [529].EN[17:17]	1
0xBB00D048	OF_VLAN_EN [530].EN[18:18]	1
0xBB00D048	OF_VLAN_EN [531].EN[19:19]	1
0xBB00D048	OF_VLAN_EN [532].EN[20:20]	1
0xBB00D048	OF_VLAN_EN [533].EN[21:21]	1
0xBB00D048	OF_VLAN_EN [534].EN[22:22]	1
0xBB00D048	OF_VLAN_EN [535].EN[23:23]	1
0xBB00D048	OF_VLAN_EN [536].EN[24:24]	1
0xBB00D048	OF_VLAN_EN [537].EN[25:25]	1
0xBB00D048	OF_VLAN_EN [538].EN[26:26]	1
0xBB00D048	OF_VLAN_EN [539].EN[27:27]	1
0xBB00D048	OF_VLAN_EN [540].EN[28:28]	1
0xBB00D048	OF_VLAN_EN [541].EN[29:29]	1
0xBB00D048	OF_VLAN_EN [542].EN[30:30]	1
0xBB00D048	OF_VLAN_EN [543].EN[31:31]	1
0xBB00D04C	OF_VLAN_EN [544].EN[0:0]	1
0xBB00D04C	OF_VLAN_EN [545].EN[1:1]	1
0xBB00D04C	OF_VLAN_EN [546].EN[2:2]	1
0xBB00D04C	OF_VLAN_EN [547].EN[3:3]	1
0xBB00D04C	OF_VLAN_EN [548].EN[4:4]	1
0xBB00D04C	OF_VLAN_EN [549].EN[5:5]	1
0xBB00D04C	OF_VLAN_EN [550].EN[6:6]	1
0xBB00D04C	OF_VLAN_EN [551].EN[7:7]	1
0xBB00D04C	OF_VLAN_EN [552].EN[8:8]	1
0xBB00D04C	OF_VLAN_EN [553].EN[9:9]	1
0xBB00D04C	OF_VLAN_EN [554].EN[10:10]	1
0xBB00D04C	OF_VLAN_EN [555].EN[11:11]	1
0xBB00D04C	OF_VLAN_EN [556].EN[12:12]	1
0xBB00D04C	OF_VLAN_EN [557].EN[13:13]	1
0xBB00D04C	OF_VLAN_EN [558].EN[14:14]	1
0xBB00D04C	OF_VLAN_EN [559].EN[15:15]	1
0xBB00D04C	OF_VLAN_EN [560].EN[16:16]	1
0xBB00D04C	OF_VLAN_EN [561].EN[17:17]	1
0xBB00D04C	OF_VLAN_EN [562].EN[18:18]	1
0xBB00D04C	OF_VLAN_EN [563].EN[19:19]	1
0xBB00D04C	OF_VLAN_EN [564].EN[20:20]	1
0xBB00D04C	OF_VLAN_EN [565].EN[21:21]	1
0xBB00D04C	OF_VLAN_EN [566].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D04C	OF_VLAN_EN [567].EN[23:23]	1
0xBB00D04C	OF_VLAN_EN [568].EN[24:24]	1
0xBB00D04C	OF_VLAN_EN [569].EN[25:25]	1
0xBB00D04C	OF_VLAN_EN [570].EN[26:26]	1
0xBB00D04C	OF_VLAN_EN [571].EN[27:27]	1
0xBB00D04C	OF_VLAN_EN [572].EN[28:28]	1
0xBB00D04C	OF_VLAN_EN [573].EN[29:29]	1
0xBB00D04C	OF_VLAN_EN [574].EN[30:30]	1
0xBB00D04C	OF_VLAN_EN [575].EN[31:31]	1
0xBB00D050	OF_VLAN_EN [576].EN[0:0]	1
0xBB00D050	OF_VLAN_EN [577].EN[1:1]	1
0xBB00D050	OF_VLAN_EN [578].EN[2:2]	1
0xBB00D050	OF_VLAN_EN [579].EN[3:3]	1
0xBB00D050	OF_VLAN_EN [580].EN[4:4]	1
0xBB00D050	OF_VLAN_EN [581].EN[5:5]	1
0xBB00D050	OF_VLAN_EN [582].EN[6:6]	1
0xBB00D050	OF_VLAN_EN [583].EN[7:7]	1
0xBB00D050	OF_VLAN_EN [584].EN[8:8]	1
0xBB00D050	OF_VLAN_EN [585].EN[9:9]	1
0xBB00D050	OF_VLAN_EN [586].EN[10:10]	1
0xBB00D050	OF_VLAN_EN [587].EN[11:11]	1
0xBB00D050	OF_VLAN_EN [588].EN[12:12]	1
0xBB00D050	OF_VLAN_EN [589].EN[13:13]	1
0xBB00D050	OF_VLAN_EN [590].EN[14:14]	1
0xBB00D050	OF_VLAN_EN [591].EN[15:15]	1
0xBB00D050	OF_VLAN_EN [592].EN[16:16]	1
0xBB00D050	OF_VLAN_EN [593].EN[17:17]	1
0xBB00D050	OF_VLAN_EN [594].EN[18:18]	1
0xBB00D050	OF_VLAN_EN [595].EN[19:19]	1
0xBB00D050	OF_VLAN_EN [596].EN[20:20]	1
0xBB00D050	OF_VLAN_EN [597].EN[21:21]	1
0xBB00D050	OF_VLAN_EN [598].EN[22:22]	1
0xBB00D050	OF_VLAN_EN [599].EN[23:23]	1
0xBB00D050	OF_VLAN_EN [600].EN[24:24]	1
0xBB00D050	OF_VLAN_EN [601].EN[25:25]	1
0xBB00D050	OF_VLAN_EN [602].EN[26:26]	1
0xBB00D050	OF_VLAN_EN [603].EN[27:27]	1
0xBB00D050	OF_VLAN_EN [604].EN[28:28]	1
0xBB00D050	OF_VLAN_EN [605].EN[29:29]	1
0xBB00D050	OF_VLAN_EN [606].EN[30:30]	1
0xBB00D050	OF_VLAN_EN [607].EN[31:31]	1
0xBB00D054	OF_VLAN_EN [608].EN[0:0]	1
0xBB00D054	OF_VLAN_EN [609].EN[1:1]	1
0xBB00D054	OF_VLAN_EN [610].EN[2:2]	1
0xBB00D054	OF_VLAN_EN [611].EN[3:3]	1
0xBB00D054	OF_VLAN_EN [612].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D054	OF_VLAN_EN [613].EN[5:5]	1
0xBB00D054	OF_VLAN_EN [614].EN[6:6]	1
0xBB00D054	OF_VLAN_EN [615].EN[7:7]	1
0xBB00D054	OF_VLAN_EN [616].EN[8:8]	1
0xBB00D054	OF_VLAN_EN [617].EN[9:9]	1
0xBB00D054	OF_VLAN_EN [618].EN[10:10]	1
0xBB00D054	OF_VLAN_EN [619].EN[11:11]	1
0xBB00D054	OF_VLAN_EN [620].EN[12:12]	1
0xBB00D054	OF_VLAN_EN [621].EN[13:13]	1
0xBB00D054	OF_VLAN_EN [622].EN[14:14]	1
0xBB00D054	OF_VLAN_EN [623].EN[15:15]	1
0xBB00D054	OF_VLAN_EN [624].EN[16:16]	1
0xBB00D054	OF_VLAN_EN [625].EN[17:17]	1
0xBB00D054	OF_VLAN_EN [626].EN[18:18]	1
0xBB00D054	OF_VLAN_EN [627].EN[19:19]	1
0xBB00D054	OF_VLAN_EN [628].EN[20:20]	1
0xBB00D054	OF_VLAN_EN [629].EN[21:21]	1
0xBB00D054	OF_VLAN_EN [630].EN[22:22]	1
0xBB00D054	OF_VLAN_EN [631].EN[23:23]	1
0xBB00D054	OF_VLAN_EN [632].EN[24:24]	1
0xBB00D054	OF_VLAN_EN [633].EN[25:25]	1
0xBB00D054	OF_VLAN_EN [634].EN[26:26]	1
0xBB00D054	OF_VLAN_EN [635].EN[27:27]	1
0xBB00D054	OF_VLAN_EN [636].EN[28:28]	1
0xBB00D054	OF_VLAN_EN [637].EN[29:29]	1
0xBB00D054	OF_VLAN_EN [638].EN[30:30]	1
0xBB00D054	OF_VLAN_EN [639].EN[31:31]	1
0xBB00D058	OF_VLAN_EN [640].EN[0:0]	1
0xBB00D058	OF_VLAN_EN [641].EN[1:1]	1
0xBB00D058	OF_VLAN_EN [642].EN[2:2]	1
0xBB00D058	OF_VLAN_EN [643].EN[3:3]	1
0xBB00D058	OF_VLAN_EN [644].EN[4:4]	1
0xBB00D058	OF_VLAN_EN [645].EN[5:5]	1
0xBB00D058	OF_VLAN_EN [646].EN[6:6]	1
0xBB00D058	OF_VLAN_EN [647].EN[7:7]	1
0xBB00D058	OF_VLAN_EN [648].EN[8:8]	1
0xBB00D058	OF_VLAN_EN [649].EN[9:9]	1
0xBB00D058	OF_VLAN_EN [650].EN[10:10]	1
0xBB00D058	OF_VLAN_EN [651].EN[11:11]	1
0xBB00D058	OF_VLAN_EN [652].EN[12:12]	1
0xBB00D058	OF_VLAN_EN [653].EN[13:13]	1
0xBB00D058	OF_VLAN_EN [654].EN[14:14]	1
0xBB00D058	OF_VLAN_EN [655].EN[15:15]	1
0xBB00D058	OF_VLAN_EN [656].EN[16:16]	1
0xBB00D058	OF_VLAN_EN [657].EN[17:17]	1
0xBB00D058	OF_VLAN_EN [658].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D058	OF_VLAN_EN [659].EN[19:19]	1
0xBB00D058	OF_VLAN_EN [660].EN[20:20]	1
0xBB00D058	OF_VLAN_EN [661].EN[21:21]	1
0xBB00D058	OF_VLAN_EN [662].EN[22:22]	1
0xBB00D058	OF_VLAN_EN [663].EN[23:23]	1
0xBB00D058	OF_VLAN_EN [664].EN[24:24]	1
0xBB00D058	OF_VLAN_EN [665].EN[25:25]	1
0xBB00D058	OF_VLAN_EN [666].EN[26:26]	1
0xBB00D058	OF_VLAN_EN [667].EN[27:27]	1
0xBB00D058	OF_VLAN_EN [668].EN[28:28]	1
0xBB00D058	OF_VLAN_EN [669].EN[29:29]	1
0xBB00D058	OF_VLAN_EN [670].EN[30:30]	1
0xBB00D058	OF_VLAN_EN [671].EN[31:31]	1
0xBB00D05C	OF_VLAN_EN [672].EN[0:0]	1
0xBB00D05C	OF_VLAN_EN [673].EN[1:1]	1
0xBB00D05C	OF_VLAN_EN [674].EN[2:2]	1
0xBB00D05C	OF_VLAN_EN [675].EN[3:3]	1
0xBB00D05C	OF_VLAN_EN [676].EN[4:4]	1
0xBB00D05C	OF_VLAN_EN [677].EN[5:5]	1
0xBB00D05C	OF_VLAN_EN [678].EN[6:6]	1
0xBB00D05C	OF_VLAN_EN [679].EN[7:7]	1
0xBB00D05C	OF_VLAN_EN [680].EN[8:8]	1
0xBB00D05C	OF_VLAN_EN [681].EN[9:9]	1
0xBB00D05C	OF_VLAN_EN [682].EN[10:10]	1
0xBB00D05C	OF_VLAN_EN [683].EN[11:11]	1
0xBB00D05C	OF_VLAN_EN [684].EN[12:12]	1
0xBB00D05C	OF_VLAN_EN [685].EN[13:13]	1
0xBB00D05C	OF_VLAN_EN [686].EN[14:14]	1
0xBB00D05C	OF_VLAN_EN [687].EN[15:15]	1
0xBB00D05C	OF_VLAN_EN [688].EN[16:16]	1
0xBB00D05C	OF_VLAN_EN [689].EN[17:17]	1
0xBB00D05C	OF_VLAN_EN [690].EN[18:18]	1
0xBB00D05C	OF_VLAN_EN [691].EN[19:19]	1
0xBB00D05C	OF_VLAN_EN [692].EN[20:20]	1
0xBB00D05C	OF_VLAN_EN [693].EN[21:21]	1
0xBB00D05C	OF_VLAN_EN [694].EN[22:22]	1
0xBB00D05C	OF_VLAN_EN [695].EN[23:23]	1
0xBB00D05C	OF_VLAN_EN [696].EN[24:24]	1
0xBB00D05C	OF_VLAN_EN [697].EN[25:25]	1
0xBB00D05C	OF_VLAN_EN [698].EN[26:26]	1
0xBB00D05C	OF_VLAN_EN [699].EN[27:27]	1
0xBB00D05C	OF_VLAN_EN [700].EN[28:28]	1
0xBB00D05C	OF_VLAN_EN [701].EN[29:29]	1
0xBB00D05C	OF_VLAN_EN [702].EN[30:30]	1
0xBB00D05C	OF_VLAN_EN [703].EN[31:31]	1
0xBB00D060	OF_VLAN_EN [704].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D060	OF_VLAN_EN [705].EN[1:1]	1
0xBB00D060	OF_VLAN_EN [706].EN[2:2]	1
0xBB00D060	OF_VLAN_EN [707].EN[3:3]	1
0xBB00D060	OF_VLAN_EN [708].EN[4:4]	1
0xBB00D060	OF_VLAN_EN [709].EN[5:5]	1
0xBB00D060	OF_VLAN_EN [710].EN[6:6]	1
0xBB00D060	OF_VLAN_EN [711].EN[7:7]	1
0xBB00D060	OF_VLAN_EN [712].EN[8:8]	1
0xBB00D060	OF_VLAN_EN [713].EN[9:9]	1
0xBB00D060	OF_VLAN_EN [714].EN[10:10]	1
0xBB00D060	OF_VLAN_EN [715].EN[11:11]	1
0xBB00D060	OF_VLAN_EN [716].EN[12:12]	1
0xBB00D060	OF_VLAN_EN [717].EN[13:13]	1
0xBB00D060	OF_VLAN_EN [718].EN[14:14]	1
0xBB00D060	OF_VLAN_EN [719].EN[15:15]	1
0xBB00D060	OF_VLAN_EN [720].EN[16:16]	1
0xBB00D060	OF_VLAN_EN [721].EN[17:17]	1
0xBB00D060	OF_VLAN_EN [722].EN[18:18]	1
0xBB00D060	OF_VLAN_EN [723].EN[19:19]	1
0xBB00D060	OF_VLAN_EN [724].EN[20:20]	1
0xBB00D060	OF_VLAN_EN [725].EN[21:21]	1
0xBB00D060	OF_VLAN_EN [726].EN[22:22]	1
0xBB00D060	OF_VLAN_EN [727].EN[23:23]	1
0xBB00D060	OF_VLAN_EN [728].EN[24:24]	1
0xBB00D060	OF_VLAN_EN [729].EN[25:25]	1
0xBB00D060	OF_VLAN_EN [730].EN[26:26]	1
0xBB00D060	OF_VLAN_EN [731].EN[27:27]	1
0xBB00D060	OF_VLAN_EN [732].EN[28:28]	1
0xBB00D060	OF_VLAN_EN [733].EN[29:29]	1
0xBB00D060	OF_VLAN_EN [734].EN[30:30]	1
0xBB00D060	OF_VLAN_EN [735].EN[31:31]	1
0xBB00D064	OF_VLAN_EN [736].EN[0:0]	1
0xBB00D064	OF_VLAN_EN [737].EN[1:1]	1
0xBB00D064	OF_VLAN_EN [738].EN[2:2]	1
0xBB00D064	OF_VLAN_EN [739].EN[3:3]	1
0xBB00D064	OF_VLAN_EN [740].EN[4:4]	1
0xBB00D064	OF_VLAN_EN [741].EN[5:5]	1
0xBB00D064	OF_VLAN_EN [742].EN[6:6]	1
0xBB00D064	OF_VLAN_EN [743].EN[7:7]	1
0xBB00D064	OF_VLAN_EN [744].EN[8:8]	1
0xBB00D064	OF_VLAN_EN [745].EN[9:9]	1
0xBB00D064	OF_VLAN_EN [746].EN[10:10]	1
0xBB00D064	OF_VLAN_EN [747].EN[11:11]	1
0xBB00D064	OF_VLAN_EN [748].EN[12:12]	1
0xBB00D064	OF_VLAN_EN [749].EN[13:13]	1
0xBB00D064	OF_VLAN_EN [750].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D064	OF_VLAN_EN [751].EN[15:15]	1
0xBB00D064	OF_VLAN_EN [752].EN[16:16]	1
0xBB00D064	OF_VLAN_EN [753].EN[17:17]	1
0xBB00D064	OF_VLAN_EN [754].EN[18:18]	1
0xBB00D064	OF_VLAN_EN [755].EN[19:19]	1
0xBB00D064	OF_VLAN_EN [756].EN[20:20]	1
0xBB00D064	OF_VLAN_EN [757].EN[21:21]	1
0xBB00D064	OF_VLAN_EN [758].EN[22:22]	1
0xBB00D064	OF_VLAN_EN [759].EN[23:23]	1
0xBB00D064	OF_VLAN_EN [760].EN[24:24]	1
0xBB00D064	OF_VLAN_EN [761].EN[25:25]	1
0xBB00D064	OF_VLAN_EN [762].EN[26:26]	1
0xBB00D064	OF_VLAN_EN [763].EN[27:27]	1
0xBB00D064	OF_VLAN_EN [764].EN[28:28]	1
0xBB00D064	OF_VLAN_EN [765].EN[29:29]	1
0xBB00D064	OF_VLAN_EN [766].EN[30:30]	1
0xBB00D064	OF_VLAN_EN [767].EN[31:31]	1
0xBB00D068	OF_VLAN_EN [768].EN[0:0]	1
0xBB00D068	OF_VLAN_EN [769].EN[1:1]	1
0xBB00D068	OF_VLAN_EN [770].EN[2:2]	1
0xBB00D068	OF_VLAN_EN [771].EN[3:3]	1
0xBB00D068	OF_VLAN_EN [772].EN[4:4]	1
0xBB00D068	OF_VLAN_EN [773].EN[5:5]	1
0xBB00D068	OF_VLAN_EN [774].EN[6:6]	1
0xBB00D068	OF_VLAN_EN [775].EN[7:7]	1
0xBB00D068	OF_VLAN_EN [776].EN[8:8]	1
0xBB00D068	OF_VLAN_EN [777].EN[9:9]	1
0xBB00D068	OF_VLAN_EN [778].EN[10:10]	1
0xBB00D068	OF_VLAN_EN [779].EN[11:11]	1
0xBB00D068	OF_VLAN_EN [780].EN[12:12]	1
0xBB00D068	OF_VLAN_EN [781].EN[13:13]	1
0xBB00D068	OF_VLAN_EN [782].EN[14:14]	1
0xBB00D068	OF_VLAN_EN [783].EN[15:15]	1
0xBB00D068	OF_VLAN_EN [784].EN[16:16]	1
0xBB00D068	OF_VLAN_EN [785].EN[17:17]	1
0xBB00D068	OF_VLAN_EN [786].EN[18:18]	1
0xBB00D068	OF_VLAN_EN [787].EN[19:19]	1
0xBB00D068	OF_VLAN_EN [788].EN[20:20]	1
0xBB00D068	OF_VLAN_EN [789].EN[21:21]	1
0xBB00D068	OF_VLAN_EN [790].EN[22:22]	1
0xBB00D068	OF_VLAN_EN [791].EN[23:23]	1
0xBB00D068	OF_VLAN_EN [792].EN[24:24]	1
0xBB00D068	OF_VLAN_EN [793].EN[25:25]	1
0xBB00D068	OF_VLAN_EN [794].EN[26:26]	1
0xBB00D068	OF_VLAN_EN [795].EN[27:27]	1
0xBB00D068	OF_VLAN_EN [796].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D068	OF_VLAN_EN [797].EN[29:29]	1
0xBB00D068	OF_VLAN_EN [798].EN[30:30]	1
0xBB00D068	OF_VLAN_EN [799].EN[31:31]	1
0xBB00D06C	OF_VLAN_EN [800].EN[0:0]	1
0xBB00D06C	OF_VLAN_EN [801].EN[1:1]	1
0xBB00D06C	OF_VLAN_EN [802].EN[2:2]	1
0xBB00D06C	OF_VLAN_EN [803].EN[3:3]	1
0xBB00D06C	OF_VLAN_EN [804].EN[4:4]	1
0xBB00D06C	OF_VLAN_EN [805].EN[5:5]	1
0xBB00D06C	OF_VLAN_EN [806].EN[6:6]	1
0xBB00D06C	OF_VLAN_EN [807].EN[7:7]	1
0xBB00D06C	OF_VLAN_EN [808].EN[8:8]	1
0xBB00D06C	OF_VLAN_EN [809].EN[9:9]	1
0xBB00D06C	OF_VLAN_EN [810].EN[10:10]	1
0xBB00D06C	OF_VLAN_EN [811].EN[11:11]	1
0xBB00D06C	OF_VLAN_EN [812].EN[12:12]	1
0xBB00D06C	OF_VLAN_EN [813].EN[13:13]	1
0xBB00D06C	OF_VLAN_EN [814].EN[14:14]	1
0xBB00D06C	OF_VLAN_EN [815].EN[15:15]	1
0xBB00D06C	OF_VLAN_EN [816].EN[16:16]	1
0xBB00D06C	OF_VLAN_EN [817].EN[17:17]	1
0xBB00D06C	OF_VLAN_EN [818].EN[18:18]	1
0xBB00D06C	OF_VLAN_EN [819].EN[19:19]	1
0xBB00D06C	OF_VLAN_EN [820].EN[20:20]	1
0xBB00D06C	OF_VLAN_EN [821].EN[21:21]	1
0xBB00D06C	OF_VLAN_EN [822].EN[22:22]	1
0xBB00D06C	OF_VLAN_EN [823].EN[23:23]	1
0xBB00D06C	OF_VLAN_EN [824].EN[24:24]	1
0xBB00D06C	OF_VLAN_EN [825].EN[25:25]	1
0xBB00D06C	OF_VLAN_EN [826].EN[26:26]	1
0xBB00D06C	OF_VLAN_EN [827].EN[27:27]	1
0xBB00D06C	OF_VLAN_EN [828].EN[28:28]	1
0xBB00D06C	OF_VLAN_EN [829].EN[29:29]	1
0xBB00D06C	OF_VLAN_EN [830].EN[30:30]	1
0xBB00D06C	OF_VLAN_EN [831].EN[31:31]	1
0xBB00D070	OF_VLAN_EN [832].EN[0:0]	1
0xBB00D070	OF_VLAN_EN [833].EN[1:1]	1
0xBB00D070	OF_VLAN_EN [834].EN[2:2]	1
0xBB00D070	OF_VLAN_EN [835].EN[3:3]	1
0xBB00D070	OF_VLAN_EN [836].EN[4:4]	1
0xBB00D070	OF_VLAN_EN [837].EN[5:5]	1
0xBB00D070	OF_VLAN_EN [838].EN[6:6]	1
0xBB00D070	OF_VLAN_EN [839].EN[7:7]	1
0xBB00D070	OF_VLAN_EN [840].EN[8:8]	1
0xBB00D070	OF_VLAN_EN [841].EN[9:9]	1
0xBB00D070	OF_VLAN_EN [842].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D070	OF_VLAN_EN [843].EN[11:11]	1
0xBB00D070	OF_VLAN_EN [844].EN[12:12]	1
0xBB00D070	OF_VLAN_EN [845].EN[13:13]	1
0xBB00D070	OF_VLAN_EN [846].EN[14:14]	1
0xBB00D070	OF_VLAN_EN [847].EN[15:15]	1
0xBB00D070	OF_VLAN_EN [848].EN[16:16]	1
0xBB00D070	OF_VLAN_EN [849].EN[17:17]	1
0xBB00D070	OF_VLAN_EN [850].EN[18:18]	1
0xBB00D070	OF_VLAN_EN [851].EN[19:19]	1
0xBB00D070	OF_VLAN_EN [852].EN[20:20]	1
0xBB00D070	OF_VLAN_EN [853].EN[21:21]	1
0xBB00D070	OF_VLAN_EN [854].EN[22:22]	1
0xBB00D070	OF_VLAN_EN [855].EN[23:23]	1
0xBB00D070	OF_VLAN_EN [856].EN[24:24]	1
0xBB00D070	OF_VLAN_EN [857].EN[25:25]	1
0xBB00D070	OF_VLAN_EN [858].EN[26:26]	1
0xBB00D070	OF_VLAN_EN [859].EN[27:27]	1
0xBB00D070	OF_VLAN_EN [860].EN[28:28]	1
0xBB00D070	OF_VLAN_EN [861].EN[29:29]	1
0xBB00D070	OF_VLAN_EN [862].EN[30:30]	1
0xBB00D070	OF_VLAN_EN [863].EN[31:31]	1
0xBB00D074	OF_VLAN_EN [864].EN[0:0]	1
0xBB00D074	OF_VLAN_EN [865].EN[1:1]	1
0xBB00D074	OF_VLAN_EN [866].EN[2:2]	1
0xBB00D074	OF_VLAN_EN [867].EN[3:3]	1
0xBB00D074	OF_VLAN_EN [868].EN[4:4]	1
0xBB00D074	OF_VLAN_EN [869].EN[5:5]	1
0xBB00D074	OF_VLAN_EN [870].EN[6:6]	1
0xBB00D074	OF_VLAN_EN [871].EN[7:7]	1
0xBB00D074	OF_VLAN_EN [872].EN[8:8]	1
0xBB00D074	OF_VLAN_EN [873].EN[9:9]	1
0xBB00D074	OF_VLAN_EN [874].EN[10:10]	1
0xBB00D074	OF_VLAN_EN [875].EN[11:11]	1
0xBB00D074	OF_VLAN_EN [876].EN[12:12]	1
0xBB00D074	OF_VLAN_EN [877].EN[13:13]	1
0xBB00D074	OF_VLAN_EN [878].EN[14:14]	1
0xBB00D074	OF_VLAN_EN [879].EN[15:15]	1
0xBB00D074	OF_VLAN_EN [880].EN[16:16]	1
0xBB00D074	OF_VLAN_EN [881].EN[17:17]	1
0xBB00D074	OF_VLAN_EN [882].EN[18:18]	1
0xBB00D074	OF_VLAN_EN [883].EN[19:19]	1
0xBB00D074	OF_VLAN_EN [884].EN[20:20]	1
0xBB00D074	OF_VLAN_EN [885].EN[21:21]	1
0xBB00D074	OF_VLAN_EN [886].EN[22:22]	1
0xBB00D074	OF_VLAN_EN [887].EN[23:23]	1
0xBB00D074	OF_VLAN_EN [888].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D074	OF_VLAN_EN [889].EN[25:25]	1
0xBB00D074	OF_VLAN_EN [890].EN[26:26]	1
0xBB00D074	OF_VLAN_EN [891].EN[27:27]	1
0xBB00D074	OF_VLAN_EN [892].EN[28:28]	1
0xBB00D074	OF_VLAN_EN [893].EN[29:29]	1
0xBB00D074	OF_VLAN_EN [894].EN[30:30]	1
0xBB00D074	OF_VLAN_EN [895].EN[31:31]	1
0xBB00D078	OF_VLAN_EN [896].EN[0:0]	1
0xBB00D078	OF_VLAN_EN [897].EN[1:1]	1
0xBB00D078	OF_VLAN_EN [898].EN[2:2]	1
0xBB00D078	OF_VLAN_EN [899].EN[3:3]	1
0xBB00D078	OF_VLAN_EN [900].EN[4:4]	1
0xBB00D078	OF_VLAN_EN [901].EN[5:5]	1
0xBB00D078	OF_VLAN_EN [902].EN[6:6]	1
0xBB00D078	OF_VLAN_EN [903].EN[7:7]	1
0xBB00D078	OF_VLAN_EN [904].EN[8:8]	1
0xBB00D078	OF_VLAN_EN [905].EN[9:9]	1
0xBB00D078	OF_VLAN_EN [906].EN[10:10]	1
0xBB00D078	OF_VLAN_EN [907].EN[11:11]	1
0xBB00D078	OF_VLAN_EN [908].EN[12:12]	1
0xBB00D078	OF_VLAN_EN [909].EN[13:13]	1
0xBB00D078	OF_VLAN_EN [910].EN[14:14]	1
0xBB00D078	OF_VLAN_EN [911].EN[15:15]	1
0xBB00D078	OF_VLAN_EN [912].EN[16:16]	1
0xBB00D078	OF_VLAN_EN [913].EN[17:17]	1
0xBB00D078	OF_VLAN_EN [914].EN[18:18]	1
0xBB00D078	OF_VLAN_EN [915].EN[19:19]	1
0xBB00D078	OF_VLAN_EN [916].EN[20:20]	1
0xBB00D078	OF_VLAN_EN [917].EN[21:21]	1
0xBB00D078	OF_VLAN_EN [918].EN[22:22]	1
0xBB00D078	OF_VLAN_EN [919].EN[23:23]	1
0xBB00D078	OF_VLAN_EN [920].EN[24:24]	1
0xBB00D078	OF_VLAN_EN [921].EN[25:25]	1
0xBB00D078	OF_VLAN_EN [922].EN[26:26]	1
0xBB00D078	OF_VLAN_EN [923].EN[27:27]	1
0xBB00D078	OF_VLAN_EN [924].EN[28:28]	1
0xBB00D078	OF_VLAN_EN [925].EN[29:29]	1
0xBB00D078	OF_VLAN_EN [926].EN[30:30]	1
0xBB00D078	OF_VLAN_EN [927].EN[31:31]	1
0xBB00D07C	OF_VLAN_EN [928].EN[0:0]	1
0xBB00D07C	OF_VLAN_EN [929].EN[1:1]	1
0xBB00D07C	OF_VLAN_EN [930].EN[2:2]	1
0xBB00D07C	OF_VLAN_EN [931].EN[3:3]	1
0xBB00D07C	OF_VLAN_EN [932].EN[4:4]	1
0xBB00D07C	OF_VLAN_EN [933].EN[5:5]	1
0xBB00D07C	OF_VLAN_EN [934].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D07C	OF_VLAN_EN [935].EN[7:7]	1
0xBB00D07C	OF_VLAN_EN [936].EN[8:8]	1
0xBB00D07C	OF_VLAN_EN [937].EN[9:9]	1
0xBB00D07C	OF_VLAN_EN [938].EN[10:10]	1
0xBB00D07C	OF_VLAN_EN [939].EN[11:11]	1
0xBB00D07C	OF_VLAN_EN [940].EN[12:12]	1
0xBB00D07C	OF_VLAN_EN [941].EN[13:13]	1
0xBB00D07C	OF_VLAN_EN [942].EN[14:14]	1
0xBB00D07C	OF_VLAN_EN [943].EN[15:15]	1
0xBB00D07C	OF_VLAN_EN [944].EN[16:16]	1
0xBB00D07C	OF_VLAN_EN [945].EN[17:17]	1
0xBB00D07C	OF_VLAN_EN [946].EN[18:18]	1
0xBB00D07C	OF_VLAN_EN [947].EN[19:19]	1
0xBB00D07C	OF_VLAN_EN [948].EN[20:20]	1
0xBB00D07C	OF_VLAN_EN [949].EN[21:21]	1
0xBB00D07C	OF_VLAN_EN [950].EN[22:22]	1
0xBB00D07C	OF_VLAN_EN [951].EN[23:23]	1
0xBB00D07C	OF_VLAN_EN [952].EN[24:24]	1
0xBB00D07C	OF_VLAN_EN [953].EN[25:25]	1
0xBB00D07C	OF_VLAN_EN [954].EN[26:26]	1
0xBB00D07C	OF_VLAN_EN [955].EN[27:27]	1
0xBB00D07C	OF_VLAN_EN [956].EN[28:28]	1
0xBB00D07C	OF_VLAN_EN [957].EN[29:29]	1
0xBB00D07C	OF_VLAN_EN [958].EN[30:30]	1
0xBB00D07C	OF_VLAN_EN [959].EN[31:31]	1
0xBB00D080	OF_VLAN_EN [960].EN[0:0]	1
0xBB00D080	OF_VLAN_EN [961].EN[1:1]	1
0xBB00D080	OF_VLAN_EN [962].EN[2:2]	1
0xBB00D080	OF_VLAN_EN [963].EN[3:3]	1
0xBB00D080	OF_VLAN_EN [964].EN[4:4]	1
0xBB00D080	OF_VLAN_EN [965].EN[5:5]	1
0xBB00D080	OF_VLAN_EN [966].EN[6:6]	1
0xBB00D080	OF_VLAN_EN [967].EN[7:7]	1
0xBB00D080	OF_VLAN_EN [968].EN[8:8]	1
0xBB00D080	OF_VLAN_EN [969].EN[9:9]	1
0xBB00D080	OF_VLAN_EN [970].EN[10:10]	1
0xBB00D080	OF_VLAN_EN [971].EN[11:11]	1
0xBB00D080	OF_VLAN_EN [972].EN[12:12]	1
0xBB00D080	OF_VLAN_EN [973].EN[13:13]	1
0xBB00D080	OF_VLAN_EN [974].EN[14:14]	1
0xBB00D080	OF_VLAN_EN [975].EN[15:15]	1
0xBB00D080	OF_VLAN_EN [976].EN[16:16]	1
0xBB00D080	OF_VLAN_EN [977].EN[17:17]	1
0xBB00D080	OF_VLAN_EN [978].EN[18:18]	1
0xBB00D080	OF_VLAN_EN [979].EN[19:19]	1
0xBB00D080	OF_VLAN_EN [980].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D080	OF_VLAN_EN [981].EN[21:21]	1
0xBB00D080	OF_VLAN_EN [982].EN[22:22]	1
0xBB00D080	OF_VLAN_EN [983].EN[23:23]	1
0xBB00D080	OF_VLAN_EN [984].EN[24:24]	1
0xBB00D080	OF_VLAN_EN [985].EN[25:25]	1
0xBB00D080	OF_VLAN_EN [986].EN[26:26]	1
0xBB00D080	OF_VLAN_EN [987].EN[27:27]	1
0xBB00D080	OF_VLAN_EN [988].EN[28:28]	1
0xBB00D080	OF_VLAN_EN [989].EN[29:29]	1
0xBB00D080	OF_VLAN_EN [990].EN[30:30]	1
0xBB00D080	OF_VLAN_EN [991].EN[31:31]	1
0xBB00D084	OF_VLAN_EN [992].EN[0:0]	1
0xBB00D084	OF_VLAN_EN [993].EN[1:1]	1
0xBB00D084	OF_VLAN_EN [994].EN[2:2]	1
0xBB00D084	OF_VLAN_EN [995].EN[3:3]	1
0xBB00D084	OF_VLAN_EN [996].EN[4:4]	1
0xBB00D084	OF_VLAN_EN [997].EN[5:5]	1
0xBB00D084	OF_VLAN_EN [998].EN[6:6]	1
0xBB00D084	OF_VLAN_EN [999].EN[7:7]	1
0xBB00D084	OF_VLAN_EN [1000].EN[8:8]	1
0xBB00D084	OF_VLAN_EN [1001].EN[9:9]	1
0xBB00D084	OF_VLAN_EN [1002].EN[10:10]	1
0xBB00D084	OF_VLAN_EN [1003].EN[11:11]	1
0xBB00D084	OF_VLAN_EN [1004].EN[12:12]	1
0xBB00D084	OF_VLAN_EN [1005].EN[13:13]	1
0xBB00D084	OF_VLAN_EN [1006].EN[14:14]	1
0xBB00D084	OF_VLAN_EN [1007].EN[15:15]	1
0xBB00D084	OF_VLAN_EN [1008].EN[16:16]	1
0xBB00D084	OF_VLAN_EN [1009].EN[17:17]	1
0xBB00D084	OF_VLAN_EN [1010].EN[18:18]	1
0xBB00D084	OF_VLAN_EN [1011].EN[19:19]	1
0xBB00D084	OF_VLAN_EN [1012].EN[20:20]	1
0xBB00D084	OF_VLAN_EN [1013].EN[21:21]	1
0xBB00D084	OF_VLAN_EN [1014].EN[22:22]	1
0xBB00D084	OF_VLAN_EN [1015].EN[23:23]	1
0xBB00D084	OF_VLAN_EN [1016].EN[24:24]	1
0xBB00D084	OF_VLAN_EN [1017].EN[25:25]	1
0xBB00D084	OF_VLAN_EN [1018].EN[26:26]	1
0xBB00D084	OF_VLAN_EN [1019].EN[27:27]	1
0xBB00D084	OF_VLAN_EN [1020].EN[28:28]	1
0xBB00D084	OF_VLAN_EN [1021].EN[29:29]	1
0xBB00D084	OF_VLAN_EN [1022].EN[30:30]	1
0xBB00D084	OF_VLAN_EN [1023].EN[31:31]	1
0xBB00D088	OF_VLAN_EN [1024].EN[0:0]	1
0xBB00D088	OF_VLAN_EN [1025].EN[1:1]	1
0xBB00D088	OF_VLAN_EN [1026].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D088	OF_VLAN_EN [1027].EN[3:3]	1
0xBB00D088	OF_VLAN_EN [1028].EN[4:4]	1
0xBB00D088	OF_VLAN_EN [1029].EN[5:5]	1
0xBB00D088	OF_VLAN_EN [1030].EN[6:6]	1
0xBB00D088	OF_VLAN_EN [1031].EN[7:7]	1
0xBB00D088	OF_VLAN_EN [1032].EN[8:8]	1
0xBB00D088	OF_VLAN_EN [1033].EN[9:9]	1
0xBB00D088	OF_VLAN_EN [1034].EN[10:10]	1
0xBB00D088	OF_VLAN_EN [1035].EN[11:11]	1
0xBB00D088	OF_VLAN_EN [1036].EN[12:12]	1
0xBB00D088	OF_VLAN_EN [1037].EN[13:13]	1
0xBB00D088	OF_VLAN_EN [1038].EN[14:14]	1
0xBB00D088	OF_VLAN_EN [1039].EN[15:15]	1
0xBB00D088	OF_VLAN_EN [1040].EN[16:16]	1
0xBB00D088	OF_VLAN_EN [1041].EN[17:17]	1
0xBB00D088	OF_VLAN_EN [1042].EN[18:18]	1
0xBB00D088	OF_VLAN_EN [1043].EN[19:19]	1
0xBB00D088	OF_VLAN_EN [1044].EN[20:20]	1
0xBB00D088	OF_VLAN_EN [1045].EN[21:21]	1
0xBB00D088	OF_VLAN_EN [1046].EN[22:22]	1
0xBB00D088	OF_VLAN_EN [1047].EN[23:23]	1
0xBB00D088	OF_VLAN_EN [1048].EN[24:24]	1
0xBB00D088	OF_VLAN_EN [1049].EN[25:25]	1
0xBB00D088	OF_VLAN_EN [1050].EN[26:26]	1
0xBB00D088	OF_VLAN_EN [1051].EN[27:27]	1
0xBB00D088	OF_VLAN_EN [1052].EN[28:28]	1
0xBB00D088	OF_VLAN_EN [1053].EN[29:29]	1
0xBB00D088	OF_VLAN_EN [1054].EN[30:30]	1
0xBB00D088	OF_VLAN_EN [1055].EN[31:31]	1
0xBB00D08C	OF_VLAN_EN [1056].EN[0:0]	1
0xBB00D08C	OF_VLAN_EN [1057].EN[1:1]	1
0xBB00D08C	OF_VLAN_EN [1058].EN[2:2]	1
0xBB00D08C	OF_VLAN_EN [1059].EN[3:3]	1
0xBB00D08C	OF_VLAN_EN [1060].EN[4:4]	1
0xBB00D08C	OF_VLAN_EN [1061].EN[5:5]	1
0xBB00D08C	OF_VLAN_EN [1062].EN[6:6]	1
0xBB00D08C	OF_VLAN_EN [1063].EN[7:7]	1
0xBB00D08C	OF_VLAN_EN [1064].EN[8:8]	1
0xBB00D08C	OF_VLAN_EN [1065].EN[9:9]	1
0xBB00D08C	OF_VLAN_EN [1066].EN[10:10]	1
0xBB00D08C	OF_VLAN_EN [1067].EN[11:11]	1
0xBB00D08C	OF_VLAN_EN [1068].EN[12:12]	1
0xBB00D08C	OF_VLAN_EN [1069].EN[13:13]	1
0xBB00D08C	OF_VLAN_EN [1070].EN[14:14]	1
0xBB00D08C	OF_VLAN_EN [1071].EN[15:15]	1
0xBB00D08C	OF_VLAN_EN [1072].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D08C	OF_VLAN_EN [1073].EN[17:17]	1
0xBB00D08C	OF_VLAN_EN [1074].EN[18:18]	1
0xBB00D08C	OF_VLAN_EN [1075].EN[19:19]	1
0xBB00D08C	OF_VLAN_EN [1076].EN[20:20]	1
0xBB00D08C	OF_VLAN_EN [1077].EN[21:21]	1
0xBB00D08C	OF_VLAN_EN [1078].EN[22:22]	1
0xBB00D08C	OF_VLAN_EN [1079].EN[23:23]	1
0xBB00D08C	OF_VLAN_EN [1080].EN[24:24]	1
0xBB00D08C	OF_VLAN_EN [1081].EN[25:25]	1
0xBB00D08C	OF_VLAN_EN [1082].EN[26:26]	1
0xBB00D08C	OF_VLAN_EN [1083].EN[27:27]	1
0xBB00D08C	OF_VLAN_EN [1084].EN[28:28]	1
0xBB00D08C	OF_VLAN_EN [1085].EN[29:29]	1
0xBB00D08C	OF_VLAN_EN [1086].EN[30:30]	1
0xBB00D08C	OF_VLAN_EN [1087].EN[31:31]	1
0xBB00D090	OF_VLAN_EN [1088].EN[0:0]	1
0xBB00D090	OF_VLAN_EN [1089].EN[1:1]	1
0xBB00D090	OF_VLAN_EN [1090].EN[2:2]	1
0xBB00D090	OF_VLAN_EN [1091].EN[3:3]	1
0xBB00D090	OF_VLAN_EN [1092].EN[4:4]	1
0xBB00D090	OF_VLAN_EN [1093].EN[5:5]	1
0xBB00D090	OF_VLAN_EN [1094].EN[6:6]	1
0xBB00D090	OF_VLAN_EN [1095].EN[7:7]	1
0xBB00D090	OF_VLAN_EN [1096].EN[8:8]	1
0xBB00D090	OF_VLAN_EN [1097].EN[9:9]	1
0xBB00D090	OF_VLAN_EN [1098].EN[10:10]	1
0xBB00D090	OF_VLAN_EN [1099].EN[11:11]	1
0xBB00D090	OF_VLAN_EN [1100].EN[12:12]	1
0xBB00D090	OF_VLAN_EN [1101].EN[13:13]	1
0xBB00D090	OF_VLAN_EN [1102].EN[14:14]	1
0xBB00D090	OF_VLAN_EN [1103].EN[15:15]	1
0xBB00D090	OF_VLAN_EN [1104].EN[16:16]	1
0xBB00D090	OF_VLAN_EN [1105].EN[17:17]	1
0xBB00D090	OF_VLAN_EN [1106].EN[18:18]	1
0xBB00D090	OF_VLAN_EN [1107].EN[19:19]	1
0xBB00D090	OF_VLAN_EN [1108].EN[20:20]	1
0xBB00D090	OF_VLAN_EN [1109].EN[21:21]	1
0xBB00D090	OF_VLAN_EN [1110].EN[22:22]	1
0xBB00D090	OF_VLAN_EN [1111].EN[23:23]	1
0xBB00D090	OF_VLAN_EN [1112].EN[24:24]	1
0xBB00D090	OF_VLAN_EN [1113].EN[25:25]	1
0xBB00D090	OF_VLAN_EN [1114].EN[26:26]	1
0xBB00D090	OF_VLAN_EN [1115].EN[27:27]	1
0xBB00D090	OF_VLAN_EN [1116].EN[28:28]	1
0xBB00D090	OF_VLAN_EN [1117].EN[29:29]	1
0xBB00D090	OF_VLAN_EN [1118].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D094	OF_VLAN_EN [1120].EN[0:0]	1
0xBB00D094	OF_VLAN_EN [1121].EN[1:1]	1
0xBB00D094	OF_VLAN_EN [1122].EN[2:2]	1
0xBB00D094	OF_VLAN_EN [1123].EN[3:3]	1
0xBB00D094	OF_VLAN_EN [1124].EN[4:4]	1
0xBB00D094	OF_VLAN_EN [1125].EN[5:5]	1
0xBB00D094	OF_VLAN_EN [1126].EN[6:6]	1
0xBB00D094	OF_VLAN_EN [1127].EN[7:7]	1
0xBB00D094	OF_VLAN_EN [1128].EN[8:8]	1
0xBB00D094	OF_VLAN_EN [1129].EN[9:9]	1
0xBB00D094	OF_VLAN_EN [1130].EN[10:10]	1
0xBB00D094	OF_VLAN_EN [1131].EN[11:11]	1
0xBB00D094	OF_VLAN_EN [1132].EN[12:12]	1
0xBB00D094	OF_VLAN_EN [1133].EN[13:13]	1
0xBB00D094	OF_VLAN_EN [1134].EN[14:14]	1
0xBB00D094	OF_VLAN_EN [1135].EN[15:15]	1
0xBB00D094	OF_VLAN_EN [1136].EN[16:16]	1
0xBB00D094	OF_VLAN_EN [1137].EN[17:17]	1
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0xBB00D094	OF_VLAN_EN [1151].EN[31:31]	1
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0xBB00D098	OF_VLAN_EN [1154].EN[2:2]	1
0xBB00D098	OF_VLAN_EN [1155].EN[3:3]	1
0xBB00D098	OF_VLAN_EN [1156].EN[4:4]	1
0xBB00D098	OF_VLAN_EN [1157].EN[5:5]	1
0xBB00D098	OF_VLAN_EN [1158].EN[6:6]	1
0xBB00D098	OF_VLAN_EN [1159].EN[7:7]	1
0xBB00D098	OF_VLAN_EN [1160].EN[8:8]	1
0xBB00D098	OF_VLAN_EN [1161].EN[9:9]	1
0xBB00D098	OF_VLAN_EN [1162].EN[10:10]	1
0xBB00D098	OF_VLAN_EN [1163].EN[11:11]	1
0xBB00D098	OF_VLAN_EN [1164].EN[12:12]	1

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0xBB00D098	OF_VLAN_EN [1172].EN[20:20]	1
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0xBB00D09C	OF_VLAN_EN [1203].EN[19:19]	1
0xBB00D09C	OF_VLAN_EN [1204].EN[20:20]	1
0xBB00D09C	OF_VLAN_EN [1205].EN[21:21]	1
0xBB00D09C	OF_VLAN_EN [1206].EN[22:22]	1
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0xBB00D09C	OF_VLAN_EN [1208].EN[24:24]	1
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0xBB00D0A0	OF_VLAN_EN [1227].EN[11:11]	1
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0xBB00D0A0	OF_VLAN_EN [1240].EN[24:24]	1
0xBB00D0A0	OF_VLAN_EN [1241].EN[25:25]	1
0xBB00D0A0	OF_VLAN_EN [1242].EN[26:26]	1
0xBB00D0A0	OF_VLAN_EN [1243].EN[27:27]	1
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0xBB00D0A4	OF_VLAN_EN [1249].EN[1:1]	1
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0xBB00D0A4	OF_VLAN_EN [1251].EN[3:3]	1
0xBB00D0A4	OF_VLAN_EN [1252].EN[4:4]	1
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0xBB00D0A4	OF_VLAN_EN [1254].EN[6:6]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0A4	OF_VLAN_EN [1261].EN[13:13]	1
0xBB00D0A4	OF_VLAN_EN [1262].EN[14:14]	1
0xBB00D0A4	OF_VLAN_EN [1263].EN[15:15]	1
0xBB00D0A4	OF_VLAN_EN [1264].EN[16:16]	1
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0xBB00D0A4	OF_VLAN_EN [1272].EN[24:24]	1
0xBB00D0A4	OF_VLAN_EN [1273].EN[25:25]	1
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0xBB00D0A4	OF_VLAN_EN [1276].EN[28:28]	1
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0xBB00D0A8	OF_VLAN_EN [1282].EN[2:2]	1
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0xBB00D0A8	OF_VLAN_EN [1284].EN[4:4]	1
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0xBB00D0A8	OF_VLAN_EN [1286].EN[6:6]	1
0xBB00D0A8	OF_VLAN_EN [1287].EN[7:7]	1
0xBB00D0A8	OF_VLAN_EN [1288].EN[8:8]	1
0xBB00D0A8	OF_VLAN_EN [1289].EN[9:9]	1
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0xBB00D0A8	OF_VLAN_EN [1294].EN[14:14]	1
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0xBB00D0A8	OF_VLAN_EN [1296].EN[16:16]	1
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0xBB00D0A8	OF_VLAN_EN [1298].EN[18:18]	1
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0xBB00D0A8	OF_VLAN_EN [1307].EN[27:27]	1
0xBB00D0A8	OF_VLAN_EN [1308].EN[28:28]	1
0xBB00D0A8	OF_VLAN_EN [1309].EN[29:29]	1
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0xBB00D0AC	OF_VLAN_EN [1339].EN[27:27]	1
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0xBB00D0B0	OF_VLAN_EN [1346].EN[2:2]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0B0	OF_VLAN_EN [1352].EN[8:8]	1
0xBB00D0B0	OF_VLAN_EN [1353].EN[9:9]	1
0xBB00D0B0	OF_VLAN_EN [1354].EN[10:10]	1
0xBB00D0B0	OF_VLAN_EN [1355].EN[11:11]	1
0xBB00D0B0	OF_VLAN_EN [1356].EN[12:12]	1
0xBB00D0B0	OF_VLAN_EN [1357].EN[13:13]	1
0xBB00D0B0	OF_VLAN_EN [1358].EN[14:14]	1
0xBB00D0B0	OF_VLAN_EN [1359].EN[15:15]	1
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0xBB00D0B0	OF_VLAN_EN [1361].EN[17:17]	1
0xBB00D0B0	OF_VLAN_EN [1362].EN[18:18]	1
0xBB00D0B0	OF_VLAN_EN [1363].EN[19:19]	1
0xBB00D0B0	OF_VLAN_EN [1364].EN[20:20]	1
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0xBB00D0B4	OF_VLAN_EN [1377].EN[1:1]	1
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0xBB00D0B4	OF_VLAN_EN [1381].EN[5:5]	1
0xBB00D0B4	OF_VLAN_EN [1382].EN[6:6]	1
0xBB00D0B4	OF_VLAN_EN [1383].EN[7:7]	1
0xBB00D0B4	OF_VLAN_EN [1384].EN[8:8]	1
0xBB00D0B4	OF_VLAN_EN [1385].EN[9:9]	1
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0xBB00D0B4	OF_VLAN_EN [1388].EN[12:12]	1
0xBB00D0B4	OF_VLAN_EN [1389].EN[13:13]	1
0xBB00D0B4	OF_VLAN_EN [1390].EN[14:14]	1
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0xBB00D0B4	OF_VLAN_EN [1392].EN[16:16]	1
0xBB00D0B4	OF_VLAN_EN [1393].EN[17:17]	1
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0xBB00D0B4	OF_VLAN_EN [1398].EN[22:22]	1
0xBB00D0B4	OF_VLAN_EN [1399].EN[23:23]	1
0xBB00D0B4	OF_VLAN_EN [1400].EN[24:24]	1
0xBB00D0B4	OF_VLAN_EN [1401].EN[25:25]	1
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0xBB00D0B4	OF_VLAN_EN [1403].EN[27:27]	1
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0xBB00D0B4	OF_VLAN_EN [1405].EN[29:29]	1
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0xBB00D0B4	OF_VLAN_EN [1407].EN[31:31]	1
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0xBB00D0B8	OF_VLAN_EN [1409].EN[1:1]	1
0xBB00D0B8	OF_VLAN_EN [1410].EN[2:2]	1
0xBB00D0B8	OF_VLAN_EN [1411].EN[3:3]	1
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0xBB00D0B8	OF_VLAN_EN [1413].EN[5:5]	1
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0xBB00D0B8	OF_VLAN_EN [1415].EN[7:7]	1
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0xBB00D0B8	OF_VLAN_EN [1417].EN[9:9]	1
0xBB00D0B8	OF_VLAN_EN [1418].EN[10:10]	1
0xBB00D0B8	OF_VLAN_EN [1419].EN[11:11]	1
0xBB00D0B8	OF_VLAN_EN [1420].EN[12:12]	1
0xBB00D0B8	OF_VLAN_EN [1421].EN[13:13]	1
0xBB00D0B8	OF_VLAN_EN [1422].EN[14:14]	1
0xBB00D0B8	OF_VLAN_EN [1423].EN[15:15]	1
0xBB00D0B8	OF_VLAN_EN [1424].EN[16:16]	1
0xBB00D0B8	OF_VLAN_EN [1425].EN[17:17]	1
0xBB00D0B8	OF_VLAN_EN [1426].EN[18:18]	1
0xBB00D0B8	OF_VLAN_EN [1427].EN[19:19]	1
0xBB00D0B8	OF_VLAN_EN [1428].EN[20:20]	1
0xBB00D0B8	OF_VLAN_EN [1429].EN[21:21]	1
0xBB00D0B8	OF_VLAN_EN [1430].EN[22:22]	1
0xBB00D0B8	OF_VLAN_EN [1431].EN[23:23]	1
0xBB00D0B8	OF_VLAN_EN [1432].EN[24:24]	1
0xBB00D0B8	OF_VLAN_EN [1433].EN[25:25]	1
0xBB00D0B8	OF_VLAN_EN [1434].EN[26:26]	1
0xBB00D0B8	OF_VLAN_EN [1435].EN[27:27]	1
0xBB00D0B8	OF_VLAN_EN [1436].EN[28:28]	1
0xBB00D0B8	OF_VLAN_EN [1437].EN[29:29]	1
0xBB00D0B8	OF_VLAN_EN [1438].EN[30:30]	1
0xBB00D0B8	OF_VLAN_EN [1439].EN[31:31]	1
0xBB00D0BC	OF_VLAN_EN [1440].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0BC	OF_VLAN_EN [1442].EN[2:2]	1
0xBB00D0BC	OF_VLAN_EN [1443].EN[3:3]	1
0xBB00D0BC	OF_VLAN_EN [1444].EN[4:4]	1
0xBB00D0BC	OF_VLAN_EN [1445].EN[5:5]	1
0xBB00D0BC	OF_VLAN_EN [1446].EN[6:6]	1
0xBB00D0BC	OF_VLAN_EN [1447].EN[7:7]	1
0xBB00D0BC	OF_VLAN_EN [1448].EN[8:8]	1
0xBB00D0BC	OF_VLAN_EN [1449].EN[9:9]	1
0xBB00D0BC	OF_VLAN_EN [1450].EN[10:10]	1
0xBB00D0BC	OF_VLAN_EN [1451].EN[11:11]	1
0xBB00D0BC	OF_VLAN_EN [1452].EN[12:12]	1
0xBB00D0BC	OF_VLAN_EN [1453].EN[13:13]	1
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0xBB00D0BC	OF_VLAN_EN [1458].EN[18:18]	1
0xBB00D0BC	OF_VLAN_EN [1459].EN[19:19]	1
0xBB00D0BC	OF_VLAN_EN [1460].EN[20:20]	1
0xBB00D0BC	OF_VLAN_EN [1461].EN[21:21]	1
0xBB00D0BC	OF_VLAN_EN [1462].EN[22:22]	1
0xBB00D0BC	OF_VLAN_EN [1463].EN[23:23]	1
0xBB00D0BC	OF_VLAN_EN [1464].EN[24:24]	1
0xBB00D0BC	OF_VLAN_EN [1465].EN[25:25]	1
0xBB00D0BC	OF_VLAN_EN [1466].EN[26:26]	1
0xBB00D0BC	OF_VLAN_EN [1467].EN[27:27]	1
0xBB00D0BC	OF_VLAN_EN [1468].EN[28:28]	1
0xBB00D0BC	OF_VLAN_EN [1469].EN[29:29]	1
0xBB00D0BC	OF_VLAN_EN [1470].EN[30:30]	1
0xBB00D0BC	OF_VLAN_EN [1471].EN[31:31]	1
0xBB00D0C0	OF_VLAN_EN [1472].EN[0:0]	1
0xBB00D0C0	OF_VLAN_EN [1473].EN[1:1]	1
0xBB00D0C0	OF_VLAN_EN [1474].EN[2:2]	1
0xBB00D0C0	OF_VLAN_EN [1475].EN[3:3]	1
0xBB00D0C0	OF_VLAN_EN [1476].EN[4:4]	1
0xBB00D0C0	OF_VLAN_EN [1477].EN[5:5]	1
0xBB00D0C0	OF_VLAN_EN [1478].EN[6:6]	1
0xBB00D0C0	OF_VLAN_EN [1479].EN[7:7]	1
0xBB00D0C0	OF_VLAN_EN [1480].EN[8:8]	1
0xBB00D0C0	OF_VLAN_EN [1481].EN[9:9]	1
0xBB00D0C0	OF_VLAN_EN [1482].EN[10:10]	1
0xBB00D0C0	OF_VLAN_EN [1483].EN[11:11]	1
0xBB00D0C0	OF_VLAN_EN [1484].EN[12:12]	1
0xBB00D0C0	OF_VLAN_EN [1485].EN[13:13]	1
0xBB00D0C0	OF_VLAN_EN [1486].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0C0	OF_VLAN_EN [1491].EN[19:19]	1
0xBB00D0C0	OF_VLAN_EN [1492].EN[20:20]	1
0xBB00D0C0	OF_VLAN_EN [1493].EN[21:21]	1
0xBB00D0C0	OF_VLAN_EN [1494].EN[22:22]	1
0xBB00D0C0	OF_VLAN_EN [1495].EN[23:23]	1
0xBB00D0C0	OF_VLAN_EN [1496].EN[24:24]	1
0xBB00D0C0	OF_VLAN_EN [1497].EN[25:25]	1
0xBB00D0C0	OF_VLAN_EN [1498].EN[26:26]	1
0xBB00D0C0	OF_VLAN_EN [1499].EN[27:27]	1
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0xBB00D0C0	OF_VLAN_EN [1502].EN[30:30]	1
0xBB00D0C0	OF_VLAN_EN [1503].EN[31:31]	1
0xBB00D0C4	OF_VLAN_EN [1504].EN[0:0]	1
0xBB00D0C4	OF_VLAN_EN [1505].EN[1:1]	1
0xBB00D0C4	OF_VLAN_EN [1506].EN[2:2]	1
0xBB00D0C4	OF_VLAN_EN [1507].EN[3:3]	1
0xBB00D0C4	OF_VLAN_EN [1508].EN[4:4]	1
0xBB00D0C4	OF_VLAN_EN [1509].EN[5:5]	1
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0xBB00D0C4	OF_VLAN_EN [1511].EN[7:7]	1
0xBB00D0C4	OF_VLAN_EN [1512].EN[8:8]	1
0xBB00D0C4	OF_VLAN_EN [1513].EN[9:9]	1
0xBB00D0C4	OF_VLAN_EN [1514].EN[10:10]	1
0xBB00D0C4	OF_VLAN_EN [1515].EN[11:11]	1
0xBB00D0C4	OF_VLAN_EN [1516].EN[12:12]	1
0xBB00D0C4	OF_VLAN_EN [1517].EN[13:13]	1
0xBB00D0C4	OF_VLAN_EN [1518].EN[14:14]	1
0xBB00D0C4	OF_VLAN_EN [1519].EN[15:15]	1
0xBB00D0C4	OF_VLAN_EN [1520].EN[16:16]	1
0xBB00D0C4	OF_VLAN_EN [1521].EN[17:17]	1
0xBB00D0C4	OF_VLAN_EN [1522].EN[18:18]	1
0xBB00D0C4	OF_VLAN_EN [1523].EN[19:19]	1
0xBB00D0C4	OF_VLAN_EN [1524].EN[20:20]	1
0xBB00D0C4	OF_VLAN_EN [1525].EN[21:21]	1
0xBB00D0C4	OF_VLAN_EN [1526].EN[22:22]	1
0xBB00D0C4	OF_VLAN_EN [1527].EN[23:23]	1
0xBB00D0C4	OF_VLAN_EN [1528].EN[24:24]	1
0xBB00D0C4	OF_VLAN_EN [1529].EN[25:25]	1
0xBB00D0C4	OF_VLAN_EN [1530].EN[26:26]	1
0xBB00D0C4	OF_VLAN_EN [1531].EN[27:27]	1
0xBB00D0C4	OF_VLAN_EN [1532].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0C8	OF_VLAN_EN [1537].EN[1:1]	1
0xBB00D0C8	OF_VLAN_EN [1538].EN[2:2]	1
0xBB00D0C8	OF_VLAN_EN [1539].EN[3:3]	1
0xBB00D0C8	OF_VLAN_EN [1540].EN[4:4]	1
0xBB00D0C8	OF_VLAN_EN [1541].EN[5:5]	1
0xBB00D0C8	OF_VLAN_EN [1542].EN[6:6]	1
0xBB00D0C8	OF_VLAN_EN [1543].EN[7:7]	1
0xBB00D0C8	OF_VLAN_EN [1544].EN[8:8]	1
0xBB00D0C8	OF_VLAN_EN [1545].EN[9:9]	1
0xBB00D0C8	OF_VLAN_EN [1546].EN[10:10]	1
0xBB00D0C8	OF_VLAN_EN [1547].EN[11:11]	1
0xBB00D0C8	OF_VLAN_EN [1548].EN[12:12]	1
0xBB00D0C8	OF_VLAN_EN [1549].EN[13:13]	1
0xBB00D0C8	OF_VLAN_EN [1550].EN[14:14]	1
0xBB00D0C8	OF_VLAN_EN [1551].EN[15:15]	1
0xBB00D0C8	OF_VLAN_EN [1552].EN[16:16]	1
0xBB00D0C8	OF_VLAN_EN [1553].EN[17:17]	1
0xBB00D0C8	OF_VLAN_EN [1554].EN[18:18]	1
0xBB00D0C8	OF_VLAN_EN [1555].EN[19:19]	1
0xBB00D0C8	OF_VLAN_EN [1556].EN[20:20]	1
0xBB00D0C8	OF_VLAN_EN [1557].EN[21:21]	1
0xBB00D0C8	OF_VLAN_EN [1558].EN[22:22]	1
0xBB00D0C8	OF_VLAN_EN [1559].EN[23:23]	1
0xBB00D0C8	OF_VLAN_EN [1560].EN[24:24]	1
0xBB00D0C8	OF_VLAN_EN [1561].EN[25:25]	1
0xBB00D0C8	OF_VLAN_EN [1562].EN[26:26]	1
0xBB00D0C8	OF_VLAN_EN [1563].EN[27:27]	1
0xBB00D0C8	OF_VLAN_EN [1564].EN[28:28]	1
0xBB00D0C8	OF_VLAN_EN [1565].EN[29:29]	1
0xBB00D0C8	OF_VLAN_EN [1566].EN[30:30]	1
0xBB00D0C8	OF_VLAN_EN [1567].EN[31:31]	1
0xBB00D0CC	OF_VLAN_EN [1568].EN[0:0]	1
0xBB00D0CC	OF_VLAN_EN [1569].EN[1:1]	1
0xBB00D0CC	OF_VLAN_EN [1570].EN[2:2]	1
0xBB00D0CC	OF_VLAN_EN [1571].EN[3:3]	1
0xBB00D0CC	OF_VLAN_EN [1572].EN[4:4]	1
0xBB00D0CC	OF_VLAN_EN [1573].EN[5:5]	1
0xBB00D0CC	OF_VLAN_EN [1574].EN[6:6]	1
0xBB00D0CC	OF_VLAN_EN [1575].EN[7:7]	1
0xBB00D0CC	OF_VLAN_EN [1576].EN[8:8]	1
0xBB00D0CC	OF_VLAN_EN [1577].EN[9:9]	1
0xBB00D0CC	OF_VLAN_EN [1578].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0CC	OF_VLAN_EN [1582].EN[14:14]	1
0xBB00D0CC	OF_VLAN_EN [1583].EN[15:15]	1
0xBB00D0CC	OF_VLAN_EN [1584].EN[16:16]	1
0xBB00D0CC	OF_VLAN_EN [1585].EN[17:17]	1
0xBB00D0CC	OF_VLAN_EN [1586].EN[18:18]	1
0xBB00D0CC	OF_VLAN_EN [1587].EN[19:19]	1
0xBB00D0CC	OF_VLAN_EN [1588].EN[20:20]	1
0xBB00D0CC	OF_VLAN_EN [1589].EN[21:21]	1
0xBB00D0CC	OF_VLAN_EN [1590].EN[22:22]	1
0xBB00D0CC	OF_VLAN_EN [1591].EN[23:23]	1
0xBB00D0CC	OF_VLAN_EN [1592].EN[24:24]	1
0xBB00D0CC	OF_VLAN_EN [1593].EN[25:25]	1
0xBB00D0CC	OF_VLAN_EN [1594].EN[26:26]	1
0xBB00D0CC	OF_VLAN_EN [1595].EN[27:27]	1
0xBB00D0CC	OF_VLAN_EN [1596].EN[28:28]	1
0xBB00D0CC	OF_VLAN_EN [1597].EN[29:29]	1
0xBB00D0CC	OF_VLAN_EN [1598].EN[30:30]	1
0xBB00D0CC	OF_VLAN_EN [1599].EN[31:31]	1
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0xBB00D0D0	OF_VLAN_EN [1602].EN[2:2]	1
0xBB00D0D0	OF_VLAN_EN [1603].EN[3:3]	1
0xBB00D0D0	OF_VLAN_EN [1604].EN[4:4]	1
0xBB00D0D0	OF_VLAN_EN [1605].EN[5:5]	1
0xBB00D0D0	OF_VLAN_EN [1606].EN[6:6]	1
0xBB00D0D0	OF_VLAN_EN [1607].EN[7:7]	1
0xBB00D0D0	OF_VLAN_EN [1608].EN[8:8]	1
0xBB00D0D0	OF_VLAN_EN [1609].EN[9:9]	1
0xBB00D0D0	OF_VLAN_EN [1610].EN[10:10]	1
0xBB00D0D0	OF_VLAN_EN [1611].EN[11:11]	1
0xBB00D0D0	OF_VLAN_EN [1612].EN[12:12]	1
0xBB00D0D0	OF_VLAN_EN [1613].EN[13:13]	1
0xBB00D0D0	OF_VLAN_EN [1614].EN[14:14]	1
0xBB00D0D0	OF_VLAN_EN [1615].EN[15:15]	1
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0xBB00D0D0	OF_VLAN_EN [1617].EN[17:17]	1
0xBB00D0D0	OF_VLAN_EN [1618].EN[18:18]	1
0xBB00D0D0	OF_VLAN_EN [1619].EN[19:19]	1
0xBB00D0D0	OF_VLAN_EN [1620].EN[20:20]	1
0xBB00D0D0	OF_VLAN_EN [1621].EN[21:21]	1
0xBB00D0D0	OF_VLAN_EN [1622].EN[22:22]	1
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0xBB00D0D0	OF_VLAN_EN [1630].EN[30:30]	1
0xBB00D0D0	OF_VLAN_EN [1631].EN[31:31]	1
0xBB00D0D4	OF_VLAN_EN [1632].EN[0:0]	1
0xBB00D0D4	OF_VLAN_EN [1633].EN[1:1]	1
0xBB00D0D4	OF_VLAN_EN [1634].EN[2:2]	1
0xBB00D0D4	OF_VLAN_EN [1635].EN[3:3]	1
0xBB00D0D4	OF_VLAN_EN [1636].EN[4:4]	1
0xBB00D0D4	OF_VLAN_EN [1637].EN[5:5]	1
0xBB00D0D4	OF_VLAN_EN [1638].EN[6:6]	1
0xBB00D0D4	OF_VLAN_EN [1639].EN[7:7]	1
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0xBB00D0D4	OF_VLAN_EN [1641].EN[9:9]	1
0xBB00D0D4	OF_VLAN_EN [1642].EN[10:10]	1
0xBB00D0D4	OF_VLAN_EN [1643].EN[11:11]	1
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0xBB00D0D4	OF_VLAN_EN [1647].EN[15:15]	1
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0xBB00D0D4	OF_VLAN_EN [1651].EN[19:19]	1
0xBB00D0D4	OF_VLAN_EN [1652].EN[20:20]	1
0xBB00D0D4	OF_VLAN_EN [1653].EN[21:21]	1
0xBB00D0D4	OF_VLAN_EN [1654].EN[22:22]	1
0xBB00D0D4	OF_VLAN_EN [1655].EN[23:23]	1
0xBB00D0D4	OF_VLAN_EN [1656].EN[24:24]	1
0xBB00D0D4	OF_VLAN_EN [1657].EN[25:25]	1
0xBB00D0D4	OF_VLAN_EN [1658].EN[26:26]	1
0xBB00D0D4	OF_VLAN_EN [1659].EN[27:27]	1
0xBB00D0D4	OF_VLAN_EN [1660].EN[28:28]	1
0xBB00D0D4	OF_VLAN_EN [1661].EN[29:29]	1
0xBB00D0D4	OF_VLAN_EN [1662].EN[30:30]	1
0xBB00D0D4	OF_VLAN_EN [1663].EN[31:31]	1
0xBB00D0D8	OF_VLAN_EN [1664].EN[0:0]	1
0xBB00D0D8	OF_VLAN_EN [1665].EN[1:1]	1
0xBB00D0D8	OF_VLAN_EN [1666].EN[2:2]	1
0xBB00D0D8	OF_VLAN_EN [1667].EN[3:3]	1
0xBB00D0D8	OF_VLAN_EN [1668].EN[4:4]	1
0xBB00D0D8	OF_VLAN_EN [1669].EN[5:5]	1
0xBB00D0D8	OF_VLAN_EN [1670].EN[6:6]	1

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0xBB00D0D8	OF_VLAN_EN [1674].EN[10:10]	1
0xBB00D0D8	OF_VLAN_EN [1675].EN[11:11]	1
0xBB00D0D8	OF_VLAN_EN [1676].EN[12:12]	1
0xBB00D0D8	OF_VLAN_EN [1677].EN[13:13]	1
0xBB00D0D8	OF_VLAN_EN [1678].EN[14:14]	1
0xBB00D0D8	OF_VLAN_EN [1679].EN[15:15]	1
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0xBB00D0D8	OF_VLAN_EN [1681].EN[17:17]	1
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0xBB00D0D8	OF_VLAN_EN [1683].EN[19:19]	1
0xBB00D0D8	OF_VLAN_EN [1684].EN[20:20]	1
0xBB00D0D8	OF_VLAN_EN [1685].EN[21:21]	1
0xBB00D0D8	OF_VLAN_EN [1686].EN[22:22]	1
0xBB00D0D8	OF_VLAN_EN [1687].EN[23:23]	1
0xBB00D0D8	OF_VLAN_EN [1688].EN[24:24]	1
0xBB00D0D8	OF_VLAN_EN [1689].EN[25:25]	1
0xBB00D0D8	OF_VLAN_EN [1690].EN[26:26]	1
0xBB00D0D8	OF_VLAN_EN [1691].EN[27:27]	1
0xBB00D0D8	OF_VLAN_EN [1692].EN[28:28]	1
0xBB00D0D8	OF_VLAN_EN [1693].EN[29:29]	1
0xBB00D0D8	OF_VLAN_EN [1694].EN[30:30]	1
0xBB00D0D8	OF_VLAN_EN [1695].EN[31:31]	1
0xBB00D0DC	OF_VLAN_EN [1696].EN[0:0]	1
0xBB00D0DC	OF_VLAN_EN [1697].EN[1:1]	1
0xBB00D0DC	OF_VLAN_EN [1698].EN[2:2]	1
0xBB00D0DC	OF_VLAN_EN [1699].EN[3:3]	1
0xBB00D0DC	OF_VLAN_EN [1700].EN[4:4]	1
0xBB00D0DC	OF_VLAN_EN [1701].EN[5:5]	1
0xBB00D0DC	OF_VLAN_EN [1702].EN[6:6]	1
0xBB00D0DC	OF_VLAN_EN [1703].EN[7:7]	1
0xBB00D0DC	OF_VLAN_EN [1704].EN[8:8]	1
0xBB00D0DC	OF_VLAN_EN [1705].EN[9:9]	1
0xBB00D0DC	OF_VLAN_EN [1706].EN[10:10]	1
0xBB00D0DC	OF_VLAN_EN [1707].EN[11:11]	1
0xBB00D0DC	OF_VLAN_EN [1708].EN[12:12]	1
0xBB00D0DC	OF_VLAN_EN [1709].EN[13:13]	1
0xBB00D0DC	OF_VLAN_EN [1710].EN[14:14]	1
0xBB00D0DC	OF_VLAN_EN [1711].EN[15:15]	1
0xBB00D0DC	OF_VLAN_EN [1712].EN[16:16]	1
0xBB00D0DC	OF_VLAN_EN [1713].EN[17:17]	1
0xBB00D0DC	OF_VLAN_EN [1714].EN[18:18]	1
0xBB00D0DC	OF_VLAN_EN [1715].EN[19:19]	1
0xBB00D0DC	OF_VLAN_EN [1716].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D0DC	OF_VLAN_EN [1717].EN[21:21]	1
0xBB00D0DC	OF_VLAN_EN [1718].EN[22:22]	1
0xBB00D0DC	OF_VLAN_EN [1719].EN[23:23]	1
0xBB00D0DC	OF_VLAN_EN [1720].EN[24:24]	1
0xBB00D0DC	OF_VLAN_EN [1721].EN[25:25]	1
0xBB00D0DC	OF_VLAN_EN [1722].EN[26:26]	1
0xBB00D0DC	OF_VLAN_EN [1723].EN[27:27]	1
0xBB00D0DC	OF_VLAN_EN [1724].EN[28:28]	1
0xBB00D0DC	OF_VLAN_EN [1725].EN[29:29]	1
0xBB00D0DC	OF_VLAN_EN [1726].EN[30:30]	1
0xBB00D0DC	OF_VLAN_EN [1727].EN[31:31]	1
0xBB00D0E0	OF_VLAN_EN [1728].EN[0:0]	1
0xBB00D0E0	OF_VLAN_EN [1729].EN[1:1]	1
0xBB00D0E0	OF_VLAN_EN [1730].EN[2:2]	1
0xBB00D0E0	OF_VLAN_EN [1731].EN[3:3]	1
0xBB00D0E0	OF_VLAN_EN [1732].EN[4:4]	1
0xBB00D0E0	OF_VLAN_EN [1733].EN[5:5]	1
0xBB00D0E0	OF_VLAN_EN [1734].EN[6:6]	1
0xBB00D0E0	OF_VLAN_EN [1735].EN[7:7]	1
0xBB00D0E0	OF_VLAN_EN [1736].EN[8:8]	1
0xBB00D0E0	OF_VLAN_EN [1737].EN[9:9]	1
0xBB00D0E0	OF_VLAN_EN [1738].EN[10:10]	1
0xBB00D0E0	OF_VLAN_EN [1739].EN[11:11]	1
0xBB00D0E0	OF_VLAN_EN [1740].EN[12:12]	1
0xBB00D0E0	OF_VLAN_EN [1741].EN[13:13]	1
0xBB00D0E0	OF_VLAN_EN [1742].EN[14:14]	1
0xBB00D0E0	OF_VLAN_EN [1743].EN[15:15]	1
0xBB00D0E0	OF_VLAN_EN [1744].EN[16:16]	1
0xBB00D0E0	OF_VLAN_EN [1745].EN[17:17]	1
0xBB00D0E0	OF_VLAN_EN [1746].EN[18:18]	1
0xBB00D0E0	OF_VLAN_EN [1747].EN[19:19]	1
0xBB00D0E0	OF_VLAN_EN [1748].EN[20:20]	1
0xBB00D0E0	OF_VLAN_EN [1749].EN[21:21]	1
0xBB00D0E0	OF_VLAN_EN [1750].EN[22:22]	1
0xBB00D0E0	OF_VLAN_EN [1751].EN[23:23]	1
0xBB00D0E0	OF_VLAN_EN [1752].EN[24:24]	1
0xBB00D0E0	OF_VLAN_EN [1753].EN[25:25]	1
0xBB00D0E0	OF_VLAN_EN [1754].EN[26:26]	1
0xBB00D0E0	OF_VLAN_EN [1755].EN[27:27]	1
0xBB00D0E0	OF_VLAN_EN [1756].EN[28:28]	1
0xBB00D0E0	OF_VLAN_EN [1757].EN[29:29]	1
0xBB00D0E0	OF_VLAN_EN [1758].EN[30:30]	1
0xBB00D0E0	OF_VLAN_EN [1759].EN[31:31]	1
0xBB00D0E4	OF_VLAN_EN [1760].EN[0:0]	1
0xBB00D0E4	OF_VLAN_EN [1761].EN[1:1]	1
0xBB00D0E4	OF_VLAN_EN [1762].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D0E4	OF_VLAN_EN [1763].EN[3:3]	1
0xBB00D0E4	OF_VLAN_EN [1764].EN[4:4]	1
0xBB00D0E4	OF_VLAN_EN [1765].EN[5:5]	1
0xBB00D0E4	OF_VLAN_EN [1766].EN[6:6]	1
0xBB00D0E4	OF_VLAN_EN [1767].EN[7:7]	1
0xBB00D0E4	OF_VLAN_EN [1768].EN[8:8]	1
0xBB00D0E4	OF_VLAN_EN [1769].EN[9:9]	1
0xBB00D0E4	OF_VLAN_EN [1770].EN[10:10]	1
0xBB00D0E4	OF_VLAN_EN [1771].EN[11:11]	1
0xBB00D0E4	OF_VLAN_EN [1772].EN[12:12]	1
0xBB00D0E4	OF_VLAN_EN [1773].EN[13:13]	1
0xBB00D0E4	OF_VLAN_EN [1774].EN[14:14]	1
0xBB00D0E4	OF_VLAN_EN [1775].EN[15:15]	1
0xBB00D0E4	OF_VLAN_EN [1776].EN[16:16]	1
0xBB00D0E4	OF_VLAN_EN [1777].EN[17:17]	1
0xBB00D0E4	OF_VLAN_EN [1778].EN[18:18]	1
0xBB00D0E4	OF_VLAN_EN [1779].EN[19:19]	1
0xBB00D0E4	OF_VLAN_EN [1780].EN[20:20]	1
0xBB00D0E4	OF_VLAN_EN [1781].EN[21:21]	1
0xBB00D0E4	OF_VLAN_EN [1782].EN[22:22]	1
0xBB00D0E4	OF_VLAN_EN [1783].EN[23:23]	1
0xBB00D0E4	OF_VLAN_EN [1784].EN[24:24]	1
0xBB00D0E4	OF_VLAN_EN [1785].EN[25:25]	1
0xBB00D0E4	OF_VLAN_EN [1786].EN[26:26]	1
0xBB00D0E4	OF_VLAN_EN [1787].EN[27:27]	1
0xBB00D0E4	OF_VLAN_EN [1788].EN[28:28]	1
0xBB00D0E4	OF_VLAN_EN [1789].EN[29:29]	1
0xBB00D0E4	OF_VLAN_EN [1790].EN[30:30]	1
0xBB00D0E4	OF_VLAN_EN [1791].EN[31:31]	1
0xBB00D0E8	OF_VLAN_EN [1792].EN[0:0]	1
0xBB00D0E8	OF_VLAN_EN [1793].EN[1:1]	1
0xBB00D0E8	OF_VLAN_EN [1794].EN[2:2]	1
0xBB00D0E8	OF_VLAN_EN [1795].EN[3:3]	1
0xBB00D0E8	OF_VLAN_EN [1796].EN[4:4]	1
0xBB00D0E8	OF_VLAN_EN [1797].EN[5:5]	1
0xBB00D0E8	OF_VLAN_EN [1798].EN[6:6]	1
0xBB00D0E8	OF_VLAN_EN [1799].EN[7:7]	1
0xBB00D0E8	OF_VLAN_EN [1800].EN[8:8]	1
0xBB00D0E8	OF_VLAN_EN [1801].EN[9:9]	1
0xBB00D0E8	OF_VLAN_EN [1802].EN[10:10]	1
0xBB00D0E8	OF_VLAN_EN [1803].EN[11:11]	1
0xBB00D0E8	OF_VLAN_EN [1804].EN[12:12]	1
0xBB00D0E8	OF_VLAN_EN [1805].EN[13:13]	1
0xBB00D0E8	OF_VLAN_EN [1806].EN[14:14]	1
0xBB00D0E8	OF_VLAN_EN [1807].EN[15:15]	1
0xBB00D0E8	OF_VLAN_EN [1808].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D0E8	OF_VLAN_EN [1809].EN[17:17]	1
0xBB00D0E8	OF_VLAN_EN [1810].EN[18:18]	1
0xBB00D0E8	OF_VLAN_EN [1811].EN[19:19]	1
0xBB00D0E8	OF_VLAN_EN [1812].EN[20:20]	1
0xBB00D0E8	OF_VLAN_EN [1813].EN[21:21]	1
0xBB00D0E8	OF_VLAN_EN [1814].EN[22:22]	1
0xBB00D0E8	OF_VLAN_EN [1815].EN[23:23]	1
0xBB00D0E8	OF_VLAN_EN [1816].EN[24:24]	1
0xBB00D0E8	OF_VLAN_EN [1817].EN[25:25]	1
0xBB00D0E8	OF_VLAN_EN [1818].EN[26:26]	1
0xBB00D0E8	OF_VLAN_EN [1819].EN[27:27]	1
0xBB00D0E8	OF_VLAN_EN [1820].EN[28:28]	1
0xBB00D0E8	OF_VLAN_EN [1821].EN[29:29]	1
0xBB00D0E8	OF_VLAN_EN [1822].EN[30:30]	1
0xBB00D0E8	OF_VLAN_EN [1823].EN[31:31]	1
0xBB00D0EC	OF_VLAN_EN [1824].EN[0:0]	1
0xBB00D0EC	OF_VLAN_EN [1825].EN[1:1]	1
0xBB00D0EC	OF_VLAN_EN [1826].EN[2:2]	1
0xBB00D0EC	OF_VLAN_EN [1827].EN[3:3]	1
0xBB00D0EC	OF_VLAN_EN [1828].EN[4:4]	1
0xBB00D0EC	OF_VLAN_EN [1829].EN[5:5]	1
0xBB00D0EC	OF_VLAN_EN [1830].EN[6:6]	1
0xBB00D0EC	OF_VLAN_EN [1831].EN[7:7]	1
0xBB00D0EC	OF_VLAN_EN [1832].EN[8:8]	1
0xBB00D0EC	OF_VLAN_EN [1833].EN[9:9]	1
0xBB00D0EC	OF_VLAN_EN [1834].EN[10:10]	1
0xBB00D0EC	OF_VLAN_EN [1835].EN[11:11]	1
0xBB00D0EC	OF_VLAN_EN [1836].EN[12:12]	1
0xBB00D0EC	OF_VLAN_EN [1837].EN[13:13]	1
0xBB00D0EC	OF_VLAN_EN [1838].EN[14:14]	1
0xBB00D0EC	OF_VLAN_EN [1839].EN[15:15]	1
0xBB00D0EC	OF_VLAN_EN [1840].EN[16:16]	1
0xBB00D0EC	OF_VLAN_EN [1841].EN[17:17]	1
0xBB00D0EC	OF_VLAN_EN [1842].EN[18:18]	1
0xBB00D0EC	OF_VLAN_EN [1843].EN[19:19]	1
0xBB00D0EC	OF_VLAN_EN [1844].EN[20:20]	1
0xBB00D0EC	OF_VLAN_EN [1845].EN[21:21]	1
0xBB00D0EC	OF_VLAN_EN [1846].EN[22:22]	1
0xBB00D0EC	OF_VLAN_EN [1847].EN[23:23]	1
0xBB00D0EC	OF_VLAN_EN [1848].EN[24:24]	1
0xBB00D0EC	OF_VLAN_EN [1849].EN[25:25]	1
0xBB00D0EC	OF_VLAN_EN [1850].EN[26:26]	1
0xBB00D0EC	OF_VLAN_EN [1851].EN[27:27]	1
0xBB00D0EC	OF_VLAN_EN [1852].EN[28:28]	1
0xBB00D0EC	OF_VLAN_EN [1853].EN[29:29]	1
0xBB00D0EC	OF_VLAN_EN [1854].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D0EC	OF_VLAN_EN [1855].EN[31:31]	1
0xBB00D0F0	OF_VLAN_EN [1856].EN[0:0]	1
0xBB00D0F0	OF_VLAN_EN [1857].EN[1:1]	1
0xBB00D0F0	OF_VLAN_EN [1858].EN[2:2]	1
0xBB00D0F0	OF_VLAN_EN [1859].EN[3:3]	1
0xBB00D0F0	OF_VLAN_EN [1860].EN[4:4]	1
0xBB00D0F0	OF_VLAN_EN [1861].EN[5:5]	1
0xBB00D0F0	OF_VLAN_EN [1862].EN[6:6]	1
0xBB00D0F0	OF_VLAN_EN [1863].EN[7:7]	1
0xBB00D0F0	OF_VLAN_EN [1864].EN[8:8]	1
0xBB00D0F0	OF_VLAN_EN [1865].EN[9:9]	1
0xBB00D0F0	OF_VLAN_EN [1866].EN[10:10]	1
0xBB00D0F0	OF_VLAN_EN [1867].EN[11:11]	1
0xBB00D0F0	OF_VLAN_EN [1868].EN[12:12]	1
0xBB00D0F0	OF_VLAN_EN [1869].EN[13:13]	1
0xBB00D0F0	OF_VLAN_EN [1870].EN[14:14]	1
0xBB00D0F0	OF_VLAN_EN [1871].EN[15:15]	1
0xBB00D0F0	OF_VLAN_EN [1872].EN[16:16]	1
0xBB00D0F0	OF_VLAN_EN [1873].EN[17:17]	1
0xBB00D0F0	OF_VLAN_EN [1874].EN[18:18]	1
0xBB00D0F0	OF_VLAN_EN [1875].EN[19:19]	1
0xBB00D0F0	OF_VLAN_EN [1876].EN[20:20]	1
0xBB00D0F0	OF_VLAN_EN [1877].EN[21:21]	1
0xBB00D0F0	OF_VLAN_EN [1878].EN[22:22]	1
0xBB00D0F0	OF_VLAN_EN [1879].EN[23:23]	1
0xBB00D0F0	OF_VLAN_EN [1880].EN[24:24]	1
0xBB00D0F0	OF_VLAN_EN [1881].EN[25:25]	1
0xBB00D0F0	OF_VLAN_EN [1882].EN[26:26]	1
0xBB00D0F0	OF_VLAN_EN [1883].EN[27:27]	1
0xBB00D0F0	OF_VLAN_EN [1884].EN[28:28]	1
0xBB00D0F0	OF_VLAN_EN [1885].EN[29:29]	1
0xBB00D0F0	OF_VLAN_EN [1886].EN[30:30]	1
0xBB00D0F0	OF_VLAN_EN [1887].EN[31:31]	1
0xBB00D0F4	OF_VLAN_EN [1888].EN[0:0]	1
0xBB00D0F4	OF_VLAN_EN [1889].EN[1:1]	1
0xBB00D0F4	OF_VLAN_EN [1890].EN[2:2]	1
0xBB00D0F4	OF_VLAN_EN [1891].EN[3:3]	1
0xBB00D0F4	OF_VLAN_EN [1892].EN[4:4]	1
0xBB00D0F4	OF_VLAN_EN [1893].EN[5:5]	1
0xBB00D0F4	OF_VLAN_EN [1894].EN[6:6]	1
0xBB00D0F4	OF_VLAN_EN [1895].EN[7:7]	1
0xBB00D0F4	OF_VLAN_EN [1896].EN[8:8]	1
0xBB00D0F4	OF_VLAN_EN [1897].EN[9:9]	1
0xBB00D0F4	OF_VLAN_EN [1898].EN[10:10]	1
0xBB00D0F4	OF_VLAN_EN [1899].EN[11:11]	1
0xBB00D0F4	OF_VLAN_EN [1900].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D0F4	OF_VLAN_EN [1901].EN[13:13]	1
0xBB00D0F4	OF_VLAN_EN [1902].EN[14:14]	1
0xBB00D0F4	OF_VLAN_EN [1903].EN[15:15]	1
0xBB00D0F4	OF_VLAN_EN [1904].EN[16:16]	1
0xBB00D0F4	OF_VLAN_EN [1905].EN[17:17]	1
0xBB00D0F4	OF_VLAN_EN [1906].EN[18:18]	1
0xBB00D0F4	OF_VLAN_EN [1907].EN[19:19]	1
0xBB00D0F4	OF_VLAN_EN [1908].EN[20:20]	1
0xBB00D0F4	OF_VLAN_EN [1909].EN[21:21]	1
0xBB00D0F4	OF_VLAN_EN [1910].EN[22:22]	1
0xBB00D0F4	OF_VLAN_EN [1911].EN[23:23]	1
0xBB00D0F4	OF_VLAN_EN [1912].EN[24:24]	1
0xBB00D0F4	OF_VLAN_EN [1913].EN[25:25]	1
0xBB00D0F4	OF_VLAN_EN [1914].EN[26:26]	1
0xBB00D0F4	OF_VLAN_EN [1915].EN[27:27]	1
0xBB00D0F4	OF_VLAN_EN [1916].EN[28:28]	1
0xBB00D0F4	OF_VLAN_EN [1917].EN[29:29]	1
0xBB00D0F4	OF_VLAN_EN [1918].EN[30:30]	1
0xBB00D0F4	OF_VLAN_EN [1919].EN[31:31]	1
0xBB00D0F8	OF_VLAN_EN [1920].EN[0:0]	1
0xBB00D0F8	OF_VLAN_EN [1921].EN[1:1]	1
0xBB00D0F8	OF_VLAN_EN [1922].EN[2:2]	1
0xBB00D0F8	OF_VLAN_EN [1923].EN[3:3]	1
0xBB00D0F8	OF_VLAN_EN [1924].EN[4:4]	1
0xBB00D0F8	OF_VLAN_EN [1925].EN[5:5]	1
0xBB00D0F8	OF_VLAN_EN [1926].EN[6:6]	1
0xBB00D0F8	OF_VLAN_EN [1927].EN[7:7]	1
0xBB00D0F8	OF_VLAN_EN [1928].EN[8:8]	1
0xBB00D0F8	OF_VLAN_EN [1929].EN[9:9]	1
0xBB00D0F8	OF_VLAN_EN [1930].EN[10:10]	1
0xBB00D0F8	OF_VLAN_EN [1931].EN[11:11]	1
0xBB00D0F8	OF_VLAN_EN [1932].EN[12:12]	1
0xBB00D0F8	OF_VLAN_EN [1933].EN[13:13]	1
0xBB00D0F8	OF_VLAN_EN [1934].EN[14:14]	1
0xBB00D0F8	OF_VLAN_EN [1935].EN[15:15]	1
0xBB00D0F8	OF_VLAN_EN [1936].EN[16:16]	1
0xBB00D0F8	OF_VLAN_EN [1937].EN[17:17]	1
0xBB00D0F8	OF_VLAN_EN [1938].EN[18:18]	1
0xBB00D0F8	OF_VLAN_EN [1939].EN[19:19]	1
0xBB00D0F8	OF_VLAN_EN [1940].EN[20:20]	1
0xBB00D0F8	OF_VLAN_EN [1941].EN[21:21]	1
0xBB00D0F8	OF_VLAN_EN [1942].EN[22:22]	1
0xBB00D0F8	OF_VLAN_EN [1943].EN[23:23]	1
0xBB00D0F8	OF_VLAN_EN [1944].EN[24:24]	1
0xBB00D0F8	OF_VLAN_EN [1945].EN[25:25]	1
0xBB00D0F8	OF_VLAN_EN [1946].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D0F8	OF_VLAN_EN [1948].EN[28:28]	1
0xBB00D0F8	OF_VLAN_EN [1949].EN[29:29]	1
0xBB00D0F8	OF_VLAN_EN [1950].EN[30:30]	1
0xBB00D0F8	OF_VLAN_EN [1951].EN[31:31]	1
0xBB00D0FC	OF_VLAN_EN [1952].EN[0:0]	1
0xBB00D0FC	OF_VLAN_EN [1953].EN[1:1]	1
0xBB00D0FC	OF_VLAN_EN [1954].EN[2:2]	1
0xBB00D0FC	OF_VLAN_EN [1955].EN[3:3]	1
0xBB00D0FC	OF_VLAN_EN [1956].EN[4:4]	1
0xBB00D0FC	OF_VLAN_EN [1957].EN[5:5]	1
0xBB00D0FC	OF_VLAN_EN [1958].EN[6:6]	1
0xBB00D0FC	OF_VLAN_EN [1959].EN[7:7]	1
0xBB00D0FC	OF_VLAN_EN [1960].EN[8:8]	1
0xBB00D0FC	OF_VLAN_EN [1961].EN[9:9]	1
0xBB00D0FC	OF_VLAN_EN [1962].EN[10:10]	1
0xBB00D0FC	OF_VLAN_EN [1963].EN[11:11]	1
0xBB00D0FC	OF_VLAN_EN [1964].EN[12:12]	1
0xBB00D0FC	OF_VLAN_EN [1965].EN[13:13]	1
0xBB00D0FC	OF_VLAN_EN [1966].EN[14:14]	1
0xBB00D0FC	OF_VLAN_EN [1967].EN[15:15]	1
0xBB00D0FC	OF_VLAN_EN [1968].EN[16:16]	1
0xBB00D0FC	OF_VLAN_EN [1969].EN[17:17]	1
0xBB00D0FC	OF_VLAN_EN [1970].EN[18:18]	1
0xBB00D0FC	OF_VLAN_EN [1971].EN[19:19]	1
0xBB00D0FC	OF_VLAN_EN [1972].EN[20:20]	1
0xBB00D0FC	OF_VLAN_EN [1973].EN[21:21]	1
0xBB00D0FC	OF_VLAN_EN [1974].EN[22:22]	1
0xBB00D0FC	OF_VLAN_EN [1975].EN[23:23]	1
0xBB00D0FC	OF_VLAN_EN [1976].EN[24:24]	1
0xBB00D0FC	OF_VLAN_EN [1977].EN[25:25]	1
0xBB00D0FC	OF_VLAN_EN [1978].EN[26:26]	1
0xBB00D0FC	OF_VLAN_EN [1979].EN[27:27]	1
0xBB00D0FC	OF_VLAN_EN [1980].EN[28:28]	1
0xBB00D0FC	OF_VLAN_EN [1981].EN[29:29]	1
0xBB00D0FC	OF_VLAN_EN [1982].EN[30:30]	1
0xBB00D0FC	OF_VLAN_EN [1983].EN[31:31]	1
0xBB00D100	OF_VLAN_EN [1984].EN[0:0]	1
0xBB00D100	OF_VLAN_EN [1985].EN[1:1]	1
0xBB00D100	OF_VLAN_EN [1986].EN[2:2]	1
0xBB00D100	OF_VLAN_EN [1987].EN[3:3]	1
0xBB00D100	OF_VLAN_EN [1988].EN[4:4]	1
0xBB00D100	OF_VLAN_EN [1989].EN[5:5]	1
0xBB00D100	OF_VLAN_EN [1990].EN[6:6]	1
0xBB00D100	OF_VLAN_EN [1991].EN[7:7]	1
0xBB00D100	OF_VLAN_EN [1992].EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D100	OF_VLAN_EN [1994].EN[10:10]	1
0xBB00D100	OF_VLAN_EN [1995].EN[11:11]	1
0xBB00D100	OF_VLAN_EN [1996].EN[12:12]	1
0xBB00D100	OF_VLAN_EN [1997].EN[13:13]	1
0xBB00D100	OF_VLAN_EN [1998].EN[14:14]	1
0xBB00D100	OF_VLAN_EN [1999].EN[15:15]	1
0xBB00D100	OF_VLAN_EN [2000].EN[16:16]	1
0xBB00D100	OF_VLAN_EN [2001].EN[17:17]	1
0xBB00D100	OF_VLAN_EN [2002].EN[18:18]	1
0xBB00D100	OF_VLAN_EN [2003].EN[19:19]	1
0xBB00D100	OF_VLAN_EN [2004].EN[20:20]	1
0xBB00D100	OF_VLAN_EN [2005].EN[21:21]	1
0xBB00D100	OF_VLAN_EN [2006].EN[22:22]	1
0xBB00D100	OF_VLAN_EN [2007].EN[23:23]	1
0xBB00D100	OF_VLAN_EN [2008].EN[24:24]	1
0xBB00D100	OF_VLAN_EN [2009].EN[25:25]	1
0xBB00D100	OF_VLAN_EN [2010].EN[26:26]	1
0xBB00D100	OF_VLAN_EN [2011].EN[27:27]	1
0xBB00D100	OF_VLAN_EN [2012].EN[28:28]	1
0xBB00D100	OF_VLAN_EN [2013].EN[29:29]	1
0xBB00D100	OF_VLAN_EN [2014].EN[30:30]	1
0xBB00D100	OF_VLAN_EN [2015].EN[31:31]	1
0xBB00D104	OF_VLAN_EN [2016].EN[0:0]	1
0xBB00D104	OF_VLAN_EN [2017].EN[1:1]	1
0xBB00D104	OF_VLAN_EN [2018].EN[2:2]	1
0xBB00D104	OF_VLAN_EN [2019].EN[3:3]	1
0xBB00D104	OF_VLAN_EN [2020].EN[4:4]	1
0xBB00D104	OF_VLAN_EN [2021].EN[5:5]	1
0xBB00D104	OF_VLAN_EN [2022].EN[6:6]	1
0xBB00D104	OF_VLAN_EN [2023].EN[7:7]	1
0xBB00D104	OF_VLAN_EN [2024].EN[8:8]	1
0xBB00D104	OF_VLAN_EN [2025].EN[9:9]	1
0xBB00D104	OF_VLAN_EN [2026].EN[10:10]	1
0xBB00D104	OF_VLAN_EN [2027].EN[11:11]	1
0xBB00D104	OF_VLAN_EN [2028].EN[12:12]	1
0xBB00D104	OF_VLAN_EN [2029].EN[13:13]	1
0xBB00D104	OF_VLAN_EN [2030].EN[14:14]	1
0xBB00D104	OF_VLAN_EN [2031].EN[15:15]	1
0xBB00D104	OF_VLAN_EN [2032].EN[16:16]	1
0xBB00D104	OF_VLAN_EN [2033].EN[17:17]	1
0xBB00D104	OF_VLAN_EN [2034].EN[18:18]	1
0xBB00D104	OF_VLAN_EN [2035].EN[19:19]	1
0xBB00D104	OF_VLAN_EN [2036].EN[20:20]	1
0xBB00D104	OF_VLAN_EN [2037].EN[21:21]	1
0xBB00D104	OF_VLAN_EN [2038].EN[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D104	OF_VLAN_EN [2040].EN[24:24]	1
0xBB00D104	OF_VLAN_EN [2041].EN[25:25]	1
0xBB00D104	OF_VLAN_EN [2042].EN[26:26]	1
0xBB00D104	OF_VLAN_EN [2043].EN[27:27]	1
0xBB00D104	OF_VLAN_EN [2044].EN[28:28]	1
0xBB00D104	OF_VLAN_EN [2045].EN[29:29]	1
0xBB00D104	OF_VLAN_EN [2046].EN[30:30]	1
0xBB00D104	OF_VLAN_EN [2047].EN[31:31]	1
0xBB00D108	OF_VLAN_EN [2048].EN[0:0]	1
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0xBB00D108	OF_VLAN_EN [2050].EN[2:2]	1
0xBB00D108	OF_VLAN_EN [2051].EN[3:3]	1
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0xBB00D108	OF_VLAN_EN [2054].EN[6:6]	1
0xBB00D108	OF_VLAN_EN [2055].EN[7:7]	1
0xBB00D108	OF_VLAN_EN [2056].EN[8:8]	1
0xBB00D108	OF_VLAN_EN [2057].EN[9:9]	1
0xBB00D108	OF_VLAN_EN [2058].EN[10:10]	1
0xBB00D108	OF_VLAN_EN [2059].EN[11:11]	1
0xBB00D108	OF_VLAN_EN [2060].EN[12:12]	1
0xBB00D108	OF_VLAN_EN [2061].EN[13:13]	1
0xBB00D108	OF_VLAN_EN [2062].EN[14:14]	1
0xBB00D108	OF_VLAN_EN [2063].EN[15:15]	1
0xBB00D108	OF_VLAN_EN [2064].EN[16:16]	1
0xBB00D108	OF_VLAN_EN [2065].EN[17:17]	1
0xBB00D108	OF_VLAN_EN [2066].EN[18:18]	1
0xBB00D108	OF_VLAN_EN [2067].EN[19:19]	1
0xBB00D108	OF_VLAN_EN [2068].EN[20:20]	1
0xBB00D108	OF_VLAN_EN [2069].EN[21:21]	1
0xBB00D108	OF_VLAN_EN [2070].EN[22:22]	1
0xBB00D108	OF_VLAN_EN [2071].EN[23:23]	1
0xBB00D108	OF_VLAN_EN [2072].EN[24:24]	1
0xBB00D108	OF_VLAN_EN [2073].EN[25:25]	1
0xBB00D108	OF_VLAN_EN [2074].EN[26:26]	1
0xBB00D108	OF_VLAN_EN [2075].EN[27:27]	1
0xBB00D108	OF_VLAN_EN [2076].EN[28:28]	1
0xBB00D108	OF_VLAN_EN [2077].EN[29:29]	1
0xBB00D108	OF_VLAN_EN [2078].EN[30:30]	1
0xBB00D108	OF_VLAN_EN [2079].EN[31:31]	1
0xBB00D10C	OF_VLAN_EN [2080].EN[0:0]	1
0xBB00D10C	OF_VLAN_EN [2081].EN[1:1]	1
0xBB00D10C	OF_VLAN_EN [2082].EN[2:2]	1
0xBB00D10C	OF_VLAN_EN [2083].EN[3:3]	1
0xBB00D10C	OF_VLAN_EN [2084].EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D10C	OF_VLAN_EN [2086].EN[6:6]	1
0xBB00D10C	OF_VLAN_EN [2087].EN[7:7]	1
0xBB00D10C	OF_VLAN_EN [2088].EN[8:8]	1
0xBB00D10C	OF_VLAN_EN [2089].EN[9:9]	1
0xBB00D10C	OF_VLAN_EN [2090].EN[10:10]	1
0xBB00D10C	OF_VLAN_EN [2091].EN[11:11]	1
0xBB00D10C	OF_VLAN_EN [2092].EN[12:12]	1
0xBB00D10C	OF_VLAN_EN [2093].EN[13:13]	1
0xBB00D10C	OF_VLAN_EN [2094].EN[14:14]	1
0xBB00D10C	OF_VLAN_EN [2095].EN[15:15]	1
0xBB00D10C	OF_VLAN_EN [2096].EN[16:16]	1
0xBB00D10C	OF_VLAN_EN [2097].EN[17:17]	1
0xBB00D10C	OF_VLAN_EN [2098].EN[18:18]	1
0xBB00D10C	OF_VLAN_EN [2099].EN[19:19]	1
0xBB00D10C	OF_VLAN_EN [2100].EN[20:20]	1
0xBB00D10C	OF_VLAN_EN [2101].EN[21:21]	1
0xBB00D10C	OF_VLAN_EN [2102].EN[22:22]	1
0xBB00D10C	OF_VLAN_EN [2103].EN[23:23]	1
0xBB00D10C	OF_VLAN_EN [2104].EN[24:24]	1
0xBB00D10C	OF_VLAN_EN [2105].EN[25:25]	1
0xBB00D10C	OF_VLAN_EN [2106].EN[26:26]	1
0xBB00D10C	OF_VLAN_EN [2107].EN[27:27]	1
0xBB00D10C	OF_VLAN_EN [2108].EN[28:28]	1
0xBB00D10C	OF_VLAN_EN [2109].EN[29:29]	1
0xBB00D10C	OF_VLAN_EN [2110].EN[30:30]	1
0xBB00D10C	OF_VLAN_EN [2111].EN[31:31]	1
0xBB00D110	OF_VLAN_EN [2112].EN[0:0]	1
0xBB00D110	OF_VLAN_EN [2113].EN[1:1]	1
0xBB00D110	OF_VLAN_EN [2114].EN[2:2]	1
0xBB00D110	OF_VLAN_EN [2115].EN[3:3]	1
0xBB00D110	OF_VLAN_EN [2116].EN[4:4]	1
0xBB00D110	OF_VLAN_EN [2117].EN[5:5]	1
0xBB00D110	OF_VLAN_EN [2118].EN[6:6]	1
0xBB00D110	OF_VLAN_EN [2119].EN[7:7]	1
0xBB00D110	OF_VLAN_EN [2120].EN[8:8]	1
0xBB00D110	OF_VLAN_EN [2121].EN[9:9]	1
0xBB00D110	OF_VLAN_EN [2122].EN[10:10]	1
0xBB00D110	OF_VLAN_EN [2123].EN[11:11]	1
0xBB00D110	OF_VLAN_EN [2124].EN[12:12]	1
0xBB00D110	OF_VLAN_EN [2125].EN[13:13]	1
0xBB00D110	OF_VLAN_EN [2126].EN[14:14]	1
0xBB00D110	OF_VLAN_EN [2127].EN[15:15]	1
0xBB00D110	OF_VLAN_EN [2128].EN[16:16]	1
0xBB00D110	OF_VLAN_EN [2129].EN[17:17]	1
0xBB00D110	OF_VLAN_EN [2130].EN[18:18]	1

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0xBB00D110	OF_VLAN_EN [2134].EN[22:22]	1
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0xBB00D110	OF_VLAN_EN [2142].EN[30:30]	1
0xBB00D110	OF_VLAN_EN [2143].EN[31:31]	1
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0xBB00D114	OF_VLAN_EN [2145].EN[1:1]	1
0xBB00D114	OF_VLAN_EN [2146].EN[2:2]	1
0xBB00D114	OF_VLAN_EN [2147].EN[3:3]	1
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0xBB00D114	OF_VLAN_EN [2149].EN[5:5]	1
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0xBB00D114	OF_VLAN_EN [2152].EN[8:8]	1
0xBB00D114	OF_VLAN_EN [2153].EN[9:9]	1
0xBB00D114	OF_VLAN_EN [2154].EN[10:10]	1
0xBB00D114	OF_VLAN_EN [2155].EN[11:11]	1
0xBB00D114	OF_VLAN_EN [2156].EN[12:12]	1
0xBB00D114	OF_VLAN_EN [2157].EN[13:13]	1
0xBB00D114	OF_VLAN_EN [2158].EN[14:14]	1
0xBB00D114	OF_VLAN_EN [2159].EN[15:15]	1
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0xBB00D114	OF_VLAN_EN [2162].EN[18:18]	1
0xBB00D114	OF_VLAN_EN [2163].EN[19:19]	1
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0xBB00D114	OF_VLAN_EN [2165].EN[21:21]	1
0xBB00D114	OF_VLAN_EN [2166].EN[22:22]	1
0xBB00D114	OF_VLAN_EN [2167].EN[23:23]	1
0xBB00D114	OF_VLAN_EN [2168].EN[24:24]	1
0xBB00D114	OF_VLAN_EN [2169].EN[25:25]	1
0xBB00D114	OF_VLAN_EN [2170].EN[26:26]	1
0xBB00D114	OF_VLAN_EN [2171].EN[27:27]	1
0xBB00D114	OF_VLAN_EN [2172].EN[28:28]	1
0xBB00D114	OF_VLAN_EN [2173].EN[29:29]	1
0xBB00D114	OF_VLAN_EN [2174].EN[30:30]	1
0xBB00D114	OF_VLAN_EN [2175].EN[31:31]	1
0xBB00D118	OF_VLAN_EN [2176].EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D118	OF_VLAN_EN [2178].EN[2:2]	1
0xBB00D118	OF_VLAN_EN [2179].EN[3:3]	1
0xBB00D118	OF_VLAN_EN [2180].EN[4:4]	1
0xBB00D118	OF_VLAN_EN [2181].EN[5:5]	1
0xBB00D118	OF_VLAN_EN [2182].EN[6:6]	1
0xBB00D118	OF_VLAN_EN [2183].EN[7:7]	1
0xBB00D118	OF_VLAN_EN [2184].EN[8:8]	1
0xBB00D118	OF_VLAN_EN [2185].EN[9:9]	1
0xBB00D118	OF_VLAN_EN [2186].EN[10:10]	1
0xBB00D118	OF_VLAN_EN [2187].EN[11:11]	1
0xBB00D118	OF_VLAN_EN [2188].EN[12:12]	1
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0xBB00D118	OF_VLAN_EN [2190].EN[14:14]	1
0xBB00D118	OF_VLAN_EN [2191].EN[15:15]	1
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0xBB00D118	OF_VLAN_EN [2193].EN[17:17]	1
0xBB00D118	OF_VLAN_EN [2194].EN[18:18]	1
0xBB00D118	OF_VLAN_EN [2195].EN[19:19]	1
0xBB00D118	OF_VLAN_EN [2196].EN[20:20]	1
0xBB00D118	OF_VLAN_EN [2197].EN[21:21]	1
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0xBB00D118	OF_VLAN_EN [2201].EN[25:25]	1
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0xBB00D118	OF_VLAN_EN [2203].EN[27:27]	1
0xBB00D118	OF_VLAN_EN [2204].EN[28:28]	1
0xBB00D118	OF_VLAN_EN [2205].EN[29:29]	1
0xBB00D118	OF_VLAN_EN [2206].EN[30:30]	1
0xBB00D118	OF_VLAN_EN [2207].EN[31:31]	1
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0xBB00D11C	OF_VLAN_EN [2209].EN[1:1]	1
0xBB00D11C	OF_VLAN_EN [2210].EN[2:2]	1
0xBB00D11C	OF_VLAN_EN [2211].EN[3:3]	1
0xBB00D11C	OF_VLAN_EN [2212].EN[4:4]	1
0xBB00D11C	OF_VLAN_EN [2213].EN[5:5]	1
0xBB00D11C	OF_VLAN_EN [2214].EN[6:6]	1
0xBB00D11C	OF_VLAN_EN [2215].EN[7:7]	1
0xBB00D11C	OF_VLAN_EN [2216].EN[8:8]	1
0xBB00D11C	OF_VLAN_EN [2217].EN[9:9]	1
0xBB00D11C	OF_VLAN_EN [2218].EN[10:10]	1
0xBB00D11C	OF_VLAN_EN [2219].EN[11:11]	1
0xBB00D11C	OF_VLAN_EN [2220].EN[12:12]	1
0xBB00D11C	OF_VLAN_EN [2221].EN[13:13]	1
0xBB00D11C	OF_VLAN_EN [2222].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D11C	OF_VLAN_EN [2226].EN[18:18]	1
0xBB00D11C	OF_VLAN_EN [2227].EN[19:19]	1
0xBB00D11C	OF_VLAN_EN [2228].EN[20:20]	1
0xBB00D11C	OF_VLAN_EN [2229].EN[21:21]	1
0xBB00D11C	OF_VLAN_EN [2230].EN[22:22]	1
0xBB00D11C	OF_VLAN_EN [2231].EN[23:23]	1
0xBB00D11C	OF_VLAN_EN [2232].EN[24:24]	1
0xBB00D11C	OF_VLAN_EN [2233].EN[25:25]	1
0xBB00D11C	OF_VLAN_EN [2234].EN[26:26]	1
0xBB00D11C	OF_VLAN_EN [2235].EN[27:27]	1
0xBB00D11C	OF_VLAN_EN [2236].EN[28:28]	1
0xBB00D11C	OF_VLAN_EN [2237].EN[29:29]	1
0xBB00D11C	OF_VLAN_EN [2238].EN[30:30]	1
0xBB00D11C	OF_VLAN_EN [2239].EN[31:31]	1
0xBB00D120	OF_VLAN_EN [2240].EN[0:0]	1
0xBB00D120	OF_VLAN_EN [2241].EN[1:1]	1
0xBB00D120	OF_VLAN_EN [2242].EN[2:2]	1
0xBB00D120	OF_VLAN_EN [2243].EN[3:3]	1
0xBB00D120	OF_VLAN_EN [2244].EN[4:4]	1
0xBB00D120	OF_VLAN_EN [2245].EN[5:5]	1
0xBB00D120	OF_VLAN_EN [2246].EN[6:6]	1
0xBB00D120	OF_VLAN_EN [2247].EN[7:7]	1
0xBB00D120	OF_VLAN_EN [2248].EN[8:8]	1
0xBB00D120	OF_VLAN_EN [2249].EN[9:9]	1
0xBB00D120	OF_VLAN_EN [2250].EN[10:10]	1
0xBB00D120	OF_VLAN_EN [2251].EN[11:11]	1
0xBB00D120	OF_VLAN_EN [2252].EN[12:12]	1
0xBB00D120	OF_VLAN_EN [2253].EN[13:13]	1
0xBB00D120	OF_VLAN_EN [2254].EN[14:14]	1
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0xBB00D120	OF_VLAN_EN [2262].EN[22:22]	1
0xBB00D120	OF_VLAN_EN [2263].EN[23:23]	1
0xBB00D120	OF_VLAN_EN [2264].EN[24:24]	1
0xBB00D120	OF_VLAN_EN [2265].EN[25:25]	1
0xBB00D120	OF_VLAN_EN [2266].EN[26:26]	1
0xBB00D120	OF_VLAN_EN [2267].EN[27:27]	1
0xBB00D120	OF_VLAN_EN [2268].EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D120	OF_VLAN_EN [2269].EN[29:29]	1
0xBB00D120	OF_VLAN_EN [2270].EN[30:30]	1
0xBB00D120	OF_VLAN_EN [2271].EN[31:31]	1
0xBB00D124	OF_VLAN_EN [2272].EN[0:0]	1
0xBB00D124	OF_VLAN_EN [2273].EN[1:1]	1
0xBB00D124	OF_VLAN_EN [2274].EN[2:2]	1
0xBB00D124	OF_VLAN_EN [2275].EN[3:3]	1
0xBB00D124	OF_VLAN_EN [2276].EN[4:4]	1
0xBB00D124	OF_VLAN_EN [2277].EN[5:5]	1
0xBB00D124	OF_VLAN_EN [2278].EN[6:6]	1
0xBB00D124	OF_VLAN_EN [2279].EN[7:7]	1
0xBB00D124	OF_VLAN_EN [2280].EN[8:8]	1
0xBB00D124	OF_VLAN_EN [2281].EN[9:9]	1
0xBB00D124	OF_VLAN_EN [2282].EN[10:10]	1
0xBB00D124	OF_VLAN_EN [2283].EN[11:11]	1
0xBB00D124	OF_VLAN_EN [2284].EN[12:12]	1
0xBB00D124	OF_VLAN_EN [2285].EN[13:13]	1
0xBB00D124	OF_VLAN_EN [2286].EN[14:14]	1
0xBB00D124	OF_VLAN_EN [2287].EN[15:15]	1
0xBB00D124	OF_VLAN_EN [2288].EN[16:16]	1
0xBB00D124	OF_VLAN_EN [2289].EN[17:17]	1
0xBB00D124	OF_VLAN_EN [2290].EN[18:18]	1
0xBB00D124	OF_VLAN_EN [2291].EN[19:19]	1
0xBB00D124	OF_VLAN_EN [2292].EN[20:20]	1
0xBB00D124	OF_VLAN_EN [2293].EN[21:21]	1
0xBB00D124	OF_VLAN_EN [2294].EN[22:22]	1
0xBB00D124	OF_VLAN_EN [2295].EN[23:23]	1
0xBB00D124	OF_VLAN_EN [2296].EN[24:24]	1
0xBB00D124	OF_VLAN_EN [2297].EN[25:25]	1
0xBB00D124	OF_VLAN_EN [2298].EN[26:26]	1
0xBB00D124	OF_VLAN_EN [2299].EN[27:27]	1
0xBB00D124	OF_VLAN_EN [2300].EN[28:28]	1
0xBB00D124	OF_VLAN_EN [2301].EN[29:29]	1
0xBB00D124	OF_VLAN_EN [2302].EN[30:30]	1
0xBB00D124	OF_VLAN_EN [2303].EN[31:31]	1
0xBB00D128	OF_VLAN_EN [2304].EN[0:0]	1
0xBB00D128	OF_VLAN_EN [2305].EN[1:1]	1
0xBB00D128	OF_VLAN_EN [2306].EN[2:2]	1
0xBB00D128	OF_VLAN_EN [2307].EN[3:3]	1
0xBB00D128	OF_VLAN_EN [2308].EN[4:4]	1
0xBB00D128	OF_VLAN_EN [2309].EN[5:5]	1
0xBB00D128	OF_VLAN_EN [2310].EN[6:6]	1
0xBB00D128	OF_VLAN_EN [2311].EN[7:7]	1
0xBB00D128	OF_VLAN_EN [2312].EN[8:8]	1
0xBB00D128	OF_VLAN_EN [2313].EN[9:9]	1
0xBB00D128	OF_VLAN_EN [2314].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D128	OF_VLAN_EN [2316].EN[12:12]	1
0xBB00D128	OF_VLAN_EN [2317].EN[13:13]	1
0xBB00D128	OF_VLAN_EN [2318].EN[14:14]	1
0xBB00D128	OF_VLAN_EN [2319].EN[15:15]	1
0xBB00D128	OF_VLAN_EN [2320].EN[16:16]	1
0xBB00D128	OF_VLAN_EN [2321].EN[17:17]	1
0xBB00D128	OF_VLAN_EN [2322].EN[18:18]	1
0xBB00D128	OF_VLAN_EN [2323].EN[19:19]	1
0xBB00D128	OF_VLAN_EN [2324].EN[20:20]	1
0xBB00D128	OF_VLAN_EN [2325].EN[21:21]	1
0xBB00D128	OF_VLAN_EN [2326].EN[22:22]	1
0xBB00D128	OF_VLAN_EN [2327].EN[23:23]	1
0xBB00D128	OF_VLAN_EN [2328].EN[24:24]	1
0xBB00D128	OF_VLAN_EN [2329].EN[25:25]	1
0xBB00D128	OF_VLAN_EN [2330].EN[26:26]	1
0xBB00D128	OF_VLAN_EN [2331].EN[27:27]	1
0xBB00D128	OF_VLAN_EN [2332].EN[28:28]	1
0xBB00D128	OF_VLAN_EN [2333].EN[29:29]	1
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0xBB00D128	OF_VLAN_EN [2335].EN[31:31]	1
0xBB00D12C	OF_VLAN_EN [2336].EN[0:0]	1
0xBB00D12C	OF_VLAN_EN [2337].EN[1:1]	1
0xBB00D12C	OF_VLAN_EN [2338].EN[2:2]	1
0xBB00D12C	OF_VLAN_EN [2339].EN[3:3]	1
0xBB00D12C	OF_VLAN_EN [2340].EN[4:4]	1
0xBB00D12C	OF_VLAN_EN [2341].EN[5:5]	1
0xBB00D12C	OF_VLAN_EN [2342].EN[6:6]	1
0xBB00D12C	OF_VLAN_EN [2343].EN[7:7]	1
0xBB00D12C	OF_VLAN_EN [2344].EN[8:8]	1
0xBB00D12C	OF_VLAN_EN [2345].EN[9:9]	1
0xBB00D12C	OF_VLAN_EN [2346].EN[10:10]	1
0xBB00D12C	OF_VLAN_EN [2347].EN[11:11]	1
0xBB00D12C	OF_VLAN_EN [2348].EN[12:12]	1
0xBB00D12C	OF_VLAN_EN [2349].EN[13:13]	1
0xBB00D12C	OF_VLAN_EN [2350].EN[14:14]	1
0xBB00D12C	OF_VLAN_EN [2351].EN[15:15]	1
0xBB00D12C	OF_VLAN_EN [2352].EN[16:16]	1
0xBB00D12C	OF_VLAN_EN [2353].EN[17:17]	1
0xBB00D12C	OF_VLAN_EN [2354].EN[18:18]	1
0xBB00D12C	OF_VLAN_EN [2355].EN[19:19]	1
0xBB00D12C	OF_VLAN_EN [2356].EN[20:20]	1
0xBB00D12C	OF_VLAN_EN [2357].EN[21:21]	1
0xBB00D12C	OF_VLAN_EN [2358].EN[22:22]	1
0xBB00D12C	OF_VLAN_EN [2359].EN[23:23]	1
0xBB00D12C	OF_VLAN_EN [2360].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D12C	OF_VLAN_EN [2362].EN[26:26]	1
0xBB00D12C	OF_VLAN_EN [2363].EN[27:27]	1
0xBB00D12C	OF_VLAN_EN [2364].EN[28:28]	1
0xBB00D12C	OF_VLAN_EN [2365].EN[29:29]	1
0xBB00D12C	OF_VLAN_EN [2366].EN[30:30]	1
0xBB00D12C	OF_VLAN_EN [2367].EN[31:31]	1
0xBB00D130	OF_VLAN_EN [2368].EN[0:0]	1
0xBB00D130	OF_VLAN_EN [2369].EN[1:1]	1
0xBB00D130	OF_VLAN_EN [2370].EN[2:2]	1
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0xBB00D130	OF_VLAN_EN [2372].EN[4:4]	1
0xBB00D130	OF_VLAN_EN [2373].EN[5:5]	1
0xBB00D130	OF_VLAN_EN [2374].EN[6:6]	1
0xBB00D130	OF_VLAN_EN [2375].EN[7:7]	1
0xBB00D130	OF_VLAN_EN [2376].EN[8:8]	1
0xBB00D130	OF_VLAN_EN [2377].EN[9:9]	1
0xBB00D130	OF_VLAN_EN [2378].EN[10:10]	1
0xBB00D130	OF_VLAN_EN [2379].EN[11:11]	1
0xBB00D130	OF_VLAN_EN [2380].EN[12:12]	1
0xBB00D130	OF_VLAN_EN [2381].EN[13:13]	1
0xBB00D130	OF_VLAN_EN [2382].EN[14:14]	1
0xBB00D130	OF_VLAN_EN [2383].EN[15:15]	1
0xBB00D130	OF_VLAN_EN [2384].EN[16:16]	1
0xBB00D130	OF_VLAN_EN [2385].EN[17:17]	1
0xBB00D130	OF_VLAN_EN [2386].EN[18:18]	1
0xBB00D130	OF_VLAN_EN [2387].EN[19:19]	1
0xBB00D130	OF_VLAN_EN [2388].EN[20:20]	1
0xBB00D130	OF_VLAN_EN [2389].EN[21:21]	1
0xBB00D130	OF_VLAN_EN [2390].EN[22:22]	1
0xBB00D130	OF_VLAN_EN [2391].EN[23:23]	1
0xBB00D130	OF_VLAN_EN [2392].EN[24:24]	1
0xBB00D130	OF_VLAN_EN [2393].EN[25:25]	1
0xBB00D130	OF_VLAN_EN [2394].EN[26:26]	1
0xBB00D130	OF_VLAN_EN [2395].EN[27:27]	1
0xBB00D130	OF_VLAN_EN [2396].EN[28:28]	1
0xBB00D130	OF_VLAN_EN [2397].EN[29:29]	1
0xBB00D130	OF_VLAN_EN [2398].EN[30:30]	1
0xBB00D130	OF_VLAN_EN [2399].EN[31:31]	1
0xBB00D134	OF_VLAN_EN [2400].EN[0:0]	1
0xBB00D134	OF_VLAN_EN [2401].EN[1:1]	1
0xBB00D134	OF_VLAN_EN [2402].EN[2:2]	1
0xBB00D134	OF_VLAN_EN [2403].EN[3:3]	1
0xBB00D134	OF_VLAN_EN [2404].EN[4:4]	1
0xBB00D134	OF_VLAN_EN [2405].EN[5:5]	1
0xBB00D134	OF_VLAN_EN [2406].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D134	OF_VLAN_EN [2409].EN[9:9]	1
0xBB00D134	OF_VLAN_EN [2410].EN[10:10]	1
0xBB00D134	OF_VLAN_EN [2411].EN[11:11]	1
0xBB00D134	OF_VLAN_EN [2412].EN[12:12]	1
0xBB00D134	OF_VLAN_EN [2413].EN[13:13]	1
0xBB00D134	OF_VLAN_EN [2414].EN[14:14]	1
0xBB00D134	OF_VLAN_EN [2415].EN[15:15]	1
0xBB00D134	OF_VLAN_EN [2416].EN[16:16]	1
0xBB00D134	OF_VLAN_EN [2417].EN[17:17]	1
0xBB00D134	OF_VLAN_EN [2418].EN[18:18]	1
0xBB00D134	OF_VLAN_EN [2419].EN[19:19]	1
0xBB00D134	OF_VLAN_EN [2420].EN[20:20]	1
0xBB00D134	OF_VLAN_EN [2421].EN[21:21]	1
0xBB00D134	OF_VLAN_EN [2422].EN[22:22]	1
0xBB00D134	OF_VLAN_EN [2423].EN[23:23]	1
0xBB00D134	OF_VLAN_EN [2424].EN[24:24]	1
0xBB00D134	OF_VLAN_EN [2425].EN[25:25]	1
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0xBB00D138	OF_VLAN_EN [2434].EN[2:2]	1
0xBB00D138	OF_VLAN_EN [2435].EN[3:3]	1
0xBB00D138	OF_VLAN_EN [2436].EN[4:4]	1
0xBB00D138	OF_VLAN_EN [2437].EN[5:5]	1
0xBB00D138	OF_VLAN_EN [2438].EN[6:6]	1
0xBB00D138	OF_VLAN_EN [2439].EN[7:7]	1
0xBB00D138	OF_VLAN_EN [2440].EN[8:8]	1
0xBB00D138	OF_VLAN_EN [2441].EN[9:9]	1
0xBB00D138	OF_VLAN_EN [2442].EN[10:10]	1
0xBB00D138	OF_VLAN_EN [2443].EN[11:11]	1
0xBB00D138	OF_VLAN_EN [2444].EN[12:12]	1
0xBB00D138	OF_VLAN_EN [2445].EN[13:13]	1
0xBB00D138	OF_VLAN_EN [2446].EN[14:14]	1
0xBB00D138	OF_VLAN_EN [2447].EN[15:15]	1
0xBB00D138	OF_VLAN_EN [2448].EN[16:16]	1
0xBB00D138	OF_VLAN_EN [2449].EN[17:17]	1
0xBB00D138	OF_VLAN_EN [2450].EN[18:18]	1
0xBB00D138	OF_VLAN_EN [2451].EN[19:19]	1
0xBB00D138	OF_VLAN_EN [2452].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D138	OF_VLAN_EN [2454].EN[22:22]	1
0xBB00D138	OF_VLAN_EN [2455].EN[23:23]	1
0xBB00D138	OF_VLAN_EN [2456].EN[24:24]	1
0xBB00D138	OF_VLAN_EN [2457].EN[25:25]	1
0xBB00D138	OF_VLAN_EN [2458].EN[26:26]	1
0xBB00D138	OF_VLAN_EN [2459].EN[27:27]	1
0xBB00D138	OF_VLAN_EN [2460].EN[28:28]	1
0xBB00D138	OF_VLAN_EN [2461].EN[29:29]	1
0xBB00D138	OF_VLAN_EN [2462].EN[30:30]	1
0xBB00D138	OF_VLAN_EN [2463].EN[31:31]	1
0xBB00D13C	OF_VLAN_EN [2464].EN[0:0]	1
0xBB00D13C	OF_VLAN_EN [2465].EN[1:1]	1
0xBB00D13C	OF_VLAN_EN [2466].EN[2:2]	1
0xBB00D13C	OF_VLAN_EN [2467].EN[3:3]	1
0xBB00D13C	OF_VLAN_EN [2468].EN[4:4]	1
0xBB00D13C	OF_VLAN_EN [2469].EN[5:5]	1
0xBB00D13C	OF_VLAN_EN [2470].EN[6:6]	1
0xBB00D13C	OF_VLAN_EN [2471].EN[7:7]	1
0xBB00D13C	OF_VLAN_EN [2472].EN[8:8]	1
0xBB00D13C	OF_VLAN_EN [2473].EN[9:9]	1
0xBB00D13C	OF_VLAN_EN [2474].EN[10:10]	1
0xBB00D13C	OF_VLAN_EN [2475].EN[11:11]	1
0xBB00D13C	OF_VLAN_EN [2476].EN[12:12]	1
0xBB00D13C	OF_VLAN_EN [2477].EN[13:13]	1
0xBB00D13C	OF_VLAN_EN [2478].EN[14:14]	1
0xBB00D13C	OF_VLAN_EN [2479].EN[15:15]	1
0xBB00D13C	OF_VLAN_EN [2480].EN[16:16]	1
0xBB00D13C	OF_VLAN_EN [2481].EN[17:17]	1
0xBB00D13C	OF_VLAN_EN [2482].EN[18:18]	1
0xBB00D13C	OF_VLAN_EN [2483].EN[19:19]	1
0xBB00D13C	OF_VLAN_EN [2484].EN[20:20]	1
0xBB00D13C	OF_VLAN_EN [2485].EN[21:21]	1
0xBB00D13C	OF_VLAN_EN [2486].EN[22:22]	1
0xBB00D13C	OF_VLAN_EN [2487].EN[23:23]	1
0xBB00D13C	OF_VLAN_EN [2488].EN[24:24]	1
0xBB00D13C	OF_VLAN_EN [2489].EN[25:25]	1
0xBB00D13C	OF_VLAN_EN [2490].EN[26:26]	1
0xBB00D13C	OF_VLAN_EN [2491].EN[27:27]	1
0xBB00D13C	OF_VLAN_EN [2492].EN[28:28]	1
0xBB00D13C	OF_VLAN_EN [2493].EN[29:29]	1
0xBB00D13C	OF_VLAN_EN [2494].EN[30:30]	1
0xBB00D13C	OF_VLAN_EN [2495].EN[31:31]	1
0xBB00D140	OF_VLAN_EN [2496].EN[0:0]	1
0xBB00D140	OF_VLAN_EN [2497].EN[1:1]	1
0xBB00D140	OF_VLAN_EN [2498].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D140	OF_VLAN_EN [2501].EN[5:5]	1
0xBB00D140	OF_VLAN_EN [2502].EN[6:6]	1
0xBB00D140	OF_VLAN_EN [2503].EN[7:7]	1
0xBB00D140	OF_VLAN_EN [2504].EN[8:8]	1
0xBB00D140	OF_VLAN_EN [2505].EN[9:9]	1
0xBB00D140	OF_VLAN_EN [2506].EN[10:10]	1
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0xBB00D140	OF_VLAN_EN [2508].EN[12:12]	1
0xBB00D140	OF_VLAN_EN [2509].EN[13:13]	1
0xBB00D140	OF_VLAN_EN [2510].EN[14:14]	1
0xBB00D140	OF_VLAN_EN [2511].EN[15:15]	1
0xBB00D140	OF_VLAN_EN [2512].EN[16:16]	1
0xBB00D140	OF_VLAN_EN [2513].EN[17:17]	1
0xBB00D140	OF_VLAN_EN [2514].EN[18:18]	1
0xBB00D140	OF_VLAN_EN [2515].EN[19:19]	1
0xBB00D140	OF_VLAN_EN [2516].EN[20:20]	1
0xBB00D140	OF_VLAN_EN [2517].EN[21:21]	1
0xBB00D140	OF_VLAN_EN [2518].EN[22:22]	1
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0xBB00D140	OF_VLAN_EN [2523].EN[27:27]	1
0xBB00D140	OF_VLAN_EN [2524].EN[28:28]	1
0xBB00D140	OF_VLAN_EN [2525].EN[29:29]	1
0xBB00D140	OF_VLAN_EN [2526].EN[30:30]	1
0xBB00D140	OF_VLAN_EN [2527].EN[31:31]	1
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0xBB00D144	OF_VLAN_EN [2529].EN[1:1]	1
0xBB00D144	OF_VLAN_EN [2530].EN[2:2]	1
0xBB00D144	OF_VLAN_EN [2531].EN[3:3]	1
0xBB00D144	OF_VLAN_EN [2532].EN[4:4]	1
0xBB00D144	OF_VLAN_EN [2533].EN[5:5]	1
0xBB00D144	OF_VLAN_EN [2534].EN[6:6]	1
0xBB00D144	OF_VLAN_EN [2535].EN[7:7]	1
0xBB00D144	OF_VLAN_EN [2536].EN[8:8]	1
0xBB00D144	OF_VLAN_EN [2537].EN[9:9]	1
0xBB00D144	OF_VLAN_EN [2538].EN[10:10]	1
0xBB00D144	OF_VLAN_EN [2539].EN[11:11]	1
0xBB00D144	OF_VLAN_EN [2540].EN[12:12]	1
0xBB00D144	OF_VLAN_EN [2541].EN[13:13]	1
0xBB00D144	OF_VLAN_EN [2542].EN[14:14]	1
0xBB00D144	OF_VLAN_EN [2543].EN[15:15]	1
0xBB00D144	OF_VLAN_EN [2544].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D144	OF_VLAN_EN [2546].EN[18:18]	1
0xBB00D144	OF_VLAN_EN [2547].EN[19:19]	1
0xBB00D144	OF_VLAN_EN [2548].EN[20:20]	1
0xBB00D144	OF_VLAN_EN [2549].EN[21:21]	1
0xBB00D144	OF_VLAN_EN [2550].EN[22:22]	1
0xBB00D144	OF_VLAN_EN [2551].EN[23:23]	1
0xBB00D144	OF_VLAN_EN [2552].EN[24:24]	1
0xBB00D144	OF_VLAN_EN [2553].EN[25:25]	1
0xBB00D144	OF_VLAN_EN [2554].EN[26:26]	1
0xBB00D144	OF_VLAN_EN [2555].EN[27:27]	1
0xBB00D144	OF_VLAN_EN [2556].EN[28:28]	1
0xBB00D144	OF_VLAN_EN [2557].EN[29:29]	1
0xBB00D144	OF_VLAN_EN [2558].EN[30:30]	1
0xBB00D144	OF_VLAN_EN [2559].EN[31:31]	1
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0xBB00D148	OF_VLAN_EN [2563].EN[3:3]	1
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0xBB00D148	OF_VLAN_EN [2568].EN[8:8]	1
0xBB00D148	OF_VLAN_EN [2569].EN[9:9]	1
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0xBB00D148	OF_VLAN_EN [2572].EN[12:12]	1
0xBB00D148	OF_VLAN_EN [2573].EN[13:13]	1
0xBB00D148	OF_VLAN_EN [2574].EN[14:14]	1
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0xBB00D148	OF_VLAN_EN [2578].EN[18:18]	1
0xBB00D148	OF_VLAN_EN [2579].EN[19:19]	1
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0xBB00D148	OF_VLAN_EN [2582].EN[22:22]	1
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0xBB00D148	OF_VLAN_EN [2584].EN[24:24]	1
0xBB00D148	OF_VLAN_EN [2585].EN[25:25]	1
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0xBB00D148	OF_VLAN_EN [2589].EN[29:29]	1
0xBB00D148	OF_VLAN_EN [2590].EN[30:30]	1

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0xBB00D14C	OF_VLAN_EN [2620].EN[28:28]	1
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0xBB00D150	OF_VLAN_EN [2635].EN[11:11]	1
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0xBB00D154	OF_VLAN_EN [2681].EN[25:25]	1
0xBB00D154	OF_VLAN_EN [2682].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D154	OF_VLAN_EN [2687].EN[31:31]	1
0xBB00D158	OF_VLAN_EN [2688].EN[0:0]	1
0xBB00D158	OF_VLAN_EN [2689].EN[1:1]	1
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0xBB00D158	OF_VLAN_EN [2693].EN[5:5]	1
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0xBB00D158	OF_VLAN_EN [2695].EN[7:7]	1
0xBB00D158	OF_VLAN_EN [2696].EN[8:8]	1
0xBB00D158	OF_VLAN_EN [2697].EN[9:9]	1
0xBB00D158	OF_VLAN_EN [2698].EN[10:10]	1
0xBB00D158	OF_VLAN_EN [2699].EN[11:11]	1
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0xBB00D15C	OF_VLAN_EN [2725].EN[5:5]	1
0xBB00D15C	OF_VLAN_EN [2726].EN[6:6]	1
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0xBB00D160	OF_VLAN_EN [2769].EN[17:17]	1
0xBB00D160	OF_VLAN_EN [2770].EN[18:18]	1
0xBB00D160	OF_VLAN_EN [2771].EN[19:19]	1
0xBB00D160	OF_VLAN_EN [2772].EN[20:20]	1
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0xBB00D160	OF_VLAN_EN [2779].EN[27:27]	1
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0xBB00D160	OF_VLAN_EN [2781].EN[29:29]	1
0xBB00D160	OF_VLAN_EN [2782].EN[30:30]	1
0xBB00D160	OF_VLAN_EN [2783].EN[31:31]	1
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0xBB00D164	OF_VLAN_EN [2787].EN[3:3]	1
0xBB00D164	OF_VLAN_EN [2788].EN[4:4]	1
0xBB00D164	OF_VLAN_EN [2789].EN[5:5]	1
0xBB00D164	OF_VLAN_EN [2790].EN[6:6]	1
0xBB00D164	OF_VLAN_EN [2791].EN[7:7]	1
0xBB00D164	OF_VLAN_EN [2792].EN[8:8]	1
0xBB00D164	OF_VLAN_EN [2793].EN[9:9]	1
0xBB00D164	OF_VLAN_EN [2794].EN[10:10]	1
0xBB00D164	OF_VLAN_EN [2795].EN[11:11]	1
0xBB00D164	OF_VLAN_EN [2796].EN[12:12]	1
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0xBB00D164	OF_VLAN_EN [2806].EN[22:22]	1
0xBB00D164	OF_VLAN_EN [2807].EN[23:23]	1
0xBB00D164	OF_VLAN_EN [2808].EN[24:24]	1
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0xBB00D168	OF_VLAN_EN [2825].EN[9:9]	1
0xBB00D168	OF_VLAN_EN [2826].EN[10:10]	1
0xBB00D168	OF_VLAN_EN [2827].EN[11:11]	1
0xBB00D168	OF_VLAN_EN [2828].EN[12:12]	1
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0xBB00D168	OF_VLAN_EN [2835].EN[19:19]	1
0xBB00D168	OF_VLAN_EN [2836].EN[20:20]	1
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0xBB00D16C	OF_VLAN_EN [2851].EN[3:3]	1
0xBB00D16C	OF_VLAN_EN [2852].EN[4:4]	1
0xBB00D16C	OF_VLAN_EN [2853].EN[5:5]	1
0xBB00D16C	OF_VLAN_EN [2854].EN[6:6]	1
0xBB00D16C	OF_VLAN_EN [2855].EN[7:7]	1
0xBB00D16C	OF_VLAN_EN [2856].EN[8:8]	1
0xBB00D16C	OF_VLAN_EN [2857].EN[9:9]	1
0xBB00D16C	OF_VLAN_EN [2858].EN[10:10]	1
0xBB00D16C	OF_VLAN_EN [2859].EN[11:11]	1
0xBB00D16C	OF_VLAN_EN [2860].EN[12:12]	1
0xBB00D16C	OF_VLAN_EN [2861].EN[13:13]	1
0xBB00D16C	OF_VLAN_EN [2862].EN[14:14]	1
0xBB00D16C	OF_VLAN_EN [2863].EN[15:15]	1
0xBB00D16C	OF_VLAN_EN [2864].EN[16:16]	1
0xBB00D16C	OF_VLAN_EN [2865].EN[17:17]	1
0xBB00D16C	OF_VLAN_EN [2866].EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D16C	OF_VLAN_EN [2869].EN[21:21]	1
0xBB00D16C	OF_VLAN_EN [2870].EN[22:22]	1
0xBB00D16C	OF_VLAN_EN [2871].EN[23:23]	1
0xBB00D16C	OF_VLAN_EN [2872].EN[24:24]	1
0xBB00D16C	OF_VLAN_EN [2873].EN[25:25]	1
0xBB00D16C	OF_VLAN_EN [2874].EN[26:26]	1
0xBB00D16C	OF_VLAN_EN [2875].EN[27:27]	1
0xBB00D16C	OF_VLAN_EN [2876].EN[28:28]	1
0xBB00D16C	OF_VLAN_EN [2877].EN[29:29]	1
0xBB00D16C	OF_VLAN_EN [2878].EN[30:30]	1
0xBB00D16C	OF_VLAN_EN [2879].EN[31:31]	1
0xBB00D170	OF_VLAN_EN [2880].EN[0:0]	1
0xBB00D170	OF_VLAN_EN [2881].EN[1:1]	1
0xBB00D170	OF_VLAN_EN [2882].EN[2:2]	1
0xBB00D170	OF_VLAN_EN [2883].EN[3:3]	1
0xBB00D170	OF_VLAN_EN [2884].EN[4:4]	1
0xBB00D170	OF_VLAN_EN [2885].EN[5:5]	1
0xBB00D170	OF_VLAN_EN [2886].EN[6:6]	1
0xBB00D170	OF_VLAN_EN [2887].EN[7:7]	1
0xBB00D170	OF_VLAN_EN [2888].EN[8:8]	1
0xBB00D170	OF_VLAN_EN [2889].EN[9:9]	1
0xBB00D170	OF_VLAN_EN [2890].EN[10:10]	1
0xBB00D170	OF_VLAN_EN [2891].EN[11:11]	1
0xBB00D170	OF_VLAN_EN [2892].EN[12:12]	1
0xBB00D170	OF_VLAN_EN [2893].EN[13:13]	1
0xBB00D170	OF_VLAN_EN [2894].EN[14:14]	1
0xBB00D170	OF_VLAN_EN [2895].EN[15:15]	1
0xBB00D170	OF_VLAN_EN [2896].EN[16:16]	1
0xBB00D170	OF_VLAN_EN [2897].EN[17:17]	1
0xBB00D170	OF_VLAN_EN [2898].EN[18:18]	1
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0xBB00D170	OF_VLAN_EN [2903].EN[23:23]	1
0xBB00D170	OF_VLAN_EN [2904].EN[24:24]	1
0xBB00D170	OF_VLAN_EN [2905].EN[25:25]	1
0xBB00D170	OF_VLAN_EN [2906].EN[26:26]	1
0xBB00D170	OF_VLAN_EN [2907].EN[27:27]	1
0xBB00D170	OF_VLAN_EN [2908].EN[28:28]	1
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0xBB00D170	OF_VLAN_EN [2910].EN[30:30]	1
0xBB00D170	OF_VLAN_EN [2911].EN[31:31]	1
0xBB00D174	OF_VLAN_EN [2912].EN[0:0]	1

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0xBB00D174	OF_VLAN_EN [2915].EN[3:3]	1
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0xBB00D174	OF_VLAN_EN [2917].EN[5:5]	1
0xBB00D174	OF_VLAN_EN [2918].EN[6:6]	1
0xBB00D174	OF_VLAN_EN [2919].EN[7:7]	1
0xBB00D174	OF_VLAN_EN [2920].EN[8:8]	1
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0xBB00D174	OF_VLAN_EN [2922].EN[10:10]	1
0xBB00D174	OF_VLAN_EN [2923].EN[11:11]	1
0xBB00D174	OF_VLAN_EN [2924].EN[12:12]	1
0xBB00D174	OF_VLAN_EN [2925].EN[13:13]	1
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0xBB00D174	OF_VLAN_EN [2940].EN[28:28]	1
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0xBB00D178	OF_VLAN_EN [2950].EN[6:6]	1
0xBB00D178	OF_VLAN_EN [2951].EN[7:7]	1
0xBB00D178	OF_VLAN_EN [2952].EN[8:8]	1
0xBB00D178	OF_VLAN_EN [2953].EN[9:9]	1
0xBB00D178	OF_VLAN_EN [2954].EN[10:10]	1
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0xBB00D178	OF_VLAN_EN [2956].EN[12:12]	1
0xBB00D178	OF_VLAN_EN [2957].EN[13:13]	1
0xBB00D178	OF_VLAN_EN [2958].EN[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D178	OF_VLAN_EN [2963].EN[19:19]	1
0xBB00D178	OF_VLAN_EN [2964].EN[20:20]	1
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0xBB00D178	OF_VLAN_EN [2966].EN[22:22]	1
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0xBB00D178	OF_VLAN_EN [2971].EN[27:27]	1
0xBB00D178	OF_VLAN_EN [2972].EN[28:28]	1
0xBB00D178	OF_VLAN_EN [2973].EN[29:29]	1
0xBB00D178	OF_VLAN_EN [2974].EN[30:30]	1
0xBB00D178	OF_VLAN_EN [2975].EN[31:31]	1
0xBB00D17C	OF_VLAN_EN [2976].EN[0:0]	1
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0xBB00D17C	OF_VLAN_EN [2978].EN[2:2]	1
0xBB00D17C	OF_VLAN_EN [2979].EN[3:3]	1
0xBB00D17C	OF_VLAN_EN [2980].EN[4:4]	1
0xBB00D17C	OF_VLAN_EN [2981].EN[5:5]	1
0xBB00D17C	OF_VLAN_EN [2982].EN[6:6]	1
0xBB00D17C	OF_VLAN_EN [2983].EN[7:7]	1
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0xBB00D17C	OF_VLAN_EN [2987].EN[11:11]	1
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0xBB00D17C	OF_VLAN_EN [2989].EN[13:13]	1
0xBB00D17C	OF_VLAN_EN [2990].EN[14:14]	1
0xBB00D17C	OF_VLAN_EN [2991].EN[15:15]	1
0xBB00D17C	OF_VLAN_EN [2992].EN[16:16]	1
0xBB00D17C	OF_VLAN_EN [2993].EN[17:17]	1
0xBB00D17C	OF_VLAN_EN [2994].EN[18:18]	1
0xBB00D17C	OF_VLAN_EN [2995].EN[19:19]	1
0xBB00D17C	OF_VLAN_EN [2996].EN[20:20]	1
0xBB00D17C	OF_VLAN_EN [2997].EN[21:21]	1
0xBB00D17C	OF_VLAN_EN [2998].EN[22:22]	1
0xBB00D17C	OF_VLAN_EN [2999].EN[23:23]	1
0xBB00D17C	OF_VLAN_EN [3000].EN[24:24]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D180	OF_VLAN_EN [3014].EN[6:6]	1
0xBB00D180	OF_VLAN_EN [3015].EN[7:7]	1
0xBB00D180	OF_VLAN_EN [3016].EN[8:8]	1
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0xBB00D180	OF_VLAN_EN [3035].EN[27:27]	1
0xBB00D180	OF_VLAN_EN [3036].EN[28:28]	1
0xBB00D180	OF_VLAN_EN [3037].EN[29:29]	1
0xBB00D180	OF_VLAN_EN [3038].EN[30:30]	1
0xBB00D180	OF_VLAN_EN [3039].EN[31:31]	1
0xBB00D184	OF_VLAN_EN [3040].EN[0:0]	1
0xBB00D184	OF_VLAN_EN [3041].EN[1:1]	1
0xBB00D184	OF_VLAN_EN [3042].EN[2:2]	1
0xBB00D184	OF_VLAN_EN [3043].EN[3:3]	1
0xBB00D184	OF_VLAN_EN [3044].EN[4:4]	1
0xBB00D184	OF_VLAN_EN [3045].EN[5:5]	1
0xBB00D184	OF_VLAN_EN [3046].EN[6:6]	1
0xBB00D184	OF_VLAN_EN [3047].EN[7:7]	1
0xBB00D184	OF_VLAN_EN [3048].EN[8:8]	1
0xBB00D184	OF_VLAN_EN [3049].EN[9:9]	1
0xBB00D184	OF_VLAN_EN [3050].EN[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D184	OF_VLAN_EN [3054].EN[14:14]	1
0xBB00D184	OF_VLAN_EN [3055].EN[15:15]	1
0xBB00D184	OF_VLAN_EN [3056].EN[16:16]	1
0xBB00D184	OF_VLAN_EN [3057].EN[17:17]	1
0xBB00D184	OF_VLAN_EN [3058].EN[18:18]	1
0xBB00D184	OF_VLAN_EN [3059].EN[19:19]	1
0xBB00D184	OF_VLAN_EN [3060].EN[20:20]	1
0xBB00D184	OF_VLAN_EN [3061].EN[21:21]	1
0xBB00D184	OF_VLAN_EN [3062].EN[22:22]	1
0xBB00D184	OF_VLAN_EN [3063].EN[23:23]	1
0xBB00D184	OF_VLAN_EN [3064].EN[24:24]	1
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0xBB00D188	OF_VLAN_EN [3079].EN[7:7]	1
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0xBB00D188	OF_VLAN_EN [3081].EN[9:9]	1
0xBB00D188	OF_VLAN_EN [3082].EN[10:10]	1
0xBB00D188	OF_VLAN_EN [3083].EN[11:11]	1
0xBB00D188	OF_VLAN_EN [3084].EN[12:12]	1
0xBB00D188	OF_VLAN_EN [3085].EN[13:13]	1
0xBB00D188	OF_VLAN_EN [3086].EN[14:14]	1
0xBB00D188	OF_VLAN_EN [3087].EN[15:15]	1
0xBB00D188	OF_VLAN_EN [3088].EN[16:16]	1
0xBB00D188	OF_VLAN_EN [3089].EN[17:17]	1
0xBB00D188	OF_VLAN_EN [3090].EN[18:18]	1
0xBB00D188	OF_VLAN_EN [3091].EN[19:19]	1
0xBB00D188	OF_VLAN_EN [3092].EN[20:20]	1
0xBB00D188	OF_VLAN_EN [3093].EN[21:21]	1
0xBB00D188	OF_VLAN_EN [3094].EN[22:22]	1
0xBB00D188	OF_VLAN_EN [3095].EN[23:23]	1
0xBB00D188	OF_VLAN_EN [3096].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D188	OF_VLAN_EN [3099].EN[27:27]	1
0xBB00D188	OF_VLAN_EN [3100].EN[28:28]	1
0xBB00D188	OF_VLAN_EN [3101].EN[29:29]	1
0xBB00D188	OF_VLAN_EN [3102].EN[30:30]	1
0xBB00D188	OF_VLAN_EN [3103].EN[31:31]	1
0xBB00D18C	OF_VLAN_EN [3104].EN[0:0]	1
0xBB00D18C	OF_VLAN_EN [3105].EN[1:1]	1
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0xBB00D18C	OF_VLAN_EN [3109].EN[5:5]	1
0xBB00D18C	OF_VLAN_EN [3110].EN[6:6]	1
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0xBB00D18C	OF_VLAN_EN [3112].EN[8:8]	1
0xBB00D18C	OF_VLAN_EN [3113].EN[9:9]	1
0xBB00D18C	OF_VLAN_EN [3114].EN[10:10]	1
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0xBB00D18C	OF_VLAN_EN [3133].EN[29:29]	1
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0xBB00D18C	OF_VLAN_EN [3135].EN[31:31]	1
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0xBB00D190	OF_VLAN_EN [3137].EN[1:1]	1
0xBB00D190	OF_VLAN_EN [3138].EN[2:2]	1
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0xBB00D190	OF_VLAN_EN [3140].EN[4:4]	1
0xBB00D190	OF_VLAN_EN [3141].EN[5:5]	1
0xBB00D190	OF_VLAN_EN [3142].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D190	OF_VLAN_EN [3144].EN[8:8]	1
0xBB00D190	OF_VLAN_EN [3145].EN[9:9]	1
0xBB00D190	OF_VLAN_EN [3146].EN[10:10]	1
0xBB00D190	OF_VLAN_EN [3147].EN[11:11]	1
0xBB00D190	OF_VLAN_EN [3148].EN[12:12]	1
0xBB00D190	OF_VLAN_EN [3149].EN[13:13]	1
0xBB00D190	OF_VLAN_EN [3150].EN[14:14]	1
0xBB00D190	OF_VLAN_EN [3151].EN[15:15]	1
0xBB00D190	OF_VLAN_EN [3152].EN[16:16]	1
0xBB00D190	OF_VLAN_EN [3153].EN[17:17]	1
0xBB00D190	OF_VLAN_EN [3154].EN[18:18]	1
0xBB00D190	OF_VLAN_EN [3155].EN[19:19]	1
0xBB00D190	OF_VLAN_EN [3156].EN[20:20]	1
0xBB00D190	OF_VLAN_EN [3157].EN[21:21]	1
0xBB00D190	OF_VLAN_EN [3158].EN[22:22]	1
0xBB00D190	OF_VLAN_EN [3159].EN[23:23]	1
0xBB00D190	OF_VLAN_EN [3160].EN[24:24]	1
0xBB00D190	OF_VLAN_EN [3161].EN[25:25]	1
0xBB00D190	OF_VLAN_EN [3162].EN[26:26]	1
0xBB00D190	OF_VLAN_EN [3163].EN[27:27]	1
0xBB00D190	OF_VLAN_EN [3164].EN[28:28]	1
0xBB00D190	OF_VLAN_EN [3165].EN[29:29]	1
0xBB00D190	OF_VLAN_EN [3166].EN[30:30]	1
0xBB00D190	OF_VLAN_EN [3167].EN[31:31]	1
0xBB00D194	OF_VLAN_EN [3168].EN[0:0]	1
0xBB00D194	OF_VLAN_EN [3169].EN[1:1]	1
0xBB00D194	OF_VLAN_EN [3170].EN[2:2]	1
0xBB00D194	OF_VLAN_EN [3171].EN[3:3]	1
0xBB00D194	OF_VLAN_EN [3172].EN[4:4]	1
0xBB00D194	OF_VLAN_EN [3173].EN[5:5]	1
0xBB00D194	OF_VLAN_EN [3174].EN[6:6]	1
0xBB00D194	OF_VLAN_EN [3175].EN[7:7]	1
0xBB00D194	OF_VLAN_EN [3176].EN[8:8]	1
0xBB00D194	OF_VLAN_EN [3177].EN[9:9]	1
0xBB00D194	OF_VLAN_EN [3178].EN[10:10]	1
0xBB00D194	OF_VLAN_EN [3179].EN[11:11]	1
0xBB00D194	OF_VLAN_EN [3180].EN[12:12]	1
0xBB00D194	OF_VLAN_EN [3181].EN[13:13]	1
0xBB00D194	OF_VLAN_EN [3182].EN[14:14]	1
0xBB00D194	OF_VLAN_EN [3183].EN[15:15]	1
0xBB00D194	OF_VLAN_EN [3184].EN[16:16]	1
0xBB00D194	OF_VLAN_EN [3185].EN[17:17]	1
0xBB00D194	OF_VLAN_EN [3186].EN[18:18]	1
0xBB00D194	OF_VLAN_EN [3187].EN[19:19]	1
0xBB00D194	OF_VLAN_EN [3188].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D194	OF_VLAN_EN [3193].EN[25:25]	1
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0xBB00D194	OF_VLAN_EN [3195].EN[27:27]	1
0xBB00D194	OF_VLAN_EN [3196].EN[28:28]	1
0xBB00D194	OF_VLAN_EN [3197].EN[29:29]	1
0xBB00D194	OF_VLAN_EN [3198].EN[30:30]	1
0xBB00D194	OF_VLAN_EN [3199].EN[31:31]	1
0xBB00D198	OF_VLAN_EN [3200].EN[0:0]	1
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0xBB00D198	OF_VLAN_EN [3202].EN[2:2]	1
0xBB00D198	OF_VLAN_EN [3203].EN[3:3]	1
0xBB00D198	OF_VLAN_EN [3204].EN[4:4]	1
0xBB00D198	OF_VLAN_EN [3205].EN[5:5]	1
0xBB00D198	OF_VLAN_EN [3206].EN[6:6]	1
0xBB00D198	OF_VLAN_EN [3207].EN[7:7]	1
0xBB00D198	OF_VLAN_EN [3208].EN[8:8]	1
0xBB00D198	OF_VLAN_EN [3209].EN[9:9]	1
0xBB00D198	OF_VLAN_EN [3210].EN[10:10]	1
0xBB00D198	OF_VLAN_EN [3211].EN[11:11]	1
0xBB00D198	OF_VLAN_EN [3212].EN[12:12]	1
0xBB00D198	OF_VLAN_EN [3213].EN[13:13]	1
0xBB00D198	OF_VLAN_EN [3214].EN[14:14]	1
0xBB00D198	OF_VLAN_EN [3215].EN[15:15]	1
0xBB00D198	OF_VLAN_EN [3216].EN[16:16]	1
0xBB00D198	OF_VLAN_EN [3217].EN[17:17]	1
0xBB00D198	OF_VLAN_EN [3218].EN[18:18]	1
0xBB00D198	OF_VLAN_EN [3219].EN[19:19]	1
0xBB00D198	OF_VLAN_EN [3220].EN[20:20]	1
0xBB00D198	OF_VLAN_EN [3221].EN[21:21]	1
0xBB00D198	OF_VLAN_EN [3222].EN[22:22]	1
0xBB00D198	OF_VLAN_EN [3223].EN[23:23]	1
0xBB00D198	OF_VLAN_EN [3224].EN[24:24]	1
0xBB00D198	OF_VLAN_EN [3225].EN[25:25]	1
0xBB00D198	OF_VLAN_EN [3226].EN[26:26]	1
0xBB00D198	OF_VLAN_EN [3227].EN[27:27]	1
0xBB00D198	OF_VLAN_EN [3228].EN[28:28]	1
0xBB00D198	OF_VLAN_EN [3229].EN[29:29]	1
0xBB00D198	OF_VLAN_EN [3230].EN[30:30]	1
0xBB00D198	OF_VLAN_EN [3231].EN[31:31]	1
0xBB00D19C	OF_VLAN_EN [3232].EN[0:0]	1
0xBB00D19C	OF_VLAN_EN [3233].EN[1:1]	1
0xBB00D19C	OF_VLAN_EN [3234].EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D19C	OF_VLAN_EN [3239].EN[7:7]	1
0xBB00D19C	OF_VLAN_EN [3240].EN[8:8]	1
0xBB00D19C	OF_VLAN_EN [3241].EN[9:9]	1
0xBB00D19C	OF_VLAN_EN [3242].EN[10:10]	1
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0xBB00D19C	OF_VLAN_EN [3244].EN[12:12]	1
0xBB00D19C	OF_VLAN_EN [3245].EN[13:13]	1
0xBB00D19C	OF_VLAN_EN [3246].EN[14:14]	1
0xBB00D19C	OF_VLAN_EN [3247].EN[15:15]	1
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0xBB00D19C	OF_VLAN_EN [3250].EN[18:18]	1
0xBB00D19C	OF_VLAN_EN [3251].EN[19:19]	1
0xBB00D19C	OF_VLAN_EN [3252].EN[20:20]	1
0xBB00D19C	OF_VLAN_EN [3253].EN[21:21]	1
0xBB00D19C	OF_VLAN_EN [3254].EN[22:22]	1
0xBB00D19C	OF_VLAN_EN [3255].EN[23:23]	1
0xBB00D19C	OF_VLAN_EN [3256].EN[24:24]	1
0xBB00D19C	OF_VLAN_EN [3257].EN[25:25]	1
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0xBB00D19C	OF_VLAN_EN [3259].EN[27:27]	1
0xBB00D19C	OF_VLAN_EN [3260].EN[28:28]	1
0xBB00D19C	OF_VLAN_EN [3261].EN[29:29]	1
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0xBB00D19C	OF_VLAN_EN [3263].EN[31:31]	1
0xBB00D1A0	OF_VLAN_EN [3264].EN[0:0]	1
0xBB00D1A0	OF_VLAN_EN [3265].EN[1:1]	1
0xBB00D1A0	OF_VLAN_EN [3266].EN[2:2]	1
0xBB00D1A0	OF_VLAN_EN [3267].EN[3:3]	1
0xBB00D1A0	OF_VLAN_EN [3268].EN[4:4]	1
0xBB00D1A0	OF_VLAN_EN [3269].EN[5:5]	1
0xBB00D1A0	OF_VLAN_EN [3270].EN[6:6]	1
0xBB00D1A0	OF_VLAN_EN [3271].EN[7:7]	1
0xBB00D1A0	OF_VLAN_EN [3272].EN[8:8]	1
0xBB00D1A0	OF_VLAN_EN [3273].EN[9:9]	1
0xBB00D1A0	OF_VLAN_EN [3274].EN[10:10]	1
0xBB00D1A0	OF_VLAN_EN [3275].EN[11:11]	1
0xBB00D1A0	OF_VLAN_EN [3276].EN[12:12]	1
0xBB00D1A0	OF_VLAN_EN [3277].EN[13:13]	1
0xBB00D1A0	OF_VLAN_EN [3278].EN[14:14]	1
0xBB00D1A0	OF_VLAN_EN [3279].EN[15:15]	1
0xBB00D1A0	OF_VLAN_EN [3280].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1A0	OF_VLAN_EN [3284].EN[20:20]	1
0xBB00D1A0	OF_VLAN_EN [3285].EN[21:21]	1
0xBB00D1A0	OF_VLAN_EN [3286].EN[22:22]	1
0xBB00D1A0	OF_VLAN_EN [3287].EN[23:23]	1
0xBB00D1A0	OF_VLAN_EN [3288].EN[24:24]	1
0xBB00D1A0	OF_VLAN_EN [3289].EN[25:25]	1
0xBB00D1A0	OF_VLAN_EN [3290].EN[26:26]	1
0xBB00D1A0	OF_VLAN_EN [3291].EN[27:27]	1
0xBB00D1A0	OF_VLAN_EN [3292].EN[28:28]	1
0xBB00D1A0	OF_VLAN_EN [3293].EN[29:29]	1
0xBB00D1A0	OF_VLAN_EN [3294].EN[30:30]	1
0xBB00D1A0	OF_VLAN_EN [3295].EN[31:31]	1
0xBB00D1A4	OF_VLAN_EN [3296].EN[0:0]	1
0xBB00D1A4	OF_VLAN_EN [3297].EN[1:1]	1
0xBB00D1A4	OF_VLAN_EN [3298].EN[2:2]	1
0xBB00D1A4	OF_VLAN_EN [3299].EN[3:3]	1
0xBB00D1A4	OF_VLAN_EN [3300].EN[4:4]	1
0xBB00D1A4	OF_VLAN_EN [3301].EN[5:5]	1
0xBB00D1A4	OF_VLAN_EN [3302].EN[6:6]	1
0xBB00D1A4	OF_VLAN_EN [3303].EN[7:7]	1
0xBB00D1A4	OF_VLAN_EN [3304].EN[8:8]	1
0xBB00D1A4	OF_VLAN_EN [3305].EN[9:9]	1
0xBB00D1A4	OF_VLAN_EN [3306].EN[10:10]	1
0xBB00D1A4	OF_VLAN_EN [3307].EN[11:11]	1
0xBB00D1A4	OF_VLAN_EN [3308].EN[12:12]	1
0xBB00D1A4	OF_VLAN_EN [3309].EN[13:13]	1
0xBB00D1A4	OF_VLAN_EN [3310].EN[14:14]	1
0xBB00D1A4	OF_VLAN_EN [3311].EN[15:15]	1
0xBB00D1A4	OF_VLAN_EN [3312].EN[16:16]	1
0xBB00D1A4	OF_VLAN_EN [3313].EN[17:17]	1
0xBB00D1A4	OF_VLAN_EN [3314].EN[18:18]	1
0xBB00D1A4	OF_VLAN_EN [3315].EN[19:19]	1
0xBB00D1A4	OF_VLAN_EN [3316].EN[20:20]	1
0xBB00D1A4	OF_VLAN_EN [3317].EN[21:21]	1
0xBB00D1A4	OF_VLAN_EN [3318].EN[22:22]	1
0xBB00D1A4	OF_VLAN_EN [3319].EN[23:23]	1
0xBB00D1A4	OF_VLAN_EN [3320].EN[24:24]	1
0xBB00D1A4	OF_VLAN_EN [3321].EN[25:25]	1
0xBB00D1A4	OF_VLAN_EN [3322].EN[26:26]	1
0xBB00D1A4	OF_VLAN_EN [3323].EN[27:27]	1
0xBB00D1A4	OF_VLAN_EN [3324].EN[28:28]	1
0xBB00D1A4	OF_VLAN_EN [3325].EN[29:29]	1
0xBB00D1A4	OF_VLAN_EN [3326].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1A8	OF_VLAN_EN [3330].EN[2:2]	1
0xBB00D1A8	OF_VLAN_EN [3331].EN[3:3]	1
0xBB00D1A8	OF_VLAN_EN [3332].EN[4:4]	1
0xBB00D1A8	OF_VLAN_EN [3333].EN[5:5]	1
0xBB00D1A8	OF_VLAN_EN [3334].EN[6:6]	1
0xBB00D1A8	OF_VLAN_EN [3335].EN[7:7]	1
0xBB00D1A8	OF_VLAN_EN [3336].EN[8:8]	1
0xBB00D1A8	OF_VLAN_EN [3337].EN[9:9]	1
0xBB00D1A8	OF_VLAN_EN [3338].EN[10:10]	1
0xBB00D1A8	OF_VLAN_EN [3339].EN[11:11]	1
0xBB00D1A8	OF_VLAN_EN [3340].EN[12:12]	1
0xBB00D1A8	OF_VLAN_EN [3341].EN[13:13]	1
0xBB00D1A8	OF_VLAN_EN [3342].EN[14:14]	1
0xBB00D1A8	OF_VLAN_EN [3343].EN[15:15]	1
0xBB00D1A8	OF_VLAN_EN [3344].EN[16:16]	1
0xBB00D1A8	OF_VLAN_EN [3345].EN[17:17]	1
0xBB00D1A8	OF_VLAN_EN [3346].EN[18:18]	1
0xBB00D1A8	OF_VLAN_EN [3347].EN[19:19]	1
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0xBB00D1A8	OF_VLAN_EN [3351].EN[23:23]	1
0xBB00D1A8	OF_VLAN_EN [3352].EN[24:24]	1
0xBB00D1A8	OF_VLAN_EN [3353].EN[25:25]	1
0xBB00D1A8	OF_VLAN_EN [3354].EN[26:26]	1
0xBB00D1A8	OF_VLAN_EN [3355].EN[27:27]	1
0xBB00D1A8	OF_VLAN_EN [3356].EN[28:28]	1
0xBB00D1A8	OF_VLAN_EN [3357].EN[29:29]	1
0xBB00D1A8	OF_VLAN_EN [3358].EN[30:30]	1
0xBB00D1A8	OF_VLAN_EN [3359].EN[31:31]	1
0xBB00D1AC	OF_VLAN_EN [3360].EN[0:0]	1
0xBB00D1AC	OF_VLAN_EN [3361].EN[1:1]	1
0xBB00D1AC	OF_VLAN_EN [3362].EN[2:2]	1
0xBB00D1AC	OF_VLAN_EN [3363].EN[3:3]	1
0xBB00D1AC	OF_VLAN_EN [3364].EN[4:4]	1
0xBB00D1AC	OF_VLAN_EN [3365].EN[5:5]	1
0xBB00D1AC	OF_VLAN_EN [3366].EN[6:6]	1
0xBB00D1AC	OF_VLAN_EN [3367].EN[7:7]	1
0xBB00D1AC	OF_VLAN_EN [3368].EN[8:8]	1
0xBB00D1AC	OF_VLAN_EN [3369].EN[9:9]	1
0xBB00D1AC	OF_VLAN_EN [3370].EN[10:10]	1
0xBB00D1AC	OF_VLAN_EN [3371].EN[11:11]	1
0xBB00D1AC	OF_VLAN_EN [3372].EN[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1AC	OF_VLAN_EN [3376].EN[16:16]	1
0xBB00D1AC	OF_VLAN_EN [3377].EN[17:17]	1
0xBB00D1AC	OF_VLAN_EN [3378].EN[18:18]	1
0xBB00D1AC	OF_VLAN_EN [3379].EN[19:19]	1
0xBB00D1AC	OF_VLAN_EN [3380].EN[20:20]	1
0xBB00D1AC	OF_VLAN_EN [3381].EN[21:21]	1
0xBB00D1AC	OF_VLAN_EN [3382].EN[22:22]	1
0xBB00D1AC	OF_VLAN_EN [3383].EN[23:23]	1
0xBB00D1AC	OF_VLAN_EN [3384].EN[24:24]	1
0xBB00D1AC	OF_VLAN_EN [3385].EN[25:25]	1
0xBB00D1AC	OF_VLAN_EN [3386].EN[26:26]	1
0xBB00D1AC	OF_VLAN_EN [3387].EN[27:27]	1
0xBB00D1AC	OF_VLAN_EN [3388].EN[28:28]	1
0xBB00D1AC	OF_VLAN_EN [3389].EN[29:29]	1
0xBB00D1AC	OF_VLAN_EN [3390].EN[30:30]	1
0xBB00D1AC	OF_VLAN_EN [3391].EN[31:31]	1
0xBB00D1B0	OF_VLAN_EN [3392].EN[0:0]	1
0xBB00D1B0	OF_VLAN_EN [3393].EN[1:1]	1
0xBB00D1B0	OF_VLAN_EN [3394].EN[2:2]	1
0xBB00D1B0	OF_VLAN_EN [3395].EN[3:3]	1
0xBB00D1B0	OF_VLAN_EN [3396].EN[4:4]	1
0xBB00D1B0	OF_VLAN_EN [3397].EN[5:5]	1
0xBB00D1B0	OF_VLAN_EN [3398].EN[6:6]	1
0xBB00D1B0	OF_VLAN_EN [3399].EN[7:7]	1
0xBB00D1B0	OF_VLAN_EN [3400].EN[8:8]	1
0xBB00D1B0	OF_VLAN_EN [3401].EN[9:9]	1
0xBB00D1B0	OF_VLAN_EN [3402].EN[10:10]	1
0xBB00D1B0	OF_VLAN_EN [3403].EN[11:11]	1
0xBB00D1B0	OF_VLAN_EN [3404].EN[12:12]	1
0xBB00D1B0	OF_VLAN_EN [3405].EN[13:13]	1
0xBB00D1B0	OF_VLAN_EN [3406].EN[14:14]	1
0xBB00D1B0	OF_VLAN_EN [3407].EN[15:15]	1
0xBB00D1B0	OF_VLAN_EN [3408].EN[16:16]	1
0xBB00D1B0	OF_VLAN_EN [3409].EN[17:17]	1
0xBB00D1B0	OF_VLAN_EN [3410].EN[18:18]	1
0xBB00D1B0	OF_VLAN_EN [3411].EN[19:19]	1
0xBB00D1B0	OF_VLAN_EN [3412].EN[20:20]	1
0xBB00D1B0	OF_VLAN_EN [3413].EN[21:21]	1
0xBB00D1B0	OF_VLAN_EN [3414].EN[22:22]	1
0xBB00D1B0	OF_VLAN_EN [3415].EN[23:23]	1
0xBB00D1B0	OF_VLAN_EN [3416].EN[24:24]	1
0xBB00D1B0	OF_VLAN_EN [3417].EN[25:25]	1
0xBB00D1B0	OF_VLAN_EN [3418].EN[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1B0	OF_VLAN_EN [3421].EN[29:29]	1
0xBB00D1B0	OF_VLAN_EN [3422].EN[30:30]	1
0xBB00D1B0	OF_VLAN_EN [3423].EN[31:31]	1
0xBB00D1B4	OF_VLAN_EN [3424].EN[0:0]	1
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0xBB00D1B4	OF_VLAN_EN [3430].EN[6:6]	1
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0xBB00D1B4	OF_VLAN_EN [3432].EN[8:8]	1
0xBB00D1B4	OF_VLAN_EN [3433].EN[9:9]	1
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0xBB00D1B4	OF_VLAN_EN [3435].EN[11:11]	1
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0xBB00D1B4	OF_VLAN_EN [3438].EN[14:14]	1
0xBB00D1B4	OF_VLAN_EN [3439].EN[15:15]	1
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0xBB00D1B4	OF_VLAN_EN [3441].EN[17:17]	1
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0xBB00D1B4	OF_VLAN_EN [3451].EN[27:27]	1
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0xBB00D1B4	OF_VLAN_EN [3453].EN[29:29]	1
0xBB00D1B4	OF_VLAN_EN [3454].EN[30:30]	1
0xBB00D1B4	OF_VLAN_EN [3455].EN[31:31]	1
0xBB00D1B8	OF_VLAN_EN [3456].EN[0:0]	1
0xBB00D1B8	OF_VLAN_EN [3457].EN[1:1]	1
0xBB00D1B8	OF_VLAN_EN [3458].EN[2:2]	1
0xBB00D1B8	OF_VLAN_EN [3459].EN[3:3]	1
0xBB00D1B8	OF_VLAN_EN [3460].EN[4:4]	1
0xBB00D1B8	OF_VLAN_EN [3461].EN[5:5]	1
0xBB00D1B8	OF_VLAN_EN [3462].EN[6:6]	1
0xBB00D1B8	OF_VLAN_EN [3463].EN[7:7]	1
0xBB00D1B8	OF_VLAN_EN [3464].EN[8:8]	1

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0xBB00D1B8	OF_VLAN_EN [3469].EN[13:13]	1
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0xBB00D1B8	OF_VLAN_EN [3472].EN[16:16]	1
0xBB00D1B8	OF_VLAN_EN [3473].EN[17:17]	1
0xBB00D1B8	OF_VLAN_EN [3474].EN[18:18]	1
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0xBB00D1BC	OF_VLAN_EN [3501].EN[13:13]	1
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0xBB00D1BC	OF_VLAN_EN [3506].EN[18:18]	1
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0xBB00D1C0	OF_VLAN_EN [3527].EN[7:7]	1
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0xBB00D1C0	OF_VLAN_EN [3544].EN[24:24]	1
0xBB00D1C0	OF_VLAN_EN [3545].EN[25:25]	1
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0xBB00D1C4	OF_VLAN_EN [3554].EN[2:2]	1
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0xBB00D1C4	OF_VLAN_EN [3561].EN[9:9]	1
0xBB00D1C4	OF_VLAN_EN [3562].EN[10:10]	1
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0xBB00D1C4	OF_VLAN_EN [3568].EN[16:16]	1
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0xBB00D1C4	OF_VLAN_EN [3571].EN[19:19]	1
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0xBB00D1C4	OF_VLAN_EN [3583].EN[31:31]	1
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0xBB00D1C8	OF_VLAN_EN [3586].EN[2:2]	1
0xBB00D1C8	OF_VLAN_EN [3587].EN[3:3]	1
0xBB00D1C8	OF_VLAN_EN [3588].EN[4:4]	1
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0xBB00D1C8	OF_VLAN_EN [3590].EN[6:6]	1
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0xBB00D1C8	OF_VLAN_EN [3592].EN[8:8]	1
0xBB00D1C8	OF_VLAN_EN [3593].EN[9:9]	1
0xBB00D1C8	OF_VLAN_EN [3594].EN[10:10]	1
0xBB00D1C8	OF_VLAN_EN [3595].EN[11:11]	1
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0xBB00D1C8	OF_VLAN_EN [3598].EN[14:14]	1
0xBB00D1C8	OF_VLAN_EN [3599].EN[15:15]	1
0xBB00D1C8	OF_VLAN_EN [3600].EN[16:16]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1CC	OF_VLAN_EN [3626].EN[10:10]	1
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0xBB00D1CC	OF_VLAN_EN [3629].EN[13:13]	1
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0xBB00D1CC	OF_VLAN_EN [3631].EN[15:15]	1
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0xBB00D1CC	OF_VLAN_EN [3635].EN[19:19]	1
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0xBB00D1CC	OF_VLAN_EN [3638].EN[22:22]	1
0xBB00D1CC	OF_VLAN_EN [3639].EN[23:23]	1
0xBB00D1CC	OF_VLAN_EN [3640].EN[24:24]	1
0xBB00D1CC	OF_VLAN_EN [3641].EN[25:25]	1
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0xBB00D1CC	OF_VLAN_EN [3645].EN[29:29]	1
0xBB00D1CC	OF_VLAN_EN [3646].EN[30:30]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1D0	OF_VLAN_EN [3653].EN[5:5]	1
0xBB00D1D0	OF_VLAN_EN [3654].EN[6:6]	1
0xBB00D1D0	OF_VLAN_EN [3655].EN[7:7]	1
0xBB00D1D0	OF_VLAN_EN [3656].EN[8:8]	1
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0xBB00D1D0	OF_VLAN_EN [3660].EN[12:12]	1
0xBB00D1D0	OF_VLAN_EN [3661].EN[13:13]	1
0xBB00D1D0	OF_VLAN_EN [3662].EN[14:14]	1
0xBB00D1D0	OF_VLAN_EN [3663].EN[15:15]	1
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0xBB00D1D0	OF_VLAN_EN [3673].EN[25:25]	1
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0xBB00D1D0	OF_VLAN_EN [3678].EN[30:30]	1
0xBB00D1D0	OF_VLAN_EN [3679].EN[31:31]	1
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0xBB00D1D4	OF_VLAN_EN [3681].EN[1:1]	1
0xBB00D1D4	OF_VLAN_EN [3682].EN[2:2]	1
0xBB00D1D4	OF_VLAN_EN [3683].EN[3:3]	1
0xBB00D1D4	OF_VLAN_EN [3684].EN[4:4]	1
0xBB00D1D4	OF_VLAN_EN [3685].EN[5:5]	1
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0xBB00D1D4	OF_VLAN_EN [3687].EN[7:7]	1
0xBB00D1D4	OF_VLAN_EN [3688].EN[8:8]	1
0xBB00D1D4	OF_VLAN_EN [3689].EN[9:9]	1
0xBB00D1D4	OF_VLAN_EN [3690].EN[10:10]	1
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0xBB00D1D4	OF_VLAN_EN [3692].EN[12:12]	1
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0xBB00D1D4	OF_VLAN_EN [3694].EN[14:14]	1

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0xBB00D1D4	OF_VLAN_EN [3698].EN[18:18]	1
0xBB00D1D4	OF_VLAN_EN [3699].EN[19:19]	1
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0xBB00D1D4	OF_VLAN_EN [3707].EN[27:27]	1
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0xBB00D1D4	OF_VLAN_EN [3709].EN[29:29]	1
0xBB00D1D4	OF_VLAN_EN [3710].EN[30:30]	1
0xBB00D1D4	OF_VLAN_EN [3711].EN[31:31]	1
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0xBB00D1D8	OF_VLAN_EN [3713].EN[1:1]	1
0xBB00D1D8	OF_VLAN_EN [3714].EN[2:2]	1
0xBB00D1D8	OF_VLAN_EN [3715].EN[3:3]	1
0xBB00D1D8	OF_VLAN_EN [3716].EN[4:4]	1
0xBB00D1D8	OF_VLAN_EN [3717].EN[5:5]	1
0xBB00D1D8	OF_VLAN_EN [3718].EN[6:6]	1
0xBB00D1D8	OF_VLAN_EN [3719].EN[7:7]	1
0xBB00D1D8	OF_VLAN_EN [3720].EN[8:8]	1
0xBB00D1D8	OF_VLAN_EN [3721].EN[9:9]	1
0xBB00D1D8	OF_VLAN_EN [3722].EN[10:10]	1
0xBB00D1D8	OF_VLAN_EN [3723].EN[11:11]	1
0xBB00D1D8	OF_VLAN_EN [3724].EN[12:12]	1
0xBB00D1D8	OF_VLAN_EN [3725].EN[13:13]	1
0xBB00D1D8	OF_VLAN_EN [3726].EN[14:14]	1
0xBB00D1D8	OF_VLAN_EN [3727].EN[15:15]	1
0xBB00D1D8	OF_VLAN_EN [3728].EN[16:16]	1
0xBB00D1D8	OF_VLAN_EN [3729].EN[17:17]	1
0xBB00D1D8	OF_VLAN_EN [3730].EN[18:18]	1
0xBB00D1D8	OF_VLAN_EN [3731].EN[19:19]	1
0xBB00D1D8	OF_VLAN_EN [3732].EN[20:20]	1
0xBB00D1D8	OF_VLAN_EN [3733].EN[21:21]	1
0xBB00D1D8	OF_VLAN_EN [3734].EN[22:22]	1
0xBB00D1D8	OF_VLAN_EN [3735].EN[23:23]	1
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0xBB00D1D8	OF_VLAN_EN [3740].EN[28:28]	1

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0xBB00D1DC	OF_VLAN_EN [3766].EN[22:22]	1
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0xBB00D1DC	OF_VLAN_EN [3768].EN[24:24]	1
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0xBB00D1DC	OF_VLAN_EN [3774].EN[30:30]	1
0xBB00D1DC	OF_VLAN_EN [3775].EN[31:31]	1
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0xBB00D1E0	OF_VLAN_EN [3777].EN[1:1]	1
0xBB00D1E0	OF_VLAN_EN [3778].EN[2:2]	1
0xBB00D1E0	OF_VLAN_EN [3779].EN[3:3]	1
0xBB00D1E0	OF_VLAN_EN [3780].EN[4:4]	1
0xBB00D1E0	OF_VLAN_EN [3781].EN[5:5]	1
0xBB00D1E0	OF_VLAN_EN [3782].EN[6:6]	1
0xBB00D1E0	OF_VLAN_EN [3783].EN[7:7]	1
0xBB00D1E0	OF_VLAN_EN [3784].EN[8:8]	1
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0xBB00D1E0	OF_VLAN_EN [3792].EN[16:16]	1
0xBB00D1E0	OF_VLAN_EN [3793].EN[17:17]	1
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0xBB00D1E4	OF_VLAN_EN [3817].EN[9:9]	1
0xBB00D1E4	OF_VLAN_EN [3818].EN[10:10]	1
0xBB00D1E4	OF_VLAN_EN [3819].EN[11:11]	1
0xBB00D1E4	OF_VLAN_EN [3820].EN[12:12]	1
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0xBB00D1E4	OF_VLAN_EN [3824].EN[16:16]	1
0xBB00D1E4	OF_VLAN_EN [3825].EN[17:17]	1
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0xBB00D1E4	OF_VLAN_EN [3827].EN[19:19]	1
0xBB00D1E4	OF_VLAN_EN [3828].EN[20:20]	1
0xBB00D1E4	OF_VLAN_EN [3829].EN[21:21]	1
0xBB00D1E4	OF_VLAN_EN [3830].EN[22:22]	1
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0xBB00D1E8	OF_VLAN_EN [3841].EN[1:1]	1
0xBB00D1E8	OF_VLAN_EN [3842].EN[2:2]	1
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0xBB00D1E8	OF_VLAN_EN [3846].EN[6:6]	1
0xBB00D1E8	OF_VLAN_EN [3847].EN[7:7]	1
0xBB00D1E8	OF_VLAN_EN [3848].EN[8:8]	1
0xBB00D1E8	OF_VLAN_EN [3849].EN[9:9]	1
0xBB00D1E8	OF_VLAN_EN [3850].EN[10:10]	1
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0xBB00D1E8	OF_VLAN_EN [3854].EN[14:14]	1
0xBB00D1E8	OF_VLAN_EN [3855].EN[15:15]	1
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0xBB00D1E8	OF_VLAN_EN [3857].EN[17:17]	1
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0xBB00D1E8	OF_VLAN_EN [3866].EN[26:26]	1
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0xBB00D1E8	OF_VLAN_EN [3868].EN[28:28]	1
0xBB00D1E8	OF_VLAN_EN [3869].EN[29:29]	1
0xBB00D1E8	OF_VLAN_EN [3870].EN[30:30]	1
0xBB00D1E8	OF_VLAN_EN [3871].EN[31:31]	1
0xBB00D1EC	OF_VLAN_EN [3872].EN[0:0]	1
0xBB00D1EC	OF_VLAN_EN [3873].EN[1:1]	1
0xBB00D1EC	OF_VLAN_EN [3874].EN[2:2]	1
0xBB00D1EC	OF_VLAN_EN [3875].EN[3:3]	1
0xBB00D1EC	OF_VLAN_EN [3876].EN[4:4]	1
0xBB00D1EC	OF_VLAN_EN [3877].EN[5:5]	1
0xBB00D1EC	OF_VLAN_EN [3878].EN[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB00D1EC	OF_VLAN_EN [3883].EN[11:11]	1
0xBB00D1EC	OF_VLAN_EN [3884].EN[12:12]	1
0xBB00D1EC	OF_VLAN_EN [3885].EN[13:13]	1
0xBB00D1EC	OF_VLAN_EN [3886].EN[14:14]	1
0xBB00D1EC	OF_VLAN_EN [3887].EN[15:15]	1
0xBB00D1EC	OF_VLAN_EN [3888].EN[16:16]	1
0xBB00D1EC	OF_VLAN_EN [3889].EN[17:17]	1
0xBB00D1EC	OF_VLAN_EN [3890].EN[18:18]	1
0xBB00D1EC	OF_VLAN_EN [3891].EN[19:19]	1
0xBB00D1EC	OF_VLAN_EN [3892].EN[20:20]	1
0xBB00D1EC	OF_VLAN_EN [3893].EN[21:21]	1
0xBB00D1EC	OF_VLAN_EN [3894].EN[22:22]	1
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0xBB00D1EC	OF_VLAN_EN [3896].EN[24:24]	1
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0xBB00D1EC	OF_VLAN_EN [3899].EN[27:27]	1
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0xBB00D1EC	OF_VLAN_EN [3902].EN[30:30]	1
0xBB00D1EC	OF_VLAN_EN [3903].EN[31:31]	1
0xBB00D1F0	OF_VLAN_EN [3904].EN[0:0]	1
0xBB00D1F0	OF_VLAN_EN [3905].EN[1:1]	1
0xBB00D1F0	OF_VLAN_EN [3906].EN[2:2]	1
0xBB00D1F0	OF_VLAN_EN [3907].EN[3:3]	1
0xBB00D1F0	OF_VLAN_EN [3908].EN[4:4]	1
0xBB00D1F0	OF_VLAN_EN [3909].EN[5:5]	1
0xBB00D1F0	OF_VLAN_EN [3910].EN[6:6]	1
0xBB00D1F0	OF_VLAN_EN [3911].EN[7:7]	1
0xBB00D1F0	OF_VLAN_EN [3912].EN[8:8]	1
0xBB00D1F0	OF_VLAN_EN [3913].EN[9:9]	1
0xBB00D1F0	OF_VLAN_EN [3914].EN[10:10]	1
0xBB00D1F0	OF_VLAN_EN [3915].EN[11:11]	1
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0xBB00D1F0	OF_VLAN_EN [3917].EN[13:13]	1
0xBB00D1F0	OF_VLAN_EN [3918].EN[14:14]	1
0xBB00D1F0	OF_VLAN_EN [3919].EN[15:15]	1
0xBB00D1F0	OF_VLAN_EN [3920].EN[16:16]	1
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0xBB00D1F0	OF_VLAN_EN [3922].EN[18:18]	1
0xBB00D1F0	OF_VLAN_EN [3923].EN[19:19]	1
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0xBB00D1F4	OF_VLAN_EN [3954].EN[18:18]	1
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0xBB00D1F4	OF_VLAN_EN [3957].EN[21:21]	1
0xBB00D1F4	OF_VLAN_EN [3958].EN[22:22]	1
0xBB00D1F4	OF_VLAN_EN [3959].EN[23:23]	1
0xBB00D1F4	OF_VLAN_EN [3960].EN[24:24]	1
0xBB00D1F4	OF_VLAN_EN [3961].EN[25:25]	1
0xBB00D1F4	OF_VLAN_EN [3962].EN[26:26]	1
0xBB00D1F4	OF_VLAN_EN [3963].EN[27:27]	1
0xBB00D1F4	OF_VLAN_EN [3964].EN[28:28]	1
0xBB00D1F4	OF_VLAN_EN [3965].EN[29:29]	1
0xBB00D1F4	OF_VLAN_EN [3966].EN[30:30]	1
0xBB00D1F4	OF_VLAN_EN [3967].EN[31:31]	1
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0xBB00D1F8	OF_VLAN_EN [3969].EN[1:1]	1
0xBB00D1F8	OF_VLAN_EN [3970].EN[2:2]	1

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0xBB00D1F8	OF_VLAN_EN [3974].EN[6:6]	1
0xBB00D1F8	OF_VLAN_EN [3975].EN[7:7]	1
0xBB00D1F8	OF_VLAN_EN [3976].EN[8:8]	1
0xBB00D1F8	OF_VLAN_EN [3977].EN[9:9]	1
0xBB00D1F8	OF_VLAN_EN [3978].EN[10:10]	1
0xBB00D1F8	OF_VLAN_EN [3979].EN[11:11]	1
0xBB00D1F8	OF_VLAN_EN [3980].EN[12:12]	1
0xBB00D1F8	OF_VLAN_EN [3981].EN[13:13]	1
0xBB00D1F8	OF_VLAN_EN [3982].EN[14:14]	1
0xBB00D1F8	OF_VLAN_EN [3983].EN[15:15]	1
0xBB00D1F8	OF_VLAN_EN [3984].EN[16:16]	1
0xBB00D1F8	OF_VLAN_EN [3985].EN[17:17]	1
0xBB00D1F8	OF_VLAN_EN [3986].EN[18:18]	1
0xBB00D1F8	OF_VLAN_EN [3987].EN[19:19]	1
0xBB00D1F8	OF_VLAN_EN [3988].EN[20:20]	1
0xBB00D1F8	OF_VLAN_EN [3989].EN[21:21]	1
0xBB00D1F8	OF_VLAN_EN [3990].EN[22:22]	1
0xBB00D1F8	OF_VLAN_EN [3991].EN[23:23]	1
0xBB00D1F8	OF_VLAN_EN [3992].EN[24:24]	1
0xBB00D1F8	OF_VLAN_EN [3993].EN[25:25]	1
0xBB00D1F8	OF_VLAN_EN [3994].EN[26:26]	1
0xBB00D1F8	OF_VLAN_EN [3995].EN[27:27]	1
0xBB00D1F8	OF_VLAN_EN [3996].EN[28:28]	1
0xBB00D1F8	OF_VLAN_EN [3997].EN[29:29]	1
0xBB00D1F8	OF_VLAN_EN [3998].EN[30:30]	1
0xBB00D1F8	OF_VLAN_EN [3999].EN[31:31]	1
0xBB00D1FC	OF_VLAN_EN [4000].EN[0:0]	1
0xBB00D1FC	OF_VLAN_EN [4001].EN[1:1]	1
0xBB00D1FC	OF_VLAN_EN [4002].EN[2:2]	1
0xBB00D1FC	OF_VLAN_EN [4003].EN[3:3]	1
0xBB00D1FC	OF_VLAN_EN [4004].EN[4:4]	1
0xBB00D1FC	OF_VLAN_EN [4005].EN[5:5]	1
0xBB00D1FC	OF_VLAN_EN [4006].EN[6:6]	1
0xBB00D1FC	OF_VLAN_EN [4007].EN[7:7]	1
0xBB00D1FC	OF_VLAN_EN [4008].EN[8:8]	1
0xBB00D1FC	OF_VLAN_EN [4009].EN[9:9]	1
0xBB00D1FC	OF_VLAN_EN [4010].EN[10:10]	1
0xBB00D1FC	OF_VLAN_EN [4011].EN[11:11]	1
0xBB00D1FC	OF_VLAN_EN [4012].EN[12:12]	1
0xBB00D1FC	OF_VLAN_EN [4013].EN[13:13]	1
0xBB00D1FC	OF_VLAN_EN [4014].EN[14:14]	1
0xBB00D1FC	OF_VLAN_EN [4015].EN[15:15]	1
0xBB00D1FC	OF_VLAN_EN [4016].EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D1FC	OF_VLAN_EN [4017].EN[17:17]	1
0xBB00D1FC	OF_VLAN_EN [4018].EN[18:18]	1
0xBB00D1FC	OF_VLAN_EN [4019].EN[19:19]	1
0xBB00D1FC	OF_VLAN_EN [4020].EN[20:20]	1
0xBB00D1FC	OF_VLAN_EN [4021].EN[21:21]	1
0xBB00D1FC	OF_VLAN_EN [4022].EN[22:22]	1
0xBB00D1FC	OF_VLAN_EN [4023].EN[23:23]	1
0xBB00D1FC	OF_VLAN_EN [4024].EN[24:24]	1
0xBB00D1FC	OF_VLAN_EN [4025].EN[25:25]	1
0xBB00D1FC	OF_VLAN_EN [4026].EN[26:26]	1
0xBB00D1FC	OF_VLAN_EN [4027].EN[27:27]	1
0xBB00D1FC	OF_VLAN_EN [4028].EN[28:28]	1
0xBB00D1FC	OF_VLAN_EN [4029].EN[29:29]	1
0xBB00D1FC	OF_VLAN_EN [4030].EN[30:30]	1
0xBB00D1FC	OF_VLAN_EN [4031].EN[31:31]	1
0xBB00D200	OF_VLAN_EN [4032].EN[0:0]	1
0xBB00D200	OF_VLAN_EN [4033].EN[1:1]	1
0xBB00D200	OF_VLAN_EN [4034].EN[2:2]	1
0xBB00D200	OF_VLAN_EN [4035].EN[3:3]	1
0xBB00D200	OF_VLAN_EN [4036].EN[4:4]	1
0xBB00D200	OF_VLAN_EN [4037].EN[5:5]	1
0xBB00D200	OF_VLAN_EN [4038].EN[6:6]	1
0xBB00D200	OF_VLAN_EN [4039].EN[7:7]	1
0xBB00D200	OF_VLAN_EN [4040].EN[8:8]	1
0xBB00D200	OF_VLAN_EN [4041].EN[9:9]	1
0xBB00D200	OF_VLAN_EN [4042].EN[10:10]	1
0xBB00D200	OF_VLAN_EN [4043].EN[11:11]	1
0xBB00D200	OF_VLAN_EN [4044].EN[12:12]	1
0xBB00D200	OF_VLAN_EN [4045].EN[13:13]	1
0xBB00D200	OF_VLAN_EN [4046].EN[14:14]	1
0xBB00D200	OF_VLAN_EN [4047].EN[15:15]	1
0xBB00D200	OF_VLAN_EN [4048].EN[16:16]	1
0xBB00D200	OF_VLAN_EN [4049].EN[17:17]	1
0xBB00D200	OF_VLAN_EN [4050].EN[18:18]	1
0xBB00D200	OF_VLAN_EN [4051].EN[19:19]	1
0xBB00D200	OF_VLAN_EN [4052].EN[20:20]	1
0xBB00D200	OF_VLAN_EN [4053].EN[21:21]	1
0xBB00D200	OF_VLAN_EN [4054].EN[22:22]	1
0xBB00D200	OF_VLAN_EN [4055].EN[23:23]	1
0xBB00D200	OF_VLAN_EN [4056].EN[24:24]	1
0xBB00D200	OF_VLAN_EN [4057].EN[25:25]	1
0xBB00D200	OF_VLAN_EN [4058].EN[26:26]	1
0xBB00D200	OF_VLAN_EN [4059].EN[27:27]	1
0xBB00D200	OF_VLAN_EN [4060].EN[28:28]	1
0xBB00D200	OF_VLAN_EN [4061].EN[29:29]	1
0xBB00D200	OF_VLAN_EN [4062].EN[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D200	OF_VLAN_EN [4063].EN[31:31]	1
0xBB00D204	OF_VLAN_EN [4064].EN[0:0]	1
0xBB00D204	OF_VLAN_EN [4065].EN[1:1]	1
0xBB00D204	OF_VLAN_EN [4066].EN[2:2]	1
0xBB00D204	OF_VLAN_EN [4067].EN[3:3]	1
0xBB00D204	OF_VLAN_EN [4068].EN[4:4]	1
0xBB00D204	OF_VLAN_EN [4069].EN[5:5]	1
0xBB00D204	OF_VLAN_EN [4070].EN[6:6]	1
0xBB00D204	OF_VLAN_EN [4071].EN[7:7]	1
0xBB00D204	OF_VLAN_EN [4072].EN[8:8]	1
0xBB00D204	OF_VLAN_EN [4073].EN[9:9]	1
0xBB00D204	OF_VLAN_EN [4074].EN[10:10]	1
0xBB00D204	OF_VLAN_EN [4075].EN[11:11]	1
0xBB00D204	OF_VLAN_EN [4076].EN[12:12]	1
0xBB00D204	OF_VLAN_EN [4077].EN[13:13]	1
0xBB00D204	OF_VLAN_EN [4078].EN[14:14]	1
0xBB00D204	OF_VLAN_EN [4079].EN[15:15]	1
0xBB00D204	OF_VLAN_EN [4080].EN[16:16]	1
0xBB00D204	OF_VLAN_EN [4081].EN[17:17]	1
0xBB00D204	OF_VLAN_EN [4082].EN[18:18]	1
0xBB00D204	OF_VLAN_EN [4083].EN[19:19]	1
0xBB00D204	OF_VLAN_EN [4084].EN[20:20]	1
0xBB00D204	OF_VLAN_EN [4085].EN[21:21]	1
0xBB00D204	OF_VLAN_EN [4086].EN[22:22]	1
0xBB00D204	OF_VLAN_EN [4087].EN[23:23]	1
0xBB00D204	OF_VLAN_EN [4088].EN[24:24]	1
0xBB00D204	OF_VLAN_EN [4089].EN[25:25]	1
0xBB00D204	OF_VLAN_EN [4090].EN[26:26]	1
0xBB00D204	OF_VLAN_EN [4091].EN[27:27]	1
0xBB00D204	OF_VLAN_EN [4092].EN[28:28]	1
0xBB00D204	OF_VLAN_EN [4093].EN[29:29]	1
0xBB00D204	OF_VLAN_EN [4094].EN[30:30]	1
0xBB00D204	OF_VLAN_EN [4095].EN[31:31]	1
0xBB00056C	PARSER_CTRL.VXL_AN_GPE_UDP_PORT[31:16]	16
0xBB00056C	PARSER_CTRL.RESERVED[15:7]	9
0xBB00056C	PARSER_CTRL.MALFORMED_PKT_ACT[6:5]	2
0xBB00056C	PARSER_CTRL.PARSER_CANT_HANDLE_ACT[4:3]	2
0xBB00056C	PARSER_CTRL.WL_OFFSET_EN[2:2]	1
0xBB00056C	PARSER_CTRL.PPPOE_PARSE_EN[1:1]	1
0xBB00056C	PARSER_CTRL.RFC1042_OUI_IGNORE[0:0]	1
0xBB001444	PE_ETAG_CTRL.RESERVED[31:1]	31
0xBB001444	PE_ETAG_CTRL.CPU_KEEP[0:0]	1
0xBB00572C	PE_ETAG_MAC_CTRL.RESERVED[31:16]	16
0xBB00572C	PE_ETAG_MAC_CTRL.TPID[15:0]	16
0xBB000228	PE_ETAG_RMK_CTRL [0].PRI[2:0]	3
0xBB000228	PE_ETAG_RMK_CTRL [1].PRI[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000228	PE_ETAG_RMK_CTRL [2].PRI[8:6]	3
0xBB000228	PE_ETAG_RMK_CTRL [3].PRI[11:9]	3
0xBB000228	PE_ETAG_RMK_CTRL [4].PRI[14:12]	3
0xBB000228	PE_ETAG_RMK_CTRL [5].PRI[17:15]	3
0xBB000228	PE_ETAG_RMK_CTRL [6].PRI[20:18]	3
0xBB000228	PE_ETAG_RMK_CTRL [7].PRI[23:21]	3
0xBB00022C	PE_PORT_ETAG_EGR_CTRL [0].RESERVED[31:27]	5
0xBB00022C	PE_PORT_ETAG_EGR_CTRL [0].VLAN_TAG_STS_MODE[26:26]	1
0xBB00022C	PE_PORT_ETAG_EGR_CTRL [0].TAG_DEI_RMK_EN[25:25]	1
0xBB00022C	PE_PORT_ETAG_EGR_CTRL [0].TAG_PRI_KEEP[24:24]	1
0xBB00022C	PE_PORT_ETAG_EGR_CTRL [0].TAG_STS_MODE[23:22]	2
0xBB00022C	PE_PORT_ETAG_EGR_CTRL [0].RESERVED[21:0]	22
0xBB000230	PE_PORT_ETAG_EGR_CTRL [1].RESERVED[31:27]	5
0xBB000230	PE_PORT_ETAG_EGR_CTRL [1].VLAN_TAG_STS_MODE[26:26]	1
0xBB000230	PE_PORT_ETAG_EGR_CTRL [1].TAG_DEI_RMK_EN[25:25]	1
0xBB000230	PE_PORT_ETAG_EGR_CTRL [1].TAG_PRI_KEEP[24:24]	1
0xBB000230	PE_PORT_ETAG_EGR_CTRL [1].TAG_STS_MODE[23:22]	2
0xBB000230	PE_PORT_ETAG_EGR_CTRL [1].RESERVED[21:0]	22
0xBB000234	PE_PORT_ETAG_EGR_CTRL [2].RESERVED[31:27]	5
0xBB000234	PE_PORT_ETAG_EGR_CTRL [2].VLAN_TAG_STS_MODE[26:26]	1
0xBB000234	PE_PORT_ETAG_EGR_CTRL [2].TAG_DEI_RMK_EN[25:25]	1
0xBB000234	PE_PORT_ETAG_EGR_CTRL [2].TAG_PRI_KEEP[24:24]	1
0xBB000234	PE_PORT_ETAG_EGR_CTRL [2].TAG_STS_MODE[23:22]	2
0xBB000234	PE_PORT_ETAG_EGR_CTRL [2].RESERVED[21:0]	22
0xBB000238	PE_PORT_ETAG_EGR_CTRL [3].RESERVED[31:27]	5
0xBB000238	PE_PORT_ETAG_EGR_CTRL [3].VLAN_TAG_STS_MODE[26:26]	1
0xBB000238	PE_PORT_ETAG_EGR_CTRL [3].TAG_DEI_RMK_EN[25:25]	1
0xBB000238	PE_PORT_ETAG_EGR_CTRL [3].TAG_PRI_KEEP[24:24]	1
0xBB000238	PE_PORT_ETAG_EGR_CTRL [3].TAG_STS_MODE[23:22]	2
0xBB000238	PE_PORT_ETAG_EGR_CTRL [3].RESERVED[21:0]	22
0xBB00023C	PE_PORT_ETAG_EGR_CTRL [4].RESERVED[31:27]	5
0xBB00023C	PE_PORT_ETAG_EGR_CTRL [4].VLAN_TAG_STS_MODE[26:26]	1
0xBB00023C	PE_PORT_ETAG_EGR_CTRL [4].TAG_DEI_RMK_EN[25:25]	1
0xBB00023C	PE_PORT_ETAG_EGR_CTRL [4].TAG_PRI_KEEP[24:24]	1
0xBB00023C	PE_PORT_ETAG_EGR_CTRL [4].TAG_STS_MODE[23:22]	2
0xBB00023C	PE_PORT_ETAG_EGR_CTRL [4].RESERVED[21:0]	22
0xBB000240	PE_PORT_ETAG_EGR_CTRL [5].RESERVED[31:27]	5
0xBB000240	PE_PORT_ETAG_EGR_CTRL [5].VLAN_TAG_STS_MODE[26:26]	1
0xBB000240	PE_PORT_ETAG_EGR_CTRL [5].TAG_DEI_RMK_EN[25:25]	1
0xBB000240	PE_PORT_ETAG_EGR_CTRL [5].TAG_PRI_KEEP[24:24]	1
0xBB000240	PE_PORT_ETAG_EGR_CTRL [5].TAG_STS_MODE[23:22]	2
0xBB000240	PE_PORT_ETAG_EGR_CTRL [5].RESERVED[21:0]	22
0xBB000244	PE_PORT_ETAG_EGR_CTRL [6].RESERVED[31:27]	5
0xBB000244	PE_PORT_ETAG_EGR_CTRL [6].VLAN_TAG_STS_MODE[26:26]	1
0xBB000244	PE_PORT_ETAG_EGR_CTRL [6].TAG_DEI_RMK_EN[25:25]	1
0xBB000244	PE_PORT_ETAG_EGR_CTRL [6].TAG_PRI_KEEP[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000244	PE_PORT_ETAG_EGR_CTRL [6].TAG_STS_MODE[23:22]	2
0xBB000244	PE_PORT_ETAG_EGR_CTRL [6].RESERVED[21:0]	22
0xBB000248	PE_PORT_ETAG_EGR_CTRL [7].RESERVED[31:27]	5
0xBB000248	PE_PORT_ETAG_EGR_CTRL [7].VLAN_TAG_STS_MODE[26:26]	1
0xBB000248	PE_PORT_ETAG_EGR_CTRL [7].TAG_DEL_RMK_EN[25:25]	1
0xBB000248	PE_PORT_ETAG_EGR_CTRL [7].TAG_PRI_KEEP[24:24]	1
0xBB000248	PE_PORT_ETAG_EGR_CTRL [7].TAG_STS_MODE[23:22]	2
0xBB000248	PE_PORT_ETAG_EGR_CTRL [7].RESERVED[21:0]	22
0xBB00024C	PE_PORT_ETAG_EGR_CTRL [8].RESERVED[31:27]	5
0xBB00024C	PE_PORT_ETAG_EGR_CTRL [8].VLAN_TAG_STS_MODE[26:26]	1
0xBB00024C	PE_PORT_ETAG_EGR_CTRL [8].TAG_DEL_RMK_EN[25:25]	1
0xBB00024C	PE_PORT_ETAG_EGR_CTRL [8].TAG_PRI_KEEP[24:24]	1
0xBB00024C	PE_PORT_ETAG_EGR_CTRL [8].TAG_STS_MODE[23:22]	2
0xBB00024C	PE_PORT_ETAG_EGR_CTRL [8].RESERVED[21:0]	22
0xBB000250	PE_PORT_ETAG_EGR_CTRL [9].RESERVED[31:27]	5
0xBB000250	PE_PORT_ETAG_EGR_CTRL [9].VLAN_TAG_STS_MODE[26:26]	1
0xBB000250	PE_PORT_ETAG_EGR_CTRL [9].TAG_DEL_RMK_EN[25:25]	1
0xBB000250	PE_PORT_ETAG_EGR_CTRL [9].TAG_PRI_KEEP[24:24]	1
0xBB000250	PE_PORT_ETAG_EGR_CTRL [9].TAG_STS_MODE[23:22]	2
0xBB000250	PE_PORT_ETAG_EGR_CTRL [9].RESERVED[21:0]	22
0xBB000254	PE_PORT_ETAG_EGR_CTRL [10].RESERVED[31:27]	5
0xBB000254	PE_PORT_ETAG_EGR_CTRL [10].VLAN_TAG_STS_MODE[26:26]	1
0xBB000254	PE_PORT_ETAG_EGR_CTRL [10].TAG_DEL_RMK_EN[25:25]	1
0xBB000254	PE_PORT_ETAG_EGR_CTRL [10].TAG_PRI_KEEP[24:24]	1
0xBB000254	PE_PORT_ETAG_EGR_CTRL [10].TAG_STS_MODE[23:22]	2
0xBB000254	PE_PORT_ETAG_EGR_CTRL [10].RESERVED[21:0]	22
0xBB000258	PE_PORT_ETAG_EGR_CTRL [11].RESERVED[31:27]	5
0xBB000258	PE_PORT_ETAG_EGR_CTRL [11].VLAN_TAG_STS_MODE[26:26]	1
0xBB000258	PE_PORT_ETAG_EGR_CTRL [11].TAG_DEL_RMK_EN[25:25]	1
0xBB000258	PE_PORT_ETAG_EGR_CTRL [11].TAG_PRI_KEEP[24:24]	1
0xBB000258	PE_PORT_ETAG_EGR_CTRL [11].TAG_STS_MODE[23:22]	2
0xBB000258	PE_PORT_ETAG_EGR_CTRL [11].RESERVED[21:0]	22
0xBB00025C	PE_PORT_ETAG_EGR_CTRL [12].RESERVED[31:27]	5
0xBB00025C	PE_PORT_ETAG_EGR_CTRL [12].VLAN_TAG_STS_MODE[26:26]	1
0xBB00025C	PE_PORT_ETAG_EGR_CTRL [12].TAG_DEL_RMK_EN[25:25]	1
0xBB00025C	PE_PORT_ETAG_EGR_CTRL [12].TAG_PRI_KEEP[24:24]	1
0xBB00025C	PE_PORT_ETAG_EGR_CTRL [12].TAG_STS_MODE[23:22]	2
0xBB00025C	PE_PORT_ETAG_EGR_CTRL [12].RESERVED[21:0]	22
0xBB000260	PE_PORT_ETAG_EGR_CTRL [13].RESERVED[31:27]	5
0xBB000260	PE_PORT_ETAG_EGR_CTRL [13].VLAN_TAG_STS_MODE[26:26]	1
0xBB000260	PE_PORT_ETAG_EGR_CTRL [13].TAG_DEL_RMK_EN[25:25]	1
0xBB000260	PE_PORT_ETAG_EGR_CTRL [13].TAG_PRI_KEEP[24:24]	1
0xBB000260	PE_PORT_ETAG_EGR_CTRL [13].TAG_STS_MODE[23:22]	2
0xBB000260	PE_PORT_ETAG_EGR_CTRL [13].RESERVED[21:0]	22
0xBB000264	PE_PORT_ETAG_EGR_CTRL [14].RESERVED[31:27]	5
0xBB000264	PE_PORT_ETAG_EGR_CTRL [14].VLAN_TAG_STS_MODE[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000264	PE_PORT_ETAG_EGR_CTRL [14].TAG_DEI_RMK_EN[25:25]	1
0xBB000264	PE_PORT_ETAG_EGR_CTRL [14].TAG_PRI_KEEP[24:24]	1
0xBB000264	PE_PORT_ETAG_EGR_CTRL [14].TAG_STS_MODE[23:22]	2
0xBB000264	PE_PORT_ETAG_EGR_CTRL [14].RESERVED[21:0]	22
0xBB000268	PE_PORT_ETAG_EGR_CTRL [15].RESERVED[31:27]	5
0xBB000268	PE_PORT_ETAG_EGR_CTRL [15].VLAN_TAG_STS_MODE[26:26]	1
0xBB000268	PE_PORT_ETAG_EGR_CTRL [15].TAG_DEI_RMK_EN[25:25]	1
0xBB000268	PE_PORT_ETAG_EGR_CTRL [15].TAG_PRI_KEEP[24:24]	1
0xBB000268	PE_PORT_ETAG_EGR_CTRL [15].TAG_STS_MODE[23:22]	2
0xBB000268	PE_PORT_ETAG_EGR_CTRL [15].RESERVED[21:0]	22
0xBB00026C	PE_PORT_ETAG_EGR_CTRL [16].RESERVED[31:27]	5
0xBB00026C	PE_PORT_ETAG_EGR_CTRL [16].VLAN_TAG_STS_MODE[26:26]	1
0xBB00026C	PE_PORT_ETAG_EGR_CTRL [16].TAG_DEI_RMK_EN[25:25]	1
0xBB00026C	PE_PORT_ETAG_EGR_CTRL [16].TAG_PRI_KEEP[24:24]	1
0xBB00026C	PE_PORT_ETAG_EGR_CTRL [16].TAG_STS_MODE[23:22]	2
0xBB00026C	PE_PORT_ETAG_EGR_CTRL [16].RESERVED[21:0]	22
0xBB000270	PE_PORT_ETAG_EGR_CTRL [17].RESERVED[31:27]	5
0xBB000270	PE_PORT_ETAG_EGR_CTRL [17].VLAN_TAG_STS_MODE[26:26]	1
0xBB000270	PE_PORT_ETAG_EGR_CTRL [17].TAG_DEI_RMK_EN[25:25]	1
0xBB000270	PE_PORT_ETAG_EGR_CTRL [17].TAG_PRI_KEEP[24:24]	1
0xBB000270	PE_PORT_ETAG_EGR_CTRL [17].TAG_STS_MODE[23:22]	2
0xBB000270	PE_PORT_ETAG_EGR_CTRL [17].RESERVED[21:0]	22
0xBB000274	PE_PORT_ETAG_EGR_CTRL [18].RESERVED[31:27]	5
0xBB000274	PE_PORT_ETAG_EGR_CTRL [18].VLAN_TAG_STS_MODE[26:26]	1
0xBB000274	PE_PORT_ETAG_EGR_CTRL [18].TAG_DEI_RMK_EN[25:25]	1
0xBB000274	PE_PORT_ETAG_EGR_CTRL [18].TAG_PRI_KEEP[24:24]	1
0xBB000274	PE_PORT_ETAG_EGR_CTRL [18].TAG_STS_MODE[23:22]	2
0xBB000274	PE_PORT_ETAG_EGR_CTRL [18].RESERVED[21:0]	22
0xBB000278	PE_PORT_ETAG_EGR_CTRL [19].RESERVED[31:27]	5
0xBB000278	PE_PORT_ETAG_EGR_CTRL [19].VLAN_TAG_STS_MODE[26:26]	1
0xBB000278	PE_PORT_ETAG_EGR_CTRL [19].TAG_DEI_RMK_EN[25:25]	1
0xBB000278	PE_PORT_ETAG_EGR_CTRL [19].TAG_PRI_KEEP[24:24]	1
0xBB000278	PE_PORT_ETAG_EGR_CTRL [19].TAG_STS_MODE[23:22]	2
0xBB000278	PE_PORT_ETAG_EGR_CTRL [19].RESERVED[21:0]	22
0xBB00027C	PE_PORT_ETAG_EGR_CTRL [20].RESERVED[31:27]	5
0xBB00027C	PE_PORT_ETAG_EGR_CTRL [20].VLAN_TAG_STS_MODE[26:26]	1
0xBB00027C	PE_PORT_ETAG_EGR_CTRL [20].TAG_DEI_RMK_EN[25:25]	1
0xBB00027C	PE_PORT_ETAG_EGR_CTRL [20].TAG_PRI_KEEP[24:24]	1
0xBB00027C	PE_PORT_ETAG_EGR_CTRL [20].TAG_STS_MODE[23:22]	2
0xBB00027C	PE_PORT_ETAG_EGR_CTRL [20].RESERVED[21:0]	22
0xBB000280	PE_PORT_ETAG_EGR_CTRL [21].RESERVED[31:27]	5
0xBB000280	PE_PORT_ETAG_EGR_CTRL [21].VLAN_TAG_STS_MODE[26:26]	1
0xBB000280	PE_PORT_ETAG_EGR_CTRL [21].TAG_DEI_RMK_EN[25:25]	1
0xBB000280	PE_PORT_ETAG_EGR_CTRL [21].TAG_PRI_KEEP[24:24]	1
0xBB000280	PE_PORT_ETAG_EGR_CTRL [21].TAG_STS_MODE[23:22]	2
0xBB000280	PE_PORT_ETAG_EGR_CTRL [21].RESERVED[21:0]	22

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000284	PE_PORT_ETAG_EGR_CTRL [22].RESERVED[31:27]	5
0xBB000284	PE_PORT_ETAG_EGR_CTRL [22].VLAN_TAG_STS_MODE[26:26]	1
0xBB000284	PE_PORT_ETAG_EGR_CTRL [22].TAG_DEI_RMKE[25:25]	1
0xBB000284	PE_PORT_ETAG_EGR_CTRL [22].TAG_PRI_KEEP[24:24]	1
0xBB000284	PE_PORT_ETAG_EGR_CTRL [22].TAG_STS_MODE[23:22]	2
0xBB000284	PE_PORT_ETAG_EGR_CTRL [22].RESERVED[21:0]	22
0xBB000288	PE_PORT_ETAG_EGR_CTRL [23].RESERVED[31:27]	5
0xBB000288	PE_PORT_ETAG_EGR_CTRL [23].VLAN_TAG_STS_MODE[26:26]	1
0xBB000288	PE_PORT_ETAG_EGR_CTRL [23].TAG_DEI_RMKE[25:25]	1
0xBB000288	PE_PORT_ETAG_EGR_CTRL [23].TAG_PRI_KEEP[24:24]	1
0xBB000288	PE_PORT_ETAG_EGR_CTRL [23].TAG_STS_MODE[23:22]	2
0xBB000288	PE_PORT_ETAG_EGR_CTRL [23].RESERVED[21:0]	22
0xBB00028C	PE_PORT_ETAG_EGR_CTRL [24].RESERVED[31:27]	5
0xBB00028C	PE_PORT_ETAG_EGR_CTRL [24].VLAN_TAG_STS_MODE[26:26]	1
0xBB00028C	PE_PORT_ETAG_EGR_CTRL [24].TAG_DEI_RMKE[25:25]	1
0xBB00028C	PE_PORT_ETAG_EGR_CTRL [24].TAG_PRI_KEEP[24:24]	1
0xBB00028C	PE_PORT_ETAG_EGR_CTRL [24].TAG_STS_MODE[23:22]	2
0xBB00028C	PE_PORT_ETAG_EGR_CTRL [24].RESERVED[21:0]	22
0xBB000290	PE_PORT_ETAG_EGR_CTRL [25].RESERVED[31:27]	5
0xBB000290	PE_PORT_ETAG_EGR_CTRL [25].VLAN_TAG_STS_MODE[26:26]	1
0xBB000290	PE_PORT_ETAG_EGR_CTRL [25].TAG_DEI_RMKE[25:25]	1
0xBB000290	PE_PORT_ETAG_EGR_CTRL [25].TAG_PRI_KEEP[24:24]	1
0xBB000290	PE_PORT_ETAG_EGR_CTRL [25].TAG_STS_MODE[23:22]	2
0xBB000290	PE_PORT_ETAG_EGR_CTRL [25].RESERVED[21:0]	22
0xBB000294	PE_PORT_ETAG_EGR_CTRL [26].RESERVED[31:27]	5
0xBB000294	PE_PORT_ETAG_EGR_CTRL [26].VLAN_TAG_STS_MODE[26:26]	1
0xBB000294	PE_PORT_ETAG_EGR_CTRL [26].TAG_DEI_RMKE[25:25]	1
0xBB000294	PE_PORT_ETAG_EGR_CTRL [26].TAG_PRI_KEEP[24:24]	1
0xBB000294	PE_PORT_ETAG_EGR_CTRL [26].TAG_STS_MODE[23:22]	2
0xBB000294	PE_PORT_ETAG_EGR_CTRL [26].RESERVED[21:0]	22
0xBB000298	PE_PORT_ETAG_EGR_CTRL [27].RESERVED[31:27]	5
0xBB000298	PE_PORT_ETAG_EGR_CTRL [27].VLAN_TAG_STS_MODE[26:26]	1
0xBB000298	PE_PORT_ETAG_EGR_CTRL [27].TAG_DEI_RMKE[25:25]	1
0xBB000298	PE_PORT_ETAG_EGR_CTRL [27].TAG_PRI_KEEP[24:24]	1
0xBB000298	PE_PORT_ETAG_EGR_CTRL [27].TAG_STS_MODE[23:22]	2
0xBB000298	PE_PORT_ETAG_EGR_CTRL [27].RESERVED[21:0]	22
0xBB00029C	PE_PORT_ETAG_EGR_CTRL [28].RESERVED[31:27]	5
0xBB00029C	PE_PORT_ETAG_EGR_CTRL [28].VLAN_TAG_STS_MODE[26:26]	1
0xBB00029C	PE_PORT_ETAG_EGR_CTRL [28].TAG_DEI_RMKE[25:25]	1
0xBB00029C	PE_PORT_ETAG_EGR_CTRL [28].TAG_PRI_KEEP[24:24]	1
0xBB00029C	PE_PORT_ETAG_EGR_CTRL [28].TAG_STS_MODE[23:22]	2
0xBB00029C	PE_PORT_ETAG_EGR_CTRL [28].RESERVED[21:0]	22
0xBB0002A0	PE_PORT_ETAG_EGR_CTRL [29].RESERVED[31:27]	5
0xBB0002A0	PE_PORT_ETAG_EGR_CTRL [29].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002A0	PE_PORT_ETAG_EGR_CTRL [29].TAG_DEI_RMKE[25:25]	1
0xBB0002A0	PE_PORT_ETAG_EGR_CTRL [29].TAG_PRI_KEEP[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0002A0	PE_PORT_ETAG_EGR_CTRL [29].TAG_STS_MODE[23:22]	2
0xBB0002A0	PE_PORT_ETAG_EGR_CTRL [29].RESERVED[21:0]	22
0xBB0002A4	PE_PORT_ETAG_EGR_CTRL [30].RESERVED[31:27]	5
0xBB0002A4	PE_PORT_ETAG_EGR_CTRL [30].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002A4	PE_PORT_ETAG_EGR_CTRL [30].TAG_DEL_RMK_EN[25:25]	1
0xBB0002A4	PE_PORT_ETAG_EGR_CTRL [30].TAG_PRI_KEEP[24:24]	1
0xBB0002A4	PE_PORT_ETAG_EGR_CTRL [30].TAG_STS_MODE[23:22]	2
0xBB0002A4	PE_PORT_ETAG_EGR_CTRL [30].RESERVED[21:0]	22
0xBB0002A8	PE_PORT_ETAG_EGR_CTRL [31].RESERVED[31:27]	5
0xBB0002A8	PE_PORT_ETAG_EGR_CTRL [31].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002A8	PE_PORT_ETAG_EGR_CTRL [31].TAG_DEL_RMK_EN[25:25]	1
0xBB0002A8	PE_PORT_ETAG_EGR_CTRL [31].TAG_PRI_KEEP[24:24]	1
0xBB0002A8	PE_PORT_ETAG_EGR_CTRL [31].TAG_STS_MODE[23:22]	2
0xBB0002A8	PE_PORT_ETAG_EGR_CTRL [31].RESERVED[21:0]	22
0xBB0002AC	PE_PORT_ETAG_EGR_CTRL [32].RESERVED[31:27]	5
0xBB0002AC	PE_PORT_ETAG_EGR_CTRL [32].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002AC	PE_PORT_ETAG_EGR_CTRL [32].TAG_DEL_RMK_EN[25:25]	1
0xBB0002AC	PE_PORT_ETAG_EGR_CTRL [32].TAG_PRI_KEEP[24:24]	1
0xBB0002AC	PE_PORT_ETAG_EGR_CTRL [32].TAG_STS_MODE[23:22]	2
0xBB0002AC	PE_PORT_ETAG_EGR_CTRL [32].RESERVED[21:0]	22
0xBB0002B0	PE_PORT_ETAG_EGR_CTRL [33].RESERVED[31:27]	5
0xBB0002B0	PE_PORT_ETAG_EGR_CTRL [33].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002B0	PE_PORT_ETAG_EGR_CTRL [33].TAG_DEL_RMK_EN[25:25]	1
0xBB0002B0	PE_PORT_ETAG_EGR_CTRL [33].TAG_PRI_KEEP[24:24]	1
0xBB0002B0	PE_PORT_ETAG_EGR_CTRL [33].TAG_STS_MODE[23:22]	2
0xBB0002B0	PE_PORT_ETAG_EGR_CTRL [33].RESERVED[21:0]	22
0xBB0002B4	PE_PORT_ETAG_EGR_CTRL [34].RESERVED[31:27]	5
0xBB0002B4	PE_PORT_ETAG_EGR_CTRL [34].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002B4	PE_PORT_ETAG_EGR_CTRL [34].TAG_DEL_RMK_EN[25:25]	1
0xBB0002B4	PE_PORT_ETAG_EGR_CTRL [34].TAG_PRI_KEEP[24:24]	1
0xBB0002B4	PE_PORT_ETAG_EGR_CTRL [34].TAG_STS_MODE[23:22]	2
0xBB0002B4	PE_PORT_ETAG_EGR_CTRL [34].RESERVED[21:0]	22
0xBB0002B8	PE_PORT_ETAG_EGR_CTRL [35].RESERVED[31:27]	5
0xBB0002B8	PE_PORT_ETAG_EGR_CTRL [35].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002B8	PE_PORT_ETAG_EGR_CTRL [35].TAG_DEL_RMK_EN[25:25]	1
0xBB0002B8	PE_PORT_ETAG_EGR_CTRL [35].TAG_PRI_KEEP[24:24]	1
0xBB0002B8	PE_PORT_ETAG_EGR_CTRL [35].TAG_STS_MODE[23:22]	2
0xBB0002B8	PE_PORT_ETAG_EGR_CTRL [35].RESERVED[21:0]	22
0xBB0002BC	PE_PORT_ETAG_EGR_CTRL [36].RESERVED[31:27]	5
0xBB0002BC	PE_PORT_ETAG_EGR_CTRL [36].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002BC	PE_PORT_ETAG_EGR_CTRL [36].TAG_DEL_RMK_EN[25:25]	1
0xBB0002BC	PE_PORT_ETAG_EGR_CTRL [36].TAG_PRI_KEEP[24:24]	1
0xBB0002BC	PE_PORT_ETAG_EGR_CTRL [36].TAG_STS_MODE[23:22]	2
0xBB0002BC	PE_PORT_ETAG_EGR_CTRL [36].RESERVED[21:0]	22
0xBB0002C0	PE_PORT_ETAG_EGR_CTRL [37].RESERVED[31:27]	5
0xBB0002C0	PE_PORT_ETAG_EGR_CTRL [37].VLAN_TAG_STS_MODE[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0002C0	PE_PORT_ETAG_EGR_CTRL [37].TAG_DEI_RMK_EN[25:25]	1
0xBB0002C0	PE_PORT_ETAG_EGR_CTRL [37].TAG_PRI_KEEP[24:24]	1
0xBB0002C0	PE_PORT_ETAG_EGR_CTRL [37].TAG_STS_MODE[23:22]	2
0xBB0002C0	PE_PORT_ETAG_EGR_CTRL [37].RESERVED[21:0]	22
0xBB0002C4	PE_PORT_ETAG_EGR_CTRL [38].RESERVED[31:27]	5
0xBB0002C4	PE_PORT_ETAG_EGR_CTRL [38].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002C4	PE_PORT_ETAG_EGR_CTRL [38].TAG_DEI_RMK_EN[25:25]	1
0xBB0002C4	PE_PORT_ETAG_EGR_CTRL [38].TAG_PRI_KEEP[24:24]	1
0xBB0002C4	PE_PORT_ETAG_EGR_CTRL [38].TAG_STS_MODE[23:22]	2
0xBB0002C4	PE_PORT_ETAG_EGR_CTRL [38].RESERVED[21:0]	22
0xBB0002C8	PE_PORT_ETAG_EGR_CTRL [39].RESERVED[31:27]	5
0xBB0002C8	PE_PORT_ETAG_EGR_CTRL [39].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002C8	PE_PORT_ETAG_EGR_CTRL [39].TAG_DEI_RMK_EN[25:25]	1
0xBB0002C8	PE_PORT_ETAG_EGR_CTRL [39].TAG_PRI_KEEP[24:24]	1
0xBB0002C8	PE_PORT_ETAG_EGR_CTRL [39].TAG_STS_MODE[23:22]	2
0xBB0002C8	PE_PORT_ETAG_EGR_CTRL [39].RESERVED[21:0]	22
0xBB0002CC	PE_PORT_ETAG_EGR_CTRL [40].RESERVED[31:27]	5
0xBB0002CC	PE_PORT_ETAG_EGR_CTRL [40].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002CC	PE_PORT_ETAG_EGR_CTRL [40].TAG_DEI_RMK_EN[25:25]	1
0xBB0002CC	PE_PORT_ETAG_EGR_CTRL [40].TAG_PRI_KEEP[24:24]	1
0xBB0002CC	PE_PORT_ETAG_EGR_CTRL [40].TAG_STS_MODE[23:22]	2
0xBB0002CC	PE_PORT_ETAG_EGR_CTRL [40].RESERVED[21:0]	22
0xBB0002D0	PE_PORT_ETAG_EGR_CTRL [41].RESERVED[31:27]	5
0xBB0002D0	PE_PORT_ETAG_EGR_CTRL [41].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002D0	PE_PORT_ETAG_EGR_CTRL [41].TAG_DEI_RMK_EN[25:25]	1
0xBB0002D0	PE_PORT_ETAG_EGR_CTRL [41].TAG_PRI_KEEP[24:24]	1
0xBB0002D0	PE_PORT_ETAG_EGR_CTRL [41].TAG_STS_MODE[23:22]	2
0xBB0002D0	PE_PORT_ETAG_EGR_CTRL [41].RESERVED[21:0]	22
0xBB0002D4	PE_PORT_ETAG_EGR_CTRL [42].RESERVED[31:27]	5
0xBB0002D4	PE_PORT_ETAG_EGR_CTRL [42].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002D4	PE_PORT_ETAG_EGR_CTRL [42].TAG_DEI_RMK_EN[25:25]	1
0xBB0002D4	PE_PORT_ETAG_EGR_CTRL [42].TAG_PRI_KEEP[24:24]	1
0xBB0002D4	PE_PORT_ETAG_EGR_CTRL [42].TAG_STS_MODE[23:22]	2
0xBB0002D4	PE_PORT_ETAG_EGR_CTRL [42].RESERVED[21:0]	22
0xBB0002D8	PE_PORT_ETAG_EGR_CTRL [43].RESERVED[31:27]	5
0xBB0002D8	PE_PORT_ETAG_EGR_CTRL [43].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002D8	PE_PORT_ETAG_EGR_CTRL [43].TAG_DEI_RMK_EN[25:25]	1
0xBB0002D8	PE_PORT_ETAG_EGR_CTRL [43].TAG_PRI_KEEP[24:24]	1
0xBB0002D8	PE_PORT_ETAG_EGR_CTRL [43].TAG_STS_MODE[23:22]	2
0xBB0002D8	PE_PORT_ETAG_EGR_CTRL [43].RESERVED[21:0]	22
0xBB0002DC	PE_PORT_ETAG_EGR_CTRL [44].RESERVED[31:27]	5
0xBB0002DC	PE_PORT_ETAG_EGR_CTRL [44].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002DC	PE_PORT_ETAG_EGR_CTRL [44].TAG_DEI_RMK_EN[25:25]	1
0xBB0002DC	PE_PORT_ETAG_EGR_CTRL [44].TAG_PRI_KEEP[24:24]	1
0xBB0002DC	PE_PORT_ETAG_EGR_CTRL [44].TAG_STS_MODE[23:22]	2
0xBB0002DC	PE_PORT_ETAG_EGR_CTRL [44].RESERVED[21:0]	22

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0002E0	PE_PORT_ETAG_EGR_CTRL [45].RESERVED[31:27]	5
0xBB0002E0	PE_PORT_ETAG_EGR_CTRL [45].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002E0	PE_PORT_ETAG_EGR_CTRL [45].TAG_DEI_RMKE[25:25]	1
0xBB0002E0	PE_PORT_ETAG_EGR_CTRL [45].TAG_PRI_KEEP[24:24]	1
0xBB0002E0	PE_PORT_ETAG_EGR_CTRL [45].TAG_STS_MODE[23:22]	2
0xBB0002E0	PE_PORT_ETAG_EGR_CTRL [45].RESERVED[21:0]	22
0xBB0002E4	PE_PORT_ETAG_EGR_CTRL [46].RESERVED[31:27]	5
0xBB0002E4	PE_PORT_ETAG_EGR_CTRL [46].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002E4	PE_PORT_ETAG_EGR_CTRL [46].TAG_DEI_RMKE[25:25]	1
0xBB0002E4	PE_PORT_ETAG_EGR_CTRL [46].TAG_PRI_KEEP[24:24]	1
0xBB0002E4	PE_PORT_ETAG_EGR_CTRL [46].TAG_STS_MODE[23:22]	2
0xBB0002E4	PE_PORT_ETAG_EGR_CTRL [46].RESERVED[21:0]	22
0xBB0002E8	PE_PORT_ETAG_EGR_CTRL [47].RESERVED[31:27]	5
0xBB0002E8	PE_PORT_ETAG_EGR_CTRL [47].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002E8	PE_PORT_ETAG_EGR_CTRL [47].TAG_DEI_RMKE[25:25]	1
0xBB0002E8	PE_PORT_ETAG_EGR_CTRL [47].TAG_PRI_KEEP[24:24]	1
0xBB0002E8	PE_PORT_ETAG_EGR_CTRL [47].TAG_STS_MODE[23:22]	2
0xBB0002E8	PE_PORT_ETAG_EGR_CTRL [47].RESERVED[21:0]	22
0xBB0002EC	PE_PORT_ETAG_EGR_CTRL [48].RESERVED[31:27]	5
0xBB0002EC	PE_PORT_ETAG_EGR_CTRL [48].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002EC	PE_PORT_ETAG_EGR_CTRL [48].TAG_DEI_RMKE[25:25]	1
0xBB0002EC	PE_PORT_ETAG_EGR_CTRL [48].TAG_PRI_KEEP[24:24]	1
0xBB0002EC	PE_PORT_ETAG_EGR_CTRL [48].TAG_STS_MODE[23:22]	2
0xBB0002EC	PE_PORT_ETAG_EGR_CTRL [48].RESERVED[21:0]	22
0xBB0002F0	PE_PORT_ETAG_EGR_CTRL [49].RESERVED[31:27]	5
0xBB0002F0	PE_PORT_ETAG_EGR_CTRL [49].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002F0	PE_PORT_ETAG_EGR_CTRL [49].TAG_DEI_RMKE[25:25]	1
0xBB0002F0	PE_PORT_ETAG_EGR_CTRL [49].TAG_PRI_KEEP[24:24]	1
0xBB0002F0	PE_PORT_ETAG_EGR_CTRL [49].TAG_STS_MODE[23:22]	2
0xBB0002F0	PE_PORT_ETAG_EGR_CTRL [49].RESERVED[21:0]	22
0xBB0002F4	PE_PORT_ETAG_EGR_CTRL [50].RESERVED[31:27]	5
0xBB0002F4	PE_PORT_ETAG_EGR_CTRL [50].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002F4	PE_PORT_ETAG_EGR_CTRL [50].TAG_DEI_RMKE[25:25]	1
0xBB0002F4	PE_PORT_ETAG_EGR_CTRL [50].TAG_PRI_KEEP[24:24]	1
0xBB0002F4	PE_PORT_ETAG_EGR_CTRL [50].TAG_STS_MODE[23:22]	2
0xBB0002F4	PE_PORT_ETAG_EGR_CTRL [50].RESERVED[21:0]	22
0xBB0002F8	PE_PORT_ETAG_EGR_CTRL [51].RESERVED[31:27]	5
0xBB0002F8	PE_PORT_ETAG_EGR_CTRL [51].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002F8	PE_PORT_ETAG_EGR_CTRL [51].TAG_DEI_RMKE[25:25]	1
0xBB0002F8	PE_PORT_ETAG_EGR_CTRL [51].TAG_PRI_KEEP[24:24]	1
0xBB0002F8	PE_PORT_ETAG_EGR_CTRL [51].TAG_STS_MODE[23:22]	2
0xBB0002F8	PE_PORT_ETAG_EGR_CTRL [51].RESERVED[21:0]	22
0xBB0002FC	PE_PORT_ETAG_EGR_CTRL [52].RESERVED[31:27]	5
0xBB0002FC	PE_PORT_ETAG_EGR_CTRL [52].VLAN_TAG_STS_MODE[26:26]	1
0xBB0002FC	PE_PORT_ETAG_EGR_CTRL [52].TAG_DEI_RMKE[25:25]	1
0xBB0002FC	PE_PORT_ETAG_EGR_CTRL [52].TAG_PRI_KEEP[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0002FC	PE_PORT_ETAG_EGR_CTRL [52].TAG_STS_MODE[23:22]	2
0xBB0002FC	PE_PORT_ETAG_EGR_CTRL [52].RESERVED[21:0]	22
0xBB000300	PE_PORT_ETAG_EGR_CTRL [53].RESERVED[31:27]	5
0xBB000300	PE_PORT_ETAG_EGR_CTRL [53].VLAN_TAG_STS_MODE[26:26]	1
0xBB000300	PE_PORT_ETAG_EGR_CTRL [53].TAG_DEI_RMK_EN[25:25]	1
0xBB000300	PE_PORT_ETAG_EGR_CTRL [53].TAG_PRI_KEEP[24:24]	1
0xBB000300	PE_PORT_ETAG_EGR_CTRL [53].TAG_STS_MODE[23:22]	2
0xBB000300	PE_PORT_ETAG_EGR_CTRL [53].RESERVED[21:0]	22
0xBB000304	PE_PORT_ETAG_EGR_CTRL [54].RESERVED[31:27]	5
0xBB000304	PE_PORT_ETAG_EGR_CTRL [54].VLAN_TAG_STS_MODE[26:26]	1
0xBB000304	PE_PORT_ETAG_EGR_CTRL [54].TAG_DEI_RMK_EN[25:25]	1
0xBB000304	PE_PORT_ETAG_EGR_CTRL [54].TAG_PRI_KEEP[24:24]	1
0xBB000304	PE_PORT_ETAG_EGR_CTRL [54].TAG_STS_MODE[23:22]	2
0xBB000304	PE_PORT_ETAG_EGR_CTRL [54].RESERVED[21:0]	22
0xBB000308	PE_PORT_ETAG_EGR_CTRL [55].RESERVED[31:27]	5
0xBB000308	PE_PORT_ETAG_EGR_CTRL [55].VLAN_TAG_STS_MODE[26:26]	1
0xBB000308	PE_PORT_ETAG_EGR_CTRL [55].TAG_DEI_RMK_EN[25:25]	1
0xBB000308	PE_PORT_ETAG_EGR_CTRL [55].TAG_PRI_KEEP[24:24]	1
0xBB000308	PE_PORT_ETAG_EGR_CTRL [55].TAG_STS_MODE[23:22]	2
0xBB000308	PE_PORT_ETAG_EGR_CTRL [55].RESERVED[21:0]	22
0xBB00030C	PE_PORT_ETAG_EGR_CTRL [56].RESERVED[31:27]	5
0xBB00030C	PE_PORT_ETAG_EGR_CTRL [56].VLAN_TAG_STS_MODE[26:26]	1
0xBB00030C	PE_PORT_ETAG_EGR_CTRL [56].TAG_DEI_RMK_EN[25:25]	1
0xBB00030C	PE_PORT_ETAG_EGR_CTRL [56].TAG_PRI_KEEP[24:24]	1
0xBB00030C	PE_PORT_ETAG_EGR_CTRL [56].TAG_STS_MODE[23:22]	2
0xBB00030C	PE_PORT_ETAG_EGR_CTRL [56].RESERVED[21:0]	22
0xBB00CDD4	PE_PORT_ETAG_IGR_CTRL [0].RESERVED[31:12]	20
0xBB00CDD4	PE_PORT_ETAG_IGR_CTRL [0].ECID_RPF_CHK[11:11]	1
0xBB00CDD4	PE_PORT_ETAG_IGR_CTRL [0].RSVD_ECID_FLTR[10:10]	1
0xBB00CDD4	PE_PORT_ETAG_IGR_CTRL [0].MC_ECID_FLTR[9:9]	1
0xBB00CDD4	PE_PORT_ETAG_IGR_CTRL [0].ECID_NSG[8:1]	8
0xBB00CDD4	PE_PORT_ETAG_IGR_CTRL [0].LU_KEY_SEL[0:0]	1
0xBB00CDD8	PE_PORT_ETAG_IGR_CTRL [1].RESERVED[31:12]	20
0xBB00CDD8	PE_PORT_ETAG_IGR_CTRL [1].ECID_RPF_CHK[11:11]	1
0xBB00CDD8	PE_PORT_ETAG_IGR_CTRL [1].RSVD_ECID_FLTR[10:10]	1
0xBB00CDD8	PE_PORT_ETAG_IGR_CTRL [1].MC_ECID_FLTR[9:9]	1
0xBB00CDD8	PE_PORT_ETAG_IGR_CTRL [1].ECID_NSG[8:1]	8
0xBB00CDD8	PE_PORT_ETAG_IGR_CTRL [1].LU_KEY_SEL[0:0]	1
0xBB00CDDC	PE_PORT_ETAG_IGR_CTRL [2].RESERVED[31:12]	20
0xBB00CDDC	PE_PORT_ETAG_IGR_CTRL [2].ECID_RPF_CHK[11:11]	1
0xBB00CDDC	PE_PORT_ETAG_IGR_CTRL [2].RSVD_ECID_FLTR[10:10]	1
0xBB00CDDC	PE_PORT_ETAG_IGR_CTRL [2].MC_ECID_FLTR[9:9]	1
0xBB00CDDC	PE_PORT_ETAG_IGR_CTRL [2].ECID_NSG[8:1]	8
0xBB00CDDC	PE_PORT_ETAG_IGR_CTRL [2].LU_KEY_SEL[0:0]	1
0xBB00CDE0	PE_PORT_ETAG_IGR_CTRL [3].RESERVED[31:12]	20
0xBB00CDE0	PE_PORT_ETAG_IGR_CTRL [3].ECID_RPF_CHK[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CDE0	PE_PORT_ETYPE_IGR_CTRL [3].RSVD_ECID_FLTR[10:10]	1
0xBB00CDE0	PE_PORT_ETYPE_IGR_CTRL [3].MC_ECID_FLTR[9:9]	1
0xBB00CDE0	PE_PORT_ETYPE_IGR_CTRL [3].ECID_NSG[8:1]	8
0xBB00CDE0	PE_PORT_ETYPE_IGR_CTRL [3].LU_KEY_SEL[0:0]	1
0xBB00CDE4	PE_PORT_ETYPE_IGR_CTRL [4].RESERVED[31:12]	20
0xBB00CDE4	PE_PORT_ETYPE_IGR_CTRL [4].ECID_RPF_CHK[11:11]	1
0xBB00CDE4	PE_PORT_ETYPE_IGR_CTRL [4].RSVD_ECID_FLTR[10:10]	1
0xBB00CDE4	PE_PORT_ETYPE_IGR_CTRL [4].MC_ECID_FLTR[9:9]	1
0xBB00CDE4	PE_PORT_ETYPE_IGR_CTRL [4].ECID_NSG[8:1]	8
0xBB00CDE4	PE_PORT_ETYPE_IGR_CTRL [4].LU_KEY_SEL[0:0]	1
0xBB00CDE8	PE_PORT_ETYPE_IGR_CTRL [5].RESERVED[31:12]	20
0xBB00CDE8	PE_PORT_ETYPE_IGR_CTRL [5].ECID_RPF_CHK[11:11]	1
0xBB00CDE8	PE_PORT_ETYPE_IGR_CTRL [5].RSVD_ECID_FLTR[10:10]	1
0xBB00CDE8	PE_PORT_ETYPE_IGR_CTRL [5].MC_ECID_FLTR[9:9]	1
0xBB00CDE8	PE_PORT_ETYPE_IGR_CTRL [5].ECID_NSG[8:1]	8
0xBB00CDE8	PE_PORT_ETYPE_IGR_CTRL [5].LU_KEY_SEL[0:0]	1
0xBB00CDEC	PE_PORT_ETYPE_IGR_CTRL [6].RESERVED[31:12]	20
0xBB00CDEC	PE_PORT_ETYPE_IGR_CTRL [6].ECID_RPF_CHK[11:11]	1
0xBB00CDEC	PE_PORT_ETYPE_IGR_CTRL [6].RSVD_ECID_FLTR[10:10]	1
0xBB00CDEC	PE_PORT_ETYPE_IGR_CTRL [6].MC_ECID_FLTR[9:9]	1
0xBB00CDEC	PE_PORT_ETYPE_IGR_CTRL [6].ECID_NSG[8:1]	8
0xBB00CDEC	PE_PORT_ETYPE_IGR_CTRL [6].LU_KEY_SEL[0:0]	1
0xBB00CDF0	PE_PORT_ETYPE_IGR_CTRL [7].RESERVED[31:12]	20
0xBB00CDF0	PE_PORT_ETYPE_IGR_CTRL [7].ECID_RPF_CHK[11:11]	1
0xBB00CDF0	PE_PORT_ETYPE_IGR_CTRL [7].RSVD_ECID_FLTR[10:10]	1
0xBB00CDF0	PE_PORT_ETYPE_IGR_CTRL [7].MC_ECID_FLTR[9:9]	1
0xBB00CDF0	PE_PORT_ETYPE_IGR_CTRL [7].ECID_NSG[8:1]	8
0xBB00CDF0	PE_PORT_ETYPE_IGR_CTRL [7].LU_KEY_SEL[0:0]	1
0xBB00CDF4	PE_PORT_ETYPE_IGR_CTRL [8].RESERVED[31:12]	20
0xBB00CDF4	PE_PORT_ETYPE_IGR_CTRL [8].ECID_RPF_CHK[11:11]	1
0xBB00CDF4	PE_PORT_ETYPE_IGR_CTRL [8].RSVD_ECID_FLTR[10:10]	1
0xBB00CDF4	PE_PORT_ETYPE_IGR_CTRL [8].MC_ECID_FLTR[9:9]	1
0xBB00CDF4	PE_PORT_ETYPE_IGR_CTRL [8].ECID_NSG[8:1]	8
0xBB00CDF4	PE_PORT_ETYPE_IGR_CTRL [8].LU_KEY_SEL[0:0]	1
0xBB00CDF8	PE_PORT_ETYPE_IGR_CTRL [9].RESERVED[31:12]	20
0xBB00CDF8	PE_PORT_ETYPE_IGR_CTRL [9].ECID_RPF_CHK[11:11]	1
0xBB00CDF8	PE_PORT_ETYPE_IGR_CTRL [9].RSVD_ECID_FLTR[10:10]	1
0xBB00CDF8	PE_PORT_ETYPE_IGR_CTRL [9].MC_ECID_FLTR[9:9]	1
0xBB00CDF8	PE_PORT_ETYPE_IGR_CTRL [9].ECID_NSG[8:1]	8
0xBB00CDF8	PE_PORT_ETYPE_IGR_CTRL [9].LU_KEY_SEL[0:0]	1
0xBB00CDFA	PE_PORT_ETYPE_IGR_CTRL [10].RESERVED[31:12]	20
0xBB00CDFA	PE_PORT_ETYPE_IGR_CTRL [10].ECID_RPF_CHK[11:11]	1
0xBB00CDFA	PE_PORT_ETYPE_IGR_CTRL [10].RSVD_ECID_FLTR[10:10]	1
0xBB00CDFA	PE_PORT_ETYPE_IGR_CTRL [10].MC_ECID_FLTR[9:9]	1
0xBB00CDFA	PE_PORT_ETYPE_IGR_CTRL [10].ECID_NSG[8:1]	8
0xBB00CDFA	PE_PORT_ETYPE_IGR_CTRL [10].LU_KEY_SEL[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CE00	PE_PORT_ETAG_IGR_CTRL [11].RESERVED[31:12]	20
0xBB00CE00	PE_PORT_ETAG_IGR_CTRL [11].ECID_RPF_CHK[11:11]	1
0xBB00CE00	PE_PORT_ETAG_IGR_CTRL [11].RSVD_ECID_FLTR[10:10]	1
0xBB00CE00	PE_PORT_ETAG_IGR_CTRL [11].MC_ECID_FLTR[9:9]	1
0xBB00CE00	PE_PORT_ETAG_IGR_CTRL [11].ECID_NSG[8:1]	8
0xBB00CE00	PE_PORT_ETAG_IGR_CTRL [11].LU_KEY_SEL[0:0]	1
0xBB00CE04	PE_PORT_ETAG_IGR_CTRL [12].RESERVED[31:12]	20
0xBB00CE04	PE_PORT_ETAG_IGR_CTRL [12].ECID_RPF_CHK[11:11]	1
0xBB00CE04	PE_PORT_ETAG_IGR_CTRL [12].RSVD_ECID_FLTR[10:10]	1
0xBB00CE04	PE_PORT_ETAG_IGR_CTRL [12].MC_ECID_FLTR[9:9]	1
0xBB00CE04	PE_PORT_ETAG_IGR_CTRL [12].ECID_NSG[8:1]	8
0xBB00CE04	PE_PORT_ETAG_IGR_CTRL [12].LU_KEY_SEL[0:0]	1
0xBB00CE08	PE_PORT_ETAG_IGR_CTRL [13].RESERVED[31:12]	20
0xBB00CE08	PE_PORT_ETAG_IGR_CTRL [13].ECID_RPF_CHK[11:11]	1
0xBB00CE08	PE_PORT_ETAG_IGR_CTRL [13].RSVD_ECID_FLTR[10:10]	1
0xBB00CE08	PE_PORT_ETAG_IGR_CTRL [13].MC_ECID_FLTR[9:9]	1
0xBB00CE08	PE_PORT_ETAG_IGR_CTRL [13].ECID_NSG[8:1]	8
0xBB00CE08	PE_PORT_ETAG_IGR_CTRL [13].LU_KEY_SEL[0:0]	1
0xBB00CE0C	PE_PORT_ETAG_IGR_CTRL [14].RESERVED[31:12]	20
0xBB00CE0C	PE_PORT_ETAG_IGR_CTRL [14].ECID_RPF_CHK[11:11]	1
0xBB00CE0C	PE_PORT_ETAG_IGR_CTRL [14].RSVD_ECID_FLTR[10:10]	1
0xBB00CE0C	PE_PORT_ETAG_IGR_CTRL [14].MC_ECID_FLTR[9:9]	1
0xBB00CE0C	PE_PORT_ETAG_IGR_CTRL [14].ECID_NSG[8:1]	8
0xBB00CE0C	PE_PORT_ETAG_IGR_CTRL [14].LU_KEY_SEL[0:0]	1
0xBB00CE10	PE_PORT_ETAG_IGR_CTRL [15].RESERVED[31:12]	20
0xBB00CE10	PE_PORT_ETAG_IGR_CTRL [15].ECID_RPF_CHK[11:11]	1
0xBB00CE10	PE_PORT_ETAG_IGR_CTRL [15].RSVD_ECID_FLTR[10:10]	1
0xBB00CE10	PE_PORT_ETAG_IGR_CTRL [15].MC_ECID_FLTR[9:9]	1
0xBB00CE10	PE_PORT_ETAG_IGR_CTRL [15].ECID_NSG[8:1]	8
0xBB00CE10	PE_PORT_ETAG_IGR_CTRL [15].LU_KEY_SEL[0:0]	1
0xBB00CE14	PE_PORT_ETAG_IGR_CTRL [16].RESERVED[31:12]	20
0xBB00CE14	PE_PORT_ETAG_IGR_CTRL [16].ECID_RPF_CHK[11:11]	1
0xBB00CE14	PE_PORT_ETAG_IGR_CTRL [16].RSVD_ECID_FLTR[10:10]	1
0xBB00CE14	PE_PORT_ETAG_IGR_CTRL [16].MC_ECID_FLTR[9:9]	1
0xBB00CE14	PE_PORT_ETAG_IGR_CTRL [16].ECID_NSG[8:1]	8
0xBB00CE14	PE_PORT_ETAG_IGR_CTRL [16].LU_KEY_SEL[0:0]	1
0xBB00CE18	PE_PORT_ETAG_IGR_CTRL [17].RESERVED[31:12]	20
0xBB00CE18	PE_PORT_ETAG_IGR_CTRL [17].ECID_RPF_CHK[11:11]	1
0xBB00CE18	PE_PORT_ETAG_IGR_CTRL [17].RSVD_ECID_FLTR[10:10]	1
0xBB00CE18	PE_PORT_ETAG_IGR_CTRL [17].MC_ECID_FLTR[9:9]	1
0xBB00CE18	PE_PORT_ETAG_IGR_CTRL [17].ECID_NSG[8:1]	8
0xBB00CE18	PE_PORT_ETAG_IGR_CTRL [17].LU_KEY_SEL[0:0]	1
0xBB00CE1C	PE_PORT_ETAG_IGR_CTRL [18].RESERVED[31:12]	20
0xBB00CE1C	PE_PORT_ETAG_IGR_CTRL [18].ECID_RPF_CHK[11:11]	1
0xBB00CE1C	PE_PORT_ETAG_IGR_CTRL [18].RSVD_ECID_FLTR[10:10]	1
0xBB00CE1C	PE_PORT_ETAG_IGR_CTRL [18].MC_ECID_FLTR[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CE1C	PE_PORT_ETAG_IGR_CTRL [18].ECID_NSG[8:1]	8
0xBB00CE1C	PE_PORT_ETAG_IGR_CTRL [18].LU_KEY_SEL[0:0]	1
0xBB00CE20	PE_PORT_ETAG_IGR_CTRL [19].RESERVED[31:12]	20
0xBB00CE20	PE_PORT_ETAG_IGR_CTRL [19].ECID_RPF_CHK[11:11]	1
0xBB00CE20	PE_PORT_ETAG_IGR_CTRL [19].RSVD_ECID_FLTR[10:10]	1
0xBB00CE20	PE_PORT_ETAG_IGR_CTRL [19].MC_ECID_FLTR[9:9]	1
0xBB00CE20	PE_PORT_ETAG_IGR_CTRL [19].ECID_NSG[8:1]	8
0xBB00CE20	PE_PORT_ETAG_IGR_CTRL [19].LU_KEY_SEL[0:0]	1
0xBB00CE24	PE_PORT_ETAG_IGR_CTRL [20].RESERVED[31:12]	20
0xBB00CE24	PE_PORT_ETAG_IGR_CTRL [20].ECID_RPF_CHK[11:11]	1
0xBB00CE24	PE_PORT_ETAG_IGR_CTRL [20].RSVD_ECID_FLTR[10:10]	1
0xBB00CE24	PE_PORT_ETAG_IGR_CTRL [20].MC_ECID_FLTR[9:9]	1
0xBB00CE24	PE_PORT_ETAG_IGR_CTRL [20].ECID_NSG[8:1]	8
0xBB00CE24	PE_PORT_ETAG_IGR_CTRL [20].LU_KEY_SEL[0:0]	1
0xBB00CE28	PE_PORT_ETAG_IGR_CTRL [21].RESERVED[31:12]	20
0xBB00CE28	PE_PORT_ETAG_IGR_CTRL [21].ECID_RPF_CHK[11:11]	1
0xBB00CE28	PE_PORT_ETAG_IGR_CTRL [21].RSVD_ECID_FLTR[10:10]	1
0xBB00CE28	PE_PORT_ETAG_IGR_CTRL [21].MC_ECID_FLTR[9:9]	1
0xBB00CE28	PE_PORT_ETAG_IGR_CTRL [21].ECID_NSG[8:1]	8
0xBB00CE28	PE_PORT_ETAG_IGR_CTRL [21].LU_KEY_SEL[0:0]	1
0xBB00CE2C	PE_PORT_ETAG_IGR_CTRL [22].RESERVED[31:12]	20
0xBB00CE2C	PE_PORT_ETAG_IGR_CTRL [22].ECID_RPF_CHK[11:11]	1
0xBB00CE2C	PE_PORT_ETAG_IGR_CTRL [22].RSVD_ECID_FLTR[10:10]	1
0xBB00CE2C	PE_PORT_ETAG_IGR_CTRL [22].MC_ECID_FLTR[9:9]	1
0xBB00CE2C	PE_PORT_ETAG_IGR_CTRL [22].ECID_NSG[8:1]	8
0xBB00CE2C	PE_PORT_ETAG_IGR_CTRL [22].LU_KEY_SEL[0:0]	1
0xBB00CE30	PE_PORT_ETAG_IGR_CTRL [23].RESERVED[31:12]	20
0xBB00CE30	PE_PORT_ETAG_IGR_CTRL [23].ECID_RPF_CHK[11:11]	1
0xBB00CE30	PE_PORT_ETAG_IGR_CTRL [23].RSVD_ECID_FLTR[10:10]	1
0xBB00CE30	PE_PORT_ETAG_IGR_CTRL [23].MC_ECID_FLTR[9:9]	1
0xBB00CE30	PE_PORT_ETAG_IGR_CTRL [23].ECID_NSG[8:1]	8
0xBB00CE30	PE_PORT_ETAG_IGR_CTRL [23].LU_KEY_SEL[0:0]	1
0xBB00CE34	PE_PORT_ETAG_IGR_CTRL [24].RESERVED[31:12]	20
0xBB00CE34	PE_PORT_ETAG_IGR_CTRL [24].ECID_RPF_CHK[11:11]	1
0xBB00CE34	PE_PORT_ETAG_IGR_CTRL [24].RSVD_ECID_FLTR[10:10]	1
0xBB00CE34	PE_PORT_ETAG_IGR_CTRL [24].MC_ECID_FLTR[9:9]	1
0xBB00CE34	PE_PORT_ETAG_IGR_CTRL [24].ECID_NSG[8:1]	8
0xBB00CE34	PE_PORT_ETAG_IGR_CTRL [24].LU_KEY_SEL[0:0]	1
0xBB00CE38	PE_PORT_ETAG_IGR_CTRL [25].RESERVED[31:12]	20
0xBB00CE38	PE_PORT_ETAG_IGR_CTRL [25].ECID_RPF_CHK[11:11]	1
0xBB00CE38	PE_PORT_ETAG_IGR_CTRL [25].RSVD_ECID_FLTR[10:10]	1
0xBB00CE38	PE_PORT_ETAG_IGR_CTRL [25].MC_ECID_FLTR[9:9]	1
0xBB00CE38	PE_PORT_ETAG_IGR_CTRL [25].ECID_NSG[8:1]	8
0xBB00CE38	PE_PORT_ETAG_IGR_CTRL [25].LU_KEY_SEL[0:0]	1
0xBB00CE3C	PE_PORT_ETAG_IGR_CTRL [26].RESERVED[31:12]	20
0xBB00CE3C	PE_PORT_ETAG_IGR_CTRL [26].ECID_RPF_CHK[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CE3C	PE_PORT_ETAG_IGR_CTRL [26].RSVD_ECID_FLTR[10:10]	1
0xBB00CE3C	PE_PORT_ETAG_IGR_CTRL [26].MC_ECID_FLTR[9:9]	1
0xBB00CE3C	PE_PORT_ETAG_IGR_CTRL [26].ECID_NSG[8:1]	8
0xBB00CE3C	PE_PORT_ETAG_IGR_CTRL [26].LU_KEY_SEL[0:0]	1
0xBB00CE40	PE_PORT_ETAG_IGR_CTRL [27].RESERVED[31:12]	20
0xBB00CE40	PE_PORT_ETAG_IGR_CTRL [27].ECID_RPF_CHK[11:11]	1
0xBB00CE40	PE_PORT_ETAG_IGR_CTRL [27].RSVD_ECID_FLTR[10:10]	1
0xBB00CE40	PE_PORT_ETAG_IGR_CTRL [27].MC_ECID_FLTR[9:9]	1
0xBB00CE40	PE_PORT_ETAG_IGR_CTRL [27].ECID_NSG[8:1]	8
0xBB00CE40	PE_PORT_ETAG_IGR_CTRL [27].LU_KEY_SEL[0:0]	1
0xBB00CE44	PE_PORT_ETAG_IGR_CTRL [28].RESERVED[31:12]	20
0xBB00CE44	PE_PORT_ETAG_IGR_CTRL [28].ECID_RPF_CHK[11:11]	1
0xBB00CE44	PE_PORT_ETAG_IGR_CTRL [28].RSVD_ECID_FLTR[10:10]	1
0xBB00CE44	PE_PORT_ETAG_IGR_CTRL [28].MC_ECID_FLTR[9:9]	1
0xBB00CE44	PE_PORT_ETAG_IGR_CTRL [28].ECID_NSG[8:1]	8
0xBB00CE44	PE_PORT_ETAG_IGR_CTRL [28].LU_KEY_SEL[0:0]	1
0xBB00CE48	PE_PORT_ETAG_IGR_CTRL [29].RESERVED[31:12]	20
0xBB00CE48	PE_PORT_ETAG_IGR_CTRL [29].ECID_RPF_CHK[11:11]	1
0xBB00CE48	PE_PORT_ETAG_IGR_CTRL [29].RSVD_ECID_FLTR[10:10]	1
0xBB00CE48	PE_PORT_ETAG_IGR_CTRL [29].MC_ECID_FLTR[9:9]	1
0xBB00CE48	PE_PORT_ETAG_IGR_CTRL [29].ECID_NSG[8:1]	8
0xBB00CE48	PE_PORT_ETAG_IGR_CTRL [29].LU_KEY_SEL[0:0]	1
0xBB00CE4C	PE_PORT_ETAG_IGR_CTRL [30].RESERVED[31:12]	20
0xBB00CE4C	PE_PORT_ETAG_IGR_CTRL [30].ECID_RPF_CHK[11:11]	1
0xBB00CE4C	PE_PORT_ETAG_IGR_CTRL [30].RSVD_ECID_FLTR[10:10]	1
0xBB00CE4C	PE_PORT_ETAG_IGR_CTRL [30].MC_ECID_FLTR[9:9]	1
0xBB00CE4C	PE_PORT_ETAG_IGR_CTRL [30].ECID_NSG[8:1]	8
0xBB00CE4C	PE_PORT_ETAG_IGR_CTRL [30].LU_KEY_SEL[0:0]	1
0xBB00CE50	PE_PORT_ETAG_IGR_CTRL [31].RESERVED[31:12]	20
0xBB00CE50	PE_PORT_ETAG_IGR_CTRL [31].ECID_RPF_CHK[11:11]	1
0xBB00CE50	PE_PORT_ETAG_IGR_CTRL [31].RSVD_ECID_FLTR[10:10]	1
0xBB00CE50	PE_PORT_ETAG_IGR_CTRL [31].MC_ECID_FLTR[9:9]	1
0xBB00CE50	PE_PORT_ETAG_IGR_CTRL [31].ECID_NSG[8:1]	8
0xBB00CE50	PE_PORT_ETAG_IGR_CTRL [31].LU_KEY_SEL[0:0]	1
0xBB00CE54	PE_PORT_ETAG_IGR_CTRL [32].RESERVED[31:12]	20
0xBB00CE54	PE_PORT_ETAG_IGR_CTRL [32].ECID_RPF_CHK[11:11]	1
0xBB00CE54	PE_PORT_ETAG_IGR_CTRL [32].RSVD_ECID_FLTR[10:10]	1
0xBB00CE54	PE_PORT_ETAG_IGR_CTRL [32].MC_ECID_FLTR[9:9]	1
0xBB00CE54	PE_PORT_ETAG_IGR_CTRL [32].ECID_NSG[8:1]	8
0xBB00CE54	PE_PORT_ETAG_IGR_CTRL [32].LU_KEY_SEL[0:0]	1
0xBB00CE58	PE_PORT_ETAG_IGR_CTRL [33].RESERVED[31:12]	20
0xBB00CE58	PE_PORT_ETAG_IGR_CTRL [33].ECID_RPF_CHK[11:11]	1
0xBB00CE58	PE_PORT_ETAG_IGR_CTRL [33].RSVD_ECID_FLTR[10:10]	1
0xBB00CE58	PE_PORT_ETAG_IGR_CTRL [33].MC_ECID_FLTR[9:9]	1
0xBB00CE58	PE_PORT_ETAG_IGR_CTRL [33].ECID_NSG[8:1]	8
0xBB00CE58	PE_PORT_ETAG_IGR_CTRL [33].LU_KEY_SEL[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CE5C	PE_PORT_ETAG_IGR_CTRL [34].RESERVED[31:12]	20
0xBB00CE5C	PE_PORT_ETAG_IGR_CTRL [34].ECID_RPF_CHK[11:11]	1
0xBB00CE5C	PE_PORT_ETAG_IGR_CTRL [34].RSVD_ECID_FLTR[10:10]	1
0xBB00CE5C	PE_PORT_ETAG_IGR_CTRL [34].MC_ECID_FLTR[9:9]	1
0xBB00CE5C	PE_PORT_ETAG_IGR_CTRL [34].ECID_NSG[8:1]	8
0xBB00CE5C	PE_PORT_ETAG_IGR_CTRL [34].LU_KEY_SEL[0:0]	1
0xBB00CE60	PE_PORT_ETAG_IGR_CTRL [35].RESERVED[31:12]	20
0xBB00CE60	PE_PORT_ETAG_IGR_CTRL [35].ECID_RPF_CHK[11:11]	1
0xBB00CE60	PE_PORT_ETAG_IGR_CTRL [35].RSVD_ECID_FLTR[10:10]	1
0xBB00CE60	PE_PORT_ETAG_IGR_CTRL [35].MC_ECID_FLTR[9:9]	1
0xBB00CE60	PE_PORT_ETAG_IGR_CTRL [35].ECID_NSG[8:1]	8
0xBB00CE60	PE_PORT_ETAG_IGR_CTRL [35].LU_KEY_SEL[0:0]	1
0xBB00CE64	PE_PORT_ETAG_IGR_CTRL [36].RESERVED[31:12]	20
0xBB00CE64	PE_PORT_ETAG_IGR_CTRL [36].ECID_RPF_CHK[11:11]	1
0xBB00CE64	PE_PORT_ETAG_IGR_CTRL [36].RSVD_ECID_FLTR[10:10]	1
0xBB00CE64	PE_PORT_ETAG_IGR_CTRL [36].MC_ECID_FLTR[9:9]	1
0xBB00CE64	PE_PORT_ETAG_IGR_CTRL [36].ECID_NSG[8:1]	8
0xBB00CE64	PE_PORT_ETAG_IGR_CTRL [36].LU_KEY_SEL[0:0]	1
0xBB00CE68	PE_PORT_ETAG_IGR_CTRL [37].RESERVED[31:12]	20
0xBB00CE68	PE_PORT_ETAG_IGR_CTRL [37].ECID_RPF_CHK[11:11]	1
0xBB00CE68	PE_PORT_ETAG_IGR_CTRL [37].RSVD_ECID_FLTR[10:10]	1
0xBB00CE68	PE_PORT_ETAG_IGR_CTRL [37].MC_ECID_FLTR[9:9]	1
0xBB00CE68	PE_PORT_ETAG_IGR_CTRL [37].ECID_NSG[8:1]	8
0xBB00CE68	PE_PORT_ETAG_IGR_CTRL [37].LU_KEY_SEL[0:0]	1
0xBB00CE6C	PE_PORT_ETAG_IGR_CTRL [38].RESERVED[31:12]	20
0xBB00CE6C	PE_PORT_ETAG_IGR_CTRL [38].ECID_RPF_CHK[11:11]	1
0xBB00CE6C	PE_PORT_ETAG_IGR_CTRL [38].RSVD_ECID_FLTR[10:10]	1
0xBB00CE6C	PE_PORT_ETAG_IGR_CTRL [38].MC_ECID_FLTR[9:9]	1
0xBB00CE6C	PE_PORT_ETAG_IGR_CTRL [38].ECID_NSG[8:1]	8
0xBB00CE6C	PE_PORT_ETAG_IGR_CTRL [38].LU_KEY_SEL[0:0]	1
0xBB00CE70	PE_PORT_ETAG_IGR_CTRL [39].RESERVED[31:12]	20
0xBB00CE70	PE_PORT_ETAG_IGR_CTRL [39].ECID_RPF_CHK[11:11]	1
0xBB00CE70	PE_PORT_ETAG_IGR_CTRL [39].RSVD_ECID_FLTR[10:10]	1
0xBB00CE70	PE_PORT_ETAG_IGR_CTRL [39].MC_ECID_FLTR[9:9]	1
0xBB00CE70	PE_PORT_ETAG_IGR_CTRL [39].ECID_NSG[8:1]	8
0xBB00CE70	PE_PORT_ETAG_IGR_CTRL [39].LU_KEY_SEL[0:0]	1
0xBB00CE74	PE_PORT_ETAG_IGR_CTRL [40].RESERVED[31:12]	20
0xBB00CE74	PE_PORT_ETAG_IGR_CTRL [40].ECID_RPF_CHK[11:11]	1
0xBB00CE74	PE_PORT_ETAG_IGR_CTRL [40].RSVD_ECID_FLTR[10:10]	1
0xBB00CE74	PE_PORT_ETAG_IGR_CTRL [40].MC_ECID_FLTR[9:9]	1
0xBB00CE74	PE_PORT_ETAG_IGR_CTRL [40].ECID_NSG[8:1]	8
0xBB00CE74	PE_PORT_ETAG_IGR_CTRL [40].LU_KEY_SEL[0:0]	1
0xBB00CE78	PE_PORT_ETAG_IGR_CTRL [41].RESERVED[31:12]	20
0xBB00CE78	PE_PORT_ETAG_IGR_CTRL [41].ECID_RPF_CHK[11:11]	1
0xBB00CE78	PE_PORT_ETAG_IGR_CTRL [41].RSVD_ECID_FLTR[10:10]	1
0xBB00CE78	PE_PORT_ETAG_IGR_CTRL [41].MC_ECID_FLTR[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CE78	PE_PORT_ETAG_IGR_CTRL [41].ECID_NSG[8:1]	8
0xBB00CE78	PE_PORT_ETAG_IGR_CTRL [41].LU_KEY_SEL[0:0]	1
0xBB00CE7C	PE_PORT_ETAG_IGR_CTRL [42].RESERVED[31:12]	20
0xBB00CE7C	PE_PORT_ETAG_IGR_CTRL [42].ECID_RPF_CHK[11:11]	1
0xBB00CE7C	PE_PORT_ETAG_IGR_CTRL [42].RSVD_ECID_FLTR[10:10]	1
0xBB00CE7C	PE_PORT_ETAG_IGR_CTRL [42].MC_ECID_FLTR[9:9]	1
0xBB00CE7C	PE_PORT_ETAG_IGR_CTRL [42].ECID_NSG[8:1]	8
0xBB00CE7C	PE_PORT_ETAG_IGR_CTRL [42].LU_KEY_SEL[0:0]	1
0xBB00CE80	PE_PORT_ETAG_IGR_CTRL [43].RESERVED[31:12]	20
0xBB00CE80	PE_PORT_ETAG_IGR_CTRL [43].ECID_RPF_CHK[11:11]	1
0xBB00CE80	PE_PORT_ETAG_IGR_CTRL [43].RSVD_ECID_FLTR[10:10]	1
0xBB00CE80	PE_PORT_ETAG_IGR_CTRL [43].MC_ECID_FLTR[9:9]	1
0xBB00CE80	PE_PORT_ETAG_IGR_CTRL [43].ECID_NSG[8:1]	8
0xBB00CE80	PE_PORT_ETAG_IGR_CTRL [43].LU_KEY_SEL[0:0]	1
0xBB00CE84	PE_PORT_ETAG_IGR_CTRL [44].RESERVED[31:12]	20
0xBB00CE84	PE_PORT_ETAG_IGR_CTRL [44].ECID_RPF_CHK[11:11]	1
0xBB00CE84	PE_PORT_ETAG_IGR_CTRL [44].RSVD_ECID_FLTR[10:10]	1
0xBB00CE84	PE_PORT_ETAG_IGR_CTRL [44].MC_ECID_FLTR[9:9]	1
0xBB00CE84	PE_PORT_ETAG_IGR_CTRL [44].ECID_NSG[8:1]	8
0xBB00CE84	PE_PORT_ETAG_IGR_CTRL [44].LU_KEY_SEL[0:0]	1
0xBB00CE88	PE_PORT_ETAG_IGR_CTRL [45].RESERVED[31:12]	20
0xBB00CE88	PE_PORT_ETAG_IGR_CTRL [45].ECID_RPF_CHK[11:11]	1
0xBB00CE88	PE_PORT_ETAG_IGR_CTRL [45].RSVD_ECID_FLTR[10:10]	1
0xBB00CE88	PE_PORT_ETAG_IGR_CTRL [45].MC_ECID_FLTR[9:9]	1
0xBB00CE88	PE_PORT_ETAG_IGR_CTRL [45].ECID_NSG[8:1]	8
0xBB00CE88	PE_PORT_ETAG_IGR_CTRL [45].LU_KEY_SEL[0:0]	1
0xBB00CE8C	PE_PORT_ETAG_IGR_CTRL [46].RESERVED[31:12]	20
0xBB00CE8C	PE_PORT_ETAG_IGR_CTRL [46].ECID_RPF_CHK[11:11]	1
0xBB00CE8C	PE_PORT_ETAG_IGR_CTRL [46].RSVD_ECID_FLTR[10:10]	1
0xBB00CE8C	PE_PORT_ETAG_IGR_CTRL [46].MC_ECID_FLTR[9:9]	1
0xBB00CE8C	PE_PORT_ETAG_IGR_CTRL [46].ECID_NSG[8:1]	8
0xBB00CE8C	PE_PORT_ETAG_IGR_CTRL [46].LU_KEY_SEL[0:0]	1
0xBB00CE90	PE_PORT_ETAG_IGR_CTRL [47].RESERVED[31:12]	20
0xBB00CE90	PE_PORT_ETAG_IGR_CTRL [47].ECID_RPF_CHK[11:11]	1
0xBB00CE90	PE_PORT_ETAG_IGR_CTRL [47].RSVD_ECID_FLTR[10:10]	1
0xBB00CE90	PE_PORT_ETAG_IGR_CTRL [47].MC_ECID_FLTR[9:9]	1
0xBB00CE90	PE_PORT_ETAG_IGR_CTRL [47].ECID_NSG[8:1]	8
0xBB00CE90	PE_PORT_ETAG_IGR_CTRL [47].LU_KEY_SEL[0:0]	1
0xBB00CE94	PE_PORT_ETAG_IGR_CTRL [48].RESERVED[31:12]	20
0xBB00CE94	PE_PORT_ETAG_IGR_CTRL [48].ECID_RPF_CHK[11:11]	1
0xBB00CE94	PE_PORT_ETAG_IGR_CTRL [48].RSVD_ECID_FLTR[10:10]	1
0xBB00CE94	PE_PORT_ETAG_IGR_CTRL [48].MC_ECID_FLTR[9:9]	1
0xBB00CE94	PE_PORT_ETAG_IGR_CTRL [48].ECID_NSG[8:1]	8
0xBB00CE94	PE_PORT_ETAG_IGR_CTRL [48].LU_KEY_SEL[0:0]	1
0xBB00CE98	PE_PORT_ETAG_IGR_CTRL [49].RESERVED[31:12]	20
0xBB00CE98	PE_PORT_ETAG_IGR_CTRL [49].ECID_RPF_CHK[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CE98	PE_PORT_ETAG_IGR_CTRL [49].RSVD_ECID_FLTR[10:10]	1
0xBB00CE98	PE_PORT_ETAG_IGR_CTRL [49].MC_ECID_FLTR[9:9]	1
0xBB00CE98	PE_PORT_ETAG_IGR_CTRL [49].ECID_NSG[8:1]	8
0xBB00CE98	PE_PORT_ETAG_IGR_CTRL [49].LU_KEY_SEL[0:0]	1
0xBB00CE9C	PE_PORT_ETAG_IGR_CTRL [50].RESERVED[31:12]	20
0xBB00CE9C	PE_PORT_ETAG_IGR_CTRL [50].ECID_RPF_CHK[11:11]	1
0xBB00CE9C	PE_PORT_ETAG_IGR_CTRL [50].RSVD_ECID_FLTR[10:10]	1
0xBB00CE9C	PE_PORT_ETAG_IGR_CTRL [50].MC_ECID_FLTR[9:9]	1
0xBB00CE9C	PE_PORT_ETAG_IGR_CTRL [50].ECID_NSG[8:1]	8
0xBB00CE9C	PE_PORT_ETAG_IGR_CTRL [50].LU_KEY_SEL[0:0]	1
0xBB00CEA0	PE_PORT_ETAG_IGR_CTRL [51].RESERVED[31:12]	20
0xBB00CEA0	PE_PORT_ETAG_IGR_CTRL [51].ECID_RPF_CHK[11:11]	1
0xBB00CEA0	PE_PORT_ETAG_IGR_CTRL [51].RSVD_ECID_FLTR[10:10]	1
0xBB00CEA0	PE_PORT_ETAG_IGR_CTRL [51].MC_ECID_FLTR[9:9]	1
0xBB00CEA0	PE_PORT_ETAG_IGR_CTRL [51].ECID_NSG[8:1]	8
0xBB00CEA0	PE_PORT_ETAG_IGR_CTRL [51].LU_KEY_SEL[0:0]	1
0xBB00CEA4	PE_PORT_ETAG_IGR_CTRL [52].RESERVED[31:12]	20
0xBB00CEA4	PE_PORT_ETAG_IGR_CTRL [52].ECID_RPF_CHK[11:11]	1
0xBB00CEA4	PE_PORT_ETAG_IGR_CTRL [52].RSVD_ECID_FLTR[10:10]	1
0xBB00CEA4	PE_PORT_ETAG_IGR_CTRL [52].MC_ECID_FLTR[9:9]	1
0xBB00CEA4	PE_PORT_ETAG_IGR_CTRL [52].ECID_NSG[8:1]	8
0xBB00CEA4	PE_PORT_ETAG_IGR_CTRL [52].LU_KEY_SEL[0:0]	1
0xBB00CEA8	PE_PORT_ETAG_IGR_CTRL [53].RESERVED[31:12]	20
0xBB00CEA8	PE_PORT_ETAG_IGR_CTRL [53].ECID_RPF_CHK[11:11]	1
0xBB00CEA8	PE_PORT_ETAG_IGR_CTRL [53].RSVD_ECID_FLTR[10:10]	1
0xBB00CEA8	PE_PORT_ETAG_IGR_CTRL [53].MC_ECID_FLTR[9:9]	1
0xBB00CEA8	PE_PORT_ETAG_IGR_CTRL [53].ECID_NSG[8:1]	8
0xBB00CEA8	PE_PORT_ETAG_IGR_CTRL [53].LU_KEY_SEL[0:0]	1
0xBB00CEAC	PE_PORT_ETAG_IGR_CTRL [54].RESERVED[31:12]	20
0xBB00CEAC	PE_PORT_ETAG_IGR_CTRL [54].ECID_RPF_CHK[11:11]	1
0xBB00CEAC	PE_PORT_ETAG_IGR_CTRL [54].RSVD_ECID_FLTR[10:10]	1
0xBB00CEAC	PE_PORT_ETAG_IGR_CTRL [54].MC_ECID_FLTR[9:9]	1
0xBB00CEAC	PE_PORT_ETAG_IGR_CTRL [54].ECID_NSG[8:1]	8
0xBB00CEAC	PE_PORT_ETAG_IGR_CTRL [54].LU_KEY_SEL[0:0]	1
0xBB00CEB0	PE_PORT_ETAG_IGR_CTRL [55].RESERVED[31:12]	20
0xBB00CEB0	PE_PORT_ETAG_IGR_CTRL [55].ECID_RPF_CHK[11:11]	1
0xBB00CEB0	PE_PORT_ETAG_IGR_CTRL [55].RSVD_ECID_FLTR[10:10]	1
0xBB00CEB0	PE_PORT_ETAG_IGR_CTRL [55].MC_ECID_FLTR[9:9]	1
0xBB00CEB0	PE_PORT_ETAG_IGR_CTRL [55].ECID_NSG[8:1]	8
0xBB00CEB0	PE_PORT_ETAG_IGR_CTRL [55].LU_KEY_SEL[0:0]	1
0xBB006018	PE_PORT_ETAG_MAC_CTRL [0].RESERVED[31:1]	31
0xBB006018	PE_PORT_ETAG_MAC_CTRL [0].ETAG_EN[0:0]	1
0xBB006098	PE_PORT_ETAG_MAC_CTRL [1].RESERVED[31:1]	31
0xBB006098	PE_PORT_ETAG_MAC_CTRL [1].ETAG_EN[0:0]	1
0xBB006118	PE_PORT_ETAG_MAC_CTRL [2].RESERVED[31:1]	31
0xBB006118	PE_PORT_ETAG_MAC_CTRL [2].ETAG_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006198	PE_PORT_ETAG_MAC_CTRL [3].RESERVED[31:1]	31
0xBB006198	PE_PORT_ETAG_MAC_CTRL [3].ETAG_EN[0:0]	1
0xBB006218	PE_PORT_ETAG_MAC_CTRL [4].RESERVED[31:1]	31
0xBB006218	PE_PORT_ETAG_MAC_CTRL [4].ETAG_EN[0:0]	1
0xBB006298	PE_PORT_ETAG_MAC_CTRL [5].RESERVED[31:1]	31
0xBB006298	PE_PORT_ETAG_MAC_CTRL [5].ETAG_EN[0:0]	1
0xBB006318	PE_PORT_ETAG_MAC_CTRL [6].RESERVED[31:1]	31
0xBB006318	PE_PORT_ETAG_MAC_CTRL [6].ETAG_EN[0:0]	1
0xBB006398	PE_PORT_ETAG_MAC_CTRL [7].RESERVED[31:1]	31
0xBB006398	PE_PORT_ETAG_MAC_CTRL [7].ETAG_EN[0:0]	1
0xBB006418	PE_PORT_ETAG_MAC_CTRL [8].RESERVED[31:1]	31
0xBB006418	PE_PORT_ETAG_MAC_CTRL [8].ETAG_EN[0:0]	1
0xBB006498	PE_PORT_ETAG_MAC_CTRL [9].RESERVED[31:1]	31
0xBB006498	PE_PORT_ETAG_MAC_CTRL [9].ETAG_EN[0:0]	1
0xBB006518	PE_PORT_ETAG_MAC_CTRL [10].RESERVED[31:1]	31
0xBB006518	PE_PORT_ETAG_MAC_CTRL [10].ETAG_EN[0:0]	1
0xBB006598	PE_PORT_ETAG_MAC_CTRL [11].RESERVED[31:1]	31
0xBB006598	PE_PORT_ETAG_MAC_CTRL [11].ETAG_EN[0:0]	1
0xBB006618	PE_PORT_ETAG_MAC_CTRL [12].RESERVED[31:1]	31
0xBB006618	PE_PORT_ETAG_MAC_CTRL [12].ETAG_EN[0:0]	1
0xBB006698	PE_PORT_ETAG_MAC_CTRL [13].RESERVED[31:1]	31
0xBB006698	PE_PORT_ETAG_MAC_CTRL [13].ETAG_EN[0:0]	1
0xBB006718	PE_PORT_ETAG_MAC_CTRL [14].RESERVED[31:1]	31
0xBB006718	PE_PORT_ETAG_MAC_CTRL [14].ETAG_EN[0:0]	1
0xBB006798	PE_PORT_ETAG_MAC_CTRL [15].RESERVED[31:1]	31
0xBB006798	PE_PORT_ETAG_MAC_CTRL [15].ETAG_EN[0:0]	1
0xBB006818	PE_PORT_ETAG_MAC_CTRL [16].RESERVED[31:1]	31
0xBB006818	PE_PORT_ETAG_MAC_CTRL [16].ETAG_EN[0:0]	1
0xBB006898	PE_PORT_ETAG_MAC_CTRL [17].RESERVED[31:1]	31
0xBB006898	PE_PORT_ETAG_MAC_CTRL [17].ETAG_EN[0:0]	1
0xBB006918	PE_PORT_ETAG_MAC_CTRL [18].RESERVED[31:1]	31
0xBB006918	PE_PORT_ETAG_MAC_CTRL [18].ETAG_EN[0:0]	1
0xBB006998	PE_PORT_ETAG_MAC_CTRL [19].RESERVED[31:1]	31
0xBB006998	PE_PORT_ETAG_MAC_CTRL [19].ETAG_EN[0:0]	1
0xBB006A18	PE_PORT_ETAG_MAC_CTRL [20].RESERVED[31:1]	31
0xBB006A18	PE_PORT_ETAG_MAC_CTRL [20].ETAG_EN[0:0]	1
0xBB006A98	PE_PORT_ETAG_MAC_CTRL [21].RESERVED[31:1]	31
0xBB006A98	PE_PORT_ETAG_MAC_CTRL [21].ETAG_EN[0:0]	1
0xBB006B18	PE_PORT_ETAG_MAC_CTRL [22].RESERVED[31:1]	31
0xBB006B18	PE_PORT_ETAG_MAC_CTRL [22].ETAG_EN[0:0]	1
0xBB006B98	PE_PORT_ETAG_MAC_CTRL [23].RESERVED[31:1]	31
0xBB006B98	PE_PORT_ETAG_MAC_CTRL [23].ETAG_EN[0:0]	1
0xBB006C18	PE_PORT_ETAG_MAC_CTRL [24].RESERVED[31:1]	31
0xBB006C18	PE_PORT_ETAG_MAC_CTRL [24].ETAG_EN[0:0]	1
0xBB006C98	PE_PORT_ETAG_MAC_CTRL [25].RESERVED[31:1]	31
0xBB006C98	PE_PORT_ETAG_MAC_CTRL [25].ETAG_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006D18	PE_PORT_ETAG_MAC_CTRL [26].RESERVED[31:1]	31
0xBB006D18	PE_PORT_ETAG_MAC_CTRL [26].ETAG_EN[0:0]	1
0xBB006D98	PE_PORT_ETAG_MAC_CTRL [27].RESERVED[31:1]	31
0xBB006D98	PE_PORT_ETAG_MAC_CTRL [27].ETAG_EN[0:0]	1
0xBB006E18	PE_PORT_ETAG_MAC_CTRL [28].RESERVED[31:1]	31
0xBB006E18	PE_PORT_ETAG_MAC_CTRL [28].ETAG_EN[0:0]	1
0xBB006E98	PE_PORT_ETAG_MAC_CTRL [29].RESERVED[31:1]	31
0xBB006E98	PE_PORT_ETAG_MAC_CTRL [29].ETAG_EN[0:0]	1
0xBB006F18	PE_PORT_ETAG_MAC_CTRL [30].RESERVED[31:1]	31
0xBB006F18	PE_PORT_ETAG_MAC_CTRL [30].ETAG_EN[0:0]	1
0xBB006F98	PE_PORT_ETAG_MAC_CTRL [31].RESERVED[31:1]	31
0xBB006F98	PE_PORT_ETAG_MAC_CTRL [31].ETAG_EN[0:0]	1
0xBB007018	PE_PORT_ETAG_MAC_CTRL [32].RESERVED[31:1]	31
0xBB007018	PE_PORT_ETAG_MAC_CTRL [32].ETAG_EN[0:0]	1
0xBB007098	PE_PORT_ETAG_MAC_CTRL [33].RESERVED[31:1]	31
0xBB007098	PE_PORT_ETAG_MAC_CTRL [33].ETAG_EN[0:0]	1
0xBB007118	PE_PORT_ETAG_MAC_CTRL [34].RESERVED[31:1]	31
0xBB007118	PE_PORT_ETAG_MAC_CTRL [34].ETAG_EN[0:0]	1
0xBB007198	PE_PORT_ETAG_MAC_CTRL [35].RESERVED[31:1]	31
0xBB007198	PE_PORT_ETAG_MAC_CTRL [35].ETAG_EN[0:0]	1
0xBB007218	PE_PORT_ETAG_MAC_CTRL [36].RESERVED[31:1]	31
0xBB007218	PE_PORT_ETAG_MAC_CTRL [36].ETAG_EN[0:0]	1
0xBB007298	PE_PORT_ETAG_MAC_CTRL [37].RESERVED[31:1]	31
0xBB007298	PE_PORT_ETAG_MAC_CTRL [37].ETAG_EN[0:0]	1
0xBB007318	PE_PORT_ETAG_MAC_CTRL [38].RESERVED[31:1]	31
0xBB007318	PE_PORT_ETAG_MAC_CTRL [38].ETAG_EN[0:0]	1
0xBB007398	PE_PORT_ETAG_MAC_CTRL [39].RESERVED[31:1]	31
0xBB007398	PE_PORT_ETAG_MAC_CTRL [39].ETAG_EN[0:0]	1
0xBB007418	PE_PORT_ETAG_MAC_CTRL [40].RESERVED[31:1]	31
0xBB007418	PE_PORT_ETAG_MAC_CTRL [40].ETAG_EN[0:0]	1
0xBB007498	PE_PORT_ETAG_MAC_CTRL [41].RESERVED[31:1]	31
0xBB007498	PE_PORT_ETAG_MAC_CTRL [41].ETAG_EN[0:0]	1
0xBB007518	PE_PORT_ETAG_MAC_CTRL [42].RESERVED[31:1]	31
0xBB007518	PE_PORT_ETAG_MAC_CTRL [42].ETAG_EN[0:0]	1
0xBB007598	PE_PORT_ETAG_MAC_CTRL [43].RESERVED[31:1]	31
0xBB007598	PE_PORT_ETAG_MAC_CTRL [43].ETAG_EN[0:0]	1
0xBB007618	PE_PORT_ETAG_MAC_CTRL [44].RESERVED[31:1]	31
0xBB007618	PE_PORT_ETAG_MAC_CTRL [44].ETAG_EN[0:0]	1
0xBB007698	PE_PORT_ETAG_MAC_CTRL [45].RESERVED[31:1]	31
0xBB007698	PE_PORT_ETAG_MAC_CTRL [45].ETAG_EN[0:0]	1
0xBB007718	PE_PORT_ETAG_MAC_CTRL [46].RESERVED[31:1]	31
0xBB007718	PE_PORT_ETAG_MAC_CTRL [46].ETAG_EN[0:0]	1
0xBB007798	PE_PORT_ETAG_MAC_CTRL [47].RESERVED[31:1]	31
0xBB007798	PE_PORT_ETAG_MAC_CTRL [47].ETAG_EN[0:0]	1
0xBB007818	PE_PORT_ETAG_MAC_CTRL [48].RESERVED[31:1]	31
0xBB007818	PE_PORT_ETAG_MAC_CTRL [48].ETAG_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007898	PE_PORT_ETAG_MAC_CTRL [49].RESERVED[31:1]	31
0xBB007898	PE_PORT_ETAG_MAC_CTRL [49].ETAG_EN[0:0]	1
0xBB007918	PE_PORT_ETAG_MAC_CTRL [50].RESERVED[31:1]	31
0xBB007918	PE_PORT_ETAG_MAC_CTRL [50].ETAG_EN[0:0]	1
0xBB007998	PE_PORT_ETAG_MAC_CTRL [51].RESERVED[31:1]	31
0xBB007998	PE_PORT_ETAG_MAC_CTRL [51].ETAG_EN[0:0]	1
0xBB007A18	PE_PORT_ETAG_MAC_CTRL [52].RESERVED[31:1]	31
0xBB007A18	PE_PORT_ETAG_MAC_CTRL [52].ETAG_EN[0:0]	1
0xBB007A98	PE_PORT_ETAG_MAC_CTRL [53].RESERVED[31:1]	31
0xBB007A98	PE_PORT_ETAG_MAC_CTRL [53].ETAG_EN[0:0]	1
0xBB007B18	PE_PORT_ETAG_MAC_CTRL [54].RESERVED[31:1]	31
0xBB007B18	PE_PORT_ETAG_MAC_CTRL [54].ETAG_EN[0:0]	1
0xBB007B98	PE_PORT_ETAG_MAC_CTRL [55].RESERVED[31:1]	31
0xBB007B98	PE_PORT_ETAG_MAC_CTRL [55].ETAG_EN[0:0]	1
0xBB007C18	PE_PORT_ETAG_MAC_CTRL [56].RESERVED[31:1]	31
0xBB007C18	PE_PORT_ETAG_MAC_CTRL [56].ETAG_EN[0:0]	1
0xBB00CEB4	PE_PORT_PCID_CTRL [0].RESERVED[31:25]	7
0xBB00CEB4	PE_PORT_PCID_CTRL [0].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEB4	PE_PORT_PCID_CTRL [0].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEB4	PE_PORT_PCID_CTRL [0].USE_DEFAULT[21:21]	1
0xBB00CEB4	PE_PORT_PCID_CTRL [0].BASE_PE[20:20]	1
0xBB00CEB4	PE_PORT_PCID_CTRL [0].PCID_EXT[19:12]	8
0xBB00CEB4	PE_PORT_PCID_CTRL [0].PCID_BASE[11:0]	12
0xBB00CEB8	PE_PORT_PCID_CTRL [1].RESERVED[31:25]	7
0xBB00CEB8	PE_PORT_PCID_CTRL [1].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEB8	PE_PORT_PCID_CTRL [1].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEB8	PE_PORT_PCID_CTRL [1].USE_DEFAULT[21:21]	1
0xBB00CEB8	PE_PORT_PCID_CTRL [1].BASE_PE[20:20]	1
0xBB00CEB8	PE_PORT_PCID_CTRL [1].PCID_EXT[19:12]	8
0xBB00CEB8	PE_PORT_PCID_CTRL [1].PCID_BASE[11:0]	12
0xBB00CEBC	PE_PORT_PCID_CTRL [2].RESERVED[31:25]	7
0xBB00CEBC	PE_PORT_PCID_CTRL [2].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEBC	PE_PORT_PCID_CTRL [2].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEBC	PE_PORT_PCID_CTRL [2].USE_DEFAULT[21:21]	1
0xBB00CEBC	PE_PORT_PCID_CTRL [2].BASE_PE[20:20]	1
0xBB00CEBC	PE_PORT_PCID_CTRL [2].PCID_EXT[19:12]	8
0xBB00CEBC	PE_PORT_PCID_CTRL [2].PCID_BASE[11:0]	12
0xBB00CEC0	PE_PORT_PCID_CTRL [3].RESERVED[31:25]	7
0xBB00CEC0	PE_PORT_PCID_CTRL [3].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEC0	PE_PORT_PCID_CTRL [3].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEC0	PE_PORT_PCID_CTRL [3].USE_DEFAULT[21:21]	1
0xBB00CEC0	PE_PORT_PCID_CTRL [3].BASE_PE[20:20]	1
0xBB00CEC0	PE_PORT_PCID_CTRL [3].PCID_EXT[19:12]	8
0xBB00CEC0	PE_PORT_PCID_CTRL [3].PCID_BASE[11:0]	12
0xBB00CEC4	PE_PORT_PCID_CTRL [4].RESERVED[31:25]	7
0xBB00CEC4	PE_PORT_PCID_CTRL [4].ECID_EQ_PCID_ACT[24:23]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CEC4	PE_PORT_PCID_CTRL [4].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEC4	PE_PORT_PCID_CTRL [4].USE_DEFAULT[21:21]	1
0xBB00CEC4	PE_PORT_PCID_CTRL [4].BASE_PE[20:20]	1
0xBB00CEC4	PE_PORT_PCID_CTRL [4].PCID_EXT[19:12]	8
0xBB00CEC4	PE_PORT_PCID_CTRL [4].PCID_BASE[11:0]	12
0xBB00CEC8	PE_PORT_PCID_CTRL [5].RESERVED[31:25]	7
0xBB00CEC8	PE_PORT_PCID_CTRL [5].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEC8	PE_PORT_PCID_CTRL [5].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEC8	PE_PORT_PCID_CTRL [5].USE_DEFAULT[21:21]	1
0xBB00CEC8	PE_PORT_PCID_CTRL [5].BASE_PE[20:20]	1
0xBB00CEC8	PE_PORT_PCID_CTRL [5].PCID_EXT[19:12]	8
0xBB00CEC8	PE_PORT_PCID_CTRL [5].PCID_BASE[11:0]	12
0xBB00CECC	PE_PORT_PCID_CTRL [6].RESERVED[31:25]	7
0xBB00CECC	PE_PORT_PCID_CTRL [6].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CECC	PE_PORT_PCID_CTRL [6].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CECC	PE_PORT_PCID_CTRL [6].USE_DEFAULT[21:21]	1
0xBB00CECC	PE_PORT_PCID_CTRL [6].BASE_PE[20:20]	1
0xBB00CECC	PE_PORT_PCID_CTRL [6].PCID_EXT[19:12]	8
0xBB00CECC	PE_PORT_PCID_CTRL [6].PCID_BASE[11:0]	12
0xBB00CED0	PE_PORT_PCID_CTRL [7].RESERVED[31:25]	7
0xBB00CED0	PE_PORT_PCID_CTRL [7].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CED0	PE_PORT_PCID_CTRL [7].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CED0	PE_PORT_PCID_CTRL [7].USE_DEFAULT[21:21]	1
0xBB00CED0	PE_PORT_PCID_CTRL [7].BASE_PE[20:20]	1
0xBB00CED0	PE_PORT_PCID_CTRL [7].PCID_EXT[19:12]	8
0xBB00CED0	PE_PORT_PCID_CTRL [7].PCID_BASE[11:0]	12
0xBB00CED4	PE_PORT_PCID_CTRL [8].RESERVED[31:25]	7
0xBB00CED4	PE_PORT_PCID_CTRL [8].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CED4	PE_PORT_PCID_CTRL [8].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CED4	PE_PORT_PCID_CTRL [8].USE_DEFAULT[21:21]	1
0xBB00CED4	PE_PORT_PCID_CTRL [8].BASE_PE[20:20]	1
0xBB00CED4	PE_PORT_PCID_CTRL [8].PCID_EXT[19:12]	8
0xBB00CED4	PE_PORT_PCID_CTRL [8].PCID_BASE[11:0]	12
0xBB00CED8	PE_PORT_PCID_CTRL [9].RESERVED[31:25]	7
0xBB00CED8	PE_PORT_PCID_CTRL [9].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CED8	PE_PORT_PCID_CTRL [9].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CED8	PE_PORT_PCID_CTRL [9].USE_DEFAULT[21:21]	1
0xBB00CED8	PE_PORT_PCID_CTRL [9].BASE_PE[20:20]	1
0xBB00CED8	PE_PORT_PCID_CTRL [9].PCID_EXT[19:12]	8
0xBB00CED8	PE_PORT_PCID_CTRL [9].PCID_BASE[11:0]	12
0xBB00CEDC	PE_PORT_PCID_CTRL [10].RESERVED[31:25]	7
0xBB00CEDC	PE_PORT_PCID_CTRL [10].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEDC	PE_PORT_PCID_CTRL [10].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEDC	PE_PORT_PCID_CTRL [10].USE_DEFAULT[21:21]	1
0xBB00CEDC	PE_PORT_PCID_CTRL [10].BASE_PE[20:20]	1
0xBB00CEDC	PE_PORT_PCID_CTRL [10].PCID_EXT[19:12]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CEDC	PE_PORT_PCID_CTRL [10].PCID_BASE[11:0]	12
0xBB00CEE0	PE_PORT_PCID_CTRL [11].RESERVED[31:25]	7
0xBB00CEE0	PE_PORT_PCID_CTRL [11].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEE0	PE_PORT_PCID_CTRL [11].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEE0	PE_PORT_PCID_CTRL [11].USE_DEFAULT[21:21]	1
0xBB00CEE0	PE_PORT_PCID_CTRL [11].BASE_PE[20:20]	1
0xBB00CEE0	PE_PORT_PCID_CTRL [11].PCID_EXT[19:12]	8
0xBB00CEE0	PE_PORT_PCID_CTRL [11].PCID_BASE[11:0]	12
0xBB00CEE4	PE_PORT_PCID_CTRL [12].RESERVED[31:25]	7
0xBB00CEE4	PE_PORT_PCID_CTRL [12].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEE4	PE_PORT_PCID_CTRL [12].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEE4	PE_PORT_PCID_CTRL [12].USE_DEFAULT[21:21]	1
0xBB00CEE4	PE_PORT_PCID_CTRL [12].BASE_PE[20:20]	1
0xBB00CEE4	PE_PORT_PCID_CTRL [12].PCID_EXT[19:12]	8
0xBB00CEE4	PE_PORT_PCID_CTRL [12].PCID_BASE[11:0]	12
0xBB00CEE8	PE_PORT_PCID_CTRL [13].RESERVED[31:25]	7
0xBB00CEE8	PE_PORT_PCID_CTRL [13].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEE8	PE_PORT_PCID_CTRL [13].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEE8	PE_PORT_PCID_CTRL [13].USE_DEFAULT[21:21]	1
0xBB00CEE8	PE_PORT_PCID_CTRL [13].BASE_PE[20:20]	1
0xBB00CEE8	PE_PORT_PCID_CTRL [13].PCID_EXT[19:12]	8
0xBB00CEE8	PE_PORT_PCID_CTRL [13].PCID_BASE[11:0]	12
0xBB00CEEC	PE_PORT_PCID_CTRL [14].RESERVED[31:25]	7
0xBB00CEEC	PE_PORT_PCID_CTRL [14].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEEC	PE_PORT_PCID_CTRL [14].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEEC	PE_PORT_PCID_CTRL [14].USE_DEFAULT[21:21]	1
0xBB00CEEC	PE_PORT_PCID_CTRL [14].BASE_PE[20:20]	1
0xBB00CEEC	PE_PORT_PCID_CTRL [14].PCID_EXT[19:12]	8
0xBB00CEEC	PE_PORT_PCID_CTRL [14].PCID_BASE[11:0]	12
0xBB00CEF0	PE_PORT_PCID_CTRL [15].RESERVED[31:25]	7
0xBB00CEF0	PE_PORT_PCID_CTRL [15].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEF0	PE_PORT_PCID_CTRL [15].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEF0	PE_PORT_PCID_CTRL [15].USE_DEFAULT[21:21]	1
0xBB00CEF0	PE_PORT_PCID_CTRL [15].BASE_PE[20:20]	1
0xBB00CEF0	PE_PORT_PCID_CTRL [15].PCID_EXT[19:12]	8
0xBB00CEF0	PE_PORT_PCID_CTRL [15].PCID_BASE[11:0]	12
0xBB00CEF4	PE_PORT_PCID_CTRL [16].RESERVED[31:25]	7
0xBB00CEF4	PE_PORT_PCID_CTRL [16].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEF4	PE_PORT_PCID_CTRL [16].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEF4	PE_PORT_PCID_CTRL [16].USE_DEFAULT[21:21]	1
0xBB00CEF4	PE_PORT_PCID_CTRL [16].BASE_PE[20:20]	1
0xBB00CEF4	PE_PORT_PCID_CTRL [16].PCID_EXT[19:12]	8
0xBB00CEF4	PE_PORT_PCID_CTRL [16].PCID_BASE[11:0]	12
0xBB00CEF8	PE_PORT_PCID_CTRL [17].RESERVED[31:25]	7
0xBB00CEF8	PE_PORT_PCID_CTRL [17].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEF8	PE_PORT_PCID_CTRL [17].PE_SRC_PORT_FLTR[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CEF8	PE_PORT_PCID_CTRL [17].USE_DEFAULT[21:21]	1
0xBB00CEF8	PE_PORT_PCID_CTRL [17].BASE_PE[20:20]	1
0xBB00CEF8	PE_PORT_PCID_CTRL [17].PCID_EXT[19:12]	8
0xBB00CEF8	PE_PORT_PCID_CTRL [17].PCID_BASE[11:0]	12
0xBB00CEFC	PE_PORT_PCID_CTRL [18].RESERVED[31:25]	7
0xBB00CEFC	PE_PORT_PCID_CTRL [18].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CEFC	PE_PORT_PCID_CTRL [18].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CEFC	PE_PORT_PCID_CTRL [18].USE_DEFAULT[21:21]	1
0xBB00CEFC	PE_PORT_PCID_CTRL [18].BASE_PE[20:20]	1
0xBB00CEFC	PE_PORT_PCID_CTRL [18].PCID_EXT[19:12]	8
0xBB00CEFC	PE_PORT_PCID_CTRL [18].PCID_BASE[11:0]	12
0xBB00CF00	PE_PORT_PCID_CTRL [19].RESERVED[31:25]	7
0xBB00CF00	PE_PORT_PCID_CTRL [19].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF00	PE_PORT_PCID_CTRL [19].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF00	PE_PORT_PCID_CTRL [19].USE_DEFAULT[21:21]	1
0xBB00CF00	PE_PORT_PCID_CTRL [19].BASE_PE[20:20]	1
0xBB00CF00	PE_PORT_PCID_CTRL [19].PCID_EXT[19:12]	8
0xBB00CF00	PE_PORT_PCID_CTRL [19].PCID_BASE[11:0]	12
0xBB00CF04	PE_PORT_PCID_CTRL [20].RESERVED[31:25]	7
0xBB00CF04	PE_PORT_PCID_CTRL [20].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF04	PE_PORT_PCID_CTRL [20].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF04	PE_PORT_PCID_CTRL [20].USE_DEFAULT[21:21]	1
0xBB00CF04	PE_PORT_PCID_CTRL [20].BASE_PE[20:20]	1
0xBB00CF04	PE_PORT_PCID_CTRL [20].PCID_EXT[19:12]	8
0xBB00CF04	PE_PORT_PCID_CTRL [20].PCID_BASE[11:0]	12
0xBB00CF08	PE_PORT_PCID_CTRL [21].RESERVED[31:25]	7
0xBB00CF08	PE_PORT_PCID_CTRL [21].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF08	PE_PORT_PCID_CTRL [21].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF08	PE_PORT_PCID_CTRL [21].USE_DEFAULT[21:21]	1
0xBB00CF08	PE_PORT_PCID_CTRL [21].BASE_PE[20:20]	1
0xBB00CF08	PE_PORT_PCID_CTRL [21].PCID_EXT[19:12]	8
0xBB00CF08	PE_PORT_PCID_CTRL [21].PCID_BASE[11:0]	12
0xBB00CF0C	PE_PORT_PCID_CTRL [22].RESERVED[31:25]	7
0xBB00CF0C	PE_PORT_PCID_CTRL [22].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF0C	PE_PORT_PCID_CTRL [22].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF0C	PE_PORT_PCID_CTRL [22].USE_DEFAULT[21:21]	1
0xBB00CF0C	PE_PORT_PCID_CTRL [22].BASE_PE[20:20]	1
0xBB00CF0C	PE_PORT_PCID_CTRL [22].PCID_EXT[19:12]	8
0xBB00CF0C	PE_PORT_PCID_CTRL [22].PCID_BASE[11:0]	12
0xBB00CF10	PE_PORT_PCID_CTRL [23].RESERVED[31:25]	7
0xBB00CF10	PE_PORT_PCID_CTRL [23].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF10	PE_PORT_PCID_CTRL [23].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF10	PE_PORT_PCID_CTRL [23].USE_DEFAULT[21:21]	1
0xBB00CF10	PE_PORT_PCID_CTRL [23].BASE_PE[20:20]	1
0xBB00CF10	PE_PORT_PCID_CTRL [23].PCID_EXT[19:12]	8
0xBB00CF10	PE_PORT_PCID_CTRL [23].PCID_BASE[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CF14	PE_PORT_PCID_CTRL [24].RESERVED[31:25]	7
0xBB00CF14	PE_PORT_PCID_CTRL [24].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF14	PE_PORT_PCID_CTRL [24].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF14	PE_PORT_PCID_CTRL [24].USE_DEFAULT[21:21]	1
0xBB00CF14	PE_PORT_PCID_CTRL [24].BASE_PE[20:20]	1
0xBB00CF14	PE_PORT_PCID_CTRL [24].PCID_EXT[19:12]	8
0xBB00CF14	PE_PORT_PCID_CTRL [24].PCID_BASE[11:0]	12
0xBB00CF18	PE_PORT_PCID_CTRL [25].RESERVED[31:25]	7
0xBB00CF18	PE_PORT_PCID_CTRL [25].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF18	PE_PORT_PCID_CTRL [25].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF18	PE_PORT_PCID_CTRL [25].USE_DEFAULT[21:21]	1
0xBB00CF18	PE_PORT_PCID_CTRL [25].BASE_PE[20:20]	1
0xBB00CF18	PE_PORT_PCID_CTRL [25].PCID_EXT[19:12]	8
0xBB00CF18	PE_PORT_PCID_CTRL [25].PCID_BASE[11:0]	12
0xBB00CF1C	PE_PORT_PCID_CTRL [26].RESERVED[31:25]	7
0xBB00CF1C	PE_PORT_PCID_CTRL [26].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF1C	PE_PORT_PCID_CTRL [26].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF1C	PE_PORT_PCID_CTRL [26].USE_DEFAULT[21:21]	1
0xBB00CF1C	PE_PORT_PCID_CTRL [26].BASE_PE[20:20]	1
0xBB00CF1C	PE_PORT_PCID_CTRL [26].PCID_EXT[19:12]	8
0xBB00CF1C	PE_PORT_PCID_CTRL [26].PCID_BASE[11:0]	12
0xBB00CF20	PE_PORT_PCID_CTRL [27].RESERVED[31:25]	7
0xBB00CF20	PE_PORT_PCID_CTRL [27].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF20	PE_PORT_PCID_CTRL [27].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF20	PE_PORT_PCID_CTRL [27].USE_DEFAULT[21:21]	1
0xBB00CF20	PE_PORT_PCID_CTRL [27].BASE_PE[20:20]	1
0xBB00CF20	PE_PORT_PCID_CTRL [27].PCID_EXT[19:12]	8
0xBB00CF20	PE_PORT_PCID_CTRL [27].PCID_BASE[11:0]	12
0xBB00CF24	PE_PORT_PCID_CTRL [28].RESERVED[31:25]	7
0xBB00CF24	PE_PORT_PCID_CTRL [28].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF24	PE_PORT_PCID_CTRL [28].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF24	PE_PORT_PCID_CTRL [28].USE_DEFAULT[21:21]	1
0xBB00CF24	PE_PORT_PCID_CTRL [28].BASE_PE[20:20]	1
0xBB00CF24	PE_PORT_PCID_CTRL [28].PCID_EXT[19:12]	8
0xBB00CF24	PE_PORT_PCID_CTRL [28].PCID_BASE[11:0]	12
0xBB00CF28	PE_PORT_PCID_CTRL [29].RESERVED[31:25]	7
0xBB00CF28	PE_PORT_PCID_CTRL [29].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF28	PE_PORT_PCID_CTRL [29].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF28	PE_PORT_PCID_CTRL [29].USE_DEFAULT[21:21]	1
0xBB00CF28	PE_PORT_PCID_CTRL [29].BASE_PE[20:20]	1
0xBB00CF28	PE_PORT_PCID_CTRL [29].PCID_EXT[19:12]	8
0xBB00CF28	PE_PORT_PCID_CTRL [29].PCID_BASE[11:0]	12
0xBB00CF2C	PE_PORT_PCID_CTRL [30].RESERVED[31:25]	7
0xBB00CF2C	PE_PORT_PCID_CTRL [30].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF2C	PE_PORT_PCID_CTRL [30].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF2C	PE_PORT_PCID_CTRL [30].USE_DEFAULT[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CF2C	PE_PORT_PCID_CTRL [30].BASE_PE[20:20]	1
0xBB00CF2C	PE_PORT_PCID_CTRL [30].PCID_EXT[19:12]	8
0xBB00CF2C	PE_PORT_PCID_CTRL [30].PCID_BASE[11:0]	12
0xBB00CF30	PE_PORT_PCID_CTRL [31].RESERVED[31:25]	7
0xBB00CF30	PE_PORT_PCID_CTRL [31].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF30	PE_PORT_PCID_CTRL [31].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF30	PE_PORT_PCID_CTRL [31].USE_DEFAULT[21:21]	1
0xBB00CF30	PE_PORT_PCID_CTRL [31].BASE_PE[20:20]	1
0xBB00CF30	PE_PORT_PCID_CTRL [31].PCID_EXT[19:12]	8
0xBB00CF30	PE_PORT_PCID_CTRL [31].PCID_BASE[11:0]	12
0xBB00CF34	PE_PORT_PCID_CTRL [32].RESERVED[31:25]	7
0xBB00CF34	PE_PORT_PCID_CTRL [32].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF34	PE_PORT_PCID_CTRL [32].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF34	PE_PORT_PCID_CTRL [32].USE_DEFAULT[21:21]	1
0xBB00CF34	PE_PORT_PCID_CTRL [32].BASE_PE[20:20]	1
0xBB00CF34	PE_PORT_PCID_CTRL [32].PCID_EXT[19:12]	8
0xBB00CF34	PE_PORT_PCID_CTRL [32].PCID_BASE[11:0]	12
0xBB00CF38	PE_PORT_PCID_CTRL [33].RESERVED[31:25]	7
0xBB00CF38	PE_PORT_PCID_CTRL [33].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF38	PE_PORT_PCID_CTRL [33].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF38	PE_PORT_PCID_CTRL [33].USE_DEFAULT[21:21]	1
0xBB00CF38	PE_PORT_PCID_CTRL [33].BASE_PE[20:20]	1
0xBB00CF38	PE_PORT_PCID_CTRL [33].PCID_EXT[19:12]	8
0xBB00CF38	PE_PORT_PCID_CTRL [33].PCID_BASE[11:0]	12
0xBB00CF3C	PE_PORT_PCID_CTRL [34].RESERVED[31:25]	7
0xBB00CF3C	PE_PORT_PCID_CTRL [34].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF3C	PE_PORT_PCID_CTRL [34].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF3C	PE_PORT_PCID_CTRL [34].USE_DEFAULT[21:21]	1
0xBB00CF3C	PE_PORT_PCID_CTRL [34].BASE_PE[20:20]	1
0xBB00CF3C	PE_PORT_PCID_CTRL [34].PCID_EXT[19:12]	8
0xBB00CF3C	PE_PORT_PCID_CTRL [34].PCID_BASE[11:0]	12
0xBB00CF40	PE_PORT_PCID_CTRL [35].RESERVED[31:25]	7
0xBB00CF40	PE_PORT_PCID_CTRL [35].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF40	PE_PORT_PCID_CTRL [35].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF40	PE_PORT_PCID_CTRL [35].USE_DEFAULT[21:21]	1
0xBB00CF40	PE_PORT_PCID_CTRL [35].BASE_PE[20:20]	1
0xBB00CF40	PE_PORT_PCID_CTRL [35].PCID_EXT[19:12]	8
0xBB00CF40	PE_PORT_PCID_CTRL [35].PCID_BASE[11:0]	12
0xBB00CF44	PE_PORT_PCID_CTRL [36].RESERVED[31:25]	7
0xBB00CF44	PE_PORT_PCID_CTRL [36].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF44	PE_PORT_PCID_CTRL [36].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF44	PE_PORT_PCID_CTRL [36].USE_DEFAULT[21:21]	1
0xBB00CF44	PE_PORT_PCID_CTRL [36].BASE_PE[20:20]	1
0xBB00CF44	PE_PORT_PCID_CTRL [36].PCID_EXT[19:12]	8
0xBB00CF44	PE_PORT_PCID_CTRL [36].PCID_BASE[11:0]	12
0xBB00CF48	PE_PORT_PCID_CTRL [37].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CF48	PE_PORT_PCID_CTRL [37].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF48	PE_PORT_PCID_CTRL [37].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF48	PE_PORT_PCID_CTRL [37].USE_DEFAULT[21:21]	1
0xBB00CF48	PE_PORT_PCID_CTRL [37].BASE_PE[20:20]	1
0xBB00CF48	PE_PORT_PCID_CTRL [37].PCID_EXT[19:12]	8
0xBB00CF48	PE_PORT_PCID_CTRL [37].PCID_BASE[11:0]	12
0xBB00CF4C	PE_PORT_PCID_CTRL [38].RESERVED[31:25]	7
0xBB00CF4C	PE_PORT_PCID_CTRL [38].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF4C	PE_PORT_PCID_CTRL [38].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF4C	PE_PORT_PCID_CTRL [38].USE_DEFAULT[21:21]	1
0xBB00CF4C	PE_PORT_PCID_CTRL [38].BASE_PE[20:20]	1
0xBB00CF4C	PE_PORT_PCID_CTRL [38].PCID_EXT[19:12]	8
0xBB00CF4C	PE_PORT_PCID_CTRL [38].PCID_BASE[11:0]	12
0xBB00CF50	PE_PORT_PCID_CTRL [39].RESERVED[31:25]	7
0xBB00CF50	PE_PORT_PCID_CTRL [39].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF50	PE_PORT_PCID_CTRL [39].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF50	PE_PORT_PCID_CTRL [39].USE_DEFAULT[21:21]	1
0xBB00CF50	PE_PORT_PCID_CTRL [39].BASE_PE[20:20]	1
0xBB00CF50	PE_PORT_PCID_CTRL [39].PCID_EXT[19:12]	8
0xBB00CF50	PE_PORT_PCID_CTRL [39].PCID_BASE[11:0]	12
0xBB00CF54	PE_PORT_PCID_CTRL [40].RESERVED[31:25]	7
0xBB00CF54	PE_PORT_PCID_CTRL [40].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF54	PE_PORT_PCID_CTRL [40].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF54	PE_PORT_PCID_CTRL [40].USE_DEFAULT[21:21]	1
0xBB00CF54	PE_PORT_PCID_CTRL [40].BASE_PE[20:20]	1
0xBB00CF54	PE_PORT_PCID_CTRL [40].PCID_EXT[19:12]	8
0xBB00CF54	PE_PORT_PCID_CTRL [40].PCID_BASE[11:0]	12
0xBB00CF58	PE_PORT_PCID_CTRL [41].RESERVED[31:25]	7
0xBB00CF58	PE_PORT_PCID_CTRL [41].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF58	PE_PORT_PCID_CTRL [41].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF58	PE_PORT_PCID_CTRL [41].USE_DEFAULT[21:21]	1
0xBB00CF58	PE_PORT_PCID_CTRL [41].BASE_PE[20:20]	1
0xBB00CF58	PE_PORT_PCID_CTRL [41].PCID_EXT[19:12]	8
0xBB00CF58	PE_PORT_PCID_CTRL [41].PCID_BASE[11:0]	12
0xBB00CF5C	PE_PORT_PCID_CTRL [42].RESERVED[31:25]	7
0xBB00CF5C	PE_PORT_PCID_CTRL [42].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF5C	PE_PORT_PCID_CTRL [42].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF5C	PE_PORT_PCID_CTRL [42].USE_DEFAULT[21:21]	1
0xBB00CF5C	PE_PORT_PCID_CTRL [42].BASE_PE[20:20]	1
0xBB00CF5C	PE_PORT_PCID_CTRL [42].PCID_EXT[19:12]	8
0xBB00CF5C	PE_PORT_PCID_CTRL [42].PCID_BASE[11:0]	12
0xBB00CF60	PE_PORT_PCID_CTRL [43].RESERVED[31:25]	7
0xBB00CF60	PE_PORT_PCID_CTRL [43].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF60	PE_PORT_PCID_CTRL [43].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF60	PE_PORT_PCID_CTRL [43].USE_DEFAULT[21:21]	1
0xBB00CF60	PE_PORT_PCID_CTRL [43].BASE_PE[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CF60	PE_PORT_PCID_CTRL [43].PCID_EXT[19:12]	8
0xBB00CF60	PE_PORT_PCID_CTRL [43].PCID_BASE[11:0]	12
0xBB00CF64	PE_PORT_PCID_CTRL [44].RESERVED[31:25]	7
0xBB00CF64	PE_PORT_PCID_CTRL [44].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF64	PE_PORT_PCID_CTRL [44].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF64	PE_PORT_PCID_CTRL [44].USE_DEFAULT[21:21]	1
0xBB00CF64	PE_PORT_PCID_CTRL [44].BASE_PE[20:20]	1
0xBB00CF64	PE_PORT_PCID_CTRL [44].PCID_EXT[19:12]	8
0xBB00CF64	PE_PORT_PCID_CTRL [44].PCID_BASE[11:0]	12
0xBB00CF68	PE_PORT_PCID_CTRL [45].RESERVED[31:25]	7
0xBB00CF68	PE_PORT_PCID_CTRL [45].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF68	PE_PORT_PCID_CTRL [45].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF68	PE_PORT_PCID_CTRL [45].USE_DEFAULT[21:21]	1
0xBB00CF68	PE_PORT_PCID_CTRL [45].BASE_PE[20:20]	1
0xBB00CF68	PE_PORT_PCID_CTRL [45].PCID_EXT[19:12]	8
0xBB00CF68	PE_PORT_PCID_CTRL [45].PCID_BASE[11:0]	12
0xBB00CF6C	PE_PORT_PCID_CTRL [46].RESERVED[31:25]	7
0xBB00CF6C	PE_PORT_PCID_CTRL [46].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF6C	PE_PORT_PCID_CTRL [46].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF6C	PE_PORT_PCID_CTRL [46].USE_DEFAULT[21:21]	1
0xBB00CF6C	PE_PORT_PCID_CTRL [46].BASE_PE[20:20]	1
0xBB00CF6C	PE_PORT_PCID_CTRL [46].PCID_EXT[19:12]	8
0xBB00CF6C	PE_PORT_PCID_CTRL [46].PCID_BASE[11:0]	12
0xBB00CF70	PE_PORT_PCID_CTRL [47].RESERVED[31:25]	7
0xBB00CF70	PE_PORT_PCID_CTRL [47].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF70	PE_PORT_PCID_CTRL [47].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF70	PE_PORT_PCID_CTRL [47].USE_DEFAULT[21:21]	1
0xBB00CF70	PE_PORT_PCID_CTRL [47].BASE_PE[20:20]	1
0xBB00CF70	PE_PORT_PCID_CTRL [47].PCID_EXT[19:12]	8
0xBB00CF70	PE_PORT_PCID_CTRL [47].PCID_BASE[11:0]	12
0xBB00CF74	PE_PORT_PCID_CTRL [48].RESERVED[31:25]	7
0xBB00CF74	PE_PORT_PCID_CTRL [48].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF74	PE_PORT_PCID_CTRL [48].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF74	PE_PORT_PCID_CTRL [48].USE_DEFAULT[21:21]	1
0xBB00CF74	PE_PORT_PCID_CTRL [48].BASE_PE[20:20]	1
0xBB00CF74	PE_PORT_PCID_CTRL [48].PCID_EXT[19:12]	8
0xBB00CF74	PE_PORT_PCID_CTRL [48].PCID_BASE[11:0]	12
0xBB00CF78	PE_PORT_PCID_CTRL [49].RESERVED[31:25]	7
0xBB00CF78	PE_PORT_PCID_CTRL [49].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF78	PE_PORT_PCID_CTRL [49].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF78	PE_PORT_PCID_CTRL [49].USE_DEFAULT[21:21]	1
0xBB00CF78	PE_PORT_PCID_CTRL [49].BASE_PE[20:20]	1
0xBB00CF78	PE_PORT_PCID_CTRL [49].PCID_EXT[19:12]	8
0xBB00CF78	PE_PORT_PCID_CTRL [49].PCID_BASE[11:0]	12
0xBB00CF7C	PE_PORT_PCID_CTRL [50].RESERVED[31:25]	7
0xBB00CF7C	PE_PORT_PCID_CTRL [50].ECID_EQ_PCID_ACT[24:23]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CF7C	PE_PORT_PCID_CTRL [50].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF7C	PE_PORT_PCID_CTRL [50].USE_DEFAULT[21:21]	1
0xBB00CF7C	PE_PORT_PCID_CTRL [50].BASE_PE[20:20]	1
0xBB00CF7C	PE_PORT_PCID_CTRL [50].PCID_EXT[19:12]	8
0xBB00CF7C	PE_PORT_PCID_CTRL [50].PCID_BASE[11:0]	12
0xBB00CF80	PE_PORT_PCID_CTRL [51].RESERVED[31:25]	7
0xBB00CF80	PE_PORT_PCID_CTRL [51].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF80	PE_PORT_PCID_CTRL [51].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF80	PE_PORT_PCID_CTRL [51].USE_DEFAULT[21:21]	1
0xBB00CF80	PE_PORT_PCID_CTRL [51].BASE_PE[20:20]	1
0xBB00CF80	PE_PORT_PCID_CTRL [51].PCID_EXT[19:12]	8
0xBB00CF80	PE_PORT_PCID_CTRL [51].PCID_BASE[11:0]	12
0xBB00CF84	PE_PORT_PCID_CTRL [52].RESERVED[31:25]	7
0xBB00CF84	PE_PORT_PCID_CTRL [52].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF84	PE_PORT_PCID_CTRL [52].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF84	PE_PORT_PCID_CTRL [52].USE_DEFAULT[21:21]	1
0xBB00CF84	PE_PORT_PCID_CTRL [52].BASE_PE[20:20]	1
0xBB00CF84	PE_PORT_PCID_CTRL [52].PCID_EXT[19:12]	8
0xBB00CF84	PE_PORT_PCID_CTRL [52].PCID_BASE[11:0]	12
0xBB00CF88	PE_PORT_PCID_CTRL [53].RESERVED[31:25]	7
0xBB00CF88	PE_PORT_PCID_CTRL [53].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF88	PE_PORT_PCID_CTRL [53].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF88	PE_PORT_PCID_CTRL [53].USE_DEFAULT[21:21]	1
0xBB00CF88	PE_PORT_PCID_CTRL [53].BASE_PE[20:20]	1
0xBB00CF88	PE_PORT_PCID_CTRL [53].PCID_EXT[19:12]	8
0xBB00CF88	PE_PORT_PCID_CTRL [53].PCID_BASE[11:0]	12
0xBB00CF8C	PE_PORT_PCID_CTRL [54].RESERVED[31:25]	7
0xBB00CF8C	PE_PORT_PCID_CTRL [54].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF8C	PE_PORT_PCID_CTRL [54].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF8C	PE_PORT_PCID_CTRL [54].USE_DEFAULT[21:21]	1
0xBB00CF8C	PE_PORT_PCID_CTRL [54].BASE_PE[20:20]	1
0xBB00CF8C	PE_PORT_PCID_CTRL [54].PCID_EXT[19:12]	8
0xBB00CF8C	PE_PORT_PCID_CTRL [54].PCID_BASE[11:0]	12
0xBB00CF90	PE_PORT_PCID_CTRL [55].RESERVED[31:25]	7
0xBB00CF90	PE_PORT_PCID_CTRL [55].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF90	PE_PORT_PCID_CTRL [55].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF90	PE_PORT_PCID_CTRL [55].USE_DEFAULT[21:21]	1
0xBB00CF90	PE_PORT_PCID_CTRL [55].BASE_PE[20:20]	1
0xBB00CF90	PE_PORT_PCID_CTRL [55].PCID_EXT[19:12]	8
0xBB00CF90	PE_PORT_PCID_CTRL [55].PCID_BASE[11:0]	12
0xBB00CF94	PE_PORT_PCID_CTRL [56].RESERVED[31:25]	7
0xBB00CF94	PE_PORT_PCID_CTRL [56].ECID_EQ_PCID_ACT[24:23]	2
0xBB00CF94	PE_PORT_PCID_CTRL [56].PE_SRC_PORT_FLTR[22:22]	1
0xBB00CF94	PE_PORT_PCID_CTRL [56].USE_DEFAULT[21:21]	1
0xBB00CF94	PE_PORT_PCID_CTRL [56].BASE_PE[20:20]	1
0xBB00CF94	PE_PORT_PCID_CTRL [56].PCID_EXT[19:12]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00CF94	PE_PORT_PCID_CTRL [56].PCID_BASE[11:0]	12
0xBB004194	PIE_BLK_GROUP_CTRL [0].RESERVED[31:9]	23
0xBB004194	PIE_BLK_GROUP_CTRL [0].GRP_ID[8:5]	4
0xBB004194	PIE_BLK_GROUP_CTRL [0].LOGIC_ID[4:0]	5
0xBB004198	PIE_BLK_GROUP_CTRL [1].RESERVED[31:9]	23
0xBB004198	PIE_BLK_GROUP_CTRL [1].GRP_ID[8:5]	4
0xBB004198	PIE_BLK_GROUP_CTRL [1].LOGIC_ID[4:0]	5
0xBB00419C	PIE_BLK_GROUP_CTRL [2].RESERVED[31:9]	23
0xBB00419C	PIE_BLK_GROUP_CTRL [2].GRP_ID[8:5]	4
0xBB00419C	PIE_BLK_GROUP_CTRL [2].LOGIC_ID[4:0]	5
0xBB0041A0	PIE_BLK_GROUP_CTRL [3].RESERVED[31:9]	23
0xBB0041A0	PIE_BLK_GROUP_CTRL [3].GRP_ID[8:5]	4
0xBB0041A0	PIE_BLK_GROUP_CTRL [3].LOGIC_ID[4:0]	5
0xBB0041A4	PIE_BLK_GROUP_CTRL [4].RESERVED[31:9]	23
0xBB0041A4	PIE_BLK_GROUP_CTRL [4].GRP_ID[8:5]	4
0xBB0041A4	PIE_BLK_GROUP_CTRL [4].LOGIC_ID[4:0]	5
0xBB0041A8	PIE_BLK_GROUP_CTRL [5].RESERVED[31:9]	23
0xBB0041A8	PIE_BLK_GROUP_CTRL [5].GRP_ID[8:5]	4
0xBB0041A8	PIE_BLK_GROUP_CTRL [5].LOGIC_ID[4:0]	5
0xBB0041AC	PIE_BLK_GROUP_CTRL [6].RESERVED[31:9]	23
0xBB0041AC	PIE_BLK_GROUP_CTRL [6].GRP_ID[8:5]	4
0xBB0041AC	PIE_BLK_GROUP_CTRL [6].LOGIC_ID[4:0]	5
0xBB0041B0	PIE_BLK_GROUP_CTRL [7].RESERVED[31:9]	23
0xBB0041B0	PIE_BLK_GROUP_CTRL [7].GRP_ID[8:5]	4
0xBB0041B0	PIE_BLK_GROUP_CTRL [7].LOGIC_ID[4:0]	5
0xBB0041B4	PIE_BLK_GROUP_CTRL [8].RESERVED[31:9]	23
0xBB0041B4	PIE_BLK_GROUP_CTRL [8].GRP_ID[8:5]	4
0xBB0041B4	PIE_BLK_GROUP_CTRL [8].LOGIC_ID[4:0]	5
0xBB0041B8	PIE_BLK_GROUP_CTRL [9].RESERVED[31:9]	23
0xBB0041B8	PIE_BLK_GROUP_CTRL [9].GRP_ID[8:5]	4
0xBB0041B8	PIE_BLK_GROUP_CTRL [9].LOGIC_ID[4:0]	5
0xBB0041BC	PIE_BLK_GROUP_CTRL [10].RESERVED[31:9]	23
0xBB0041BC	PIE_BLK_GROUP_CTRL [10].GRP_ID[8:5]	4
0xBB0041BC	PIE_BLK_GROUP_CTRL [10].LOGIC_ID[4:0]	5
0xBB0041C0	PIE_BLK_GROUP_CTRL [11].RESERVED[31:9]	23
0xBB0041C0	PIE_BLK_GROUP_CTRL [11].GRP_ID[8:5]	4
0xBB0041C0	PIE_BLK_GROUP_CTRL [11].LOGIC_ID[4:0]	5
0xBB0041C4	PIE_BLK_GROUP_CTRL [12].RESERVED[31:9]	23
0xBB0041C4	PIE_BLK_GROUP_CTRL [12].GRP_ID[8:5]	4
0xBB0041C4	PIE_BLK_GROUP_CTRL [12].LOGIC_ID[4:0]	5
0xBB0041C8	PIE_BLK_GROUP_CTRL [13].RESERVED[31:9]	23
0xBB0041C8	PIE_BLK_GROUP_CTRL [13].GRP_ID[8:5]	4
0xBB0041C8	PIE_BLK_GROUP_CTRL [13].LOGIC_ID[4:0]	5
0xBB0041CC	PIE_BLK_GROUP_CTRL [14].RESERVED[31:9]	23
0xBB0041CC	PIE_BLK_GROUP_CTRL [14].GRP_ID[8:5]	4
0xBB0041CC	PIE_BLK_GROUP_CTRL [14].LOGIC_ID[4:0]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0041D0	PIE_BLK_GROUP_CTRL [15].RESERVED[31:9]	23
0xBB0041D0	PIE_BLK_GROUP_CTRL [15].GRP_ID[8:5]	4
0xBB0041D0	PIE_BLK_GROUP_CTRL [15].LOGIC_ID[4:0]	5
0xBB0041D4	PIE_BLK_GROUP_CTRL [16].RESERVED[31:9]	23
0xBB0041D4	PIE_BLK_GROUP_CTRL [16].GRP_ID[8:5]	4
0xBB0041D4	PIE_BLK_GROUP_CTRL [16].LOGIC_ID[4:0]	5
0xBB0041D8	PIE_BLK_GROUP_CTRL [17].RESERVED[31:9]	23
0xBB0041D8	PIE_BLK_GROUP_CTRL [17].GRP_ID[8:5]	4
0xBB0041D8	PIE_BLK_GROUP_CTRL [17].LOGIC_ID[4:0]	5
0xBB0041DC	PIE_BLK_GROUP_CTRL [18].RESERVED[31:9]	23
0xBB0041DC	PIE_BLK_GROUP_CTRL [18].GRP_ID[8:5]	4
0xBB0041DC	PIE_BLK_GROUP_CTRL [18].LOGIC_ID[4:0]	5
0xBB0041E0	PIE_BLK_GROUP_CTRL [19].RESERVED[31:9]	23
0xBB0041E0	PIE_BLK_GROUP_CTRL [19].GRP_ID[8:5]	4
0xBB0041E0	PIE_BLK_GROUP_CTRL [19].LOGIC_ID[4:0]	5
0xBB0041E4	PIE_BLK_GROUP_CTRL [20].RESERVED[31:9]	23
0xBB0041E4	PIE_BLK_GROUP_CTRL [20].GRP_ID[8:5]	4
0xBB0041E4	PIE_BLK_GROUP_CTRL [20].LOGIC_ID[4:0]	5
0xBB0041E8	PIE_BLK_GROUP_CTRL [21].RESERVED[31:9]	23
0xBB0041E8	PIE_BLK_GROUP_CTRL [21].GRP_ID[8:5]	4
0xBB0041E8	PIE_BLK_GROUP_CTRL [21].LOGIC_ID[4:0]	5
0xBB0041EC	PIE_BLK_GROUP_CTRL [22].RESERVED[31:9]	23
0xBB0041FC	PIE_BLK_GROUP_CTRL [22].GRP_ID[8:5]	4
0xBB0041EC	PIE_BLK_GROUP_CTRL [22].LOGIC_ID[4:0]	5
0xBB0041F0	PIE_BLK_GROUP_CTRL [23].RESERVED[31:9]	23
0xBB0041F0	PIE_BLK_GROUP_CTRL [23].GRP_ID[8:5]	4
0xBB0041F0	PIE_BLK_GROUP_CTRL [23].LOGIC_ID[4:0]	5
0xBB0041F4	PIE_BLK_GROUP_CTRL [24].RESERVED[31:9]	23
0xBB0041F4	PIE_BLK_GROUP_CTRL [24].GRP_ID[8:5]	4
0xBB0041F4	PIE_BLK_GROUP_CTRL [24].LOGIC_ID[4:0]	5
0xBB0041F8	PIE_BLK_GROUP_CTRL [25].RESERVED[31:9]	23
0xBB0041F8	PIE_BLK_GROUP_CTRL [25].GRP_ID[8:5]	4
0xBB0041F8	PIE_BLK_GROUP_CTRL [25].LOGIC_ID[4:0]	5
0xBB0041FC	PIE_BLK_GROUP_CTRL [26].RESERVED[31:9]	23
0xBB0041FC	PIE_BLK_GROUP_CTRL [26].GRP_ID[8:5]	4
0xBB0041FC	PIE_BLK_GROUP_CTRL [26].LOGIC_ID[4:0]	5
0xBB004200	PIE_BLK_GROUP_CTRL [27].RESERVED[31:9]	23
0xBB004200	PIE_BLK_GROUP_CTRL [27].GRP_ID[8:5]	4
0xBB004200	PIE_BLK_GROUP_CTRL [27].LOGIC_ID[4:0]	5
0xBB004204	PIE_BLK_GROUP_CTRL [28].RESERVED[31:9]	23
0xBB004204	PIE_BLK_GROUP_CTRL [28].GRP_ID[8:5]	4
0xBB004204	PIE_BLK_GROUP_CTRL [28].LOGIC_ID[4:0]	5
0xBB004208	PIE_BLK_GROUP_CTRL [29].RESERVED[31:9]	23
0xBB004208	PIE_BLK_GROUP_CTRL [29].GRP_ID[8:5]	4
0xBB004208	PIE_BLK_GROUP_CTRL [29].LOGIC_ID[4:0]	5
0xBB00420C	PIE_BLK_GROUP_CTRL [30].RESERVED[31:9]	23

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00420C	PIE_BLK_GROUP_CTRL [30].GRP_ID[8:5]	4
0xBB00420C	PIE_BLK_GROUP_CTRL [30].LOGIC_ID[4:0]	5
0xBB004210	PIE_BLK_GROUP_CTRL [31].RESERVED[31:9]	23
0xBB004210	PIE_BLK_GROUP_CTRL [31].GRP_ID[8:5]	4
0xBB004210	PIE_BLK_GROUP_CTRL [31].LOGIC_ID[4:0]	5
0xBB004180	PIE_BLK_LOOKUP_CTRL [0].LOOKUP_EN[0:0]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [1].LOOKUP_EN[1:1]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [2].LOOKUP_EN[2:2]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [3].LOOKUP_EN[3:3]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [4].LOOKUP_EN[4:4]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [5].LOOKUP_EN[5:5]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [6].LOOKUP_EN[6:6]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [7].LOOKUP_EN[7:7]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [8].LOOKUP_EN[8:8]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [9].LOOKUP_EN[9:9]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [10].LOOKUP_EN[10:10]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [11].LOOKUP_EN[11:11]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [12].LOOKUP_EN[12:12]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [13].LOOKUP_EN[13:13]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [14].LOOKUP_EN[14:14]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [15].LOOKUP_EN[15:15]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [16].LOOKUP_EN[16:16]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [17].LOOKUP_EN[17:17]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [18].LOOKUP_EN[18:18]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [19].LOOKUP_EN[19:19]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [20].LOOKUP_EN[20:20]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [21].LOOKUP_EN[21:21]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [22].LOOKUP_EN[22:22]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [23].LOOKUP_EN[23:23]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [24].LOOKUP_EN[24:24]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [25].LOOKUP_EN[25:25]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [26].LOOKUP_EN[26:26]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [27].LOOKUP_EN[27:27]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [28].LOOKUP_EN[28:28]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [29].LOOKUP_EN[29:29]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [30].LOOKUP_EN[30:30]	1
0xBB004180	PIE_BLK_LOOKUP_CTRL [31].LOOKUP_EN[31:31]	1
0xBB004184	PIE_BLK_PHASE_CTRL [0].PHASE[2:0]	3
0xBB004184	PIE_BLK_PHASE_CTRL [1].PHASE[5:3]	3
0xBB004184	PIE_BLK_PHASE_CTRL [2].PHASE[8:6]	3
0xBB004184	PIE_BLK_PHASE_CTRL [3].PHASE[11:9]	3
0xBB004184	PIE_BLK_PHASE_CTRL [4].PHASE[14:12]	3
0xBB004184	PIE_BLK_PHASE_CTRL [5].PHASE[17:15]	3
0xBB004184	PIE_BLK_PHASE_CTRL [6].PHASE[20:18]	3
0xBB004184	PIE_BLK_PHASE_CTRL [7].PHASE[23:21]	3
0xBB004184	PIE_BLK_PHASE_CTRL [8].PHASE[26:24]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004184	PIE_BLK_PHASE_CTRL [9].PHASE[29:27]	3
0xBB004188	PIE_BLK_PHASE_CTRL [10].PHASE[2:0]	3
0xBB004188	PIE_BLK_PHASE_CTRL [11].PHASE[5:3]	3
0xBB004188	PIE_BLK_PHASE_CTRL [12].PHASE[8:6]	3
0xBB004188	PIE_BLK_PHASE_CTRL [13].PHASE[11:9]	3
0xBB004188	PIE_BLK_PHASE_CTRL [14].PHASE[14:12]	3
0xBB004188	PIE_BLK_PHASE_CTRL [15].PHASE[17:15]	3
0xBB004188	PIE_BLK_PHASE_CTRL [16].PHASE[20:18]	3
0xBB004188	PIE_BLK_PHASE_CTRL [17].PHASE[23:21]	3
0xBB004188	PIE_BLK_PHASE_CTRL [18].PHASE[26:24]	3
0xBB004188	PIE_BLK_PHASE_CTRL [19].PHASE[29:27]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [20].PHASE[2:0]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [21].PHASE[5:3]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [22].PHASE[8:6]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [23].PHASE[11:9]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [24].PHASE[14:12]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [25].PHASE[17:15]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [26].PHASE[20:18]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [27].PHASE[23:21]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [28].PHASE[26:24]	3
0xBB00418C	PIE_BLK_PHASE_CTRL [29].PHASE[29:27]	3
0xBB004190	PIE_BLK_PHASE_CTRL [30].PHASE[2:0]	3
0xBB004190	PIE_BLK_PHASE_CTRL [31].PHASE[5:3]	3
0xBB004214	PIE_BLK_TMPLTE_CTRL [0].RESERVED[31:8]	24
0xBB004214	PIE_BLK_TMPLTE_CTRL [0].BLK_TMPLTE2[7:4]	4
0xBB004214	PIE_BLK_TMPLTE_CTRL [0].BLK_TMPLTE1[3:0]	4
0xBB004218	PIE_BLK_TMPLTE_CTRL [1].RESERVED[31:8]	24
0xBB004218	PIE_BLK_TMPLTE_CTRL [1].BLK_TMPLTE2[7:4]	4
0xBB004218	PIE_BLK_TMPLTE_CTRL [1].BLK_TMPLTE1[3:0]	4
0xBB00421C	PIE_BLK_TMPLTE_CTRL [2].RESERVED[31:8]	24
0xBB00421C	PIE_BLK_TMPLTE_CTRL [2].BLK_TMPLTE2[7:4]	4
0xBB00421C	PIE_BLK_TMPLTE_CTRL [2].BLK_TMPLTE1[3:0]	4
0xBB004220	PIE_BLK_TMPLTE_CTRL [3].RESERVED[31:8]	24
0xBB004220	PIE_BLK_TMPLTE_CTRL [3].BLK_TMPLTE2[7:4]	4
0xBB004220	PIE_BLK_TMPLTE_CTRL [3].BLK_TMPLTE1[3:0]	4
0xBB004224	PIE_BLK_TMPLTE_CTRL [4].RESERVED[31:8]	24
0xBB004224	PIE_BLK_TMPLTE_CTRL [4].BLK_TMPLTE2[7:4]	4
0xBB004224	PIE_BLK_TMPLTE_CTRL [4].BLK_TMPLTE1[3:0]	4
0xBB004228	PIE_BLK_TMPLTE_CTRL [5].RESERVED[31:8]	24
0xBB004228	PIE_BLK_TMPLTE_CTRL [5].BLK_TMPLTE2[7:4]	4
0xBB004228	PIE_BLK_TMPLTE_CTRL [5].BLK_TMPLTE1[3:0]	4
0xBB00422C	PIE_BLK_TMPLTE_CTRL [6].RESERVED[31:8]	24
0xBB00422C	PIE_BLK_TMPLTE_CTRL [6].BLK_TMPLTE2[7:4]	4
0xBB00422C	PIE_BLK_TMPLTE_CTRL [6].BLK_TMPLTE1[3:0]	4
0xBB004230	PIE_BLK_TMPLTE_CTRL [7].RESERVED[31:8]	24
0xBB004230	PIE_BLK_TMPLTE_CTRL [7].BLK_TMPLTE2[7:4]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004230	PIE_BLK_TMPLTE_CTRL [7].BLK_TMPLTE1[3:0]	4
0xBB004234	PIE_BLK_TMPLTE_CTRL [8].RESERVED[31:8]	24
0xBB004234	PIE_BLK_TMPLTE_CTRL [8].BLK_TMPLTE2[7:4]	4
0xBB004234	PIE_BLK_TMPLTE_CTRL [8].BLK_TMPLTE1[3:0]	4
0xBB004238	PIE_BLK_TMPLTE_CTRL [9].RESERVED[31:8]	24
0xBB004238	PIE_BLK_TMPLTE_CTRL [9].BLK_TMPLTE2[7:4]	4
0xBB004238	PIE_BLK_TMPLTE_CTRL [9].BLK_TMPLTE1[3:0]	4
0xBB00423C	PIE_BLK_TMPLTE_CTRL [10].RESERVED[31:8]	24
0xBB00423C	PIE_BLK_TMPLTE_CTRL [10].BLK_TMPLTE2[7:4]	4
0xBB00423C	PIE_BLK_TMPLTE_CTRL [10].BLK_TMPLTE1[3:0]	4
0xBB004240	PIE_BLK_TMPLTE_CTRL [11].RESERVED[31:8]	24
0xBB004240	PIE_BLK_TMPLTE_CTRL [11].BLK_TMPLTE2[7:4]	4
0xBB004240	PIE_BLK_TMPLTE_CTRL [11].BLK_TMPLTE1[3:0]	4
0xBB004244	PIE_BLK_TMPLTE_CTRL [12].RESERVED[31:8]	24
0xBB004244	PIE_BLK_TMPLTE_CTRL [12].BLK_TMPLTE2[7:4]	4
0xBB004244	PIE_BLK_TMPLTE_CTRL [12].BLK_TMPLTE1[3:0]	4
0xBB004248	PIE_BLK_TMPLTE_CTRL [13].RESERVED[31:8]	24
0xBB004248	PIE_BLK_TMPLTE_CTRL [13].BLK_TMPLTE2[7:4]	4
0xBB004248	PIE_BLK_TMPLTE_CTRL [13].BLK_TMPLTE1[3:0]	4
0xBB00424C	PIE_BLK_TMPLTE_CTRL [14].RESERVED[31:8]	24
0xBB00424C	PIE_BLK_TMPLTE_CTRL [14].BLK_TMPLTE2[7:4]	4
0xBB00424C	PIE_BLK_TMPLTE_CTRL [14].BLK_TMPLTE1[3:0]	4
0xBB004250	PIE_BLK_TMPLTE_CTRL [15].RESERVED[31:8]	24
0xBB004250	PIE_BLK_TMPLTE_CTRL [15].BLK_TMPLTE2[7:4]	4
0xBB004250	PIE_BLK_TMPLTE_CTRL [15].BLK_TMPLTE1[3:0]	4
0xBB004254	PIE_BLK_TMPLTE_CTRL [16].RESERVED[31:8]	24
0xBB004254	PIE_BLK_TMPLTE_CTRL [16].BLK_TMPLTE2[7:4]	4
0xBB004254	PIE_BLK_TMPLTE_CTRL [16].BLK_TMPLTE1[3:0]	4
0xBB004258	PIE_BLK_TMPLTE_CTRL [17].RESERVED[31:8]	24
0xBB004258	PIE_BLK_TMPLTE_CTRL [17].BLK_TMPLTE2[7:4]	4
0xBB004258	PIE_BLK_TMPLTE_CTRL [17].BLK_TMPLTE1[3:0]	4
0xBB00425C	PIE_BLK_TMPLTE_CTRL [18].RESERVED[31:8]	24
0xBB00425C	PIE_BLK_TMPLTE_CTRL [18].BLK_TMPLTE2[7:4]	4
0xBB00425C	PIE_BLK_TMPLTE_CTRL [18].BLK_TMPLTE1[3:0]	4
0xBB004260	PIE_BLK_TMPLTE_CTRL [19].RESERVED[31:8]	24
0xBB004260	PIE_BLK_TMPLTE_CTRL [19].BLK_TMPLTE2[7:4]	4
0xBB004260	PIE_BLK_TMPLTE_CTRL [19].BLK_TMPLTE1[3:0]	4
0xBB004264	PIE_BLK_TMPLTE_CTRL [20].RESERVED[31:8]	24
0xBB004264	PIE_BLK_TMPLTE_CTRL [20].BLK_TMPLTE2[7:4]	4
0xBB004264	PIE_BLK_TMPLTE_CTRL [20].BLK_TMPLTE1[3:0]	4
0xBB004268	PIE_BLK_TMPLTE_CTRL [21].RESERVED[31:8]	24
0xBB004268	PIE_BLK_TMPLTE_CTRL [21].BLK_TMPLTE2[7:4]	4
0xBB004268	PIE_BLK_TMPLTE_CTRL [21].BLK_TMPLTE1[3:0]	4
0xBB00426C	PIE_BLK_TMPLTE_CTRL [22].RESERVED[31:8]	24
0xBB00426C	PIE_BLK_TMPLTE_CTRL [22].BLK_TMPLTE2[7:4]	4
0xBB00426C	PIE_BLK_TMPLTE_CTRL [22].BLK_TMPLTE1[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004270	PIE_BLK_TMPLTE_CTRL [23].RESERVED[31:8]	24
0xBB004270	PIE_BLK_TMPLTE_CTRL [23].BLK_TMPLTE2[7:4]	4
0xBB004270	PIE_BLK_TMPLTE_CTRL [23].BLK_TMPLTE1[3:0]	4
0xBB004274	PIE_BLK_TMPLTE_CTRL [24].RESERVED[31:8]	24
0xBB004274	PIE_BLK_TMPLTE_CTRL [24].BLK_TMPLTE2[7:4]	4
0xBB004274	PIE_BLK_TMPLTE_CTRL [24].BLK_TMPLTE1[3:0]	4
0xBB004278	PIE_BLK_TMPLTE_CTRL [25].RESERVED[31:8]	24
0xBB004278	PIE_BLK_TMPLTE_CTRL [25].BLK_TMPLTE2[7:4]	4
0xBB004278	PIE_BLK_TMPLTE_CTRL [25].BLK_TMPLTE1[3:0]	4
0xBB00427C	PIE_BLK_TMPLTE_CTRL [26].RESERVED[31:8]	24
0xBB00427C	PIE_BLK_TMPLTE_CTRL [26].BLK_TMPLTE2[7:4]	4
0xBB00427C	PIE_BLK_TMPLTE_CTRL [26].BLK_TMPLTE1[3:0]	4
0xBB004280	PIE_BLK_TMPLTE_CTRL [27].RESERVED[31:8]	24
0xBB004280	PIE_BLK_TMPLTE_CTRL [27].BLK_TMPLTE2[7:4]	4
0xBB004280	PIE_BLK_TMPLTE_CTRL [27].BLK_TMPLTE1[3:0]	4
0xBB004284	PIE_BLK_TMPLTE_CTRL [28].RESERVED[31:8]	24
0xBB004284	PIE_BLK_TMPLTE_CTRL [28].BLK_TMPLTE2[7:4]	4
0xBB004284	PIE_BLK_TMPLTE_CTRL [28].BLK_TMPLTE1[3:0]	4
0xBB004288	PIE_BLK_TMPLTE_CTRL [29].RESERVED[31:8]	24
0xBB004288	PIE_BLK_TMPLTE_CTRL [29].BLK_TMPLTE2[7:4]	4
0xBB004288	PIE_BLK_TMPLTE_CTRL [29].BLK_TMPLTE1[3:0]	4
0xBB00428C	PIE_BLK_TMPLTE_CTRL [30].RESERVED[31:8]	24
0xBB00428C	PIE_BLK_TMPLTE_CTRL [30].BLK_TMPLTE2[7:4]	4
0xBB00428C	PIE_BLK_TMPLTE_CTRL [30].BLK_TMPLTE1[3:0]	4
0xBB004290	PIE_BLK_TMPLTE_CTRL [31].RESERVED[31:8]	24
0xBB004290	PIE_BLK_TMPLTE_CTRL [31].BLK_TMPLTE2[7:4]	4
0xBB004290	PIE_BLK_TMPLTE_CTRL [31].BLK_TMPLTE1[3:0]	4
0xBB0042D8	PIE_CTRL_CTRL.RESERVED[31:25]	7
0xBB0042D8	PIE_CTRL_CTRL.CLR_TO[24:13]	12
0xBB0042D8	PIE_CTRL_CTRL.CLR_FROM[12:1]	12
0xBB0042D8	PIE_CTRL_CTRL.CLR[0:0]	1
0xBB0042DC	PIE_CTRL.RESERVED[31:24]	8
0xBB0042DC	PIE_CTRL_E_TMPLTE0_IOTAG_SEL[23:22]	2
0xBB0042DC	PIE_CTRL_E_TMPLTE1_IOTAG_SEL[21:20]	2
0xBB0042DC	PIE_CTRL_E_TMPLTE2_IOTAG_SEL[19:18]	2
0xBB0042DC	PIE_CTRL_E_TMPLTE4_IOTAG_SEL[17:16]	2
0xBB0042DC	PIE_CTRL_V_TMPLTE0_IOTAG_SEL[15:14]	2
0xBB0042DC	PIE_CTRL_V_TMPLTE1_IOTAG_SEL[13:12]	2
0xBB0042DC	PIE_CTRL_V_TMPLTE2_IOTAG_SEL[11:10]	2
0xBB0042DC	PIE_CTRL_V_TMPLTE4_IOTAG_SEL[9:8]	2
0xBB0042DC	PIE_CTRL_I_TMPLTE0_IOTAG_SEL[7:6]	2
0xBB0042DC	PIE_CTRL_I_TMPLTE1_IOTAG_SEL[5:4]	2
0xBB0042DC	PIE_CTRL_I_TMPLTE2_IOTAG_SEL[3:2]	2
0xBB0042DC	PIE_CTRL_I_TMPLTE4_IOTAG_SEL[1:0]	2
0xBB004F5C	PIE_ENCAP_CTRL.RESERVED[31:1]	31
0xBB004F5C	PIE_ENCAP_CTRL.EGR_MIR_SFLOW_BYPASS_POST[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000530	PIE_FIELD_SELTOR_CTRL [0].RESERVED[31:11]	21
0xBB000530	PIE_FIELD_SELTOR_CTRL [0].PAYLOAD_SEL[10:10]	1
0xBB000530	PIE_FIELD_SELTOR_CTRL [0].OFFSET[9:3]	7
0xBB000530	PIE_FIELD_SELTOR_CTRL [0].FMT[2:0]	3
0xBB000534	PIE_FIELD_SELTOR_CTRL [1].RESERVED[31:11]	21
0xBB000534	PIE_FIELD_SELTOR_CTRL [1].PAYLOAD_SEL[10:10]	1
0xBB000534	PIE_FIELD_SELTOR_CTRL [1].OFFSET[9:3]	7
0xBB000534	PIE_FIELD_SELTOR_CTRL [1].FMT[2:0]	3
0xBB000538	PIE_FIELD_SELTOR_CTRL [2].RESERVED[31:11]	21
0xBB000538	PIE_FIELD_SELTOR_CTRL [2].PAYLOAD_SEL[10:10]	1
0xBB000538	PIE_FIELD_SELTOR_CTRL [2].OFFSET[9:3]	7
0xBB000538	PIE_FIELD_SELTOR_CTRL [2].FMT[2:0]	3
0xBB00053C	PIE_FIELD_SELTOR_CTRL [3].RESERVED[31:11]	21
0xBB00053C	PIE_FIELD_SELTOR_CTRL [3].PAYLOAD_SEL[10:10]	1
0xBB00053C	PIE_FIELD_SELTOR_CTRL [3].OFFSET[9:3]	7
0xBB00053C	PIE_FIELD_SELTOR_CTRL [3].FMT[2:0]	3
0xBB000540	PIE_FIELD_SELTOR_CTRL [4].RESERVED[31:11]	21
0xBB000540	PIE_FIELD_SELTOR_CTRL [4].PAYLOAD_SEL[10:10]	1
0xBB000540	PIE_FIELD_SELTOR_CTRL [4].OFFSET[9:3]	7
0xBB000540	PIE_FIELD_SELTOR_CTRL [4].FMT[2:0]	3
0xBB000544	PIE_FIELD_SELTOR_CTRL [5].RESERVED[31:11]	21
0xBB000544	PIE_FIELD_SELTOR_CTRL [5].PAYLOAD_SEL[10:10]	1
0xBB000544	PIE_FIELD_SELTOR_CTRL [5].OFFSET[9:3]	7
0xBB000544	PIE_FIELD_SELTOR_CTRL [5].FMT[2:0]	3
0xBB000548	PIE_FIELD_SELTOR_CTRL [6].RESERVED[31:11]	21
0xBB000548	PIE_FIELD_SELTOR_CTRL [6].PAYLOAD_SEL[10:10]	1
0xBB000548	PIE_FIELD_SELTOR_CTRL [6].OFFSET[9:3]	7
0xBB000548	PIE_FIELD_SELTOR_CTRL [6].FMT[2:0]	3
0xBB00054C	PIE_FIELD_SELTOR_CTRL [7].RESERVED[31:11]	21
0xBB00054C	PIE_FIELD_SELTOR_CTRL [7].PAYLOAD_SEL[10:10]	1
0xBB00054C	PIE_FIELD_SELTOR_CTRL [7].OFFSET[9:3]	7
0xBB00054C	PIE_FIELD_SELTOR_CTRL [7].FMT[2:0]	3
0xBB000550	PIE_FIELD_SELTOR_CTRL [8].RESERVED[31:11]	21
0xBB000550	PIE_FIELD_SELTOR_CTRL [8].PAYLOAD_SEL[10:10]	1
0xBB000550	PIE_FIELD_SELTOR_CTRL [8].OFFSET[9:3]	7
0xBB000550	PIE_FIELD_SELTOR_CTRL [8].FMT[2:0]	3
0xBB000554	PIE_FIELD_SELTOR_CTRL [9].RESERVED[31:11]	21
0xBB000554	PIE_FIELD_SELTOR_CTRL [9].PAYLOAD_SEL[10:10]	1
0xBB000554	PIE_FIELD_SELTOR_CTRL [9].OFFSET[9:3]	7
0xBB000554	PIE_FIELD_SELTOR_CTRL [9].FMT[2:0]	3
0xBB000558	PIE_FIELD_SELTOR_CTRL [10].RESERVED[31:11]	21
0xBB000558	PIE_FIELD_SELTOR_CTRL [10].PAYLOAD_SEL[10:10]	1
0xBB000558	PIE_FIELD_SELTOR_CTRL [10].OFFSET[9:3]	7
0xBB000558	PIE_FIELD_SELTOR_CTRL [10].FMT[2:0]	3
0xBB00055C	PIE_FIELD_SELTOR_CTRL [11].RESERVED[31:11]	21
0xBB00055C	PIE_FIELD_SELTOR_CTRL [11].PAYLOAD_SEL[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00055C	PIE_FIELD_SELTOR_CTRL [11].OFFSET[9:3]	7
0xBB00055C	PIE_FIELD_SELTOR_CTRL [11].FMT[2:0]	3
0xBB000560	PIE_FIELD_SELTOR_CTRL [12].RESERVED[31:11]	21
0xBB000560	PIE_FIELD_SELTOR_CTRL [12].PAYLOAD_SEL[10:10]	1
0xBB000560	PIE_FIELD_SELTOR_CTRL [12].OFFSET[9:3]	7
0xBB000560	PIE_FIELD_SELTOR_CTRL [12].FMT[2:0]	3
0xBB000564	PIE_FIELD_SELTOR_CTRL [13].RESERVED[31:11]	21
0xBB000564	PIE_FIELD_SELTOR_CTRL [13].PAYLOAD_SEL[10:10]	1
0xBB000564	PIE_FIELD_SELTOR_CTRL [13].OFFSET[9:3]	7
0xBB000564	PIE_FIELD_SELTOR_CTRL [13].FMT[2:0]	3
0xBB0044E4	PIE_GLB_HIT_INDICATION [0].GLB_INDICATION[31:0]	32
0xBB0044E8	PIE_GLB_HIT_INDICATION [1].GLB_INDICATION[31:0]	32
0xBB0044EC	PIE_GLB_HIT_INDICATION [2].GLB_INDICATION[31:0]	32
0xBB0044F0	PIE_GLB_HIT_INDICATION [3].GLB_INDICATION[31:0]	32
0xBB0044F4	PIE_MISC.RESERVED[31:3]	29
0xBB0044F4	PIE_MISC.INTF_SEL[2:2]	1
0xBB0044F4	PIE_MISC.DP_SEL[1:1]	1
0xBB0044F4	PIE_MISC.ARP_MAC_CTRL[0:0]	1
0xBB008008	PIE_MISC2.RESERVED[31:1]	31
0xBB008008	PIE_MISC2.FILTER_8021BR_EN[0:0]	1
0xBB0042D0	PIE_MV_CTRL.RESERVED[31:25]	7
0xBB0042D0	PIE_MV_CTRL.MV_TO[24:13]	12
0xBB0042D0	PIE_MV_CTRL.MV_FROM[12:1]	12
0xBB0042D0	PIE_MV_CTRL.MV[0:0]	1
0xBB0042D4	PIE_MV_LEN_CTRL.RESERVED[31:13]	19
0xBB0042D4	PIE_MV_LEN_CTRL.MV_LEN[12:0]	13
0xBB0042E4	PIE_RULE_HIT_INDICATION [0].RULE_INDICATION[31:0]	32
0xBB0042E8	PIE_RULE_HIT_INDICATION [1].RULE_INDICATION[31:0]	32
0xBB0042EC	PIE_RULE_HIT_INDICATION [2].RULE_INDICATION[31:0]	32
0xBB0042F0	PIE_RULE_HIT_INDICATION [3].RULE_INDICATION[31:0]	32
0xBB0042F4	PIE_RULE_HIT_INDICATION [4].RULE_INDICATION[31:0]	32
0xBB0042F8	PIE_RULE_HIT_INDICATION [5].RULE_INDICATION[31:0]	32
0xBB0042FC	PIE_RULE_HIT_INDICATION [6].RULE_INDICATION[31:0]	32
0xBB004300	PIE_RULE_HIT_INDICATION [7].RULE_INDICATION[31:0]	32
0xBB004304	PIE_RULE_HIT_INDICATION [8].RULE_INDICATION[31:0]	32
0xBB004308	PIE_RULE_HIT_INDICATION [9].RULE_INDICATION[31:0]	32
0xBB00430C	PIE_RULE_HIT_INDICATION [10].RULE_INDICATION[31:0]	32
0xBB004310	PIE_RULE_HIT_INDICATION [11].RULE_INDICATION[31:0]	32
0xBB004314	PIE_RULE_HIT_INDICATION [12].RULE_INDICATION[31:0]	32
0xBB004318	PIE_RULE_HIT_INDICATION [13].RULE_INDICATION[31:0]	32
0xBB00431C	PIE_RULE_HIT_INDICATION [14].RULE_INDICATION[31:0]	32
0xBB004320	PIE_RULE_HIT_INDICATION [15].RULE_INDICATION[31:0]	32
0xBB004324	PIE_RULE_HIT_INDICATION [16].RULE_INDICATION[31:0]	32
0xBB004328	PIE_RULE_HIT_INDICATION [17].RULE_INDICATION[31:0]	32
0xBB00432C	PIE_RULE_HIT_INDICATION [18].RULE_INDICATION[31:0]	32
0xBB004330	PIE_RULE_HIT_INDICATION [19].RULE_INDICATION[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004334	PIE_RULE_HIT_INDICATION [20].RULE_INDICATION[31:0]	32
0xBB004338	PIE_RULE_HIT_INDICATION [21].RULE_INDICATION[31:0]	32
0xBB00433C	PIE_RULE_HIT_INDICATION [22].RULE_INDICATION[31:0]	32
0xBB004340	PIE_RULE_HIT_INDICATION [23].RULE_INDICATION[31:0]	32
0xBB004344	PIE_RULE_HIT_INDICATION [24].RULE_INDICATION[31:0]	32
0xBB004348	PIE_RULE_HIT_INDICATION [25].RULE_INDICATION[31:0]	32
0xBB00434C	PIE_RULE_HIT_INDICATION [26].RULE_INDICATION[31:0]	32
0xBB004350	PIE_RULE_HIT_INDICATION [27].RULE_INDICATION[31:0]	32
0xBB004354	PIE_RULE_HIT_INDICATION [28].RULE_INDICATION[31:0]	32
0xBB004358	PIE_RULE_HIT_INDICATION [29].RULE_INDICATION[31:0]	32
0xBB00435C	PIE_RULE_HIT_INDICATION [30].RULE_INDICATION[31:0]	32
0xBB004360	PIE_RULE_HIT_INDICATION [31].RULE_INDICATION[31:0]	32
0xBB004364	PIE_RULE_HIT_INDICATION [32].RULE_INDICATION[31:0]	32
0xBB004368	PIE_RULE_HIT_INDICATION [33].RULE_INDICATION[31:0]	32
0xBB00436C	PIE_RULE_HIT_INDICATION [34].RULE_INDICATION[31:0]	32
0xBB004370	PIE_RULE_HIT_INDICATION [35].RULE_INDICATION[31:0]	32
0xBB004374	PIE_RULE_HIT_INDICATION [36].RULE_INDICATION[31:0]	32
0xBB004378	PIE_RULE_HIT_INDICATION [37].RULE_INDICATION[31:0]	32
0xBB00437C	PIE_RULE_HIT_INDICATION [38].RULE_INDICATION[31:0]	32
0xBB004380	PIE_RULE_HIT_INDICATION [39].RULE_INDICATION[31:0]	32
0xBB004384	PIE_RULE_HIT_INDICATION [40].RULE_INDICATION[31:0]	32
0xBB004388	PIE_RULE_HIT_INDICATION [41].RULE_INDICATION[31:0]	32
0xBB00438C	PIE_RULE_HIT_INDICATION [42].RULE_INDICATION[31:0]	32
0xBB004390	PIE_RULE_HIT_INDICATION [43].RULE_INDICATION[31:0]	32
0xBB004394	PIE_RULE_HIT_INDICATION [44].RULE_INDICATION[31:0]	32
0xBB004398	PIE_RULE_HIT_INDICATION [45].RULE_INDICATION[31:0]	32
0xBB00439C	PIE_RULE_HIT_INDICATION [46].RULE_INDICATION[31:0]	32
0xBB0043A0	PIE_RULE_HIT_INDICATION [47].RULE_INDICATION[31:0]	32
0xBB0043A4	PIE_RULE_HIT_INDICATION [48].RULE_INDICATION[31:0]	32
0xBB0043A8	PIE_RULE_HIT_INDICATION [49].RULE_INDICATION[31:0]	32
0xBB0043AC	PIE_RULE_HIT_INDICATION [50].RULE_INDICATION[31:0]	32
0xBB0043B0	PIE_RULE_HIT_INDICATION [51].RULE_INDICATION[31:0]	32
0xBB0043B4	PIE_RULE_HIT_INDICATION [52].RULE_INDICATION[31:0]	32
0xBB0043B8	PIE_RULE_HIT_INDICATION [53].RULE_INDICATION[31:0]	32
0xBB0043BC	PIE_RULE_HIT_INDICATION [54].RULE_INDICATION[31:0]	32
0xBB0043C0	PIE_RULE_HIT_INDICATION [55].RULE_INDICATION[31:0]	32
0xBB0043C4	PIE_RULE_HIT_INDICATION [56].RULE_INDICATION[31:0]	32
0xBB0043C8	PIE_RULE_HIT_INDICATION [57].RULE_INDICATION[31:0]	32
0xBB0043CC	PIE_RULE_HIT_INDICATION [58].RULE_INDICATION[31:0]	32
0xBB0043D0	PIE_RULE_HIT_INDICATION [59].RULE_INDICATION[31:0]	32
0xBB0043D4	PIE_RULE_HIT_INDICATION [60].RULE_INDICATION[31:0]	32
0xBB0043D8	PIE_RULE_HIT_INDICATION [61].RULE_INDICATION[31:0]	32
0xBB0043DC	PIE_RULE_HIT_INDICATION [62].RULE_INDICATION[31:0]	32
0xBB0043E0	PIE_RULE_HIT_INDICATION [63].RULE_INDICATION[31:0]	32
0xBB0043E4	PIE_RULE_HIT_INDICATION [64].RULE_INDICATION[31:0]	32
0xBB0043E8	PIE_RULE_HIT_INDICATION [65].RULE_INDICATION[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0043EC	PIE_RULE_HIT_INDICATION [66].RULE_INDICATION[31:0]	32
0xBB0043F0	PIE_RULE_HIT_INDICATION [67].RULE_INDICATION[31:0]	32
0xBB0043F4	PIE_RULE_HIT_INDICATION [68].RULE_INDICATION[31:0]	32
0xBB0043F8	PIE_RULE_HIT_INDICATION [69].RULE_INDICATION[31:0]	32
0xBB0043FC	PIE_RULE_HIT_INDICATION [70].RULE_INDICATION[31:0]	32
0xBB004400	PIE_RULE_HIT_INDICATION [71].RULE_INDICATION[31:0]	32
0xBB004404	PIE_RULE_HIT_INDICATION [72].RULE_INDICATION[31:0]	32
0xBB004408	PIE_RULE_HIT_INDICATION [73].RULE_INDICATION[31:0]	32
0xBB00440C	PIE_RULE_HIT_INDICATION [74].RULE_INDICATION[31:0]	32
0xBB004410	PIE_RULE_HIT_INDICATION [75].RULE_INDICATION[31:0]	32
0xBB004414	PIE_RULE_HIT_INDICATION [76].RULE_INDICATION[31:0]	32
0xBB004418	PIE_RULE_HIT_INDICATION [77].RULE_INDICATION[31:0]	32
0xBB00441C	PIE_RULE_HIT_INDICATION [78].RULE_INDICATION[31:0]	32
0xBB004420	PIE_RULE_HIT_INDICATION [79].RULE_INDICATION[31:0]	32
0xBB004424	PIE_RULE_HIT_INDICATION [80].RULE_INDICATION[31:0]	32
0xBB004428	PIE_RULE_HIT_INDICATION [81].RULE_INDICATION[31:0]	32
0xBB00442C	PIE_RULE_HIT_INDICATION [82].RULE_INDICATION[31:0]	32
0xBB004430	PIE_RULE_HIT_INDICATION [83].RULE_INDICATION[31:0]	32
0xBB004434	PIE_RULE_HIT_INDICATION [84].RULE_INDICATION[31:0]	32
0xBB004438	PIE_RULE_HIT_INDICATION [85].RULE_INDICATION[31:0]	32
0xBB00443C	PIE_RULE_HIT_INDICATION [86].RULE_INDICATION[31:0]	32
0xBB004440	PIE_RULE_HIT_INDICATION [87].RULE_INDICATION[31:0]	32
0xBB004444	PIE_RULE_HIT_INDICATION [88].RULE_INDICATION[31:0]	32
0xBB004448	PIE_RULE_HIT_INDICATION [89].RULE_INDICATION[31:0]	32
0xBB00444C	PIE_RULE_HIT_INDICATION [90].RULE_INDICATION[31:0]	32
0xBB004450	PIE_RULE_HIT_INDICATION [91].RULE_INDICATION[31:0]	32
0xBB004454	PIE_RULE_HIT_INDICATION [92].RULE_INDICATION[31:0]	32
0xBB004458	PIE_RULE_HIT_INDICATION [93].RULE_INDICATION[31:0]	32
0xBB00445C	PIE_RULE_HIT_INDICATION [94].RULE_INDICATION[31:0]	32
0xBB004460	PIE_RULE_HIT_INDICATION [95].RULE_INDICATION[31:0]	32
0xBB004464	PIE_RULE_HIT_INDICATION [96].RULE_INDICATION[31:0]	32
0xBB004468	PIE_RULE_HIT_INDICATION [97].RULE_INDICATION[31:0]	32
0xBB00446C	PIE_RULE_HIT_INDICATION [98].RULE_INDICATION[31:0]	32
0xBB004470	PIE_RULE_HIT_INDICATION [99].RULE_INDICATION[31:0]	32
0xBB004474	PIE_RULE_HIT_INDICATION [100].RULE_INDICATION[31:0]	32
0xBB004478	PIE_RULE_HIT_INDICATION [101].RULE_INDICATION[31:0]	32
0xBB00447C	PIE_RULE_HIT_INDICATION [102].RULE_INDICATION[31:0]	32
0xBB004480	PIE_RULE_HIT_INDICATION [103].RULE_INDICATION[31:0]	32
0xBB004484	PIE_RULE_HIT_INDICATION [104].RULE_INDICATION[31:0]	32
0xBB004488	PIE_RULE_HIT_INDICATION [105].RULE_INDICATION[31:0]	32
0xBB00448C	PIE_RULE_HIT_INDICATION [106].RULE_INDICATION[31:0]	32
0xBB004490	PIE_RULE_HIT_INDICATION [107].RULE_INDICATION[31:0]	32
0xBB004494	PIE_RULE_HIT_INDICATION [108].RULE_INDICATION[31:0]	32
0xBB004498	PIE_RULE_HIT_INDICATION [109].RULE_INDICATION[31:0]	32
0xBB00449C	PIE_RULE_HIT_INDICATION [110].RULE_INDICATION[31:0]	32
0xBB0044A0	PIE_RULE_HIT_INDICATION [111].RULE_INDICATION[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0044A4	PIE_RULE_HIT_INDICATION [112].RULE_INDICATION[31:0]	32
0xBB0044A8	PIE_RULE_HIT_INDICATION [113].RULE_INDICATION[31:0]	32
0xBB0044AC	PIE_RULE_HIT_INDICATION [114].RULE_INDICATION[31:0]	32
0xBB0044B0	PIE_RULE_HIT_INDICATION [115].RULE_INDICATION[31:0]	32
0xBB0044B4	PIE_RULE_HIT_INDICATION [116].RULE_INDICATION[31:0]	32
0xBB0044B8	PIE_RULE_HIT_INDICATION [117].RULE_INDICATION[31:0]	32
0xBB0044BC	PIE_RULE_HIT_INDICATION [118].RULE_INDICATION[31:0]	32
0xBB0044C0	PIE_RULE_HIT_INDICATION [119].RULE_INDICATION[31:0]	32
0xBB0044C4	PIE_RULE_HIT_INDICATION [120].RULE_INDICATION[31:0]	32
0xBB0044C8	PIE_RULE_HIT_INDICATION [121].RULE_INDICATION[31:0]	32
0xBB0044CC	PIE_RULE_HIT_INDICATION [122].RULE_INDICATION[31:0]	32
0xBB0044D0	PIE_RULE_HIT_INDICATION [123].RULE_INDICATION[31:0]	32
0xBB0044D4	PIE_RULE_HIT_INDICATION [124].RULE_INDICATION[31:0]	32
0xBB0044D8	PIE_RULE_HIT_INDICATION [125].RULE_INDICATION[31:0]	32
0xBB0044DC	PIE_RULE_HIT_INDICATION [126].RULE_INDICATION[31:0]	32
0xBB0044E0	PIE_RULE_HIT_INDICATION [127].RULE_INDICATION[31:0]	32
0xBB0042E0	PIE_TAG_CTRL [0].V_TMPLTE_IOTAG_FMT[0:0]	1
0xBB0042E0	PIE_TAG_CTRL [1].V_TMPLTE_IOTAG_FMT[1:1]	1
0xBB0042E0	PIE_TAG_CTRL [2].V_TMPLTE_IOTAG_FMT[2:2]	1
0xBB0042E0	PIE_TAG_CTRL [3].V_TMPLTE_IOTAG_FMT[3:3]	1
0xBB0042E0	PIE_TAG_CTRL [4].V_TMPLTE_IOTAG_FMT[4:4]	1
0xBB0042E0	PIE_TAG_CTRL [5].V_TMPLTE_IOTAG_FMT[5:5]	1
0xBB0042E0	PIE_TAG_CTRL [6].V_TMPLTE_IOTAG_FMT[6:6]	1
0xBB0042E0	PIE_TAG_CTRL [7].V_TMPLTE_IOTAG_FMT[7:7]	1
0xBB0042E0	PIE_TAG_CTRL [8].V_TMPLTE_IOTAG_FMT[8:8]	1
0xBB0042E0	PIE_TAG_CTRL [9].V_TMPLTE_IOTAG_FMT[9:9]	1
0xBB004294	PIE_TMPLTE_CTRL [5][0].TMPLTE_FIELD[5:0]	6
0xBB004294	PIE_TMPLTE_CTRL [5][1].TMPLTE_FIELD[11:6]	6
0xBB004294	PIE_TMPLTE_CTRL [5][2].TMPLTE_FIELD[17:12]	6
0xBB004294	PIE_TMPLTE_CTRL [5][3].TMPLTE_FIELD[23:18]	6
0xBB004294	PIE_TMPLTE_CTRL [5][4].TMPLTE_FIELD[29:24]	6
0xBB004298	PIE_TMPLTE_CTRL [5][5].TMPLTE_FIELD[5:0]	6
0xBB004298	PIE_TMPLTE_CTRL [5][6].TMPLTE_FIELD[11:6]	6
0xBB004298	PIE_TMPLTE_CTRL [5][7].TMPLTE_FIELD[17:12]	6
0xBB004298	PIE_TMPLTE_CTRL [5][8].TMPLTE_FIELD[23:18]	6
0xBB004298	PIE_TMPLTE_CTRL [5][9].TMPLTE_FIELD[29:24]	6
0xBB00429C	PIE_TMPLTE_CTRL [5][10].TMPLTE_FIELD[5:0]	6
0xBB00429C	PIE_TMPLTE_CTRL [5][11].TMPLTE_FIELD[11:6]	6
0xBB00429C	PIE_TMPLTE_CTRL [5][12].TMPLTE_FIELD[17:12]	6
0xBB00429C	PIE_TMPLTE_CTRL [5][13].TMPLTE_FIELD[23:18]	6
0xBB0042A0	PIE_TMPLTE_CTRL [6][0].TMPLTE_FIELD[5:0]	6
0xBB0042A0	PIE_TMPLTE_CTRL [6][1].TMPLTE_FIELD[11:6]	6
0xBB0042A0	PIE_TMPLTE_CTRL [6][2].TMPLTE_FIELD[17:12]	6
0xBB0042A0	PIE_TMPLTE_CTRL [6][3].TMPLTE_FIELD[23:18]	6
0xBB0042A0	PIE_TMPLTE_CTRL [6][4].TMPLTE_FIELD[29:24]	6
0xBB0042A4	PIE_TMPLTE_CTRL [6][5].TMPLTE_FIELD[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0042A4	PIE_TMPLTE_CTRL [6][6].TMPLTE_FIELD[11:6]	6
0xBB0042A4	PIE_TMPLTE_CTRL [6][7].TMPLTE_FIELD[17:12]	6
0xBB0042A4	PIE_TMPLTE_CTRL [6][8].TMPLTE_FIELD[23:18]	6
0xBB0042A4	PIE_TMPLTE_CTRL [6][9].TMPLTE_FIELD[29:24]	6
0xBB0042A8	PIE_TMPLTE_CTRL [6][10].TMPLTE_FIELD[5:0]	6
0xBB0042A8	PIE_TMPLTE_CTRL [6][11].TMPLTE_FIELD[11:6]	6
0xBB0042A8	PIE_TMPLTE_CTRL [6][12].TMPLTE_FIELD[17:12]	6
0xBB0042A8	PIE_TMPLTE_CTRL [6][13].TMPLTE_FIELD[23:18]	6
0xBB0042AC	PIE_TMPLTE_CTRL [7][0].TMPLTE_FIELD[5:0]	6
0xBB0042AC	PIE_TMPLTE_CTRL [7][1].TMPLTE_FIELD[11:6]	6
0xBB0042AC	PIE_TMPLTE_CTRL [7][2].TMPLTE_FIELD[17:12]	6
0xBB0042AC	PIE_TMPLTE_CTRL [7][3].TMPLTE_FIELD[23:18]	6
0xBB0042AC	PIE_TMPLTE_CTRL [7][4].TMPLTE_FIELD[29:24]	6
0xBB0042B0	PIE_TMPLTE_CTRL [7][5].TMPLTE_FIELD[5:0]	6
0xBB0042B0	PIE_TMPLTE_CTRL [7][6].TMPLTE_FIELD[11:6]	6
0xBB0042B0	PIE_TMPLTE_CTRL [7][7].TMPLTE_FIELD[17:12]	6
0xBB0042B0	PIE_TMPLTE_CTRL [7][8].TMPLTE_FIELD[23:18]	6
0xBB0042B0	PIE_TMPLTE_CTRL [7][9].TMPLTE_FIELD[29:24]	6
0xBB0042B4	PIE_TMPLTE_CTRL [7][10].TMPLTE_FIELD[5:0]	6
0xBB0042B4	PIE_TMPLTE_CTRL [7][11].TMPLTE_FIELD[11:6]	6
0xBB0042B4	PIE_TMPLTE_CTRL [7][12].TMPLTE_FIELD[17:12]	6
0xBB0042B4	PIE_TMPLTE_CTRL [7][13].TMPLTE_FIELD[23:18]	6
0xBB0042B8	PIE_TMPLTE_CTRL [8][0].TMPLTE_FIELD[5:0]	6
0xBB0042B8	PIE_TMPLTE_CTRL [8][1].TMPLTE_FIELD[11:6]	6
0xBB0042B8	PIE_TMPLTE_CTRL [8][2].TMPLTE_FIELD[17:12]	6
0xBB0042B8	PIE_TMPLTE_CTRL [8][3].TMPLTE_FIELD[23:18]	6
0xBB0042B8	PIE_TMPLTE_CTRL [8][4].TMPLTE_FIELD[29:24]	6
0xBB0042BC	PIE_TMPLTE_CTRL [8][5].TMPLTE_FIELD[5:0]	6
0xBB0042BC	PIE_TMPLTE_CTRL [8][6].TMPLTE_FIELD[11:6]	6
0xBB0042BC	PIE_TMPLTE_CTRL [8][7].TMPLTE_FIELD[17:12]	6
0xBB0042BC	PIE_TMPLTE_CTRL [8][8].TMPLTE_FIELD[23:18]	6
0xBB0042BC	PIE_TMPLTE_CTRL [8][9].TMPLTE_FIELD[29:24]	6
0xBB0042C0	PIE_TMPLTE_CTRL [8][10].TMPLTE_FIELD[5:0]	6
0xBB0042C0	PIE_TMPLTE_CTRL [8][11].TMPLTE_FIELD[11:6]	6
0xBB0042C0	PIE_TMPLTE_CTRL [8][12].TMPLTE_FIELD[17:12]	6
0xBB0042C0	PIE_TMPLTE_CTRL [8][13].TMPLTE_FIELD[23:18]	6
0xBB0042C4	PIE_TMPLTE_CTRL [9][0].TMPLTE_FIELD[5:0]	6
0xBB0042C4	PIE_TMPLTE_CTRL [9][1].TMPLTE_FIELD[11:6]	6
0xBB0042C4	PIE_TMPLTE_CTRL [9][2].TMPLTE_FIELD[17:12]	6
0xBB0042C4	PIE_TMPLTE_CTRL [9][3].TMPLTE_FIELD[23:18]	6
0xBB0042C4	PIE_TMPLTE_CTRL [9][4].TMPLTE_FIELD[29:24]	6
0xBB0042C8	PIE_TMPLTE_CTRL [9][5].TMPLTE_FIELD[5:0]	6
0xBB0042C8	PIE_TMPLTE_CTRL [9][6].TMPLTE_FIELD[11:6]	6
0xBB0042C8	PIE_TMPLTE_CTRL [9][7].TMPLTE_FIELD[17:12]	6
0xBB0042C8	PIE_TMPLTE_CTRL [9][8].TMPLTE_FIELD[23:18]	6
0xBB0042C8	PIE_TMPLTE_CTRL [9][9].TMPLTE_FIELD[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0042CC	PIE_TMPLTE_CTRL [9][10].TMPLTE_FIELD[5:0]	6
0xBB0042CC	PIE_TMPLTE_CTRL [9][11].TMPLTE_FIELD[11:6]	6
0xBB0042CC	PIE_TMPLTE_CTRL [9][12].TMPLTE_FIELD[17:12]	6
0xBB0042CC	PIE_TMPLTE_CTRL [9][13].TMPLTE_FIELD[23:18]	6
0xBB008100	PORT_ISO_RESTRICT_ROUTE_CTRL.RESERVED[31:1]	31
0xBB008100	PORT_ISO_RESTRICT_ROUTE_CTRL.RESTRICT_ROUTE[0:0]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [0].VB_ISO_EGR_BYPASS[0:0]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [1].VB_ISO_EGR_BYPASS[1:1]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [2].VB_ISO_EGR_BYPASS[2:2]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [3].VB_ISO_EGR_BYPASS[3:3]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [4].VB_ISO_EGR_BYPASS[4:4]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [5].VB_ISO_EGR_BYPASS[5:5]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [6].VB_ISO_EGR_BYPASS[6:6]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [7].VB_ISO_EGR_BYPASS[7:7]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [8].VB_ISO_EGR_BYPASS[8:8]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [9].VB_ISO_EGR_BYPASS[9:9]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [10].VB_ISO_EGR_BYPASS[10:10]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [11].VB_ISO_EGR_BYPASS[11:11]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [12].VB_ISO_EGR_BYPASS[12:12]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [13].VB_ISO_EGR_BYPASS[13:13]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [14].VB_ISO_EGR_BYPASS[14:14]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [15].VB_ISO_EGR_BYPASS[15:15]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [16].VB_ISO_EGR_BYPASS[16:16]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [17].VB_ISO_EGR_BYPASS[17:17]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [18].VB_ISO_EGR_BYPASS[18:18]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [19].VB_ISO_EGR_BYPASS[19:19]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [20].VB_ISO_EGR_BYPASS[20:20]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [21].VB_ISO_EGR_BYPASS[21:21]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [22].VB_ISO_EGR_BYPASS[22:22]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [23].VB_ISO_EGR_BYPASS[23:23]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [24].VB_ISO_EGR_BYPASS[24:24]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [25].VB_ISO_EGR_BYPASS[25:25]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [26].VB_ISO_EGR_BYPASS[26:26]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [27].VB_ISO_EGR_BYPASS[27:27]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [28].VB_ISO_EGR_BYPASS[28:28]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [29].VB_ISO_EGR_BYPASS[29:29]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [30].VB_ISO_EGR_BYPASS[30:30]	1
0xBB008B80	PORT_ISO_VB_EGR_CTRL [31].VB_ISO_EGR_BYPASS[31:31]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [32].VB_ISO_EGR_BYPASS[0:0]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [33].VB_ISO_EGR_BYPASS[1:1]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [34].VB_ISO_EGR_BYPASS[2:2]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [35].VB_ISO_EGR_BYPASS[3:3]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [36].VB_ISO_EGR_BYPASS[4:4]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [37].VB_ISO_EGR_BYPASS[5:5]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [38].VB_ISO_EGR_BYPASS[6:6]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [39].VB_ISO_EGR_BYPASS[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008B84	PORT_ISO_VB_EGR_CTRL [40].VB_ISO_EGR_BYPASS[8:8]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [41].VB_ISO_EGR_BYPASS[9:9]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [42].VB_ISO_EGR_BYPASS[10:10]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [43].VB_ISO_EGR_BYPASS[11:11]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [44].VB_ISO_EGR_BYPASS[12:12]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [45].VB_ISO_EGR_BYPASS[13:13]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [46].VB_ISO_EGR_BYPASS[14:14]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [47].VB_ISO_EGR_BYPASS[15:15]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [48].VB_ISO_EGR_BYPASS[16:16]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [49].VB_ISO_EGR_BYPASS[17:17]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [50].VB_ISO_EGR_BYPASS[18:18]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [51].VB_ISO_EGR_BYPASS[19:19]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [52].VB_ISO_EGR_BYPASS[20:20]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [53].VB_ISO_EGR_BYPASS[21:21]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [54].VB_ISO_EGR_BYPASS[22:22]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [55].VB_ISO_EGR_BYPASS[23:23]	1
0xBB008B84	PORT_ISO_VB_EGR_CTRL [56].VB_ISO_EGR_BYPASS[24:24]	1
0xBB008A00	PORT_ISO_VB_ISO_PMSK_CTRL [0].RESERVED[31:25]	7
0xBB008A00	PORT_ISO_VB_ISO_PMSK_CTRL [0].VB_ISO_MBR_1[24:0]	25
0xBB008A04	PORT_ISO_VB_ISO_PMSK_CTRL [0].VB_ISO_MBR_0[31:0]	32
0xBB008A08	PORT_ISO_VB_ISO_PMSK_CTRL [0].RESERVED[31:25]	7
0xBB008A08	PORT_ISO_VB_ISO_PMSK_CTRL [0].VID_UPPER[24:13]	12
0xBB008A08	PORT_ISO_VB_ISO_PMSK_CTRL [0].VID_LOWER[12:1]	12
0xBB008A08	PORT_ISO_VB_ISO_PMSK_CTRL [0].VALID[0:0]	1
0xBB008A0C	PORT_ISO_VB_ISO_PMSK_CTRL [1].RESERVED[31:25]	7
0xBB008A0C	PORT_ISO_VB_ISO_PMSK_CTRL [1].VB_ISO_MBR_1[24:0]	25
0xBB008A10	PORT_ISO_VB_ISO_PMSK_CTRL [1].VB_ISO_MBR_0[31:0]	32
0xBB008A14	PORT_ISO_VB_ISO_PMSK_CTRL [1].RESERVED[31:25]	7
0xBB008A14	PORT_ISO_VB_ISO_PMSK_CTRL [1].VID_UPPER[24:13]	12
0xBB008A14	PORT_ISO_VB_ISO_PMSK_CTRL [1].VID_LOWER[12:1]	12
0xBB008A14	PORT_ISO_VB_ISO_PMSK_CTRL [1].VALID[0:0]	1
0xBB008A18	PORT_ISO_VB_ISO_PMSK_CTRL [2].RESERVED[31:25]	7
0xBB008A18	PORT_ISO_VB_ISO_PMSK_CTRL [2].VB_ISO_MBR_1[24:0]	25
0xBB008A1C	PORT_ISO_VB_ISO_PMSK_CTRL [2].VB_ISO_MBR_0[31:0]	32
0xBB008A20	PORT_ISO_VB_ISO_PMSK_CTRL [2].RESERVED[31:25]	7
0xBB008A20	PORT_ISO_VB_ISO_PMSK_CTRL [2].VID_UPPER[24:13]	12
0xBB008A20	PORT_ISO_VB_ISO_PMSK_CTRL [2].VID_LOWER[12:1]	12
0xBB008A20	PORT_ISO_VB_ISO_PMSK_CTRL [2].VALID[0:0]	1
0xBB008A24	PORT_ISO_VB_ISO_PMSK_CTRL [3].RESERVED[31:25]	7
0xBB008A24	PORT_ISO_VB_ISO_PMSK_CTRL [3].VB_ISO_MBR_1[24:0]	25
0xBB008A28	PORT_ISO_VB_ISO_PMSK_CTRL [3].VB_ISO_MBR_0[31:0]	32
0xBB008A2C	PORT_ISO_VB_ISO_PMSK_CTRL [3].RESERVED[31:25]	7
0xBB008A2C	PORT_ISO_VB_ISO_PMSK_CTRL [3].VID_UPPER[24:13]	12
0xBB008A2C	PORT_ISO_VB_ISO_PMSK_CTRL [3].VID_LOWER[12:1]	12
0xBB008A2C	PORT_ISO_VB_ISO_PMSK_CTRL [3].VALID[0:0]	1
0xBB008A30	PORT_ISO_VB_ISO_PMSK_CTRL [4].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008A30	PORT_ISO_VB_ISO_PMSK_CTRL [4].VB_ISO_MBR_1[24:0]	25
0xBB008A34	PORT_ISO_VB_ISO_PMSK_CTRL [4].VB_ISO_MBR_0[31:0]	32
0xBB008A38	PORT_ISO_VB_ISO_PMSK_CTRL [4].RESERVED[31:25]	7
0xBB008A38	PORT_ISO_VB_ISO_PMSK_CTRL [4].VID_UPPER[24:13]	12
0xBB008A38	PORT_ISO_VB_ISO_PMSK_CTRL [4].VID_LOWER[12:1]	12
0xBB008A38	PORT_ISO_VB_ISO_PMSK_CTRL [4].VALID[0:0]	1
0xBB008A3C	PORT_ISO_VB_ISO_PMSK_CTRL [5].RESERVED[31:25]	7
0xBB008A3C	PORT_ISO_VB_ISO_PMSK_CTRL [5].VB_ISO_MBR_1[24:0]	25
0xBB008A40	PORT_ISO_VB_ISO_PMSK_CTRL [5].VB_ISO_MBR_0[31:0]	32
0xBB008A44	PORT_ISO_VB_ISO_PMSK_CTRL [5].RESERVED[31:25]	7
0xBB008A44	PORT_ISO_VB_ISO_PMSK_CTRL [5].VID_UPPER[24:13]	12
0xBB008A44	PORT_ISO_VB_ISO_PMSK_CTRL [5].VID_LOWER[12:1]	12
0xBB008A44	PORT_ISO_VB_ISO_PMSK_CTRL [5].VALID[0:0]	1
0xBB008A48	PORT_ISO_VB_ISO_PMSK_CTRL [6].RESERVED[31:25]	7
0xBB008A48	PORT_ISO_VB_ISO_PMSK_CTRL [6].VB_ISO_MBR_1[24:0]	25
0xBB008A4C	PORT_ISO_VB_ISO_PMSK_CTRL [6].VB_ISO_MBR_0[31:0]	32
0xBB008A50	PORT_ISO_VB_ISO_PMSK_CTRL [6].RESERVED[31:25]	7
0xBB008A50	PORT_ISO_VB_ISO_PMSK_CTRL [6].VID_UPPER[24:13]	12
0xBB008A50	PORT_ISO_VB_ISO_PMSK_CTRL [6].VID_LOWER[12:1]	12
0xBB008A50	PORT_ISO_VB_ISO_PMSK_CTRL [6].VALID[0:0]	1
0xBB008A54	PORT_ISO_VB_ISO_PMSK_CTRL [7].RESERVED[31:25]	7
0xBB008A54	PORT_ISO_VB_ISO_PMSK_CTRL [7].VB_ISO_MBR_1[24:0]	25
0xBB008A58	PORT_ISO_VB_ISO_PMSK_CTRL [7].VB_ISO_MBR_0[31:0]	32
0xBB008A5C	PORT_ISO_VB_ISO_PMSK_CTRL [7].RESERVED[31:25]	7
0xBB008A5C	PORT_ISO_VB_ISO_PMSK_CTRL [7].VID_UPPER[24:13]	12
0xBB008A5C	PORT_ISO_VB_ISO_PMSK_CTRL [7].VID_LOWER[12:1]	12
0xBB008A5C	PORT_ISO_VB_ISO_PMSK_CTRL [7].VALID[0:0]	1
0xBB008A60	PORT_ISO_VB_ISO_PMSK_CTRL [8].RESERVED[31:25]	7
0xBB008A60	PORT_ISO_VB_ISO_PMSK_CTRL [8].VB_ISO_MBR_1[24:0]	25
0xBB008A64	PORT_ISO_VB_ISO_PMSK_CTRL [8].VB_ISO_MBR_0[31:0]	32
0xBB008A68	PORT_ISO_VB_ISO_PMSK_CTRL [8].RESERVED[31:25]	7
0xBB008A68	PORT_ISO_VB_ISO_PMSK_CTRL [8].VID_UPPER[24:13]	12
0xBB008A68	PORT_ISO_VB_ISO_PMSK_CTRL [8].VID_LOWER[12:1]	12
0xBB008A68	PORT_ISO_VB_ISO_PMSK_CTRL [8].VALID[0:0]	1
0xBB008A6C	PORT_ISO_VB_ISO_PMSK_CTRL [9].RESERVED[31:25]	7
0xBB008A6C	PORT_ISO_VB_ISO_PMSK_CTRL [9].VB_ISO_MBR_1[24:0]	25
0xBB008A70	PORT_ISO_VB_ISO_PMSK_CTRL [9].VB_ISO_MBR_0[31:0]	32
0xBB008A74	PORT_ISO_VB_ISO_PMSK_CTRL [9].RESERVED[31:25]	7
0xBB008A74	PORT_ISO_VB_ISO_PMSK_CTRL [9].VID_UPPER[24:13]	12
0xBB008A74	PORT_ISO_VB_ISO_PMSK_CTRL [9].VID_LOWER[12:1]	12
0xBB008A74	PORT_ISO_VB_ISO_PMSK_CTRL [9].VALID[0:0]	1
0xBB008A78	PORT_ISO_VB_ISO_PMSK_CTRL [10].RESERVED[31:25]	7
0xBB008A78	PORT_ISO_VB_ISO_PMSK_CTRL [10].VB_ISO_MBR_1[24:0]	25
0xBB008A7C	PORT_ISO_VB_ISO_PMSK_CTRL [10].VB_ISO_MBR_0[31:0]	32
0xBB008A80	PORT_ISO_VB_ISO_PMSK_CTRL [10].RESERVED[31:25]	7
0xBB008A80	PORT_ISO_VB_ISO_PMSK_CTRL [10].VID_UPPER[24:13]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008A80	PORT_ISO_VB_ISO_PMSK_CTRL [10].VID_LOWER[12:1]	12
0xBB008A80	PORT_ISO_VB_ISO_PMSK_CTRL [10].VALID[0:0]	1
0xBB008A84	PORT_ISO_VB_ISO_PMSK_CTRL [11].RESERVED[31:25]	7
0xBB008A84	PORT_ISO_VB_ISO_PMSK_CTRL [11].VB_ISO_MBR_1[24:0]	25
0xBB008A88	PORT_ISO_VB_ISO_PMSK_CTRL [11].VB_ISO_MBR_0[31:0]	32
0xBB008A8C	PORT_ISO_VB_ISO_PMSK_CTRL [11].RESERVED[31:25]	7
0xBB008A8C	PORT_ISO_VB_ISO_PMSK_CTRL [11].VID_UPPER[24:13]	12
0xBB008A8C	PORT_ISO_VB_ISO_PMSK_CTRL [11].VID_LOWER[12:1]	12
0xBB008A8C	PORT_ISO_VB_ISO_PMSK_CTRL [11].VALID[0:0]	1
0xBB008A90	PORT_ISO_VB_ISO_PMSK_CTRL [12].RESERVED[31:25]	7
0xBB008A90	PORT_ISO_VB_ISO_PMSK_CTRL [12].VB_ISO_MBR_1[24:0]	25
0xBB008A94	PORT_ISO_VB_ISO_PMSK_CTRL [12].VB_ISO_MBR_0[31:0]	32
0xBB008A98	PORT_ISO_VB_ISO_PMSK_CTRL [12].RESERVED[31:25]	7
0xBB008A98	PORT_ISO_VB_ISO_PMSK_CTRL [12].VID_UPPER[24:13]	12
0xBB008A98	PORT_ISO_VB_ISO_PMSK_CTRL [12].VID_LOWER[12:1]	12
0xBB008A98	PORT_ISO_VB_ISO_PMSK_CTRL [12].VALID[0:0]	1
0xBB008A9C	PORT_ISO_VB_ISO_PMSK_CTRL [13].RESERVED[31:25]	7
0xBB008A9C	PORT_ISO_VB_ISO_PMSK_CTRL [13].VB_ISO_MBR_1[24:0]	25
0xBB008AA0	PORT_ISO_VB_ISO_PMSK_CTRL [13].VB_ISO_MBR_0[31:0]	32
0xBB008AA4	PORT_ISO_VB_ISO_PMSK_CTRL [13].RESERVED[31:25]	7
0xBB008AA4	PORT_ISO_VB_ISO_PMSK_CTRL [13].VID_UPPER[24:13]	12
0xBB008AA4	PORT_ISO_VB_ISO_PMSK_CTRL [13].VID_LOWER[12:1]	12
0xBB008AA4	PORT_ISO_VB_ISO_PMSK_CTRL [13].VALID[0:0]	1
0xBB008AA8	PORT_ISO_VB_ISO_PMSK_CTRL [14].RESERVED[31:25]	7
0xBB008AA8	PORT_ISO_VB_ISO_PMSK_CTRL [14].VB_ISO_MBR_1[24:0]	25
0xBB008AAC	PORT_ISO_VB_ISO_PMSK_CTRL [14].VB_ISO_MBR_0[31:0]	32
0xBB008AB0	PORT_ISO_VB_ISO_PMSK_CTRL [14].RESERVED[31:25]	7
0xBB008AB0	PORT_ISO_VB_ISO_PMSK_CTRL [14].VID_UPPER[24:13]	12
0xBB008AB0	PORT_ISO_VB_ISO_PMSK_CTRL [14].VID_LOWER[12:1]	12
0xBB008AB0	PORT_ISO_VB_ISO_PMSK_CTRL [14].VALID[0:0]	1
0xBB008AB4	PORT_ISO_VB_ISO_PMSK_CTRL [15].RESERVED[31:25]	7
0xBB008AB4	PORT_ISO_VB_ISO_PMSK_CTRL [15].VB_ISO_MBR_1[24:0]	25
0xBB008AB8	PORT_ISO_VB_ISO_PMSK_CTRL [15].VB_ISO_MBR_0[31:0]	32
0xBB008ABC	PORT_ISO_VB_ISO_PMSK_CTRL [15].RESERVED[31:25]	7
0xBB008ABC	PORT_ISO_VB_ISO_PMSK_CTRL [15].VID_UPPER[24:13]	12
0xBB008ABC	PORT_ISO_VB_ISO_PMSK_CTRL [15].VID_LOWER[12:1]	12
0xBB008ABC	PORT_ISO_VB_ISO_PMSK_CTRL [15].VALID[0:0]	1
0xBB008AC0	PORT_ISO_VB_ISO_PMSK_CTRL [16].RESERVED[31:25]	7
0xBB008AC0	PORT_ISO_VB_ISO_PMSK_CTRL [16].VB_ISO_MBR_1[24:0]	25
0xBB008AC4	PORT_ISO_VB_ISO_PMSK_CTRL [16].VB_ISO_MBR_0[31:0]	32
0xBB008AC8	PORT_ISO_VB_ISO_PMSK_CTRL [16].RESERVED[31:25]	7
0xBB008AC8	PORT_ISO_VB_ISO_PMSK_CTRL [16].VID_UPPER[24:13]	12
0xBB008AC8	PORT_ISO_VB_ISO_PMSK_CTRL [16].VID_LOWER[12:1]	12
0xBB008AC8	PORT_ISO_VB_ISO_PMSK_CTRL [16].VALID[0:0]	1
0xBB008ACC	PORT_ISO_VB_ISO_PMSK_CTRL [17].RESERVED[31:25]	7
0xBB008ACC	PORT_ISO_VB_ISO_PMSK_CTRL [17].VB_ISO_MBR_1[24:0]	25

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008AD0	PORT_ISO_VB_ISO_PMSK_CTRL [17].VB_ISO_MBR_0[31:0]	32
0xBB008AD4	PORT_ISO_VB_ISO_PMSK_CTRL [17].RESERVED[31:25]	7
0xBB008AD4	PORT_ISO_VB_ISO_PMSK_CTRL [17].VID_UPPER[24:13]	12
0xBB008AD4	PORT_ISO_VB_ISO_PMSK_CTRL [17].VID_LOWER[12:1]	12
0xBB008AD4	PORT_ISO_VB_ISO_PMSK_CTRL [17].VALID[0:0]	1
0xBB008AD8	PORT_ISO_VB_ISO_PMSK_CTRL [18].RESERVED[31:25]	7
0xBB008AD8	PORT_ISO_VB_ISO_PMSK_CTRL [18].VB_ISO_MBR_1[24:0]	25
0xBB008ADC	PORT_ISO_VB_ISO_PMSK_CTRL [18].VB_ISO_MBR_0[31:0]	32
0xBB008AE0	PORT_ISO_VB_ISO_PMSK_CTRL [18].RESERVED[31:25]	7
0xBB008AE0	PORT_ISO_VB_ISO_PMSK_CTRL [18].VID_UPPER[24:13]	12
0xBB008AE0	PORT_ISO_VB_ISO_PMSK_CTRL [18].VID_LOWER[12:1]	12
0xBB008AE0	PORT_ISO_VB_ISO_PMSK_CTRL [18].VALID[0:0]	1
0xBB008AE4	PORT_ISO_VB_ISO_PMSK_CTRL [19].RESERVED[31:25]	7
0xBB008AE4	PORT_ISO_VB_ISO_PMSK_CTRL [19].VB_ISO_MBR_1[24:0]	25
0xBB008AE8	PORT_ISO_VB_ISO_PMSK_CTRL [19].VB_ISO_MBR_0[31:0]	32
0xBB008AEC	PORT_ISO_VB_ISO_PMSK_CTRL [19].RESERVED[31:25]	7
0xBB008AEC	PORT_ISO_VB_ISO_PMSK_CTRL [19].VID_UPPER[24:13]	12
0xBB008AEC	PORT_ISO_VB_ISO_PMSK_CTRL [19].VID_LOWER[12:1]	12
0xBB008AEC	PORT_ISO_VB_ISO_PMSK_CTRL [19].VALID[0:0]	1
0xBB008AF0	PORT_ISO_VB_ISO_PMSK_CTRL [20].RESERVED[31:25]	7
0xBB008AF0	PORT_ISO_VB_ISO_PMSK_CTRL [20].VB_ISO_MBR_1[24:0]	25
0xBB008AF4	PORT_ISO_VB_ISO_PMSK_CTRL [20].VB_ISO_MBR_0[31:0]	32
0xBB008AF8	PORT_ISO_VB_ISO_PMSK_CTRL [20].RESERVED[31:25]	7
0xBB008AF8	PORT_ISO_VB_ISO_PMSK_CTRL [20].VID_UPPER[24:13]	12
0xBB008AF8	PORT_ISO_VB_ISO_PMSK_CTRL [20].VID_LOWER[12:1]	12
0xBB008AF8	PORT_ISO_VB_ISO_PMSK_CTRL [20].VALID[0:0]	1
0xBB008AFC	PORT_ISO_VB_ISO_PMSK_CTRL [21].RESERVED[31:25]	7
0xBB008AFC	PORT_ISO_VB_ISO_PMSK_CTRL [21].VB_ISO_MBR_1[24:0]	25
0xBB008B00	PORT_ISO_VB_ISO_PMSK_CTRL [21].VB_ISO_MBR_0[31:0]	32
0xBB008B04	PORT_ISO_VB_ISO_PMSK_CTRL [21].RESERVED[31:25]	7
0xBB008B04	PORT_ISO_VB_ISO_PMSK_CTRL [21].VID_UPPER[24:13]	12
0xBB008B04	PORT_ISO_VB_ISO_PMSK_CTRL [21].VID_LOWER[12:1]	12
0xBB008B04	PORT_ISO_VB_ISO_PMSK_CTRL [21].VALID[0:0]	1
0xBB008B08	PORT_ISO_VB_ISO_PMSK_CTRL [22].RESERVED[31:25]	7
0xBB008B08	PORT_ISO_VB_ISO_PMSK_CTRL [22].VB_ISO_MBR_1[24:0]	25
0xBB008B0C	PORT_ISO_VB_ISO_PMSK_CTRL [22].VB_ISO_MBR_0[31:0]	32
0xBB008B10	PORT_ISO_VB_ISO_PMSK_CTRL [22].RESERVED[31:25]	7
0xBB008B10	PORT_ISO_VB_ISO_PMSK_CTRL [22].VID_UPPER[24:13]	12
0xBB008B10	PORT_ISO_VB_ISO_PMSK_CTRL [22].VID_LOWER[12:1]	12
0xBB008B10	PORT_ISO_VB_ISO_PMSK_CTRL [22].VALID[0:0]	1
0xBB008B14	PORT_ISO_VB_ISO_PMSK_CTRL [23].RESERVED[31:25]	7
0xBB008B14	PORT_ISO_VB_ISO_PMSK_CTRL [23].VB_ISO_MBR_1[24:0]	25
0xBB008B18	PORT_ISO_VB_ISO_PMSK_CTRL [23].VB_ISO_MBR_0[31:0]	32
0xBB008B1C	PORT_ISO_VB_ISO_PMSK_CTRL [23].RESERVED[31:25]	7
0xBB008B1C	PORT_ISO_VB_ISO_PMSK_CTRL [23].VID_UPPER[24:13]	12
0xBB008B1C	PORT_ISO_VB_ISO_PMSK_CTRL [23].VID_LOWER[12:1]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008B1C	PORT_ISO_VB_ISO_PMSK_CTRL [23].VALID[0:0]	1
0xBB008B20	PORT_ISO_VB_ISO_PMSK_CTRL [24].RESERVED[31:25]	7
0xBB008B20	PORT_ISO_VB_ISO_PMSK_CTRL [24].VB_ISO_MBR_1[24:0]	25
0xBB008B24	PORT_ISO_VB_ISO_PMSK_CTRL [24].VB_ISO_MBR_0[31:0]	32
0xBB008B28	PORT_ISO_VB_ISO_PMSK_CTRL [24].RESERVED[31:25]	7
0xBB008B28	PORT_ISO_VB_ISO_PMSK_CTRL [24].VID_UPPER[24:13]	12
0xBB008B28	PORT_ISO_VB_ISO_PMSK_CTRL [24].VID_LOWER[12:1]	12
0xBB008B28	PORT_ISO_VB_ISO_PMSK_CTRL [24].VALID[0:0]	1
0xBB008B2C	PORT_ISO_VB_ISO_PMSK_CTRL [25].RESERVED[31:25]	7
0xBB008B2C	PORT_ISO_VB_ISO_PMSK_CTRL [25].VB_ISO_MBR_1[24:0]	25
0xBB008B30	PORT_ISO_VB_ISO_PMSK_CTRL [25].VB_ISO_MBR_0[31:0]	32
0xBB008B34	PORT_ISO_VB_ISO_PMSK_CTRL [25].RESERVED[31:25]	7
0xBB008B34	PORT_ISO_VB_ISO_PMSK_CTRL [25].VID_UPPER[24:13]	12
0xBB008B34	PORT_ISO_VB_ISO_PMSK_CTRL [25].VID_LOWER[12:1]	12
0xBB008B34	PORT_ISO_VB_ISO_PMSK_CTRL [25].VALID[0:0]	1
0xBB008B38	PORT_ISO_VB_ISO_PMSK_CTRL [26].RESERVED[31:25]	7
0xBB008B38	PORT_ISO_VB_ISO_PMSK_CTRL [26].VB_ISO_MBR_1[24:0]	25
0xBB008B3C	PORT_ISO_VB_ISO_PMSK_CTRL [26].VB_ISO_MBR_0[31:0]	32
0xBB008B40	PORT_ISO_VB_ISO_PMSK_CTRL [26].RESERVED[31:25]	7
0xBB008B40	PORT_ISO_VB_ISO_PMSK_CTRL [26].VID_UPPER[24:13]	12
0xBB008B40	PORT_ISO_VB_ISO_PMSK_CTRL [26].VID_LOWER[12:1]	12
0xBB008B40	PORT_ISO_VB_ISO_PMSK_CTRL [26].VALID[0:0]	1
0xBB008B44	PORT_ISO_VB_ISO_PMSK_CTRL [27].RESERVED[31:25]	7
0xBB008B44	PORT_ISO_VB_ISO_PMSK_CTRL [27].VB_ISO_MBR_1[24:0]	25
0xBB008B48	PORT_ISO_VB_ISO_PMSK_CTRL [27].VB_ISO_MBR_0[31:0]	32
0xBB008B4C	PORT_ISO_VB_ISO_PMSK_CTRL [27].RESERVED[31:25]	7
0xBB008B4C	PORT_ISO_VB_ISO_PMSK_CTRL [27].VID_UPPER[24:13]	12
0xBB008B4C	PORT_ISO_VB_ISO_PMSK_CTRL [27].VID_LOWER[12:1]	12
0xBB008B4C	PORT_ISO_VB_ISO_PMSK_CTRL [27].VALID[0:0]	1
0xBB008B50	PORT_ISO_VB_ISO_PMSK_CTRL [28].RESERVED[31:25]	7
0xBB008B50	PORT_ISO_VB_ISO_PMSK_CTRL [28].VB_ISO_MBR_1[24:0]	25
0xBB008B54	PORT_ISO_VB_ISO_PMSK_CTRL [28].VB_ISO_MBR_0[31:0]	32
0xBB008B58	PORT_ISO_VB_ISO_PMSK_CTRL [28].RESERVED[31:25]	7
0xBB008B58	PORT_ISO_VB_ISO_PMSK_CTRL [28].VID_UPPER[24:13]	12
0xBB008B58	PORT_ISO_VB_ISO_PMSK_CTRL [28].VID_LOWER[12:1]	12
0xBB008B58	PORT_ISO_VB_ISO_PMSK_CTRL [28].VALID[0:0]	1
0xBB008B5C	PORT_ISO_VB_ISO_PMSK_CTRL [29].RESERVED[31:25]	7
0xBB008B5C	PORT_ISO_VB_ISO_PMSK_CTRL [29].VB_ISO_MBR_1[24:0]	25
0xBB008B60	PORT_ISO_VB_ISO_PMSK_CTRL [29].VB_ISO_MBR_0[31:0]	32
0xBB008B64	PORT_ISO_VB_ISO_PMSK_CTRL [29].RESERVED[31:25]	7
0xBB008B64	PORT_ISO_VB_ISO_PMSK_CTRL [29].VID_UPPER[24:13]	12
0xBB008B64	PORT_ISO_VB_ISO_PMSK_CTRL [29].VID_LOWER[12:1]	12
0xBB008B64	PORT_ISO_VB_ISO_PMSK_CTRL [29].VALID[0:0]	1
0xBB008B68	PORT_ISO_VB_ISO_PMSK_CTRL [30].RESERVED[31:25]	7
0xBB008B68	PORT_ISO_VB_ISO_PMSK_CTRL [30].VB_ISO_MBR_1[24:0]	25
0xBB008B6C	PORT_ISO_VB_ISO_PMSK_CTRL [30].VB_ISO_MBR_0[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008B70	PORT_ISO_VB_ISO_PMSK_CTRL [30].RESERVED[31:25]	7
0xBB008B70	PORT_ISO_VB_ISO_PMSK_CTRL [30].VID_UPPER[24:13]	12
0xBB008B70	PORT_ISO_VB_ISO_PMSK_CTRL [30].VID_LOWER[12:1]	12
0xBB008B70	PORT_ISO_VB_ISO_PMSK_CTRL [30].VALID[0:0]	1
0xBB008B74	PORT_ISO_VB_ISO_PMSK_CTRL [31].RESERVED[31:25]	7
0xBB008B74	PORT_ISO_VB_ISO_PMSK_CTRL [31].VB_ISO_MBR_1[24:0]	25
0xBB008B78	PORT_ISO_VB_ISO_PMSK_CTRL [31].VB_ISO_MBR_0[31:0]	32
0xBB008B7C	PORT_ISO_VB_ISO_PMSK_CTRL [31].RESERVED[31:25]	7
0xBB008B7C	PORT_ISO_VB_ISO_PMSK_CTRL [31].VID_UPPER[24:13]	12
0xBB008B7C	PORT_ISO_VB_ISO_PMSK_CTRL [31].VID_LOWER[12:1]	12
0xBB008B7C	PORT_ISO_VB_ISO_PMSK_CTRL [31].VALID[0:0]	1
0xBB009000	PRI_SEL_CTRL.RESERVED[31:12]	20
0xBB009000	PRI_SEL_CTRL.PORT_PRI_REMAP_EN[11:11]	1
0xBB009000	PRI_SEL_CTRL.PROTO_VLAN_PRI_REMAP_EN[10:10]	1
0xBB009000	PRI_SEL_CTRL.MAC_IP_VLAN_PRI_REMAP_EN[9:9]	1
0xBB009000	PRI_SEL_CTRL.DSCP_INVLD_EN[8:8]	1
0xBB009000	PRI_SEL_CTRL.RESERVED[7:6]	2
0xBB009000	PRI_SEL_CTRL.DSCP_INVLD_VAL[5:0]	6
0xBB009004	PRI_SEL_PORT_CTRL [0].PORT_PRI_REMAP_TBL_SEL[0:0]	1
0xBB009004	PRI_SEL_PORT_CTRL [1].PORT_PRI_REMAP_TBL_SEL[1:1]	1
0xBB009004	PRI_SEL_PORT_CTRL [2].PORT_PRI_REMAP_TBL_SEL[2:2]	1
0xBB009004	PRI_SEL_PORT_CTRL [3].PORT_PRI_REMAP_TBL_SEL[3:3]	1
0xBB009004	PRI_SEL_PORT_CTRL [4].PORT_PRI_REMAP_TBL_SEL[4:4]	1
0xBB009004	PRI_SEL_PORT_CTRL [5].PORT_PRI_REMAP_TBL_SEL[5:5]	1
0xBB009004	PRI_SEL_PORT_CTRL [6].PORT_PRI_REMAP_TBL_SEL[6:6]	1
0xBB009004	PRI_SEL_PORT_CTRL [7].PORT_PRI_REMAP_TBL_SEL[7:7]	1
0xBB009004	PRI_SEL_PORT_CTRL [8].PORT_PRI_REMAP_TBL_SEL[8:8]	1
0xBB009004	PRI_SEL_PORT_CTRL [9].PORT_PRI_REMAP_TBL_SEL[9:9]	1
0xBB009004	PRI_SEL_PORT_CTRL [10].PORT_PRI_REMAP_TBL_SEL[10:10]	1
0xBB009004	PRI_SEL_PORT_CTRL [11].PORT_PRI_REMAP_TBL_SEL[11:11]	1
0xBB009004	PRI_SEL_PORT_CTRL [12].PORT_PRI_REMAP_TBL_SEL[12:12]	1
0xBB009004	PRI_SEL_PORT_CTRL [13].PORT_PRI_REMAP_TBL_SEL[13:13]	1
0xBB009004	PRI_SEL_PORT_CTRL [14].PORT_PRI_REMAP_TBL_SEL[14:14]	1
0xBB009004	PRI_SEL_PORT_CTRL [15].PORT_PRI_REMAP_TBL_SEL[15:15]	1
0xBB009004	PRI_SEL_PORT_CTRL [16].PORT_PRI_REMAP_TBL_SEL[16:16]	1
0xBB009004	PRI_SEL_PORT_CTRL [17].PORT_PRI_REMAP_TBL_SEL[17:17]	1
0xBB009004	PRI_SEL_PORT_CTRL [18].PORT_PRI_REMAP_TBL_SEL[18:18]	1
0xBB009004	PRI_SEL_PORT_CTRL [19].PORT_PRI_REMAP_TBL_SEL[19:19]	1
0xBB009004	PRI_SEL_PORT_CTRL [20].PORT_PRI_REMAP_TBL_SEL[20:20]	1
0xBB009004	PRI_SEL_PORT_CTRL [21].PORT_PRI_REMAP_TBL_SEL[21:21]	1
0xBB009004	PRI_SEL_PORT_CTRL [22].PORT_PRI_REMAP_TBL_SEL[22:22]	1
0xBB009004	PRI_SEL_PORT_CTRL [23].PORT_PRI_REMAP_TBL_SEL[23:23]	1
0xBB009004	PRI_SEL_PORT_CTRL [24].PORT_PRI_REMAP_TBL_SEL[24:24]	1
0xBB009004	PRI_SEL_PORT_CTRL [25].PORT_PRI_REMAP_TBL_SEL[25:25]	1
0xBB009004	PRI_SEL_PORT_CTRL [26].PORT_PRI_REMAP_TBL_SEL[26:26]	1
0xBB009004	PRI_SEL_PORT_CTRL [27].PORT_PRI_REMAP_TBL_SEL[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009004	PRI_SEL_PORT_CTRL [28].PORT_PRI_REMAP_TBL_SEL[28:28]	1
0xBB009004	PRI_SEL_PORT_CTRL [29].PORT_PRI_REMAP_TBL_SEL[29:29]	1
0xBB009004	PRI_SEL_PORT_CTRL [30].PORT_PRI_REMAP_TBL_SEL[30:30]	1
0xBB009004	PRI_SEL_PORT_CTRL [31].PORT_PRI_REMAP_TBL_SEL[31:31]	1
0xBB009008	PRI_SEL_PORT_CTRL [32].PORT_PRI_REMAP_TBL_SEL[0:0]	1
0xBB009008	PRI_SEL_PORT_CTRL [33].PORT_PRI_REMAP_TBL_SEL[1:1]	1
0xBB009008	PRI_SEL_PORT_CTRL [34].PORT_PRI_REMAP_TBL_SEL[2:2]	1
0xBB009008	PRI_SEL_PORT_CTRL [35].PORT_PRI_REMAP_TBL_SEL[3:3]	1
0xBB009008	PRI_SEL_PORT_CTRL [36].PORT_PRI_REMAP_TBL_SEL[4:4]	1
0xBB009008	PRI_SEL_PORT_CTRL [37].PORT_PRI_REMAP_TBL_SEL[5:5]	1
0xBB009008	PRI_SEL_PORT_CTRL [38].PORT_PRI_REMAP_TBL_SEL[6:6]	1
0xBB009008	PRI_SEL_PORT_CTRL [39].PORT_PRI_REMAP_TBL_SEL[7:7]	1
0xBB009008	PRI_SEL_PORT_CTRL [40].PORT_PRI_REMAP_TBL_SEL[8:8]	1
0xBB009008	PRI_SEL_PORT_CTRL [41].PORT_PRI_REMAP_TBL_SEL[9:9]	1
0xBB009008	PRI_SEL_PORT_CTRL [42].PORT_PRI_REMAP_TBL_SEL[10:10]	1
0xBB009008	PRI_SEL_PORT_CTRL [43].PORT_PRI_REMAP_TBL_SEL[11:11]	1
0xBB009008	PRI_SEL_PORT_CTRL [44].PORT_PRI_REMAP_TBL_SEL[12:12]	1
0xBB009008	PRI_SEL_PORT_CTRL [45].PORT_PRI_REMAP_TBL_SEL[13:13]	1
0xBB009008	PRI_SEL_PORT_CTRL [46].PORT_PRI_REMAP_TBL_SEL[14:14]	1
0xBB009008	PRI_SEL_PORT_CTRL [47].PORT_PRI_REMAP_TBL_SEL[15:15]	1
0xBB009008	PRI_SEL_PORT_CTRL [48].PORT_PRI_REMAP_TBL_SEL[16:16]	1
0xBB009008	PRI_SEL_PORT_CTRL [49].PORT_PRI_REMAP_TBL_SEL[17:17]	1
0xBB009008	PRI_SEL_PORT_CTRL [50].PORT_PRI_REMAP_TBL_SEL[18:18]	1
0xBB009008	PRI_SEL_PORT_CTRL [51].PORT_PRI_REMAP_TBL_SEL[19:19]	1
0xBB009008	PRI_SEL_PORT_CTRL [52].PORT_PRI_REMAP_TBL_SEL[20:20]	1
0xBB009008	PRI_SEL_PORT_CTRL [53].PORT_PRI_REMAP_TBL_SEL[21:21]	1
0xBB009008	PRI_SEL_PORT_CTRL [54].PORT_PRI_REMAP_TBL_SEL[22:22]	1
0xBB009008	PRI_SEL_PORT_CTRL [55].PORT_PRI_REMAP_TBL_SEL[23:23]	1
0xBB009008	PRI_SEL_PORT_CTRL [56].PORT_PRI_REMAP_TBL_SEL[24:24]	1
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [0].IDX[1:0]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [1].IDX[3:2]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [2].IDX[5:4]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [3].IDX[7:6]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [4].IDX[9:8]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [5].IDX[11:10]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [6].IDX[13:12]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [7].IDX[15:14]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [8].IDX[17:16]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [9].IDX[19:18]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [10].IDX[21:20]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [11].IDX[23:22]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [12].IDX[25:24]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [13].IDX[27:26]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [14].IDX[29:28]	2
0xBB009064	PRI_SEL_PORT_TBL_IDX_CTRL [15].IDX[31:30]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [16].IDX[1:0]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [17].IDX[3:2]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [18].IDX[5:4]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [19].IDX[7:6]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [20].IDX[9:8]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [21].IDX[11:10]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [22].IDX[13:12]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [23].IDX[15:14]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [24].IDX[17:16]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [25].IDX[19:18]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [26].IDX[21:20]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [27].IDX[23:22]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [28].IDX[25:24]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [29].IDX[27:26]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [30].IDX[29:28]	2
0xBB009068	PRI_SEL_PORT_TBL_IDX_CTRL [31].IDX[31:30]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [32].IDX[1:0]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [33].IDX[3:2]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [34].IDX[5:4]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [35].IDX[7:6]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [36].IDX[9:8]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [37].IDX[11:10]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [38].IDX[13:12]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [39].IDX[15:14]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [40].IDX[17:16]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [41].IDX[19:18]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [42].IDX[21:20]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [43].IDX[23:22]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [44].IDX[25:24]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [45].IDX[27:26]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [46].IDX[29:28]	2
0xBB00906C	PRI_SEL_PORT_TBL_IDX_CTRL [47].IDX[31:30]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [48].IDX[1:0]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [49].IDX[3:2]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [50].IDX[5:4]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [51].IDX[7:6]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [52].IDX[9:8]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [53].IDX[11:10]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [54].IDX[13:12]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [55].IDX[15:14]	2
0xBB009070	PRI_SEL_PORT_TBL_IDX_CTRL [56].IDX[17:16]	2
0xBB009050	PRI_SEL_REMAP_11E [0].INTPRI_11E[2:0]	3
0xBB009050	PRI_SEL_REMAP_11E [1].INTPRI_11E[5:3]	3
0xBB009050	PRI_SEL_REMAP_11E [2].INTPRI_11E[8:6]	3
0xBB009050	PRI_SEL_REMAP_11E [3].INTPRI_11E[11:9]	3
0xBB009050	PRI_SEL_REMAP_11E [4].INTPRI_11E[14:12]	3
0xBB009050	PRI_SEL_REMAP_11E [5].INTPRI_11E[17:15]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009050	PRI_SEL_REMAP_11E [6].INTPRI_11E[20:18]	3
0xBB009050	PRI_SEL_REMAP_11E [7].INTPRI_11E[23:21]	3
0xBB009050	PRI_SEL_REMAP_11E [8].INTPRI_11E[26:24]	3
0xBB009050	PRI_SEL_REMAP_11E [9].INTPRI_11E[29:27]	3
0xBB009054	PRI_SEL_REMAP_11E [10].INTPRI_11E[2:0]	3
0xBB009054	PRI_SEL_REMAP_11E [11].INTPRI_11E[5:3]	3
0xBB009054	PRI_SEL_REMAP_11E [12].INTPRI_11E[8:6]	3
0xBB009054	PRI_SEL_REMAP_11E [13].INTPRI_11E[11:9]	3
0xBB009054	PRI_SEL_REMAP_11E [14].INTPRI_11E[14:12]	3
0xBB009054	PRI_SEL_REMAP_11E [15].INTPRI_11E[17:15]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][0].INTPRI_1BR[2:0]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][1].INTPRI_1BR[5:3]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][2].INTPRI_1BR[8:6]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][3].INTPRI_1BR[11:9]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][4].INTPRI_1BR[14:12]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][5].INTPRI_1BR[17:15]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][6].INTPRI_1BR[20:18]	3
0xBB00905C	PRI_SEL_REMAP_1BR [0][7].INTPRI_1BR[23:21]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][0].INTPRI_1BR[2:0]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][1].INTPRI_1BR[5:3]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][2].INTPRI_1BR[8:6]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][3].INTPRI_1BR[11:9]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][4].INTPRI_1BR[14:12]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][5].INTPRI_1BR[17:15]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][6].INTPRI_1BR[20:18]	3
0xBB009060	PRI_SEL_REMAP_1BR [1][7].INTPRI_1BR[23:21]	3
0xBB009034	PRI_SEL_REMAP_DSCP [0].INTPRI_DSCP[2:0]	3
0xBB009034	PRI_SEL_REMAP_DSCP [1].INTPRI_DSCP[5:3]	3
0xBB009034	PRI_SEL_REMAP_DSCP [2].INTPRI_DSCP[8:6]	3
0xBB009034	PRI_SEL_REMAP_DSCP [3].INTPRI_DSCP[11:9]	3
0xBB009034	PRI_SEL_REMAP_DSCP [4].INTPRI_DSCP[14:12]	3
0xBB009034	PRI_SEL_REMAP_DSCP [5].INTPRI_DSCP[17:15]	3
0xBB009034	PRI_SEL_REMAP_DSCP [6].INTPRI_DSCP[20:18]	3
0xBB009034	PRI_SEL_REMAP_DSCP [7].INTPRI_DSCP[23:21]	3
0xBB009034	PRI_SEL_REMAP_DSCP [8].INTPRI_DSCP[26:24]	3
0xBB009034	PRI_SEL_REMAP_DSCP [9].INTPRI_DSCP[29:27]	3
0xBB009038	PRI_SEL_REMAP_DSCP [10].INTPRI_DSCP[2:0]	3
0xBB009038	PRI_SEL_REMAP_DSCP [11].INTPRI_DSCP[5:3]	3
0xBB009038	PRI_SEL_REMAP_DSCP [12].INTPRI_DSCP[8:6]	3
0xBB009038	PRI_SEL_REMAP_DSCP [13].INTPRI_DSCP[11:9]	3
0xBB009038	PRI_SEL_REMAP_DSCP [14].INTPRI_DSCP[14:12]	3
0xBB009038	PRI_SEL_REMAP_DSCP [15].INTPRI_DSCP[17:15]	3
0xBB009038	PRI_SEL_REMAP_DSCP [16].INTPRI_DSCP[20:18]	3
0xBB009038	PRI_SEL_REMAP_DSCP [17].INTPRI_DSCP[23:21]	3
0xBB009038	PRI_SEL_REMAP_DSCP [18].INTPRI_DSCP[26:24]	3
0xBB009038	PRI_SEL_REMAP_DSCP [19].INTPRI_DSCP[29:27]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00903C	PRI_SEL_REMAP_DSCP [20].INTPRI_DSCP[2:0]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [21].INTPRI_DSCP[5:3]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [22].INTPRI_DSCP[8:6]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [23].INTPRI_DSCP[11:9]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [24].INTPRI_DSCP[14:12]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [25].INTPRI_DSCP[17:15]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [26].INTPRI_DSCP[20:18]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [27].INTPRI_DSCP[23:21]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [28].INTPRI_DSCP[26:24]	3
0xBB00903C	PRI_SEL_REMAP_DSCP [29].INTPRI_DSCP[29:27]	3
0xBB009040	PRI_SEL_REMAP_DSCP [30].INTPRI_DSCP[2:0]	3
0xBB009040	PRI_SEL_REMAP_DSCP [31].INTPRI_DSCP[5:3]	3
0xBB009040	PRI_SEL_REMAP_DSCP [32].INTPRI_DSCP[8:6]	3
0xBB009040	PRI_SEL_REMAP_DSCP [33].INTPRI_DSCP[11:9]	3
0xBB009040	PRI_SEL_REMAP_DSCP [34].INTPRI_DSCP[14:12]	3
0xBB009040	PRI_SEL_REMAP_DSCP [35].INTPRI_DSCP[17:15]	3
0xBB009040	PRI_SEL_REMAP_DSCP [36].INTPRI_DSCP[20:18]	3
0xBB009040	PRI_SEL_REMAP_DSCP [37].INTPRI_DSCP[23:21]	3
0xBB009040	PRI_SEL_REMAP_DSCP [38].INTPRI_DSCP[26:24]	3
0xBB009040	PRI_SEL_REMAP_DSCP [39].INTPRI_DSCP[29:27]	3
0xBB009044	PRI_SEL_REMAP_DSCP [40].INTPRI_DSCP[2:0]	3
0xBB009044	PRI_SEL_REMAP_DSCP [41].INTPRI_DSCP[5:3]	3
0xBB009044	PRI_SEL_REMAP_DSCP [42].INTPRI_DSCP[8:6]	3
0xBB009044	PRI_SEL_REMAP_DSCP [43].INTPRI_DSCP[11:9]	3
0xBB009044	PRI_SEL_REMAP_DSCP [44].INTPRI_DSCP[14:12]	3
0xBB009044	PRI_SEL_REMAP_DSCP [45].INTPRI_DSCP[17:15]	3
0xBB009044	PRI_SEL_REMAP_DSCP [46].INTPRI_DSCP[20:18]	3
0xBB009044	PRI_SEL_REMAP_DSCP [47].INTPRI_DSCP[23:21]	3
0xBB009044	PRI_SEL_REMAP_DSCP [48].INTPRI_DSCP[26:24]	3
0xBB009044	PRI_SEL_REMAP_DSCP [49].INTPRI_DSCP[29:27]	3
0xBB009048	PRI_SEL_REMAP_DSCP [50].INTPRI_DSCP[2:0]	3
0xBB009048	PRI_SEL_REMAP_DSCP [51].INTPRI_DSCP[5:3]	3
0xBB009048	PRI_SEL_REMAP_DSCP [52].INTPRI_DSCP[8:6]	3
0xBB009048	PRI_SEL_REMAP_DSCP [53].INTPRI_DSCP[11:9]	3
0xBB009048	PRI_SEL_REMAP_DSCP [54].INTPRI_DSCP[14:12]	3
0xBB009048	PRI_SEL_REMAP_DSCP [55].INTPRI_DSCP[17:15]	3
0xBB009048	PRI_SEL_REMAP_DSCP [56].INTPRI_DSCP[20:18]	3
0xBB009048	PRI_SEL_REMAP_DSCP [57].INTPRI_DSCP[23:21]	3
0xBB009048	PRI_SEL_REMAP_DSCP [58].INTPRI_DSCP[26:24]	3
0xBB009048	PRI_SEL_REMAP_DSCP [59].INTPRI_DSCP[29:27]	3
0xBB00904C	PRI_SEL_REMAP_DSCP [60].INTPRI_DSCP[2:0]	3
0xBB00904C	PRI_SEL_REMAP_DSCP [61].INTPRI_DSCP[5:3]	3
0xBB00904C	PRI_SEL_REMAP_DSCP [62].INTPRI_DSCP[8:6]	3
0xBB00904C	PRI_SEL_REMAP_DSCP [63].INTPRI_DSCP[11:9]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [0].INTPRI_CFI0_IPRI[2:0]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [1].INTPRI_CFI0_IPRI[5:3]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [2].INTPRI_CFI0_IPRI[8:6]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [3].INTPRI_CFI0_IPRI[11:9]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [4].INTPRI_CFI0_IPRI[14:12]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [5].INTPRI_CFI0_IPRI[17:15]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [6].INTPRI_CFI0_IPRI[20:18]	3
0xBB009024	PRI_SEL_REMAP_IPRI_CFI0 [7].INTPRI_CFI0_IPRI[23:21]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [0].INTPRI_CFI1_IPRI[2:0]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [1].INTPRI_CFI1_IPRI[5:3]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [2].INTPRI_CFI1_IPRI[8:6]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [3].INTPRI_CFI1_IPRI[11:9]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [4].INTPRI_CFI1_IPRI[14:12]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [5].INTPRI_CFI1_IPRI[17:15]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [6].INTPRI_CFI1_IPRI[20:18]	3
0xBB009028	PRI_SEL_REMAP_IPRI_CFI1 [7].INTPRI_CFI1_IPRI[23:21]	3
0xBB009058	PRI_SEL_REMAP_MPLS [0].INTPRI_MPLS_TC[2:0]	3
0xBB009058	PRI_SEL_REMAP_MPLS [1].INTPRI_MPLS_TC[5:3]	3
0xBB009058	PRI_SEL_REMAP_MPLS [2].INTPRI_MPLS_TC[8:6]	3
0xBB009058	PRI_SEL_REMAP_MPLS [3].INTPRI_MPLS_TC[11:9]	3
0xBB009058	PRI_SEL_REMAP_MPLS [4].INTPRI_MPLS_TC[14:12]	3
0xBB009058	PRI_SEL_REMAP_MPLS [5].INTPRI_MPLS_TC[17:15]	3
0xBB009058	PRI_SEL_REMAP_MPLS [6].INTPRI_MPLS_TC[20:18]	3
0xBB009058	PRI_SEL_REMAP_MPLS [7].INTPRI_MPLS_TC[23:21]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [0].INTPRI_DEI0_OPRI[2:0]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [1].INTPRI_DEI0_OPRI[5:3]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [2].INTPRI_DEI0_OPRI[8:6]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [3].INTPRI_DEI0_OPRI[11:9]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [4].INTPRI_DEI0_OPRI[14:12]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [5].INTPRI_DEI0_OPRI[17:15]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [6].INTPRI_DEI0_OPRI[20:18]	3
0xBB00902C	PRI_SEL_REMAP_OPRI_DEI0 [7].INTPRI_DEI0_OPRI[23:21]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [0].INTPRI_DEI1_OPRI[2:0]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [1].INTPRI_DEI1_OPRI[5:3]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [2].INTPRI_DEI1_OPRI[8:6]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [3].INTPRI_DEI1_OPRI[11:9]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [4].INTPRI_DEI1_OPRI[14:12]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [5].INTPRI_DEI1_OPRI[17:15]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [6].INTPRI_DEI1_OPRI[20:18]	3
0xBB009030	PRI_SEL_REMAP_OPRI_DEI1 [7].INTPRI_DEI1_OPRI[23:21]	3
0xBB00900C	PRI_SEL_REMAP_PORT [0].INTPRI_PORT[2:0]	3
0xBB00900C	PRI_SEL_REMAP_PORT [1].INTPRI_PORT[5:3]	3
0xBB00900C	PRI_SEL_REMAP_PORT [2].INTPRI_PORT[8:6]	3
0xBB00900C	PRI_SEL_REMAP_PORT [3].INTPRI_PORT[11:9]	3
0xBB00900C	PRI_SEL_REMAP_PORT [4].INTPRI_PORT[14:12]	3
0xBB00900C	PRI_SEL_REMAP_PORT [5].INTPRI_PORT[17:15]	3
0xBB00900C	PRI_SEL_REMAP_PORT [6].INTPRI_PORT[20:18]	3
0xBB00900C	PRI_SEL_REMAP_PORT [7].INTPRI_PORT[23:21]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00900C	PRI_SEL_REMAP_PORT [8].INTPRI_PORT[26:24]	3
0xBB00900C	PRI_SEL_REMAP_PORT [9].INTPRI_PORT[29:27]	3
0xBB009010	PRI_SEL_REMAP_PORT [10].INTPRI_PORT[2:0]	3
0xBB009010	PRI_SEL_REMAP_PORT [11].INTPRI_PORT[5:3]	3
0xBB009010	PRI_SEL_REMAP_PORT [12].INTPRI_PORT[8:6]	3
0xBB009010	PRI_SEL_REMAP_PORT [13].INTPRI_PORT[11:9]	3
0xBB009010	PRI_SEL_REMAP_PORT [14].INTPRI_PORT[14:12]	3
0xBB009010	PRI_SEL_REMAP_PORT [15].INTPRI_PORT[17:15]	3
0xBB009010	PRI_SEL_REMAP_PORT [16].INTPRI_PORT[20:18]	3
0xBB009010	PRI_SEL_REMAP_PORT [17].INTPRI_PORT[23:21]	3
0xBB009010	PRI_SEL_REMAP_PORT [18].INTPRI_PORT[26:24]	3
0xBB009010	PRI_SEL_REMAP_PORT [19].INTPRI_PORT[29:27]	3
0xBB009014	PRI_SEL_REMAP_PORT [20].INTPRI_PORT[2:0]	3
0xBB009014	PRI_SEL_REMAP_PORT [21].INTPRI_PORT[5:3]	3
0xBB009014	PRI_SEL_REMAP_PORT [22].INTPRI_PORT[8:6]	3
0xBB009014	PRI_SEL_REMAP_PORT [23].INTPRI_PORT[11:9]	3
0xBB009014	PRI_SEL_REMAP_PORT [24].INTPRI_PORT[14:12]	3
0xBB009014	PRI_SEL_REMAP_PORT [25].INTPRI_PORT[17:15]	3
0xBB009014	PRI_SEL_REMAP_PORT [26].INTPRI_PORT[20:18]	3
0xBB009014	PRI_SEL_REMAP_PORT [27].INTPRI_PORT[23:21]	3
0xBB009014	PRI_SEL_REMAP_PORT [28].INTPRI_PORT[26:24]	3
0xBB009014	PRI_SEL_REMAP_PORT [29].INTPRI_PORT[29:27]	3
0xBB009018	PRI_SEL_REMAP_PORT [30].INTPRI_PORT[2:0]	3
0xBB009018	PRI_SEL_REMAP_PORT [31].INTPRI_PORT[5:3]	3
0xBB009018	PRI_SEL_REMAP_PORT [32].INTPRI_PORT[8:6]	3
0xBB009018	PRI_SEL_REMAP_PORT [33].INTPRI_PORT[11:9]	3
0xBB009018	PRI_SEL_REMAP_PORT [34].INTPRI_PORT[14:12]	3
0xBB009018	PRI_SEL_REMAP_PORT [35].INTPRI_PORT[17:15]	3
0xBB009018	PRI_SEL_REMAP_PORT [36].INTPRI_PORT[20:18]	3
0xBB009018	PRI_SEL_REMAP_PORT [37].INTPRI_PORT[23:21]	3
0xBB009018	PRI_SEL_REMAP_PORT [38].INTPRI_PORT[26:24]	3
0xBB009018	PRI_SEL_REMAP_PORT [39].INTPRI_PORT[29:27]	3
0xBB00901C	PRI_SEL_REMAP_PORT [40].INTPRI_PORT[2:0]	3
0xBB00901C	PRI_SEL_REMAP_PORT [41].INTPRI_PORT[5:3]	3
0xBB00901C	PRI_SEL_REMAP_PORT [42].INTPRI_PORT[8:6]	3
0xBB00901C	PRI_SEL_REMAP_PORT [43].INTPRI_PORT[11:9]	3
0xBB00901C	PRI_SEL_REMAP_PORT [44].INTPRI_PORT[14:12]	3
0xBB00901C	PRI_SEL_REMAP_PORT [45].INTPRI_PORT[17:15]	3
0xBB00901C	PRI_SEL_REMAP_PORT [46].INTPRI_PORT[20:18]	3
0xBB00901C	PRI_SEL_REMAP_PORT [47].INTPRI_PORT[23:21]	3
0xBB00901C	PRI_SEL_REMAP_PORT [48].INTPRI_PORT[26:24]	3
0xBB00901C	PRI_SEL_REMAP_PORT [49].INTPRI_PORT[29:27]	3
0xBB009020	PRI_SEL_REMAP_PORT [50].INTPRI_PORT[2:0]	3
0xBB009020	PRI_SEL_REMAP_PORT [51].INTPRI_PORT[5:3]	3
0xBB009020	PRI_SEL_REMAP_PORT [52].INTPRI_PORT[8:6]	3
0xBB009020	PRI_SEL_REMAP_PORT [53].INTPRI_PORT[11:9]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009020	PRI_SEL_REMAP_PORT [54].INTPRI_PORT[14:12]	3
0xBB009020	PRI_SEL_REMAP_PORT [55].INTPRI_PORT[17:15]	3
0xBB009020	PRI_SEL_REMAP_PORT [56].INTPRI_PORT[20:18]	3
0xBB009074	PRI_SEL_TBL_CTRL [0].RESERVED[31:16]	16
0xBB009074	PRI_SEL_TBL_CTRL [0].WT_1BR[15:12]	4
0xBB009074	PRI_SEL_TBL_CTRL [0].WT_MPLS[11:8]	4
0xBB009074	PRI_SEL_TBL_CTRL [0].WT_11E[7:4]	4
0xBB009074	PRI_SEL_TBL_CTRL [0].WT_TUNNEL[3:0]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_ROUT[31:28]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_PROTO_VLAN[27:24]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_MAC_VLAN[23:20]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_OTAG[19:16]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_ITAG[15:12]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_DSCP[11:8]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_VACL[7:4]	4
0xBB009078	PRI_SEL_TBL_CTRL [0].WT_PORT[3:0]	4
0xBB00907C	PRI_SEL_TBL_CTRL [1].RESERVED[31:16]	16
0xBB00907C	PRI_SEL_TBL_CTRL [1].WT_1BR[15:12]	4
0xBB00907C	PRI_SEL_TBL_CTRL [1].WT_MPLS[11:8]	4
0xBB00907C	PRI_SEL_TBL_CTRL [1].WT_11E[7:4]	4
0xBB00907C	PRI_SEL_TBL_CTRL [1].WT_TUNNEL[3:0]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_ROUT[31:28]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_PROTO_VLAN[27:24]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_MAC_VLAN[23:20]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_OTAG[19:16]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_ITAG[15:12]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_DSCP[11:8]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_VACL[7:4]	4
0xBB009080	PRI_SEL_TBL_CTRL [1].WT_PORT[3:0]	4
0xBB009084	PRI_SEL_TBL_CTRL [2].RESERVED[31:16]	16
0xBB009084	PRI_SEL_TBL_CTRL [2].WT_1BR[15:12]	4
0xBB009084	PRI_SEL_TBL_CTRL [2].WT_MPLS[11:8]	4
0xBB009084	PRI_SEL_TBL_CTRL [2].WT_11E[7:4]	4
0xBB009084	PRI_SEL_TBL_CTRL [2].WT_TUNNEL[3:0]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_ROUT[31:28]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_PROTO_VLAN[27:24]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_MAC_VLAN[23:20]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_OTAG[19:16]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_ITAG[15:12]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_DSCP[11:8]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_VACL[7:4]	4
0xBB009088	PRI_SEL_TBL_CTRL [2].WT_PORT[3:0]	4
0xBB00908C	PRI_SEL_TBL_CTRL [3].RESERVED[31:16]	16
0xBB00908C	PRI_SEL_TBL_CTRL [3].WT_1BR[15:12]	4
0xBB00908C	PRI_SEL_TBL_CTRL [3].WT_MPLS[11:8]	4
0xBB00908C	PRI_SEL_TBL_CTRL [3].WT_11E[7:4]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00908C	PRI_SEL_TBL_CTRL [3].WT_TUNNEL[3:0]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_ROUT[31:28]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_PROTO_VLAN[27:24]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_MAC_VLAN[23:20]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_OTAG[19:16]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_ITAG[15:12]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_DSCP[11:8]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_VACL[7:4]	4
0xBB009090	PRI_SEL_TBL_CTRL [3].WT_PORT[3:0]	4
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P47_GMII_SEL[31:31]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P46_GMII_SEL[30:30]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P45_GMII_SEL[29:29]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P44_GMII_SEL[28:28]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P43_GMII_SEL[27:27]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P42_GMII_SEL[26:26]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P37_GMII_SEL[25:25]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P36_GMII_SEL[24:24]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P29_GMII_SEL[23:23]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P28_GMII_SEL[22:22]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P21_GMII_SEL[21:21]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P20_GMII_SEL[20:20]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P15_GMII_SEL[19:19]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P14_GMII_SEL[18:18]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P13_GMII_SEL[17:16]	2
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P12_GMII_SEL[15:14]	2
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P11_GMII_SEL[13:13]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P10_GMII_SEL[12:12]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P9_GMII_SEL[11:11]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P8_GMII_SEL[10:10]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P7_GMII_SEL[9:9]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P6_GMII_SEL[8:8]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P5_GMII_SEL[7:6]	2
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P4_GMII_SEL[5:4]	2
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P3_GMII_SEL[3:3]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P2_GMII_SEL[2:2]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P1_GMII_SEL[1:1]	1
0xBB0013BC	PROT_SERDSE_MUX_CTRL_0.P0_GMII_SEL[0:0]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.RESERVED[31:22]	10
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES9_0_GMII_SEL[21:21]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES7_3_GMII_SEL[20:20]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES7_2_GMII_SEL[19:19]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES6_3_GMII_SEL[18:18]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES6_2_GMII_SEL[17:17]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES5_3_GMII_SEL[16:16]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES5_2_GMII_SEL[15:15]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES4_3_GMII_SEL[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES4_2_GMII_SEL[13:13]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES3_3_GMII_SEL[12:11]	2
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES3_2_GMII_SEL[10:9]	2
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES3_1_GMII_SEL[8:8]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES3_0_GMII_SEL[7:7]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES2_3_GMII_SEL[6:5]	2
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES2_2_GMII_SEL[4:3]	2
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES2_1_GMII_SEL[2:2]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.SERDES2_0_GMII_SEL[1:1]	1
0xBB0013C0	PROT_SERDSE_MUX_CTRL_1.P50_GMII_SEL[0:0]	1
0xBB00A9D4	QM_CPUQID2QID_CTRL [0].QID[2:0]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [1].QID[5:3]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [2].QID[8:6]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [3].QID[11:9]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [4].QID[14:12]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [5].QID[17:15]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [6].QID[20:18]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [7].QID[23:21]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [8].QID[26:24]	3
0xBB00A9D4	QM_CPUQID2QID_CTRL [9].QID[29:27]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [10].QID[2:0]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [11].QID[5:3]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [12].QID[8:6]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [13].QID[11:9]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [14].QID[14:12]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [15].QID[17:15]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [16].QID[20:18]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [17].QID[23:21]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [18].QID[26:24]	3
0xBB00A9D8	QM_CPUQID2QID_CTRL [19].QID[29:27]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [20].QID[2:0]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [21].QID[5:3]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [22].QID[8:6]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [23].QID[11:9]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [24].QID[14:12]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [25].QID[17:15]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [26].QID[20:18]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [27].QID[23:21]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [28].QID[26:24]	3
0xBB00A9DC	QM_CPUQID2QID_CTRL [29].QID[29:27]	3
0xBB00A9E0	QM_CPUQID2QID_CTRL [30].QID[2:0]	3
0xBB00A9E0	QM_CPUQID2QID_CTRL [31].QID[5:3]	3
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [0].QID[3:0]	4
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [1].QID[7:4]	4
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [2].QID[11:8]	4
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [3].QID[15:12]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [4].QID[19:16]	4
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [5].QID[23:20]	4
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [6].QID[27:24]	4
0xBB00A9E4	QM_CPUQID2XGSQID_CTRL [7].QID[31:28]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [8].QID[3:0]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [9].QID[7:4]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [10].QID[11:8]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [11].QID[15:12]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [12].QID[19:16]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [13].QID[23:20]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [14].QID[27:24]	4
0xBB00A9E8	QM_CPUQID2XGSQID_CTRL [15].QID[31:28]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [16].QID[3:0]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [17].QID[7:4]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [18].QID[11:8]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [19].QID[15:12]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [20].QID[19:16]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [21].QID[23:20]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [22].QID[27:24]	4
0xBB00A9EC	QM_CPUQID2XGSQID_CTRL [23].QID[31:28]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [24].QID[3:0]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [25].QID[7:4]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [26].QID[11:8]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [27].QID[15:12]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [28].QID[19:16]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [29].QID[23:20]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [30].QID[27:24]	4
0xBB00A9F0	QM_CPUQID2XGSQID_CTRL [31].QID[31:28]	4
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.RESERVED[31:30]	2
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.MAC_CST[29:25]	5
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.NFW_SA[24:20]	5
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.PMV_FORBID[19:15]	5
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.L2_STTC_PMV[14:10]	5
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.L2_DYN_PMV[9:5]	5
0xBB00AA20	QM_FLAG2CPUQID_CTRL_0.HASH_FULL[4:0]	5
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.RESERVED[31:30]	2
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.INVALID_SA[29:25]	5
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.ATK_TYPE[24:20]	5
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.ACL_HIT[19:15]	5
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.OF_HIT[14:10]	5
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.TT_HIT[9:5]	5
0xBB00AA24	QM_FLAG2CPUQID_CTRL_1.MIR_HIT[4:0]	5
0xBB00AA28	QM_FLAG2CPUQID_CTRL_2.RESERVED[31:15]	17
0xBB00AA28	QM_FLAG2CPUQID_CTRL_2.L2_ERR_PKT[14:10]	5
0xBB00AA28	QM_FLAG2CPUQID_CTRL_2.L3_ERR_PKT[9:5]	5
0xBB00AA28	QM_FLAG2CPUQID_CTRL_2.RESERVED[4:0]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A9D0	QM_INTPRI2QID_CTRL [0].QID[2:0]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [1].QID[5:3]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [2].QID[8:6]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [3].QID[11:9]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [4].QID[14:12]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [5].QID[17:15]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [6].QID[20:18]	3
0xBB00A9D0	QM_INTPRI2QID_CTRL [7].QID[23:21]	3
0xBB00A9F4	QM_RSN2CPUQID_CTRL_0.RESERVED[31:30]	2
0xBB00A9F4	QM_RSN2CPUQID_CTRL_0.CPU2CPU_TALK[29:25]	5
0xBB00A9F4	QM_RSN2CPUQID_CTRL_0.TTL_EXCPT[24:20]	5
0xBB00A9F4	QM_RSN2CPUQID_CTRL_0.OF_TBL_LU_MISS[19:15]	5
0xBB00A9F4	QM_RSN2CPUQID_CTRL_0.RESERVED[14:0]	15
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.RESERVED[31:30]	2
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.OAMPDU[29:25]	5
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.CFM[24:20]	5
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.CFM_ETHDM[19:15]	5
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.PASR_EXCPT[14:10]	5
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.MALFORM_PKT[9:5]	5
0xBB00A9F8	QM_RSN2CPUQID_CTRL_1.IP_MAC_BINDING[4:0]	5
0xBB00AA1C	QM_RSN2CPUQID_CTRL_10.RESERVED[31:10]	22
0xBB00AA1C	QM_RSN2CPUQID_CTRL_10.L2_NTFY[9:5]	5
0xBB00AA1C	QM_RSN2CPUQID_CTRL_10.NORMAL_FWD[4:0]	5
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.RESERVED[31:30]	2
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.L3_IPUC_RPF[29:25]	5
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.INN_OUTER_CFI_EQ[1][24:20]	5
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.IVC[19:15]	5
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.IGR_VLAN_FLTR[14:10]	5
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.L2_UC_MC_BRIDGE_LU_MISS[9:5]	5
0xBB00A9FC	QM_RSN2CPUQID_CTRL_2.IP4_IP6_BRIDGE_LU_MISS[4:0]	5
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.RESERVED[31:30]	2
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.PTP[29:25]	5
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.RMA_USR_DEF[24:20]	5
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.EST_TX_LEN_EXCEED[19:15]	5
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.EST_MISMATCH[14:10]	5
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.ECID_EQ_PCID[9:5]	5
0xBB00AA00	QM_RSN2CPUQID_CTRL_3.RMA_BPDU[4:0]	5
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.RESERVED[31:30]	2
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.RMA_LACP[29:25]	5
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.RMA_LLDP[24:20]	5
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.RMA_EAPOL[19:15]	5
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.RMA_XX[14:10]	5
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.TUNL_MAC_IF[9:5]	5
0xBB00AA04	QM_RSN2CPUQID_CTRL_4.TUNL_IP_CHK[4:0]	5
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.RESERVED[31:30]	2
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.ROUTER_MAC_IF[29:25]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.L3_IPUC_NON_IP_PKT[24:20]	5
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.IP4_IP6_HDR_ERR[19:15]	5
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.ROUTING_IP_CHK[14:10]	5
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.L3_ROUTING_DIP_DMAC_MISMATCH[9:5]	5
0xBB00AA08	QM_RSN2CPUQID_CTRL_5.IP6_UC_MC_HOP_BY_HOP[4:0]	5
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.RESERVED[31:30]	2
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.IP6_UC_MC_ROUTING_HDR[29:25]	5
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.IP4_IP_OPT[24:20]	5
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.IP4_IP6_MC_ROUTING_LU_MISS[19:15]	5
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.L3_IPUC_NULL_ROUTE[14:10]	5
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.L3_IPUC_PBR_NULL_ROUTE[9:5]	5
0xBB00AA0C	QM_RSN2CPUQID_CTRL_6.L3_UC_HOST_ROUTE[4:0]	5
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.RESERVED[31:30]	2
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.L3_UC_NET_ROUTE[29:25]	5
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.L3_MC_BRIDGE_ENTRY[24:20]	5
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.L3_MC_ROUTE_ENTRY[19:15]	5
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.L3_IPMC_RPF[14:10]	5
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.ROUTING_EXCPT_NH_ERR_ACT[9:5]	5
0xBB00AA10	QM_RSN2CPUQID_CTRL_7.ROUTING_EXCPT_NH_AGE_OUT_ACT[4:0]	5
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.RESERVED[31:30]	2
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.ROUTING_EXCPT_ROUTE_TO_TUNNEL_ACT[29:25]	5
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.IP4_IP6_ICMP_REDRT[24:20]	5
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.IPUC_MTU[19:15]	5
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.IPMC_MTU[14:10]	5
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.IPUC_TTL[9:5]	5
0xBB00AA14	QM_RSN2CPUQID_CTRL_8.IPMC_TTL[4:0]	5
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.RESERVED[31:30]	2
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.IGMP_MLD[29:25]	5
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.DHCP_DHCP6[24:20]	5
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.ARPA_REQ REP[19:15]	5
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.NEIGHBOR_DISCOVER[14:10]	5
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.IP4_IP6_RSV_ADDR[9:5]	5
0xBB00AA18	QM_RSN2CPUQID_CTRL_9.MPLS_EXCPT[4:0]	5
0xBB008950	RMA_BPDU_FLD_PMSK.RESERVED[31:25]	7
0xBB008950	RMA_BPDU_FLD_PMSK.PMSK_1[24:0]	25
0xBB008954	RMA_BPDU_FLD_PMSK.PMSK_0[31:0]	32
0xBB008800	RMA_CTRL_0.RMA_OF_ACT[31:30]	2
0xBB008800	RMA_CTRL_0.RMA_OE_ACT[29:28]	2
0xBB008800	RMA_CTRL_0.RMA_OD_ACT[27:26]	2
0xBB008800	RMA_CTRL_0.RMA_OC_ACT[25:24]	2
0xBB008800	RMA_CTRL_0.RMA_OB_ACT[23:22]	2
0xBB008800	RMA_CTRL_0.RMA_OA_ACT[21:20]	2
0xBB008800	RMA_CTRL_0.RMA_09_ACT[19:18]	2
0xBB008800	RMA_CTRL_0.RMA_08_ACT[17:16]	2
0xBB008800	RMA_CTRL_0.RMA_07_ACT[15:14]	2
0xBB008800	RMA_CTRL_0.RMA_06_ACT[13:12]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008800	RMA_CTRL_0.RMA_05_ACT[11:10]	2
0xBB008800	RMA_CTRL_0.RMA_04_ACT[9:8]	2
0xBB008800	RMA_CTRL_0.RMA_03_ACT[7:6]	2
0xBB008800	RMA_CTRL_0.RMA_02_ACT[5:4]	2
0xBB008800	RMA_CTRL_0.RMA_01_ACT[3:2]	2
0xBB008800	RMA_CTRL_0.RESERVED[1:0]	2
0xBB008804	RMA_CTRL_1.RMA_1F_ACT[31:30]	2
0xBB008804	RMA_CTRL_1.RMA_1E_ACT[29:28]	2
0xBB008804	RMA_CTRL_1.RMA_1D_ACT[27:26]	2
0xBB008804	RMA_CTRL_1.RMA_1C_ACT[25:24]	2
0xBB008804	RMA_CTRL_1.RMA_1B_ACT[23:22]	2
0xBB008804	RMA_CTRL_1.RMA_1A_ACT[21:20]	2
0xBB008804	RMA_CTRL_1.RMA_19_ACT[19:18]	2
0xBB008804	RMA_CTRL_1.RMA_18_ACT[17:16]	2
0xBB008804	RMA_CTRL_1.RMA_17_ACT[15:14]	2
0xBB008804	RMA_CTRL_1.RMA_16_ACT[13:12]	2
0xBB008804	RMA_CTRL_1.RMA_15_ACT[11:10]	2
0xBB008804	RMA_CTRL_1.RMA_14_ACT[9:8]	2
0xBB008804	RMA_CTRL_1.RMA_13_ACT[7:6]	2
0xBB008804	RMA_CTRL_1.RMA_12_ACT[5:4]	2
0xBB008804	RMA_CTRL_1.RMA_11_ACT[3:2]	2
0xBB008804	RMA_CTRL_1.RMA_10_ACT[1:0]	2
0xBB008808	RMA_CTRL_2.RMA_2F_ACT[31:30]	2
0xBB008808	RMA_CTRL_2.RMA_2E_ACT[29:28]	2
0xBB008808	RMA_CTRL_2.RMA_2D_ACT[27:26]	2
0xBB008808	RMA_CTRL_2.RMA_2C_ACT[25:24]	2
0xBB008808	RMA_CTRL_2.RMA_2B_ACT[23:22]	2
0xBB008808	RMA_CTRL_2.RMA_2A_ACT[21:20]	2
0xBB008808	RMA_CTRL_2.RMA_29_ACT[19:18]	2
0xBB008808	RMA_CTRL_2.RMA_28_ACT[17:16]	2
0xBB008808	RMA_CTRL_2.RMA_27_ACT[15:14]	2
0xBB008808	RMA_CTRL_2.RMA_26_ACT[13:12]	2
0xBB008808	RMA_CTRL_2.RMA_25_ACT[11:10]	2
0xBB008808	RMA_CTRL_2.RMA_24_ACT[9:8]	2
0xBB008808	RMA_CTRL_2.RMA_23_ACT[7:6]	2
0xBB008808	RMA_CTRL_2.RMA_22_ACT[5:4]	2
0xBB008808	RMA_CTRL_2.RMA_21_ACT[3:2]	2
0xBB008808	RMA_CTRL_2.RMA_20_ACT[1:0]	2
0xBB008958	RMA_EAPOL_FLD_PMSK.RESERVED[31:25]	7
0xBB008958	RMA_EAPOL_FLD_PMSK.PMSK_1[24:0]	25
0xBB00895C	RMA_EAPOL_FLD_PMSK.PMSK_0[31:0]	32
0xBB008948	RMA_FLD_PMSK.RESERVED[31:25]	7
0xBB008948	RMA_FLD_PMSK.PMSK_1[24:0]	25
0xBB00894C	RMA_FLD_PMSK.PMSK_0[31:0]	32
0xBB008960	RMA_LLDP_FLD_PMSK.RESERVED[31:25]	7
0xBB008960	RMA_LLDP_FLD_PMSK.PMSK_1[24:0]	25

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008964	RMA_LLDP_FLD_PMSK.PMSK_0[31:0]	32
0xBB008818	RMA_MGN_LRNLN_CTRL.RESERVED[31:3]	29
0xBB008818	RMA_MGN_LRNLN_CTRL.PTP_LRNLN[2:2]	1
0xBB008818	RMA_MGN_LRNLN_CTRL.LLDP_LRNLN[1:1]	1
0xBB008818	RMA_MGN_LRNLN_CTRL.EAPOL_LRNLN[0:0]	1
0xBB00880C	RMA_MIRROR_CTRL.RESERVED[31:1]	31
0xBB00880C	RMA_MIRROR_CTRL.MIRROR_ACT[0:0]	1
0xBB00881C	RMA_PORT_BPDU_CTRL [0].ACT[2:0]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [1].ACT[5:3]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [2].ACT[8:6]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [3].ACT[11:9]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [4].ACT[14:12]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [5].ACT[17:15]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [6].ACT[20:18]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [7].ACT[23:21]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [8].ACT[26:24]	3
0xBB00881C	RMA_PORT_BPDU_CTRL [9].ACT[29:27]	3
0xBB008820	RMA_PORT_BPDU_CTRL [10].ACT[2:0]	3
0xBB008820	RMA_PORT_BPDU_CTRL [11].ACT[5:3]	3
0xBB008820	RMA_PORT_BPDU_CTRL [12].ACT[8:6]	3
0xBB008820	RMA_PORT_BPDU_CTRL [13].ACT[11:9]	3
0xBB008820	RMA_PORT_BPDU_CTRL [14].ACT[14:12]	3
0xBB008820	RMA_PORT_BPDU_CTRL [15].ACT[17:15]	3
0xBB008820	RMA_PORT_BPDU_CTRL [16].ACT[20:18]	3
0xBB008820	RMA_PORT_BPDU_CTRL [17].ACT[23:21]	3
0xBB008820	RMA_PORT_BPDU_CTRL [18].ACT[26:24]	3
0xBB008820	RMA_PORT_BPDU_CTRL [19].ACT[29:27]	3
0xBB008824	RMA_PORT_BPDU_CTRL [20].ACT[2:0]	3
0xBB008824	RMA_PORT_BPDU_CTRL [21].ACT[5:3]	3
0xBB008824	RMA_PORT_BPDU_CTRL [22].ACT[8:6]	3
0xBB008824	RMA_PORT_BPDU_CTRL [23].ACT[11:9]	3
0xBB008824	RMA_PORT_BPDU_CTRL [24].ACT[14:12]	3
0xBB008824	RMA_PORT_BPDU_CTRL [25].ACT[17:15]	3
0xBB008824	RMA_PORT_BPDU_CTRL [26].ACT[20:18]	3
0xBB008824	RMA_PORT_BPDU_CTRL [27].ACT[23:21]	3
0xBB008824	RMA_PORT_BPDU_CTRL [28].ACT[26:24]	3
0xBB008824	RMA_PORT_BPDU_CTRL [29].ACT[29:27]	3
0xBB008828	RMA_PORT_BPDU_CTRL [30].ACT[2:0]	3
0xBB008828	RMA_PORT_BPDU_CTRL [31].ACT[5:3]	3
0xBB008828	RMA_PORT_BPDU_CTRL [32].ACT[8:6]	3
0xBB008828	RMA_PORT_BPDU_CTRL [33].ACT[11:9]	3
0xBB008828	RMA_PORT_BPDU_CTRL [34].ACT[14:12]	3
0xBB008828	RMA_PORT_BPDU_CTRL [35].ACT[17:15]	3
0xBB008828	RMA_PORT_BPDU_CTRL [36].ACT[20:18]	3
0xBB008828	RMA_PORT_BPDU_CTRL [37].ACT[23:21]	3
0xBB008828	RMA_PORT_BPDU_CTRL [38].ACT[26:24]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008828	RMA_PORT_BPDU_CTRL [39].ACT[29:27]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [40].ACT[2:0]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [41].ACT[5:3]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [42].ACT[8:6]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [43].ACT[11:9]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [44].ACT[14:12]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [45].ACT[17:15]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [46].ACT[20:18]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [47].ACT[23:21]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [48].ACT[26:24]	3
0xBB00882C	RMA_PORT_BPDU_CTRL [49].ACT[29:27]	3
0xBB008830	RMA_PORT_BPDU_CTRL [50].ACT[2:0]	3
0xBB008830	RMA_PORT_BPDU_CTRL [51].ACT[5:3]	3
0xBB008830	RMA_PORT_BPDU_CTRL [52].ACT[8:6]	3
0xBB008830	RMA_PORT_BPDU_CTRL [53].ACT[11:9]	3
0xBB008830	RMA_PORT_BPDU_CTRL [54].ACT[14:12]	3
0xBB008830	RMA_PORT_BPDU_CTRL [55].ACT[17:15]	3
0xBB008830	RMA_PORT_BPDU_CTRL [56].ACT[20:18]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [0].ACT[2:0]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [1].ACT[5:3]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [2].ACT[8:6]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [3].ACT[11:9]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [4].ACT[14:12]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [5].ACT[17:15]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [6].ACT[20:18]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [7].ACT[23:21]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [8].ACT[26:24]	3
0xBB008930	RMA_PORT_EAPOL_CTRL [9].ACT[29:27]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [10].ACT[2:0]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [11].ACT[5:3]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [12].ACT[8:6]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [13].ACT[11:9]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [14].ACT[14:12]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [15].ACT[17:15]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [16].ACT[20:18]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [17].ACT[23:21]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [18].ACT[26:24]	3
0xBB008934	RMA_PORT_EAPOL_CTRL [19].ACT[29:27]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [20].ACT[2:0]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [21].ACT[5:3]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [22].ACT[8:6]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [23].ACT[11:9]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [24].ACT[14:12]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [25].ACT[17:15]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [26].ACT[20:18]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [27].ACT[23:21]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008938	RMA_PORT_EAPOL_CTRL [28].ACT[26:24]	3
0xBB008938	RMA_PORT_EAPOL_CTRL [29].ACT[29:27]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [30].ACT[2:0]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [31].ACT[5:3]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [32].ACT[8:6]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [33].ACT[11:9]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [34].ACT[14:12]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [35].ACT[17:15]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [36].ACT[20:18]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [37].ACT[23:21]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [38].ACT[26:24]	3
0xBB00893C	RMA_PORT_EAPOL_CTRL [39].ACT[29:27]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [40].ACT[2:0]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [41].ACT[5:3]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [42].ACT[8:6]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [43].ACT[11:9]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [44].ACT[14:12]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [45].ACT[17:15]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [46].ACT[20:18]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [47].ACT[23:21]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [48].ACT[26:24]	3
0xBB008940	RMA_PORT_EAPOL_CTRL [49].ACT[29:27]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [50].ACT[2:0]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [51].ACT[5:3]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [52].ACT[8:6]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [53].ACT[11:9]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [54].ACT[14:12]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [55].ACT[17:15]	3
0xBB008944	RMA_PORT_EAPOL_CTRL [56].ACT[20:18]	3
0xBB008918	RMA_PORT_LLDP_CTRL [0].ACT[2:0]	3
0xBB008918	RMA_PORT_LLDP_CTRL [1].ACT[5:3]	3
0xBB008918	RMA_PORT_LLDP_CTRL [2].ACT[8:6]	3
0xBB008918	RMA_PORT_LLDP_CTRL [3].ACT[11:9]	3
0xBB008918	RMA_PORT_LLDP_CTRL [4].ACT[14:12]	3
0xBB008918	RMA_PORT_LLDP_CTRL [5].ACT[17:15]	3
0xBB008918	RMA_PORT_LLDP_CTRL [6].ACT[20:18]	3
0xBB008918	RMA_PORT_LLDP_CTRL [7].ACT[23:21]	3
0xBB008918	RMA_PORT_LLDP_CTRL [8].ACT[26:24]	3
0xBB008918	RMA_PORT_LLDP_CTRL [9].ACT[29:27]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [10].ACT[2:0]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [11].ACT[5:3]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [12].ACT[8:6]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [13].ACT[11:9]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [14].ACT[14:12]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [15].ACT[17:15]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [16].ACT[20:18]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00891C	RMA_PORT_LLDP_CTRL [17].ACT[23:21]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [18].ACT[26:24]	3
0xBB00891C	RMA_PORT_LLDP_CTRL [19].ACT[29:27]	3
0xBB008920	RMA_PORT_LLDP_CTRL [20].ACT[2:0]	3
0xBB008920	RMA_PORT_LLDP_CTRL [21].ACT[5:3]	3
0xBB008920	RMA_PORT_LLDP_CTRL [22].ACT[8:6]	3
0xBB008920	RMA_PORT_LLDP_CTRL [23].ACT[11:9]	3
0xBB008920	RMA_PORT_LLDP_CTRL [24].ACT[14:12]	3
0xBB008920	RMA_PORT_LLDP_CTRL [25].ACT[17:15]	3
0xBB008920	RMA_PORT_LLDP_CTRL [26].ACT[20:18]	3
0xBB008920	RMA_PORT_LLDP_CTRL [27].ACT[23:21]	3
0xBB008920	RMA_PORT_LLDP_CTRL [28].ACT[26:24]	3
0xBB008920	RMA_PORT_LLDP_CTRL [29].ACT[29:27]	3
0xBB008924	RMA_PORT_LLDP_CTRL [30].ACT[2:0]	3
0xBB008924	RMA_PORT_LLDP_CTRL [31].ACT[5:3]	3
0xBB008924	RMA_PORT_LLDP_CTRL [32].ACT[8:6]	3
0xBB008924	RMA_PORT_LLDP_CTRL [33].ACT[11:9]	3
0xBB008924	RMA_PORT_LLDP_CTRL [34].ACT[14:12]	3
0xBB008924	RMA_PORT_LLDP_CTRL [35].ACT[17:15]	3
0xBB008924	RMA_PORT_LLDP_CTRL [36].ACT[20:18]	3
0xBB008924	RMA_PORT_LLDP_CTRL [37].ACT[23:21]	3
0xBB008924	RMA_PORT_LLDP_CTRL [38].ACT[26:24]	3
0xBB008924	RMA_PORT_LLDP_CTRL [39].ACT[29:27]	3
0xBB008928	RMA_PORT_LLDP_CTRL [40].ACT[2:0]	3
0xBB008928	RMA_PORT_LLDP_CTRL [41].ACT[5:3]	3
0xBB008928	RMA_PORT_LLDP_CTRL [42].ACT[8:6]	3
0xBB008928	RMA_PORT_LLDP_CTRL [43].ACT[11:9]	3
0xBB008928	RMA_PORT_LLDP_CTRL [44].ACT[14:12]	3
0xBB008928	RMA_PORT_LLDP_CTRL [45].ACT[17:15]	3
0xBB008928	RMA_PORT_LLDP_CTRL [46].ACT[20:18]	3
0xBB008928	RMA_PORT_LLDP_CTRL [47].ACT[23:21]	3
0xBB008928	RMA_PORT_LLDP_CTRL [48].ACT[26:24]	3
0xBB008928	RMA_PORT_LLDP_CTRL [49].ACT[29:27]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [50].ACT[2:0]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [51].ACT[5:3]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [52].ACT[8:6]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [53].ACT[11:9]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [54].ACT[14:12]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [55].ACT[17:15]	3
0xBB00892C	RMA_PORT_LLDP_CTRL [56].ACT[20:18]	3
0xBB008834	RMA_PORT_PTP_CTRL [0].RESERVED[31:4]	28
0xBB008834	RMA_PORT_PTP_CTRL [0].UDP_ACT[3:2]	2
0xBB008834	RMA_PORT_PTP_CTRL [0].ETH2_ACT[1:0]	2
0xBB008838	RMA_PORT_PTP_CTRL [1].RESERVED[31:4]	28
0xBB008838	RMA_PORT_PTP_CTRL [1].UDP_ACT[3:2]	2
0xBB008838	RMA_PORT_PTP_CTRL [1].ETH2_ACT[1:0]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00883C	RMA_PORT_PTP_CTRL [2].RESERVED[31:4]	28
0xBB00883C	RMA_PORT_PTP_CTRL [2].UDP_ACT[3:2]	2
0xBB00883C	RMA_PORT_PTP_CTRL [2].ETH2_ACT[1:0]	2
0xBB008840	RMA_PORT_PTP_CTRL [3].RESERVED[31:4]	28
0xBB008840	RMA_PORT_PTP_CTRL [3].UDP_ACT[3:2]	2
0xBB008840	RMA_PORT_PTP_CTRL [3].ETH2_ACT[1:0]	2
0xBB008844	RMA_PORT_PTP_CTRL [4].RESERVED[31:4]	28
0xBB008844	RMA_PORT_PTP_CTRL [4].UDP_ACT[3:2]	2
0xBB008844	RMA_PORT_PTP_CTRL [4].ETH2_ACT[1:0]	2
0xBB008848	RMA_PORT_PTP_CTRL [5].RESERVED[31:4]	28
0xBB008848	RMA_PORT_PTP_CTRL [5].UDP_ACT[3:2]	2
0xBB008848	RMA_PORT_PTP_CTRL [5].ETH2_ACT[1:0]	2
0xBB00884C	RMA_PORT_PTP_CTRL [6].RESERVED[31:4]	28
0xBB00884C	RMA_PORT_PTP_CTRL [6].UDP_ACT[3:2]	2
0xBB00884C	RMA_PORT_PTP_CTRL [6].ETH2_ACT[1:0]	2
0xBB008850	RMA_PORT_PTP_CTRL [7].RESERVED[31:4]	28
0xBB008850	RMA_PORT_PTP_CTRL [7].UDP_ACT[3:2]	2
0xBB008850	RMA_PORT_PTP_CTRL [7].ETH2_ACT[1:0]	2
0xBB008854	RMA_PORT_PTP_CTRL [8].RESERVED[31:4]	28
0xBB008854	RMA_PORT_PTP_CTRL [8].UDP_ACT[3:2]	2
0xBB008854	RMA_PORT_PTP_CTRL [8].ETH2_ACT[1:0]	2
0xBB008858	RMA_PORT_PTP_CTRL [9].RESERVED[31:4]	28
0xBB008858	RMA_PORT_PTP_CTRL [9].UDP_ACT[3:2]	2
0xBB008858	RMA_PORT_PTP_CTRL [9].ETH2_ACT[1:0]	2
0xBB00885C	RMA_PORT_PTP_CTRL [10].RESERVED[31:4]	28
0xBB00885C	RMA_PORT_PTP_CTRL [10].UDP_ACT[3:2]	2
0xBB00885C	RMA_PORT_PTP_CTRL [10].ETH2_ACT[1:0]	2
0xBB008860	RMA_PORT_PTP_CTRL [11].RESERVED[31:4]	28
0xBB008860	RMA_PORT_PTP_CTRL [11].UDP_ACT[3:2]	2
0xBB008860	RMA_PORT_PTP_CTRL [11].ETH2_ACT[1:0]	2
0xBB008864	RMA_PORT_PTP_CTRL [12].RESERVED[31:4]	28
0xBB008864	RMA_PORT_PTP_CTRL [12].UDP_ACT[3:2]	2
0xBB008864	RMA_PORT_PTP_CTRL [12].ETH2_ACT[1:0]	2
0xBB008868	RMA_PORT_PTP_CTRL [13].RESERVED[31:4]	28
0xBB008868	RMA_PORT_PTP_CTRL [13].UDP_ACT[3:2]	2
0xBB008868	RMA_PORT_PTP_CTRL [13].ETH2_ACT[1:0]	2
0xBB00886C	RMA_PORT_PTP_CTRL [14].RESERVED[31:4]	28
0xBB00886C	RMA_PORT_PTP_CTRL [14].UDP_ACT[3:2]	2
0xBB00886C	RMA_PORT_PTP_CTRL [14].ETH2_ACT[1:0]	2
0xBB008870	RMA_PORT_PTP_CTRL [15].RESERVED[31:4]	28
0xBB008870	RMA_PORT_PTP_CTRL [15].UDP_ACT[3:2]	2
0xBB008870	RMA_PORT_PTP_CTRL [15].ETH2_ACT[1:0]	2
0xBB008874	RMA_PORT_PTP_CTRL [16].RESERVED[31:4]	28
0xBB008874	RMA_PORT_PTP_CTRL [16].UDP_ACT[3:2]	2
0xBB008874	RMA_PORT_PTP_CTRL [16].ETH2_ACT[1:0]	2
0xBB008878	RMA_PORT_PTP_CTRL [17].RESERVED[31:4]	28

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008878	RMA_PORT_PTP_CTRL [17].UDP_ACT[3:2]	2
0xBB008878	RMA_PORT_PTP_CTRL [17].ETH2_ACT[1:0]	2
0xBB00887C	RMA_PORT_PTP_CTRL [18].RESERVED[31:4]	28
0xBB00887C	RMA_PORT_PTP_CTRL [18].UDP_ACT[3:2]	2
0xBB00887C	RMA_PORT_PTP_CTRL [18].ETH2_ACT[1:0]	2
0xBB008880	RMA_PORT_PTP_CTRL [19].RESERVED[31:4]	28
0xBB008880	RMA_PORT_PTP_CTRL [19].UDP_ACT[3:2]	2
0xBB008880	RMA_PORT_PTP_CTRL [19].ETH2_ACT[1:0]	2
0xBB008884	RMA_PORT_PTP_CTRL [20].RESERVED[31:4]	28
0xBB008884	RMA_PORT_PTP_CTRL [20].UDP_ACT[3:2]	2
0xBB008884	RMA_PORT_PTP_CTRL [20].ETH2_ACT[1:0]	2
0xBB008888	RMA_PORT_PTP_CTRL [21].RESERVED[31:4]	28
0xBB008888	RMA_PORT_PTP_CTRL [21].UDP_ACT[3:2]	2
0xBB008888	RMA_PORT_PTP_CTRL [21].ETH2_ACT[1:0]	2
0xBB00888C	RMA_PORT_PTP_CTRL [22].RESERVED[31:4]	28
0xBB00888C	RMA_PORT_PTP_CTRL [22].UDP_ACT[3:2]	2
0xBB00888C	RMA_PORT_PTP_CTRL [22].ETH2_ACT[1:0]	2
0xBB008890	RMA_PORT_PTP_CTRL [23].RESERVED[31:4]	28
0xBB008890	RMA_PORT_PTP_CTRL [23].UDP_ACT[3:2]	2
0xBB008890	RMA_PORT_PTP_CTRL [23].ETH2_ACT[1:0]	2
0xBB008894	RMA_PORT_PTP_CTRL [24].RESERVED[31:4]	28
0xBB008894	RMA_PORT_PTP_CTRL [24].UDP_ACT[3:2]	2
0xBB008894	RMA_PORT_PTP_CTRL [24].ETH2_ACT[1:0]	2
0xBB008898	RMA_PORT_PTP_CTRL [25].RESERVED[31:4]	28
0xBB008898	RMA_PORT_PTP_CTRL [25].UDP_ACT[3:2]	2
0xBB008898	RMA_PORT_PTP_CTRL [25].ETH2_ACT[1:0]	2
0xBB00889C	RMA_PORT_PTP_CTRL [26].RESERVED[31:4]	28
0xBB00889C	RMA_PORT_PTP_CTRL [26].UDP_ACT[3:2]	2
0xBB00889C	RMA_PORT_PTP_CTRL [26].ETH2_ACT[1:0]	2
0xBB0088A0	RMA_PORT_PTP_CTRL [27].RESERVED[31:4]	28
0xBB0088A0	RMA_PORT_PTP_CTRL [27].UDP_ACT[3:2]	2
0xBB0088A0	RMA_PORT_PTP_CTRL [27].ETH2_ACT[1:0]	2
0xBB0088A4	RMA_PORT_PTP_CTRL [28].RESERVED[31:4]	28
0xBB0088A4	RMA_PORT_PTP_CTRL [28].UDP_ACT[3:2]	2
0xBB0088A4	RMA_PORT_PTP_CTRL [28].ETH2_ACT[1:0]	2
0xBB0088A8	RMA_PORT_PTP_CTRL [29].RESERVED[31:4]	28
0xBB0088A8	RMA_PORT_PTP_CTRL [29].UDP_ACT[3:2]	2
0xBB0088A8	RMA_PORT_PTP_CTRL [29].ETH2_ACT[1:0]	2
0xBB0088AC	RMA_PORT_PTP_CTRL [30].RESERVED[31:4]	28
0xBB0088AC	RMA_PORT_PTP_CTRL [30].UDP_ACT[3:2]	2
0xBB0088AC	RMA_PORT_PTP_CTRL [30].ETH2_ACT[1:0]	2
0xBB0088B0	RMA_PORT_PTP_CTRL [31].RESERVED[31:4]	28
0xBB0088B0	RMA_PORT_PTP_CTRL [31].UDP_ACT[3:2]	2
0xBB0088B0	RMA_PORT_PTP_CTRL [31].ETH2_ACT[1:0]	2
0xBB0088B4	RMA_PORT_PTP_CTRL [32].RESERVED[31:4]	28
0xBB0088B4	RMA_PORT_PTP_CTRL [32].UDP_ACT[3:2]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0088B4	RMA_PORT_PTP_CTRL [32].ETH2_ACT[1:0]	2
0xBB0088B8	RMA_PORT_PTP_CTRL [33].RESERVED[31:4]	28
0xBB0088B8	RMA_PORT_PTP_CTRL [33].UDP_ACT[3:2]	2
0xBB0088B8	RMA_PORT_PTP_CTRL [33].ETH2_ACT[1:0]	2
0xBB0088BC	RMA_PORT_PTP_CTRL [34].RESERVED[31:4]	28
0xBB0088BC	RMA_PORT_PTP_CTRL [34].UDP_ACT[3:2]	2
0xBB0088BC	RMA_PORT_PTP_CTRL [34].ETH2_ACT[1:0]	2
0xBB0088C0	RMA_PORT_PTP_CTRL [35].RESERVED[31:4]	28
0xBB0088C0	RMA_PORT_PTP_CTRL [35].UDP_ACT[3:2]	2
0xBB0088C0	RMA_PORT_PTP_CTRL [35].ETH2_ACT[1:0]	2
0xBB0088C4	RMA_PORT_PTP_CTRL [36].RESERVED[31:4]	28
0xBB0088C4	RMA_PORT_PTP_CTRL [36].UDP_ACT[3:2]	2
0xBB0088C4	RMA_PORT_PTP_CTRL [36].ETH2_ACT[1:0]	2
0xBB0088C8	RMA_PORT_PTP_CTRL [37].RESERVED[31:4]	28
0xBB0088C8	RMA_PORT_PTP_CTRL [37].UDP_ACT[3:2]	2
0xBB0088C8	RMA_PORT_PTP_CTRL [37].ETH2_ACT[1:0]	2
0xBB0088CC	RMA_PORT_PTP_CTRL [38].RESERVED[31:4]	28
0xBB0088CC	RMA_PORT_PTP_CTRL [38].UDP_ACT[3:2]	2
0xBB0088CC	RMA_PORT_PTP_CTRL [38].ETH2_ACT[1:0]	2
0xBB0088D0	RMA_PORT_PTP_CTRL [39].RESERVED[31:4]	28
0xBB0088D0	RMA_PORT_PTP_CTRL [39].UDP_ACT[3:2]	2
0xBB0088D0	RMA_PORT_PTP_CTRL [39].ETH2_ACT[1:0]	2
0xBB0088D4	RMA_PORT_PTP_CTRL [40].RESERVED[31:4]	28
0xBB0088D4	RMA_PORT_PTP_CTRL [40].UDP_ACT[3:2]	2
0xBB0088D4	RMA_PORT_PTP_CTRL [40].ETH2_ACT[1:0]	2
0xBB0088D8	RMA_PORT_PTP_CTRL [41].RESERVED[31:4]	28
0xBB0088D8	RMA_PORT_PTP_CTRL [41].UDP_ACT[3:2]	2
0xBB0088D8	RMA_PORT_PTP_CTRL [41].ETH2_ACT[1:0]	2
0xBB0088DC	RMA_PORT_PTP_CTRL [42].RESERVED[31:4]	28
0xBB0088DC	RMA_PORT_PTP_CTRL [42].UDP_ACT[3:2]	2
0xBB0088DC	RMA_PORT_PTP_CTRL [42].ETH2_ACT[1:0]	2
0xBB0088E0	RMA_PORT_PTP_CTRL [43].RESERVED[31:4]	28
0xBB0088E0	RMA_PORT_PTP_CTRL [43].UDP_ACT[3:2]	2
0xBB0088E0	RMA_PORT_PTP_CTRL [43].ETH2_ACT[1:0]	2
0xBB0088E4	RMA_PORT_PTP_CTRL [44].RESERVED[31:4]	28
0xBB0088E4	RMA_PORT_PTP_CTRL [44].UDP_ACT[3:2]	2
0xBB0088E4	RMA_PORT_PTP_CTRL [44].ETH2_ACT[1:0]	2
0xBB0088E8	RMA_PORT_PTP_CTRL [45].RESERVED[31:4]	28
0xBB0088E8	RMA_PORT_PTP_CTRL [45].UDP_ACT[3:2]	2
0xBB0088E8	RMA_PORT_PTP_CTRL [45].ETH2_ACT[1:0]	2
0xBB0088EC	RMA_PORT_PTP_CTRL [46].RESERVED[31:4]	28
0xBB0088EC	RMA_PORT_PTP_CTRL [46].UDP_ACT[3:2]	2
0xBB0088EC	RMA_PORT_PTP_CTRL [46].ETH2_ACT[1:0]	2
0xBB0088F0	RMA_PORT_PTP_CTRL [47].RESERVED[31:4]	28
0xBB0088F0	RMA_PORT_PTP_CTRL [47].UDP_ACT[3:2]	2
0xBB0088F0	RMA_PORT_PTP_CTRL [47].ETH2_ACT[1:0]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0088F4	RMA_PORT_PTP_CTRL [48].RESERVED[31:4]	28
0xBB0088F4	RMA_PORT_PTP_CTRL [48].UDP_ACT[3:2]	2
0xBB0088F4	RMA_PORT_PTP_CTRL [48].ETH2_ACT[1:0]	2
0xBB0088F8	RMA_PORT_PTP_CTRL [49].RESERVED[31:4]	28
0xBB0088F8	RMA_PORT_PTP_CTRL [49].UDP_ACT[3:2]	2
0xBB0088F8	RMA_PORT_PTP_CTRL [49].ETH2_ACT[1:0]	2
0xBB0088FC	RMA_PORT_PTP_CTRL [50].RESERVED[31:4]	28
0xBB0088FC	RMA_PORT_PTP_CTRL [50].UDP_ACT[3:2]	2
0xBB0088FC	RMA_PORT_PTP_CTRL [50].ETH2_ACT[1:0]	2
0xBB008900	RMA_PORT_PTP_CTRL [51].RESERVED[31:4]	28
0xBB008900	RMA_PORT_PTP_CTRL [51].UDP_ACT[3:2]	2
0xBB008900	RMA_PORT_PTP_CTRL [51].ETH2_ACT[1:0]	2
0xBB008904	RMA_PORT_PTP_CTRL [52].RESERVED[31:4]	28
0xBB008904	RMA_PORT_PTP_CTRL [52].UDP_ACT[3:2]	2
0xBB008904	RMA_PORT_PTP_CTRL [52].ETH2_ACT[1:0]	2
0xBB008908	RMA_PORT_PTP_CTRL [53].RESERVED[31:4]	28
0xBB008908	RMA_PORT_PTP_CTRL [53].UDP_ACT[3:2]	2
0xBB008908	RMA_PORT_PTP_CTRL [53].ETH2_ACT[1:0]	2
0xBB00890C	RMA_PORT_PTP_CTRL [54].RESERVED[31:4]	28
0xBB00890C	RMA_PORT_PTP_CTRL [54].UDP_ACT[3:2]	2
0xBB00890C	RMA_PORT_PTP_CTRL [54].ETH2_ACT[1:0]	2
0xBB008910	RMA_PORT_PTP_CTRL [55].RESERVED[31:4]	28
0xBB008910	RMA_PORT_PTP_CTRL [55].UDP_ACT[3:2]	2
0xBB008910	RMA_PORT_PTP_CTRL [55].ETH2_ACT[1:0]	2
0xBB008914	RMA_PORT_PTP_CTRL [56].RESERVED[31:4]	28
0xBB008914	RMA_PORT_PTP_CTRL [56].UDP_ACT[3:2]	2
0xBB008914	RMA_PORT_PTP_CTRL [56].ETH2_ACT[1:0]	2
0xBB008810	RMA_SMAC_LRN_CTRL [0].LRN[0:0]	1
0xBB008810	RMA_SMAC_LRN_CTRL [1].LRN[1:1]	1
0xBB008810	RMA_SMAC_LRN_CTRL [2].LRN[2:2]	1
0xBB008810	RMA_SMAC_LRN_CTRL [3].LRN[3:3]	1
0xBB008810	RMA_SMAC_LRN_CTRL [4].LRN[4:4]	1
0xBB008810	RMA_SMAC_LRN_CTRL [5].LRN[5:5]	1
0xBB008810	RMA_SMAC_LRN_CTRL [6].LRN[6:6]	1
0xBB008810	RMA_SMAC_LRN_CTRL [7].LRN[7:7]	1
0xBB008810	RMA_SMAC_LRN_CTRL [8].LRN[8:8]	1
0xBB008810	RMA_SMAC_LRN_CTRL [9].LRN[9:9]	1
0xBB008810	RMA_SMAC_LRN_CTRL [10].LRN[10:10]	1
0xBB008810	RMA_SMAC_LRN_CTRL [11].LRN[11:11]	1
0xBB008810	RMA_SMAC_LRN_CTRL [12].LRN[12:12]	1
0xBB008810	RMA_SMAC_LRN_CTRL [13].LRN[13:13]	1
0xBB008810	RMA_SMAC_LRN_CTRL [14].LRN[14:14]	1
0xBB008810	RMA_SMAC_LRN_CTRL [15].LRN[15:15]	1
0xBB008810	RMA_SMAC_LRN_CTRL [16].LRN[16:16]	1
0xBB008810	RMA_SMAC_LRN_CTRL [17].LRN[17:17]	1
0xBB008810	RMA_SMAC_LRN_CTRL [18].LRN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008810	RMA_SMAC_LRN_CTRL [19].LRN[19:19]	1
0xBB008810	RMA_SMAC_LRN_CTRL [20].LRN[20:20]	1
0xBB008810	RMA_SMAC_LRN_CTRL [21].LRN[21:21]	1
0xBB008810	RMA_SMAC_LRN_CTRL [22].LRN[22:22]	1
0xBB008810	RMA_SMAC_LRN_CTRL [23].LRN[23:23]	1
0xBB008810	RMA_SMAC_LRN_CTRL [24].LRN[24:24]	1
0xBB008810	RMA_SMAC_LRN_CTRL [25].LRN[25:25]	1
0xBB008810	RMA_SMAC_LRN_CTRL [26].LRN[26:26]	1
0xBB008810	RMA_SMAC_LRN_CTRL [27].LRN[27:27]	1
0xBB008810	RMA_SMAC_LRN_CTRL [28].LRN[28:28]	1
0xBB008810	RMA_SMAC_LRN_CTRL [29].LRN[29:29]	1
0xBB008810	RMA_SMAC_LRN_CTRL [30].LRN[30:30]	1
0xBB008810	RMA_SMAC_LRN_CTRL [31].LRN[31:31]	1
0xBB008814	RMA_SMAC_LRN_CTRL [32].LRN[0:0]	1
0xBB008814	RMA_SMAC_LRN_CTRL [33].LRN[1:1]	1
0xBB008814	RMA_SMAC_LRN_CTRL [34].LRN[2:2]	1
0xBB008814	RMA_SMAC_LRN_CTRL [35].LRN[3:3]	1
0xBB008814	RMA_SMAC_LRN_CTRL [36].LRN[4:4]	1
0xBB008814	RMA_SMAC_LRN_CTRL [37].LRN[5:5]	1
0xBB008814	RMA_SMAC_LRN_CTRL [38].LRN[6:6]	1
0xBB008814	RMA_SMAC_LRN_CTRL [39].LRN[7:7]	1
0xBB008814	RMA_SMAC_LRN_CTRL [40].LRN[8:8]	1
0xBB008814	RMA_SMAC_LRN_CTRL [41].LRN[9:9]	1
0xBB008814	RMA_SMAC_LRN_CTRL [42].LRN[10:10]	1
0xBB008814	RMA_SMAC_LRN_CTRL [43].LRN[11:11]	1
0xBB008814	RMA_SMAC_LRN_CTRL [44].LRN[12:12]	1
0xBB008814	RMA_SMAC_LRN_CTRL [45].LRN[13:13]	1
0xBB008814	RMA_SMAC_LRN_CTRL [46].LRN[14:14]	1
0xBB008814	RMA_SMAC_LRN_CTRL [47].LRN[15:15]	1
0xBB008970	RMA_USR_DEF_CTRL [0].RESERVED[31:16]	16
0xBB008970	RMA_USR_DEF_CTRL [0].ADDR_MAX_HI[15:0]	16
0xBB008974	RMA_USR_DEF_CTRL [0].ADDR_MAX_LO[31:0]	32
0xBB008978	RMA_USR_DEF_CTRL [0].RESERVED[31:16]	16
0xBB008978	RMA_USR_DEF_CTRL [0].ADDR_MIN_HI[15:0]	16
0xBB00897C	RMA_USR_DEF_CTRL [0].ADDR_MIN_LO[31:0]	32
0xBB008980	RMA_USR_DEF_CTRL [0].RESERVED[31:25]	7
0xBB008980	RMA_USR_DEF_CTRL [0].ETHER_TYPE[24:9]	16
0xBB008980	RMA_USR_DEF_CTRL [0].BYPASS_VLAN[8:8]	1
0xBB008980	RMA_USR_DEF_CTRL [0].LRN[7:7]	1
0xBB008980	RMA_USR_DEF_CTRL [0].BYPASS_STP[6:6]	1
0xBB008980	RMA_USR_DEF_CTRL [0].ACT[5:3]	3
0xBB008980	RMA_USR_DEF_CTRL [0].TYPE[2:1]	2
0xBB008980	RMA_USR_DEF_CTRL [0].EN[0:0]	1
0xBB008984	RMA_USR_DEF_CTRL [1].RESERVED[31:16]	16
0xBB008984	RMA_USR_DEF_CTRL [1].ADDR_MAX_HI[15:0]	16
0xBB008988	RMA_USR_DEF_CTRL [1].ADDR_MAX_LO[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00898C	RMA_USR_DEF_CTRL [1].RESERVED[31:16]	16
0xBB00898C	RMA_USR_DEF_CTRL [1].ADDR_MIN_HI[15:0]	16
0xBB008990	RMA_USR_DEF_CTRL [1].ADDR_MIN_LO[31:0]	32
0xBB008994	RMA_USR_DEF_CTRL [1].RESERVED[31:25]	7
0xBB008994	RMA_USR_DEF_CTRL [1].ETHER_TYPE[24:9]	16
0xBB008994	RMA_USR_DEF_CTRL [1].BYPASS_VLAN[8:8]	1
0xBB008994	RMA_USR_DEF_CTRL [1].LRN[7:7]	1
0xBB008994	RMA_USR_DEF_CTRL [1].BYPASS_STP[6:6]	1
0xBB008994	RMA_USR_DEF_CTRL [1].ACT[5:3]	3
0xBB008994	RMA_USR_DEF_CTRL [1].TYPE[2:1]	2
0xBB008994	RMA_USR_DEF_CTRL [1].EN[0:0]	1
0xBB008998	RMA_USR_DEF_CTRL [2].RESERVED[31:16]	16
0xBB008998	RMA_USR_DEF_CTRL [2].ADDR_MAX_HI[15:0]	16
0xBB00899C	RMA_USR_DEF_CTRL [2].ADDR_MAX_LO[31:0]	32
0xBB0089A0	RMA_USR_DEF_CTRL [2].RESERVED[31:16]	16
0xBB0089A0	RMA_USR_DEF_CTRL [2].ADDR_MIN_HI[15:0]	16
0xBB0089A4	RMA_USR_DEF_CTRL [2].ADDR_MIN_LO[31:0]	32
0xBB0089A8	RMA_USR_DEF_CTRL [2].RESERVED[31:25]	7
0xBB0089A8	RMA_USR_DEF_CTRL [2].ETHER_TYPE[24:9]	16
0xBB0089A8	RMA_USR_DEF_CTRL [2].BYPASS_VLAN[8:8]	1
0xBB0089A8	RMA_USR_DEF_CTRL [2].LRN[7:7]	1
0xBB0089A8	RMA_USR_DEF_CTRL [2].BYPASS_STP[6:6]	1
0xBB0089A8	RMA_USR_DEF_CTRL [2].ACT[5:3]	3
0xBB0089A8	RMA_USR_DEF_CTRL [2].TYPE[2:1]	2
0xBB0089A8	RMA_USR_DEF_CTRL [2].EN[0:0]	1
0xBB0089AC	RMA_USR_DEF_CTRL [3].RESERVED[31:16]	16
0xBB0089AC	RMA_USR_DEF_CTRL [3].ADDR_MAX_HI[15:0]	16
0xBB0089B0	RMA_USR_DEF_CTRL [3].ADDR_MAX_LO[31:0]	32
0xBB0089B4	RMA_USR_DEF_CTRL [3].RESERVED[31:16]	16
0xBB0089B4	RMA_USR_DEF_CTRL [3].ADDR_MIN_HI[15:0]	16
0xBB0089B8	RMA_USR_DEF_CTRL [3].ADDR_MIN_LO[31:0]	32
0xBB0089BC	RMA_USR_DEF_CTRL [3].RESERVED[31:25]	7
0xBB0089BC	RMA_USR_DEF_CTRL [3].ETHER_TYPE[24:9]	16
0xBB0089BC	RMA_USR_DEF_CTRL [3].BYPASS_VLAN[8:8]	1
0xBB0089BC	RMA_USR_DEF_CTRL [3].LRN[7:7]	1
0xBB0089BC	RMA_USR_DEF_CTRL [3].BYPASS_STP[6:6]	1
0xBB0089BC	RMA_USR_DEF_CTRL [3].ACT[5:3]	3
0xBB0089BC	RMA_USR_DEF_CTRL [3].TYPE[2:1]	2
0xBB0089BC	RMA_USR_DEF_CTRL [3].EN[0:0]	1
0xBB008968	RMA_USR_DEF_FLD_PMSK.RESERVED[31:25]	7
0xBB008968	RMA_USR_DEF_FLD_PMSK.PMSK_1[24:0]	25
0xBB00896C	RMA_USR_DEF_FLD_PMSK.PMSK_0[31:0]	32
0xBB004D90	RMK_CTRL.RESERVED[31:10]	22
0xBB004D90	RMK_CTRL.IPRI_RMK_SRC[9:8]	2
0xBB004D90	RMK_CTRL.OPRI_RMK_SRC[7:6]	2
0xBB004D90	RMK_CTRL.DEI_RMK_SRC[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004D90	RMK_CTRL.DSCP_RMK_SRC[4:2]	3
0xBB004D90	RMK_CTRL.IPRI_DFLT_CFG[1:1]	1
0xBB004D90	RMK_CTRL.OPRI_DFLT_CFG[0:0]	1
0xBB004ECC	RMK_DP2DEI_CTRL [0].DEI[0:0]	1
0xBB004ECC	RMK_DP2DEI_CTRL [1].DEI[1:1]	1
0xBB004ECC	RMK_DP2DEI_CTRL [2].DEI[2:2]	1
0xBB004F1C	RMK_DPINTPRI2DSCP_CTRL [0][0].DSCP[5:0]	6
0xBB004F1C	RMK_DPINTPRI2DSCP_CTRL [0][1].DSCP[11:6]	6
0xBB004F1C	RMK_DPINTPRI2DSCP_CTRL [0][2].DSCP[17:12]	6
0xBB004F1C	RMK_DPINTPRI2DSCP_CTRL [0][3].DSCP[23:18]	6
0xBB004F1C	RMK_DPINTPRI2DSCP_CTRL [0][4].DSCP[29:24]	6
0xBB004F20	RMK_DPINTPRI2DSCP_CTRL [0][5].DSCP[5:0]	6
0xBB004F20	RMK_DPINTPRI2DSCP_CTRL [0][6].DSCP[11:6]	6
0xBB004F20	RMK_DPINTPRI2DSCP_CTRL [0][7].DSCP[17:12]	6
0xBB004F24	RMK_DPINTPRI2DSCP_CTRL [1][0].DSCP[5:0]	6
0xBB004F24	RMK_DPINTPRI2DSCP_CTRL [1][1].DSCP[11:6]	6
0xBB004F24	RMK_DPINTPRI2DSCP_CTRL [1][2].DSCP[17:12]	6
0xBB004F24	RMK_DPINTPRI2DSCP_CTRL [1][3].DSCP[23:18]	6
0xBB004F24	RMK_DPINTPRI2DSCP_CTRL [1][4].DSCP[29:24]	6
0xBB004F28	RMK_DPINTPRI2DSCP_CTRL [1][5].DSCP[5:0]	6
0xBB004F28	RMK_DPINTPRI2DSCP_CTRL [1][6].DSCP[11:6]	6
0xBB004F28	RMK_DPINTPRI2DSCP_CTRL [1][7].DSCP[17:12]	6
0xBB004F2C	RMK_DPINTPRI2DSCP_CTRL [2][0].DSCP[5:0]	6
0xBB004F2C	RMK_DPINTPRI2DSCP_CTRL [2][1].DSCP[11:6]	6
0xBB004F2C	RMK_DPINTPRI2DSCP_CTRL [2][2].DSCP[17:12]	6
0xBB004F2C	RMK_DPINTPRI2DSCP_CTRL [2][3].DSCP[23:18]	6
0xBB004F2C	RMK_DPINTPRI2DSCP_CTRL [2][4].DSCP[29:24]	6
0xBB004F30	RMK_DPINTPRI2DSCP_CTRL [2][5].DSCP[5:0]	6
0xBB004F30	RMK_DPINTPRI2DSCP_CTRL [2][6].DSCP[11:6]	6
0xBB004F30	RMK_DPINTPRI2DSCP_CTRL [2][7].DSCP[17:12]	6
0xBB004EE8	RMK_DSCP2DSCP_CTRL [0].DSCP[5:0]	6
0xBB004EE8	RMK_DSCP2DSCP_CTRL [1].DSCP[11:6]	6
0xBB004EE8	RMK_DSCP2DSCP_CTRL [2].DSCP[17:12]	6
0xBB004EE8	RMK_DSCP2DSCP_CTRL [3].DSCP[23:18]	6
0xBB004EE8	RMK_DSCP2DSCP_CTRL [4].DSCP[29:24]	6
0xBB004EEC	RMK_DSCP2DSCP_CTRL [5].DSCP[5:0]	6
0xBB004EEC	RMK_DSCP2DSCP_CTRL [6].DSCP[11:6]	6
0xBB004EEC	RMK_DSCP2DSCP_CTRL [7].DSCP[17:12]	6
0xBB004EEC	RMK_DSCP2DSCP_CTRL [8].DSCP[23:18]	6
0xBB004EEC	RMK_DSCP2DSCP_CTRL [9].DSCP[29:24]	6
0xBB004EF0	RMK_DSCP2DSCP_CTRL [10].DSCP[5:0]	6
0xBB004EF0	RMK_DSCP2DSCP_CTRL [11].DSCP[11:6]	6
0xBB004EF0	RMK_DSCP2DSCP_CTRL [12].DSCP[17:12]	6
0xBB004EF0	RMK_DSCP2DSCP_CTRL [13].DSCP[23:18]	6
0xBB004EF0	RMK_DSCP2DSCP_CTRL [14].DSCP[29:24]	6
0xBB004EF4	RMK_DSCP2DSCP_CTRL [15].DSCP[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004EF4	RMK_DSCP2DSCP_CTRL [16].DSCP[11:6]	6
0xBB004EF4	RMK_DSCP2DSCP_CTRL [17].DSCP[17:12]	6
0xBB004EF4	RMK_DSCP2DSCP_CTRL [18].DSCP[23:18]	6
0xBB004EF4	RMK_DSCP2DSCP_CTRL [19].DSCP[29:24]	6
0xBB004EF8	RMK_DSCP2DSCP_CTRL [20].DSCP[5:0]	6
0xBB004EF8	RMK_DSCP2DSCP_CTRL [21].DSCP[11:6]	6
0xBB004EF8	RMK_DSCP2DSCP_CTRL [22].DSCP[17:12]	6
0xBB004EF8	RMK_DSCP2DSCP_CTRL [23].DSCP[23:18]	6
0xBB004EF8	RMK_DSCP2DSCP_CTRL [24].DSCP[29:24]	6
0xBB004EFC	RMK_DSCP2DSCP_CTRL [25].DSCP[5:0]	6
0xBB004EFC	RMK_DSCP2DSCP_CTRL [26].DSCP[11:6]	6
0xBB004EFC	RMK_DSCP2DSCP_CTRL [27].DSCP[17:12]	6
0xBB004EFC	RMK_DSCP2DSCP_CTRL [28].DSCP[23:18]	6
0xBB004EFC	RMK_DSCP2DSCP_CTRL [29].DSCP[29:24]	6
0xBB004F00	RMK_DSCP2DSCP_CTRL [30].DSCP[5:0]	6
0xBB004F00	RMK_DSCP2DSCP_CTRL [31].DSCP[11:6]	6
0xBB004F00	RMK_DSCP2DSCP_CTRL [32].DSCP[17:12]	6
0xBB004F00	RMK_DSCP2DSCP_CTRL [33].DSCP[23:18]	6
0xBB004F00	RMK_DSCP2DSCP_CTRL [34].DSCP[29:24]	6
0xBB004F04	RMK_DSCP2DSCP_CTRL [35].DSCP[5:0]	6
0xBB004F04	RMK_DSCP2DSCP_CTRL [36].DSCP[11:6]	6
0xBB004F04	RMK_DSCP2DSCP_CTRL [37].DSCP[17:12]	6
0xBB004F04	RMK_DSCP2DSCP_CTRL [38].DSCP[23:18]	6
0xBB004F04	RMK_DSCP2DSCP_CTRL [39].DSCP[29:24]	6
0xBB004F08	RMK_DSCP2DSCP_CTRL [40].DSCP[5:0]	6
0xBB004F08	RMK_DSCP2DSCP_CTRL [41].DSCP[11:6]	6
0xBB004F08	RMK_DSCP2DSCP_CTRL [42].DSCP[17:12]	6
0xBB004F08	RMK_DSCP2DSCP_CTRL [43].DSCP[23:18]	6
0xBB004F08	RMK_DSCP2DSCP_CTRL [44].DSCP[29:24]	6
0xBB004F0C	RMK_DSCP2DSCP_CTRL [45].DSCP[5:0]	6
0xBB004F0C	RMK_DSCP2DSCP_CTRL [46].DSCP[11:6]	6
0xBB004F0C	RMK_DSCP2DSCP_CTRL [47].DSCP[17:12]	6
0xBB004F0C	RMK_DSCP2DSCP_CTRL [48].DSCP[23:18]	6
0xBB004F0C	RMK_DSCP2DSCP_CTRL [49].DSCP[29:24]	6
0xBB004F10	RMK_DSCP2DSCP_CTRL [50].DSCP[5:0]	6
0xBB004F10	RMK_DSCP2DSCP_CTRL [51].DSCP[11:6]	6
0xBB004F10	RMK_DSCP2DSCP_CTRL [52].DSCP[17:12]	6
0xBB004F10	RMK_DSCP2DSCP_CTRL [53].DSCP[23:18]	6
0xBB004F10	RMK_DSCP2DSCP_CTRL [54].DSCP[29:24]	6
0xBB004F14	RMK_DSCP2DSCP_CTRL [55].DSCP[5:0]	6
0xBB004F14	RMK_DSCP2DSCP_CTRL [56].DSCP[11:6]	6
0xBB004F14	RMK_DSCP2DSCP_CTRL [57].DSCP[17:12]	6
0xBB004F14	RMK_DSCP2DSCP_CTRL [58].DSCP[23:18]	6
0xBB004F14	RMK_DSCP2DSCP_CTRL [59].DSCP[29:24]	6
0xBB004F18	RMK_DSCP2DSCP_CTRL [60].DSCP[5:0]	6
0xBB004F18	RMK_DSCP2DSCP_CTRL [61].DSCP[11:6]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004F18	RMK_DSCP2DSCP_CTRL [62].DSCP[17:12]	6
0xBB004F18	RMK_DSCP2DSCP_CTRL [63].DSCP[23:18]	6
0xBB004E84	RMK_DSCP2IPRI_CTRL [0].IPRI[2:0]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [1].IPRI[5:3]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [2].IPRI[8:6]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [3].IPRI[11:9]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [4].IPRI[14:12]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [5].IPRI[17:15]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [6].IPRI[20:18]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [7].IPRI[23:21]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [8].IPRI[26:24]	3
0xBB004E84	RMK_DSCP2IPRI_CTRL [9].IPRI[29:27]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [10].IPRI[2:0]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [11].IPRI[5:3]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [12].IPRI[8:6]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [13].IPRI[11:9]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [14].IPRI[14:12]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [15].IPRI[17:15]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [16].IPRI[20:18]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [17].IPRI[23:21]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [18].IPRI[26:24]	3
0xBB004E88	RMK_DSCP2IPRI_CTRL [19].IPRI[29:27]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [20].IPRI[2:0]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [21].IPRI[5:3]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [22].IPRI[8:6]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [23].IPRI[11:9]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [24].IPRI[14:12]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [25].IPRI[17:15]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [26].IPRI[20:18]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [27].IPRI[23:21]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [28].IPRI[26:24]	3
0xBB004E8C	RMK_DSCP2IPRI_CTRL [29].IPRI[29:27]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [30].IPRI[2:0]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [31].IPRI[5:3]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [32].IPRI[8:6]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [33].IPRI[11:9]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [34].IPRI[14:12]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [35].IPRI[17:15]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [36].IPRI[20:18]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [37].IPRI[23:21]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [38].IPRI[26:24]	3
0xBB004E90	RMK_DSCP2IPRI_CTRL [39].IPRI[29:27]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [40].IPRI[2:0]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [41].IPRI[5:3]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [42].IPRI[8:6]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [43].IPRI[11:9]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004E94	RMK_DSCP2IPRI_CTRL [44].IPRI[14:12]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [45].IPRI[17:15]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [46].IPRI[20:18]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [47].IPRI[23:21]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [48].IPRI[26:24]	3
0xBB004E94	RMK_DSCP2IPRI_CTRL [49].IPRI[29:27]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [50].IPRI[2:0]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [51].IPRI[5:3]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [52].IPRI[8:6]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [53].IPRI[11:9]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [54].IPRI[14:12]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [55].IPRI[17:15]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [56].IPRI[20:18]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [57].IPRI[23:21]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [58].IPRI[26:24]	3
0xBB004E98	RMK_DSCP2IPRI_CTRL [59].IPRI[29:27]	3
0xBB004E9C	RMK_DSCP2IPRI_CTRL [60].IPRI[2:0]	3
0xBB004E9C	RMK_DSCP2IPRI_CTRL [61].IPRI[5:3]	3
0xBB004E9C	RMK_DSCP2IPRI_CTRL [62].IPRI[8:6]	3
0xBB004E9C	RMK_DSCP2IPRI_CTRL [63].IPRI[11:9]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [0].OPRI[2:0]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [1].OPRI[5:3]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [2].OPRI[8:6]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [3].OPRI[11:9]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [4].OPRI[14:12]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [5].OPRI[17:15]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [6].OPRI[20:18]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [7].OPRI[23:21]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [8].OPRI[26:24]	3
0xBB004EAC	RMK_DSCP2OPRI_CTRL [9].OPRI[29:27]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [10].OPRI[2:0]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [11].OPRI[5:3]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [12].OPRI[8:6]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [13].OPRI[11:9]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [14].OPRI[14:12]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [15].OPRI[17:15]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [16].OPRI[20:18]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [17].OPRI[23:21]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [18].OPRI[26:24]	3
0xBB004EB0	RMK_DSCP2OPRI_CTRL [19].OPRI[29:27]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [20].OPRI[2:0]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [21].OPRI[5:3]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [22].OPRI[8:6]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [23].OPRI[11:9]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [24].OPRI[14:12]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [25].OPRI[17:15]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004EB4	RMK_DSCP2OPRI_CTRL [26].OPRI[20:18]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [27].OPRI[23:21]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [28].OPRI[26:24]	3
0xBB004EB4	RMK_DSCP2OPRI_CTRL [29].OPRI[29:27]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [30].OPRI[2:0]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [31].OPRI[5:3]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [32].OPRI[8:6]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [33].OPRI[11:9]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [34].OPRI[14:12]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [35].OPRI[17:15]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [36].OPRI[20:18]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [37].OPRI[23:21]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [38].OPRI[26:24]	3
0xBB004EB8	RMK_DSCP2OPRI_CTRL [39].OPRI[29:27]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [40].OPRI[2:0]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [41].OPRI[5:3]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [42].OPRI[8:6]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [43].OPRI[11:9]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [44].OPRI[14:12]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [45].OPRI[17:15]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [46].OPRI[20:18]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [47].OPRI[23:21]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [48].OPRI[26:24]	3
0xBB004EBC	RMK_DSCP2OPRI_CTRL [49].OPRI[29:27]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [50].OPRI[2:0]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [51].OPRI[5:3]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [52].OPRI[8:6]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [53].OPRI[11:9]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [54].OPRI[14:12]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [55].OPRI[17:15]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [56].OPRI[20:18]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [57].OPRI[23:21]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [58].OPRI[26:24]	3
0xBB004EC0	RMK_DSCP2OPRI_CTRL [59].OPRI[29:27]	3
0xBB004EC4	RMK_DSCP2OPRI_CTRL [60].OPRI[2:0]	3
0xBB004EC4	RMK_DSCP2OPRI_CTRL [61].OPRI[5:3]	3
0xBB004EC4	RMK_DSCP2OPRI_CTRL [62].OPRI[8:6]	3
0xBB004EC4	RMK_DSCP2OPRI_CTRL [63].OPRI[11:9]	3
0xBB004EC8	RMK_INTPRI2DEI_CTRL [0].DEI[0:0]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [1].DEI[1:1]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [2].DEI[2:2]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [3].DEI[3:3]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [4].DEI[4:4]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [5].DEI[5:5]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [6].DEI[6:6]	1
0xBB004EC8	RMK_INTPRI2DEI_CTRL [7].DEI[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004ED0	RMK_INTPRI2DSCP_CTRL [0].DSCP[5:0]	6
0xBB004ED0	RMK_INTPRI2DSCP_CTRL [1].DSCP[11:6]	6
0xBB004ED0	RMK_INTPRI2DSCP_CTRL [2].DSCP[17:12]	6
0xBB004ED0	RMK_INTPRI2DSCP_CTRL [3].DSCP[23:18]	6
0xBB004ED0	RMK_INTPRI2DSCP_CTRL [4].DSCP[29:24]	6
0xBB004ED4	RMK_INTPRI2DSCP_CTRL [5].DSCP[5:0]	6
0xBB004ED4	RMK_INTPRI2DSCP_CTRL [6].DSCP[11:6]	6
0xBB004ED4	RMK_INTPRI2DSCP_CTRL [7].DSCP[17:12]	6
0xBB004E78	RMK_INTPRI2IPRI_CTRL [0].IPRI[2:0]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [1].IPRI[5:3]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [2].IPRI[8:6]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [3].IPRI[11:9]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [4].IPRI[14:12]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [5].IPRI[17:15]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [6].IPRI[20:18]	3
0xBB004E78	RMK_INTPRI2IPRI_CTRL [7].IPRI[23:21]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [0].OPRI[2:0]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [1].OPRI[5:3]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [2].OPRI[8:6]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [3].OPRI[11:9]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [4].OPRI[14:12]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [5].OPRI[17:15]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [6].OPRI[20:18]	3
0xBB004EA0	RMK_INTPRI2OPRI_CTRL [7].OPRI[23:21]	3
0xBB004ED8	RMK_IPRI2DSCP_CTRL [0].DSCP[5:0]	6
0xBB004ED8	RMK_IPRI2DSCP_CTRL [1].DSCP[11:6]	6
0xBB004ED8	RMK_IPRI2DSCP_CTRL [2].DSCP[17:12]	6
0xBB004ED8	RMK_IPRI2DSCP_CTRL [3].DSCP[23:18]	6
0xBB004ED8	RMK_IPRI2DSCP_CTRL [4].DSCP[29:24]	6
0xBB004EDC	RMK_IPRI2DSCP_CTRL [5].DSCP[5:0]	6
0xBB004EDC	RMK_IPRI2DSCP_CTRL [6].DSCP[11:6]	6
0xBB004EDC	RMK_IPRI2DSCP_CTRL [7].DSCP[17:12]	6
0xBB004E7C	RMK_IPRI2IPRI_CTRL [0].IPRI[2:0]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [1].IPRI[5:3]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [2].IPRI[8:6]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [3].IPRI[11:9]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [4].IPRI[14:12]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [5].IPRI[17:15]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [6].IPRI[20:18]	3
0xBB004E7C	RMK_IPRI2IPRI_CTRL [7].IPRI[23:21]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [0].OPRI[2:0]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [1].OPRI[5:3]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [2].OPRI[8:6]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [3].OPRI[11:9]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [4].OPRI[14:12]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [5].OPRI[17:15]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004EA4	RMK_IPRI2OPRI_CTRL [6].OPRI[20:18]	3
0xBB004EA4	RMK_IPRI2OPRI_CTRL [7].OPRI[23:21]	3
0xBB004EE0	RMK_OPRI2DSCP_CTRL [0].DSCP[5:0]	6
0xBB004EE0	RMK_OPRI2DSCP_CTRL [1].DSCP[11:6]	6
0xBB004EE0	RMK_OPRI2DSCP_CTRL [2].DSCP[17:12]	6
0xBB004EE0	RMK_OPRI2DSCP_CTRL [3].DSCP[23:18]	6
0xBB004EE0	RMK_OPRI2DSCP_CTRL [4].DSCP[29:24]	6
0xBB004EE4	RMK_OPRI2DSCP_CTRL [5].DSCP[5:0]	6
0xBB004EE4	RMK_OPRI2DSCP_CTRL [6].DSCP[11:6]	6
0xBB004EE4	RMK_OPRI2DSCP_CTRL [7].DSCP[17:12]	6
0xBB004E80	RMK_OPRI2IPRI_CTRL [0].IPRI[2:0]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [1].IPRI[5:3]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [2].IPRI[8:6]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [3].IPRI[11:9]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [4].IPRI[14:12]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [5].IPRI[17:15]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [6].IPRI[20:18]	3
0xBB004E80	RMK_OPRI2IPRI_CTRL [7].IPRI[23:21]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [0].OPRI[2:0]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [1].OPRI[5:3]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [2].OPRI[8:6]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [3].OPRI[11:9]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [4].OPRI[14:12]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [5].OPRI[17:15]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [6].OPRI[20:18]	3
0xBB004EA8	RMK_OPRI2OPRI_CTRL [7].OPRI[23:21]	3
0xBB004D94	RMK_PORT_CTRL [0].RESERVED[31:5]	27
0xBB004D94	RMK_PORT_CTRL [0].DEI_RMK_TAG_SEL[4:4]	1
0xBB004D94	RMK_PORT_CTRL [0].DEI_RMK_EN[3:3]	1
0xBB004D94	RMK_PORT_CTRL [0].DSCP_RMK_EN[2:2]	1
0xBB004D94	RMK_PORT_CTRL [0].OPRI_RMK_EN[1:1]	1
0xBB004D94	RMK_PORT_CTRL [0].IPRI_RMK_EN[0:0]	1
0xBB004D98	RMK_PORT_CTRL [1].RESERVED[31:5]	27
0xBB004D98	RMK_PORT_CTRL [1].DEI_RMK_TAG_SEL[4:4]	1
0xBB004D98	RMK_PORT_CTRL [1].DEI_RMK_EN[3:3]	1
0xBB004D98	RMK_PORT_CTRL [1].DSCP_RMK_EN[2:2]	1
0xBB004D98	RMK_PORT_CTRL [1].OPRI_RMK_EN[1:1]	1
0xBB004D98	RMK_PORT_CTRL [1].IPRI_RMK_EN[0:0]	1
0xBB004D9C	RMK_PORT_CTRL [2].RESERVED[31:5]	27
0xBB004D9C	RMK_PORT_CTRL [2].DEI_RMK_TAG_SEL[4:4]	1
0xBB004D9C	RMK_PORT_CTRL [2].DEI_RMK_EN[3:3]	1
0xBB004D9C	RMK_PORT_CTRL [2].DSCP_RMK_EN[2:2]	1
0xBB004D9C	RMK_PORT_CTRL [2].OPRI_RMK_EN[1:1]	1
0xBB004D9C	RMK_PORT_CTRL [2].IPRI_RMK_EN[0:0]	1
0xBB004DA0	RMK_PORT_CTRL [3].RESERVED[31:5]	27
0xBB004DA0	RMK_PORT_CTRL [3].DEI_RMK_TAG_SEL[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004DA0	RMK_PORT_CTRL [3].DEI_RMK_EN[3:3]	1
0xBB004DA0	RMK_PORT_CTRL [3].DSCP_RMK_EN[2:2]	1
0xBB004DA0	RMK_PORT_CTRL [3].OPRI_RMK_EN[1:1]	1
0xBB004DA0	RMK_PORT_CTRL [3].IPRI_RMK_EN[0:0]	1
0xBB004DA4	RMK_PORT_CTRL [4].RESERVED[31:5]	27
0xBB004DA4	RMK_PORT_CTRL [4].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DA4	RMK_PORT_CTRL [4].DEI_RMK_EN[3:3]	1
0xBB004DA4	RMK_PORT_CTRL [4].DSCP_RMK_EN[2:2]	1
0xBB004DA4	RMK_PORT_CTRL [4].OPRI_RMK_EN[1:1]	1
0xBB004DA4	RMK_PORT_CTRL [4].IPRI_RMK_EN[0:0]	1
0xBB004DA8	RMK_PORT_CTRL [5].RESERVED[31:5]	27
0xBB004DA8	RMK_PORT_CTRL [5].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DA8	RMK_PORT_CTRL [5].DEI_RMK_EN[3:3]	1
0xBB004DA8	RMK_PORT_CTRL [5].DSCP_RMK_EN[2:2]	1
0xBB004DA8	RMK_PORT_CTRL [5].OPRI_RMK_EN[1:1]	1
0xBB004DA8	RMK_PORT_CTRL [5].IPRI_RMK_EN[0:0]	1
0xBB004DAC	RMK_PORT_CTRL [6].RESERVED[31:5]	27
0xBB004DAC	RMK_PORT_CTRL [6].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DAC	RMK_PORT_CTRL [6].DEI_RMK_EN[3:3]	1
0xBB004DAC	RMK_PORT_CTRL [6].DSCP_RMK_EN[2:2]	1
0xBB004DAC	RMK_PORT_CTRL [6].OPRI_RMK_EN[1:1]	1
0xBB004DAC	RMK_PORT_CTRL [6].IPRI_RMK_EN[0:0]	1
0xBB004DB0	RMK_PORT_CTRL [7].RESERVED[31:5]	27
0xBB004DB0	RMK_PORT_CTRL [7].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DB0	RMK_PORT_CTRL [7].DEI_RMK_EN[3:3]	1
0xBB004DB0	RMK_PORT_CTRL [7].DSCP_RMK_EN[2:2]	1
0xBB004DB0	RMK_PORT_CTRL [7].OPRI_RMK_EN[1:1]	1
0xBB004DB0	RMK_PORT_CTRL [7].IPRI_RMK_EN[0:0]	1
0xBB004DB4	RMK_PORT_CTRL [8].RESERVED[31:5]	27
0xBB004DB4	RMK_PORT_CTRL [8].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DB4	RMK_PORT_CTRL [8].DEI_RMK_EN[3:3]	1
0xBB004DB4	RMK_PORT_CTRL [8].DSCP_RMK_EN[2:2]	1
0xBB004DB4	RMK_PORT_CTRL [8].OPRI_RMK_EN[1:1]	1
0xBB004DB4	RMK_PORT_CTRL [8].IPRI_RMK_EN[0:0]	1
0xBB004DB8	RMK_PORT_CTRL [9].RESERVED[31:5]	27
0xBB004DB8	RMK_PORT_CTRL [9].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DB8	RMK_PORT_CTRL [9].DEI_RMK_EN[3:3]	1
0xBB004DB8	RMK_PORT_CTRL [9].DSCP_RMK_EN[2:2]	1
0xBB004DB8	RMK_PORT_CTRL [9].OPRI_RMK_EN[1:1]	1
0xBB004DB8	RMK_PORT_CTRL [9].IPRI_RMK_EN[0:0]	1
0xBB004DBC	RMK_PORT_CTRL [10].RESERVED[31:5]	27
0xBB004DBC	RMK_PORT_CTRL [10].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DBC	RMK_PORT_CTRL [10].DEI_RMK_EN[3:3]	1
0xBB004DBC	RMK_PORT_CTRL [10].DSCP_RMK_EN[2:2]	1
0xBB004DBC	RMK_PORT_CTRL [10].OPRI_RMK_EN[1:1]	1
0xBB004DBC	RMK_PORT_CTRL [10].IPRI_RMK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004DC0	RMK_PORT_CTRL [11].RESERVED[31:5]	27
0xBB004DC0	RMK_PORT_CTRL [11].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DC0	RMK_PORT_CTRL [11].DEI_RMK_EN[3:3]	1
0xBB004DC0	RMK_PORT_CTRL [11].DSCP_RMK_EN[2:2]	1
0xBB004DC0	RMK_PORT_CTRL [11].OPRI_RMK_EN[1:1]	1
0xBB004DC0	RMK_PORT_CTRL [11].IPRI_RMK_EN[0:0]	1
0xBB004DC4	RMK_PORT_CTRL [12].RESERVED[31:5]	27
0xBB004DC4	RMK_PORT_CTRL [12].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DC4	RMK_PORT_CTRL [12].DEI_RMK_EN[3:3]	1
0xBB004DC4	RMK_PORT_CTRL [12].DSCP_RMK_EN[2:2]	1
0xBB004DC4	RMK_PORT_CTRL [12].OPRI_RMK_EN[1:1]	1
0xBB004DC4	RMK_PORT_CTRL [12].IPRI_RMK_EN[0:0]	1
0xBB004DC8	RMK_PORT_CTRL [13].RESERVED[31:5]	27
0xBB004DC8	RMK_PORT_CTRL [13].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DC8	RMK_PORT_CTRL [13].DEI_RMK_EN[3:3]	1
0xBB004DC8	RMK_PORT_CTRL [13].DSCP_RMK_EN[2:2]	1
0xBB004DC8	RMK_PORT_CTRL [13].OPRI_RMK_EN[1:1]	1
0xBB004DC8	RMK_PORT_CTRL [13].IPRI_RMK_EN[0:0]	1
0xBB004DCC	RMK_PORT_CTRL [14].RESERVED[31:5]	27
0xBB004DCC	RMK_PORT_CTRL [14].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DCC	RMK_PORT_CTRL [14].DEI_RMK_EN[3:3]	1
0xBB004DCC	RMK_PORT_CTRL [14].DSCP_RMK_EN[2:2]	1
0xBB004DCC	RMK_PORT_CTRL [14].OPRI_RMK_EN[1:1]	1
0xBB004DCC	RMK_PORT_CTRL [14].IPRI_RMK_EN[0:0]	1
0xBB004DD0	RMK_PORT_CTRL [15].RESERVED[31:5]	27
0xBB004DD0	RMK_PORT_CTRL [15].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DD0	RMK_PORT_CTRL [15].DEI_RMK_EN[3:3]	1
0xBB004DD0	RMK_PORT_CTRL [15].DSCP_RMK_EN[2:2]	1
0xBB004DD0	RMK_PORT_CTRL [15].OPRI_RMK_EN[1:1]	1
0xBB004DD0	RMK_PORT_CTRL [15].IPRI_RMK_EN[0:0]	1
0xBB004DD4	RMK_PORT_CTRL [16].RESERVED[31:5]	27
0xBB004DD4	RMK_PORT_CTRL [16].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DD4	RMK_PORT_CTRL [16].DEI_RMK_EN[3:3]	1
0xBB004DD4	RMK_PORT_CTRL [16].DSCP_RMK_EN[2:2]	1
0xBB004DD4	RMK_PORT_CTRL [16].OPRI_RMK_EN[1:1]	1
0xBB004DD4	RMK_PORT_CTRL [16].IPRI_RMK_EN[0:0]	1
0xBB004DD8	RMK_PORT_CTRL [17].RESERVED[31:5]	27
0xBB004DD8	RMK_PORT_CTRL [17].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DD8	RMK_PORT_CTRL [17].DEI_RMK_EN[3:3]	1
0xBB004DD8	RMK_PORT_CTRL [17].DSCP_RMK_EN[2:2]	1
0xBB004DD8	RMK_PORT_CTRL [17].OPRI_RMK_EN[1:1]	1
0xBB004DD8	RMK_PORT_CTRL [17].IPRI_RMK_EN[0:0]	1
0xBB004DDC	RMK_PORT_CTRL [18].RESERVED[31:5]	27
0xBB004DDC	RMK_PORT_CTRL [18].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DDC	RMK_PORT_CTRL [18].DEI_RMK_EN[3:3]	1
0xBB004DDC	RMK_PORT_CTRL [18].DSCP_RMK_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004DDC	RMK_PORT_CTRL [18].OPRI_RMK_EN[1:1]	1
0xBB004DDC	RMK_PORT_CTRL [18].IPRI_RMK_EN[0:0]	1
0xBB004DE0	RMK_PORT_CTRL [19].RESERVED[31:5]	27
0xBB004DE0	RMK_PORT_CTRL [19].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DE0	RMK_PORT_CTRL [19].DEI_RMK_EN[3:3]	1
0xBB004DE0	RMK_PORT_CTRL [19].DSCP_RMK_EN[2:2]	1
0xBB004DE0	RMK_PORT_CTRL [19].OPRI_RMK_EN[1:1]	1
0xBB004DE0	RMK_PORT_CTRL [19].IPRI_RMK_EN[0:0]	1
0xBB004DE4	RMK_PORT_CTRL [20].RESERVED[31:5]	27
0xBB004DE4	RMK_PORT_CTRL [20].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DE4	RMK_PORT_CTRL [20].DEI_RMK_EN[3:3]	1
0xBB004DE4	RMK_PORT_CTRL [20].DSCP_RMK_EN[2:2]	1
0xBB004DE4	RMK_PORT_CTRL [20].OPRI_RMK_EN[1:1]	1
0xBB004DE4	RMK_PORT_CTRL [20].IPRI_RMK_EN[0:0]	1
0xBB004DE8	RMK_PORT_CTRL [21].RESERVED[31:5]	27
0xBB004DE8	RMK_PORT_CTRL [21].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DE8	RMK_PORT_CTRL [21].DEI_RMK_EN[3:3]	1
0xBB004DE8	RMK_PORT_CTRL [21].DSCP_RMK_EN[2:2]	1
0xBB004DE8	RMK_PORT_CTRL [21].OPRI_RMK_EN[1:1]	1
0xBB004DE8	RMK_PORT_CTRL [21].IPRI_RMK_EN[0:0]	1
0xBB004DEC	RMK_PORT_CTRL [22].RESERVED[31:5]	27
0xBB004DEC	RMK_PORT_CTRL [22].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DEC	RMK_PORT_CTRL [22].DEI_RMK_EN[3:3]	1
0xBB004DEC	RMK_PORT_CTRL [22].DSCP_RMK_EN[2:2]	1
0xBB004DEC	RMK_PORT_CTRL [22].OPRI_RMK_EN[1:1]	1
0xBB004DEC	RMK_PORT_CTRL [22].IPRI_RMK_EN[0:0]	1
0xBB004DF0	RMK_PORT_CTRL [23].RESERVED[31:5]	27
0xBB004DF0	RMK_PORT_CTRL [23].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DF0	RMK_PORT_CTRL [23].DEI_RMK_EN[3:3]	1
0xBB004DF0	RMK_PORT_CTRL [23].DSCP_RMK_EN[2:2]	1
0xBB004DF0	RMK_PORT_CTRL [23].OPRI_RMK_EN[1:1]	1
0xBB004DF0	RMK_PORT_CTRL [23].IPRI_RMK_EN[0:0]	1
0xBB004DF4	RMK_PORT_CTRL [24].RESERVED[31:5]	27
0xBB004DF4	RMK_PORT_CTRL [24].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DF4	RMK_PORT_CTRL [24].DEI_RMK_EN[3:3]	1
0xBB004DF4	RMK_PORT_CTRL [24].DSCP_RMK_EN[2:2]	1
0xBB004DF4	RMK_PORT_CTRL [24].OPRI_RMK_EN[1:1]	1
0xBB004DF4	RMK_PORT_CTRL [24].IPRI_RMK_EN[0:0]	1
0xBB004DF8	RMK_PORT_CTRL [25].RESERVED[31:5]	27
0xBB004DF8	RMK_PORT_CTRL [25].DEI_RMK_TAG_SEL[4:4]	1
0xBB004DF8	RMK_PORT_CTRL [25].DEI_RMK_EN[3:3]	1
0xBB004DF8	RMK_PORT_CTRL [25].DSCP_RMK_EN[2:2]	1
0xBB004DF8	RMK_PORT_CTRL [25].OPRI_RMK_EN[1:1]	1
0xBB004DF8	RMK_PORT_CTRL [25].IPRI_RMK_EN[0:0]	1
0xBB004DFC	RMK_PORT_CTRL [26].RESERVED[31:5]	27
0xBB004DFC	RMK_PORT_CTRL [26].DEI_RMK_TAG_SEL[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004DFC	RMK_PORT_CTRL [26].DEI_RMK_EN[3:3]	1
0xBB004DFC	RMK_PORT_CTRL [26].DSCP_RMK_EN[2:2]	1
0xBB004DFC	RMK_PORT_CTRL [26].OPRI_RMK_EN[1:1]	1
0xBB004DFC	RMK_PORT_CTRL [26].IPRI_RMK_EN[0:0]	1
0xBB004E00	RMK_PORT_CTRL [27].RESERVED[31:5]	27
0xBB004E00	RMK_PORT_CTRL [27].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E00	RMK_PORT_CTRL [27].DEI_RMK_EN[3:3]	1
0xBB004E00	RMK_PORT_CTRL [27].DSCP_RMK_EN[2:2]	1
0xBB004E00	RMK_PORT_CTRL [27].OPRI_RMK_EN[1:1]	1
0xBB004E00	RMK_PORT_CTRL [27].IPRI_RMK_EN[0:0]	1
0xBB004E04	RMK_PORT_CTRL [28].RESERVED[31:5]	27
0xBB004E04	RMK_PORT_CTRL [28].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E04	RMK_PORT_CTRL [28].DEI_RMK_EN[3:3]	1
0xBB004E04	RMK_PORT_CTRL [28].DSCP_RMK_EN[2:2]	1
0xBB004E04	RMK_PORT_CTRL [28].OPRI_RMK_EN[1:1]	1
0xBB004E04	RMK_PORT_CTRL [28].IPRI_RMK_EN[0:0]	1
0xBB004E08	RMK_PORT_CTRL [29].RESERVED[31:5]	27
0xBB004E08	RMK_PORT_CTRL [29].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E08	RMK_PORT_CTRL [29].DEI_RMK_EN[3:3]	1
0xBB004E08	RMK_PORT_CTRL [29].DSCP_RMK_EN[2:2]	1
0xBB004E08	RMK_PORT_CTRL [29].OPRI_RMK_EN[1:1]	1
0xBB004E08	RMK_PORT_CTRL [29].IPRI_RMK_EN[0:0]	1
0xBB004E0C	RMK_PORT_CTRL [30].RESERVED[31:5]	27
0xBB004E0C	RMK_PORT_CTRL [30].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E0C	RMK_PORT_CTRL [30].DEI_RMK_EN[3:3]	1
0xBB004E0C	RMK_PORT_CTRL [30].DSCP_RMK_EN[2:2]	1
0xBB004E0C	RMK_PORT_CTRL [30].OPRI_RMK_EN[1:1]	1
0xBB004E0C	RMK_PORT_CTRL [30].IPRI_RMK_EN[0:0]	1
0xBB004E10	RMK_PORT_CTRL [31].RESERVED[31:5]	27
0xBB004E10	RMK_PORT_CTRL [31].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E10	RMK_PORT_CTRL [31].DEI_RMK_EN[3:3]	1
0xBB004E10	RMK_PORT_CTRL [31].DSCP_RMK_EN[2:2]	1
0xBB004E10	RMK_PORT_CTRL [31].OPRI_RMK_EN[1:1]	1
0xBB004E10	RMK_PORT_CTRL [31].IPRI_RMK_EN[0:0]	1
0xBB004E14	RMK_PORT_CTRL [32].RESERVED[31:5]	27
0xBB004E14	RMK_PORT_CTRL [32].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E14	RMK_PORT_CTRL [32].DEI_RMK_EN[3:3]	1
0xBB004E14	RMK_PORT_CTRL [32].DSCP_RMK_EN[2:2]	1
0xBB004E14	RMK_PORT_CTRL [32].OPRI_RMK_EN[1:1]	1
0xBB004E14	RMK_PORT_CTRL [32].IPRI_RMK_EN[0:0]	1
0xBB004E18	RMK_PORT_CTRL [33].RESERVED[31:5]	27
0xBB004E18	RMK_PORT_CTRL [33].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E18	RMK_PORT_CTRL [33].DEI_RMK_EN[3:3]	1
0xBB004E18	RMK_PORT_CTRL [33].DSCP_RMK_EN[2:2]	1
0xBB004E18	RMK_PORT_CTRL [33].OPRI_RMK_EN[1:1]	1
0xBB004E18	RMK_PORT_CTRL [33].IPRI_RMK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004E1C	RMK_PORT_CTRL [34].RESERVED[31:5]	27
0xBB004E1C	RMK_PORT_CTRL [34].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E1C	RMK_PORT_CTRL [34].DEI_RMK_EN[3:3]	1
0xBB004E1C	RMK_PORT_CTRL [34].DSCP_RMK_EN[2:2]	1
0xBB004E1C	RMK_PORT_CTRL [34].OPRI_RMK_EN[1:1]	1
0xBB004E1C	RMK_PORT_CTRL [34].IPRI_RMK_EN[0:0]	1
0xBB004E20	RMK_PORT_CTRL [35].RESERVED[31:5]	27
0xBB004E20	RMK_PORT_CTRL [35].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E20	RMK_PORT_CTRL [35].DEI_RMK_EN[3:3]	1
0xBB004E20	RMK_PORT_CTRL [35].DSCP_RMK_EN[2:2]	1
0xBB004E20	RMK_PORT_CTRL [35].OPRI_RMK_EN[1:1]	1
0xBB004E20	RMK_PORT_CTRL [35].IPRI_RMK_EN[0:0]	1
0xBB004E24	RMK_PORT_CTRL [36].RESERVED[31:5]	27
0xBB004E24	RMK_PORT_CTRL [36].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E24	RMK_PORT_CTRL [36].DEI_RMK_EN[3:3]	1
0xBB004E24	RMK_PORT_CTRL [36].DSCP_RMK_EN[2:2]	1
0xBB004E24	RMK_PORT_CTRL [36].OPRI_RMK_EN[1:1]	1
0xBB004E24	RMK_PORT_CTRL [36].IPRI_RMK_EN[0:0]	1
0xBB004E28	RMK_PORT_CTRL [37].RESERVED[31:5]	27
0xBB004E28	RMK_PORT_CTRL [37].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E28	RMK_PORT_CTRL [37].DEI_RMK_EN[3:3]	1
0xBB004E28	RMK_PORT_CTRL [37].DSCP_RMK_EN[2:2]	1
0xBB004E28	RMK_PORT_CTRL [37].OPRI_RMK_EN[1:1]	1
0xBB004E28	RMK_PORT_CTRL [37].IPRI_RMK_EN[0:0]	1
0xBB004E2C	RMK_PORT_CTRL [38].RESERVED[31:5]	27
0xBB004E2C	RMK_PORT_CTRL [38].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E2C	RMK_PORT_CTRL [38].DEI_RMK_EN[3:3]	1
0xBB004E2C	RMK_PORT_CTRL [38].DSCP_RMK_EN[2:2]	1
0xBB004E2C	RMK_PORT_CTRL [38].OPRI_RMK_EN[1:1]	1
0xBB004E2C	RMK_PORT_CTRL [38].IPRI_RMK_EN[0:0]	1
0xBB004E30	RMK_PORT_CTRL [39].RESERVED[31:5]	27
0xBB004E30	RMK_PORT_CTRL [39].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E30	RMK_PORT_CTRL [39].DEI_RMK_EN[3:3]	1
0xBB004E30	RMK_PORT_CTRL [39].DSCP_RMK_EN[2:2]	1
0xBB004E30	RMK_PORT_CTRL [39].OPRI_RMK_EN[1:1]	1
0xBB004E30	RMK_PORT_CTRL [39].IPRI_RMK_EN[0:0]	1
0xBB004E34	RMK_PORT_CTRL [40].RESERVED[31:5]	27
0xBB004E34	RMK_PORT_CTRL [40].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E34	RMK_PORT_CTRL [40].DEI_RMK_EN[3:3]	1
0xBB004E34	RMK_PORT_CTRL [40].DSCP_RMK_EN[2:2]	1
0xBB004E34	RMK_PORT_CTRL [40].OPRI_RMK_EN[1:1]	1
0xBB004E34	RMK_PORT_CTRL [40].IPRI_RMK_EN[0:0]	1
0xBB004E38	RMK_PORT_CTRL [41].RESERVED[31:5]	27
0xBB004E38	RMK_PORT_CTRL [41].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E38	RMK_PORT_CTRL [41].DEI_RMK_EN[3:3]	1
0xBB004E38	RMK_PORT_CTRL [41].DSCP_RMK_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004E38	RMK_PORT_CTRL [41].OPRI_RMK_EN[1:1]	1
0xBB004E38	RMK_PORT_CTRL [41].IPRI_RMK_EN[0:0]	1
0xBB004E3C	RMK_PORT_CTRL [42].RESERVED[31:5]	27
0xBB004E3C	RMK_PORT_CTRL [42].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E3C	RMK_PORT_CTRL [42].DEI_RMK_EN[3:3]	1
0xBB004E3C	RMK_PORT_CTRL [42].DSCP_RMK_EN[2:2]	1
0xBB004E3C	RMK_PORT_CTRL [42].OPRI_RMK_EN[1:1]	1
0xBB004E3C	RMK_PORT_CTRL [42].IPRI_RMK_EN[0:0]	1
0xBB004E40	RMK_PORT_CTRL [43].RESERVED[31:5]	27
0xBB004E40	RMK_PORT_CTRL [43].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E40	RMK_PORT_CTRL [43].DEI_RMK_EN[3:3]	1
0xBB004E40	RMK_PORT_CTRL [43].DSCP_RMK_EN[2:2]	1
0xBB004E40	RMK_PORT_CTRL [43].OPRI_RMK_EN[1:1]	1
0xBB004E40	RMK_PORT_CTRL [43].IPRI_RMK_EN[0:0]	1
0xBB004E44	RMK_PORT_CTRL [44].RESERVED[31:5]	27
0xBB004E44	RMK_PORT_CTRL [44].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E44	RMK_PORT_CTRL [44].DEI_RMK_EN[3:3]	1
0xBB004E44	RMK_PORT_CTRL [44].DSCP_RMK_EN[2:2]	1
0xBB004E44	RMK_PORT_CTRL [44].OPRI_RMK_EN[1:1]	1
0xBB004E44	RMK_PORT_CTRL [44].IPRI_RMK_EN[0:0]	1
0xBB004E48	RMK_PORT_CTRL [45].RESERVED[31:5]	27
0xBB004E48	RMK_PORT_CTRL [45].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E48	RMK_PORT_CTRL [45].DEI_RMK_EN[3:3]	1
0xBB004E48	RMK_PORT_CTRL [45].DSCP_RMK_EN[2:2]	1
0xBB004E48	RMK_PORT_CTRL [45].OPRI_RMK_EN[1:1]	1
0xBB004E48	RMK_PORT_CTRL [45].IPRI_RMK_EN[0:0]	1
0xBB004E4C	RMK_PORT_CTRL [46].RESERVED[31:5]	27
0xBB004E4C	RMK_PORT_CTRL [46].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E4C	RMK_PORT_CTRL [46].DEI_RMK_EN[3:3]	1
0xBB004E4C	RMK_PORT_CTRL [46].DSCP_RMK_EN[2:2]	1
0xBB004E4C	RMK_PORT_CTRL [46].OPRI_RMK_EN[1:1]	1
0xBB004E4C	RMK_PORT_CTRL [46].IPRI_RMK_EN[0:0]	1
0xBB004E50	RMK_PORT_CTRL [47].RESERVED[31:5]	27
0xBB004E50	RMK_PORT_CTRL [47].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E50	RMK_PORT_CTRL [47].DEI_RMK_EN[3:3]	1
0xBB004E50	RMK_PORT_CTRL [47].DSCP_RMK_EN[2:2]	1
0xBB004E50	RMK_PORT_CTRL [47].OPRI_RMK_EN[1:1]	1
0xBB004E50	RMK_PORT_CTRL [47].IPRI_RMK_EN[0:0]	1
0xBB004E54	RMK_PORT_CTRL [48].RESERVED[31:5]	27
0xBB004E54	RMK_PORT_CTRL [48].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E54	RMK_PORT_CTRL [48].DEI_RMK_EN[3:3]	1
0xBB004E54	RMK_PORT_CTRL [48].DSCP_RMK_EN[2:2]	1
0xBB004E54	RMK_PORT_CTRL [48].OPRI_RMK_EN[1:1]	1
0xBB004E54	RMK_PORT_CTRL [48].IPRI_RMK_EN[0:0]	1
0xBB004E58	RMK_PORT_CTRL [49].RESERVED[31:5]	27
0xBB004E58	RMK_PORT_CTRL [49].DEI_RMK_TAG_SEL[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004E58	RMK_PORT_CTRL [49].DEI_RMK_EN[3:3]	1
0xBB004E58	RMK_PORT_CTRL [49].DSCP_RMK_EN[2:2]	1
0xBB004E58	RMK_PORT_CTRL [49].OPRI_RMK_EN[1:1]	1
0xBB004E58	RMK_PORT_CTRL [49].IPRI_RMK_EN[0:0]	1
0xBB004E5C	RMK_PORT_CTRL [50].RESERVED[31:5]	27
0xBB004E5C	RMK_PORT_CTRL [50].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E5C	RMK_PORT_CTRL [50].DEI_RMK_EN[3:3]	1
0xBB004E5C	RMK_PORT_CTRL [50].DSCP_RMK_EN[2:2]	1
0xBB004E5C	RMK_PORT_CTRL [50].OPRI_RMK_EN[1:1]	1
0xBB004E5C	RMK_PORT_CTRL [50].IPRI_RMK_EN[0:0]	1
0xBB004E60	RMK_PORT_CTRL [51].RESERVED[31:5]	27
0xBB004E60	RMK_PORT_CTRL [51].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E60	RMK_PORT_CTRL [51].DEI_RMK_EN[3:3]	1
0xBB004E60	RMK_PORT_CTRL [51].DSCP_RMK_EN[2:2]	1
0xBB004E60	RMK_PORT_CTRL [51].OPRI_RMK_EN[1:1]	1
0xBB004E60	RMK_PORT_CTRL [51].IPRI_RMK_EN[0:0]	1
0xBB004E64	RMK_PORT_CTRL [52].RESERVED[31:5]	27
0xBB004E64	RMK_PORT_CTRL [52].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E64	RMK_PORT_CTRL [52].DEI_RMK_EN[3:3]	1
0xBB004E64	RMK_PORT_CTRL [52].DSCP_RMK_EN[2:2]	1
0xBB004E64	RMK_PORT_CTRL [52].OPRI_RMK_EN[1:1]	1
0xBB004E64	RMK_PORT_CTRL [52].IPRI_RMK_EN[0:0]	1
0xBB004E68	RMK_PORT_CTRL [53].RESERVED[31:5]	27
0xBB004E68	RMK_PORT_CTRL [53].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E68	RMK_PORT_CTRL [53].DEI_RMK_EN[3:3]	1
0xBB004E68	RMK_PORT_CTRL [53].DSCP_RMK_EN[2:2]	1
0xBB004E68	RMK_PORT_CTRL [53].OPRI_RMK_EN[1:1]	1
0xBB004E68	RMK_PORT_CTRL [53].IPRI_RMK_EN[0:0]	1
0xBB004E6C	RMK_PORT_CTRL [54].RESERVED[31:5]	27
0xBB004E6C	RMK_PORT_CTRL [54].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E6C	RMK_PORT_CTRL [54].DEI_RMK_EN[3:3]	1
0xBB004E6C	RMK_PORT_CTRL [54].DSCP_RMK_EN[2:2]	1
0xBB004E6C	RMK_PORT_CTRL [54].OPRI_RMK_EN[1:1]	1
0xBB004E6C	RMK_PORT_CTRL [54].IPRI_RMK_EN[0:0]	1
0xBB004E70	RMK_PORT_CTRL [55].RESERVED[31:5]	27
0xBB004E70	RMK_PORT_CTRL [55].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E70	RMK_PORT_CTRL [55].DEI_RMK_EN[3:3]	1
0xBB004E70	RMK_PORT_CTRL [55].DSCP_RMK_EN[2:2]	1
0xBB004E70	RMK_PORT_CTRL [55].OPRI_RMK_EN[1:1]	1
0xBB004E70	RMK_PORT_CTRL [55].IPRI_RMK_EN[0:0]	1
0xBB004E74	RMK_PORT_CTRL [56].RESERVED[31:5]	27
0xBB004E74	RMK_PORT_CTRL [56].DEI_RMK_TAG_SEL[4:4]	1
0xBB004E74	RMK_PORT_CTRL [56].DEI_RMK_EN[3:3]	1
0xBB004E74	RMK_PORT_CTRL [56].DSCP_RMK_EN[2:2]	1
0xBB004E74	RMK_PORT_CTRL [56].OPRI_RMK_EN[1:1]	1
0xBB004E74	RMK_PORT_CTRL [56].IPRI_RMK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0014AC	RMT_ACCESS_SEM [0].RESERVED[31:9]	23
0xBB0014AC	RMT_ACCESS_SEM [0].LOCK[8:8]	1
0xBB0014AC	RMT_ACCESS_SEM [0].DEV[7:4]	4
0xBB0014AC	RMT_ACCESS_SEM [0].TIMEOUT_TIMER[3:0]	4
0xBB0014B0	RMT_ACCESS_SEM [1].RESERVED[31:9]	23
0xBB0014B0	RMT_ACCESS_SEM [1].LOCK[8:8]	1
0xBB0014B0	RMT_ACCESS_SEM [1].DEV[7:4]	4
0xBB0014B0	RMT_ACCESS_SEM [1].TIMEOUT_TIMER[3:0]	4
0xBB0014B4	RMT_ACCESS_SEM [2].RESERVED[31:9]	23
0xBB0014B4	RMT_ACCESS_SEM [2].LOCK[8:8]	1
0xBB0014B4	RMT_ACCESS_SEM [2].DEV[7:4]	4
0xBB0014B4	RMT_ACCESS_SEM [2].TIMEOUT_TIMER[3:0]	4
0xBB0014B8	RMT_ACCESS_SEM [3].RESERVED[31:9]	23
0xBB0014B8	RMT_ACCESS_SEM [3].LOCK[8:8]	1
0xBB0014B8	RMT_ACCESS_SEM [3].DEV[7:4]	4
0xBB0014B8	RMT_ACCESS_SEM [3].TIMEOUT_TIMER[3:0]	4
0xBB0014BC	RMT_ACCESS_SEM [4].RESERVED[31:9]	23
0xBB0014BC	RMT_ACCESS_SEM [4].LOCK[8:8]	1
0xBB0014BC	RMT_ACCESS_SEM [4].DEV[7:4]	4
0xBB0014BC	RMT_ACCESS_SEM [4].TIMEOUT_TIMER[3:0]	4
0xBB0014C0	RMT_ACCESS_SEM [5].RESERVED[31:9]	23
0xBB0014C0	RMT_ACCESS_SEM [5].LOCK[8:8]	1
0xBB0014C0	RMT_ACCESS_SEM [5].DEV[7:4]	4
0xBB0014C0	RMT_ACCESS_SEM [5].TIMEOUT_TIMER[3:0]	4
0xBB0014C4	RMT_ACCESS_SEM [6].RESERVED[31:9]	23
0xBB0014C4	RMT_ACCESS_SEM [6].LOCK[8:8]	1
0xBB0014C4	RMT_ACCESS_SEM [6].DEV[7:4]	4
0xBB0014C4	RMT_ACCESS_SEM [6].TIMEOUT_TIMER[3:0]	4
0xBB0014C8	RMT_ACCESS_SEM [7].RESERVED[31:9]	23
0xBB0014C8	RMT_ACCESS_SEM [7].LOCK[8:8]	1
0xBB0014C8	RMT_ACCESS_SEM [7].DEV[7:4]	4
0xBB0014C8	RMT_ACCESS_SEM [7].TIMEOUT_TIMER[3:0]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [0].TIMEOUT[3:0]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [1].TIMEOUT[7:4]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [2].TIMEOUT[11:8]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [3].TIMEOUT[15:12]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [4].TIMEOUT[19:16]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [5].TIMEOUT[23:20]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [6].TIMEOUT[27:24]	4
0xBB0014CC	RMT_ACCESS_SEM_CTRL [7].TIMEOUT[31:28]	4
0xBB0014D0	RMT_INTR_CTRL.RESERVED[31:25]	7
0xBB0014D0	RMT_INTR_CTRL.TX_DELAY[24:23]	2
0xBB0014D0	RMT_INTR_CTRL.TX_EN[22:22]	1
0xBB0014D0	RMT_INTR_CTRL.TX_TRIG[21:21]	1
0xBB0014D0	RMT_INTR_CTRL.CMP_SEQ_MARGIN[20:16]	5
0xBB0014D0	RMT_INTR_CTRL.RX_FORCE_UPDATE[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001514	RMT_INTR_DATA_CCM [0].ISR_CCM[15:0]	16
0xBB001514	RMT_INTR_DATA_CCM [1].ISR_CCM[31:16]	16
0xBB001518	RMT_INTR_DATA_CCM [2].ISR_CCM[15:0]	16
0xBB001518	RMT_INTR_DATA_CCM [3].ISR_CCM[31:16]	16
0xBB00151C	RMT_INTR_DATA_CCM [4].ISR_CCM[15:0]	16
0xBB00151C	RMT_INTR_DATA_CCM [5].ISR_CCM[31:16]	16
0xBB001520	RMT_INTR_DATA_CCM [6].ISR_CCM[15:0]	16
0xBB001520	RMT_INTR_DATA_CCM [7].ISR_CCM[31:16]	16
0xBB001524	RMT_INTR_DATA_CCM [8].ISR_CCM[15:0]	16
0xBB001524	RMT_INTR_DATA_CCM [9].ISR_CCM[31:16]	16
0xBB001528	RMT_INTR_DATA_CCM [10].ISR_CCM[15:0]	16
0xBB001528	RMT_INTR_DATA_CCM [11].ISR_CCM[31:16]	16
0xBB00152C	RMT_INTR_DATA_CCM [12].ISR_CCM[15:0]	16
0xBB00152C	RMT_INTR_DATA_CCM [13].ISR_CCM[31:16]	16
0xBB001530	RMT_INTR_DATA_CCM [14].ISR_CCM[15:0]	16
0xBB001530	RMT_INTR_DATA_CCM [15].ISR_CCM[31:16]	16
0xBB001534	RMT_INTR_DATA_LINK_STS [0].RESERVED[31:24]	8
0xBB001534	RMT_INTR_DATA_LINK_STS [0].PORT_LINK_STS_HI[23:0]	24
0xBB001538	RMT_INTR_DATA_LINK_STS [0].PORT_LINK_STS_LO[31:0]	32
0xBB00153C	RMT_INTR_DATA_LINK_STS [1].RESERVED[31:24]	8
0xBB00153C	RMT_INTR_DATA_LINK_STS [1].PORT_LINK_STS_HI[23:0]	24
0xBB001540	RMT_INTR_DATA_LINK_STS [1].PORT_LINK_STS_LO[31:0]	32
0xBB001544	RMT_INTR_DATA_LINK_STS [2].RESERVED[31:24]	8
0xBB001544	RMT_INTR_DATA_LINK_STS [2].PORT_LINK_STS_HI[23:0]	24
0xBB001548	RMT_INTR_DATA_LINK_STS [2].PORT_LINK_STS_LO[31:0]	32
0xBB00154C	RMT_INTR_DATA_LINK_STS [3].RESERVED[31:24]	8
0xBB00154C	RMT_INTR_DATA_LINK_STS [3].PORT_LINK_STS_HI[23:0]	24
0xBB001550	RMT_INTR_DATA_LINK_STS [3].PORT_LINK_STS_LO[31:0]	32
0xBB001554	RMT_INTR_DATA_LINK_STS [4].RESERVED[31:24]	8
0xBB001554	RMT_INTR_DATA_LINK_STS [4].PORT_LINK_STS_HI[23:0]	24
0xBB001558	RMT_INTR_DATA_LINK_STS [4].PORT_LINK_STS_LO[31:0]	32
0xBB00155C	RMT_INTR_DATA_LINK_STS [5].RESERVED[31:24]	8
0xBB00155C	RMT_INTR_DATA_LINK_STS [5].PORT_LINK_STS_HI[23:0]	24
0xBB001560	RMT_INTR_DATA_LINK_STS [5].PORT_LINK_STS_LO[31:0]	32
0xBB001564	RMT_INTR_DATA_LINK_STS [6].RESERVED[31:24]	8
0xBB001564	RMT_INTR_DATA_LINK_STS [6].PORT_LINK_STS_HI[23:0]	24
0xBB001568	RMT_INTR_DATA_LINK_STS [6].PORT_LINK_STS_LO[31:0]	32
0xBB00156C	RMT_INTR_DATA_LINK_STS [7].RESERVED[31:24]	8
0xBB00156C	RMT_INTR_DATA_LINK_STS [7].PORT_LINK_STS_HI[23:0]	24
0xBB001570	RMT_INTR_DATA_LINK_STS [7].PORT_LINK_STS_LO[31:0]	32
0xBB001574	RMT_INTR_DATA_LINK_STS [8].RESERVED[31:24]	8
0xBB001574	RMT_INTR_DATA_LINK_STS [8].PORT_LINK_STS_HI[23:0]	24
0xBB001578	RMT_INTR_DATA_LINK_STS [8].PORT_LINK_STS_LO[31:0]	32
0xBB00157C	RMT_INTR_DATA_LINK_STS [9].RESERVED[31:24]	8
0xBB00157C	RMT_INTR_DATA_LINK_STS [9].PORT_LINK_STS_HI[23:0]	24
0xBB001580	RMT_INTR_DATA_LINK_STS [9].PORT_LINK_STS_LO[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001584	RMT_INTR_DATA_LINK_STS [10].RESERVED[31:24]	8
0xBB001584	RMT_INTR_DATA_LINK_STS [10].PORT_LINK_STS_HI[23:0]	24
0xBB001588	RMT_INTR_DATA_LINK_STS [10].PORT_LINK_STS_LO[31:0]	32
0xBB00158C	RMT_INTR_DATA_LINK_STS [11].RESERVED[31:24]	8
0xBB00158C	RMT_INTR_DATA_LINK_STS [11].PORT_LINK_STS_HI[23:0]	24
0xBB001590	RMT_INTR_DATA_LINK_STS [11].PORT_LINK_STS_LO[31:0]	32
0xBB001594	RMT_INTR_DATA_LINK_STS [12].RESERVED[31:24]	8
0xBB001594	RMT_INTR_DATA_LINK_STS [12].PORT_LINK_STS_HI[23:0]	24
0xBB001598	RMT_INTR_DATA_LINK_STS [12].PORT_LINK_STS_LO[31:0]	32
0xBB00159C	RMT_INTR_DATA_LINK_STS [13].RESERVED[31:24]	8
0xBB00159C	RMT_INTR_DATA_LINK_STS [13].PORT_LINK_STS_HI[23:0]	24
0xBB0015A0	RMT_INTR_DATA_LINK_STS [13].PORT_LINK_STS_LO[31:0]	32
0xBB0015A4	RMT_INTR_DATA_LINK_STS [14].RESERVED[31:24]	8
0xBB0015A4	RMT_INTR_DATA_LINK_STS [14].PORT_LINK_STS_HI[23:0]	24
0xBB0015A8	RMT_INTR_DATA_LINK_STS [14].PORT_LINK_STS_LO[31:0]	32
0xBB0015AC	RMT_INTR_DATA_LINK_STS [15].RESERVED[31:24]	8
0xBB0015AC	RMT_INTR_DATA_LINK_STS [15].PORT_LINK_STS_HI[23:0]	24
0xBB0015B0	RMT_INTR_DATA_LINK_STS [15].PORT_LINK_STS_LO[31:0]	32
0xBB0014D4	RMT_INTR_INFO [0].RESERVED[31:10]	22
0xBB0014D4	RMT_INTR_INFO [0].RX_SEQ_ID[9:5]	5
0xBB0014D4	RMT_INTR_INFO [0].SEQ_ID[4:0]	5
0xBB0014D8	RMT_INTR_INFO [1].RESERVED[31:10]	22
0xBB0014D8	RMT_INTR_INFO [1].RX_SEQ_ID[9:5]	5
0xBB0014D8	RMT_INTR_INFO [1].SEQ_ID[4:0]	5
0xBB0014DC	RMT_INTR_INFO [2].RESERVED[31:10]	22
0xBB0014DC	RMT_INTR_INFO [2].RX_SEQ_ID[9:5]	5
0xBB0014DC	RMT_INTR_INFO [2].SEQ_ID[4:0]	5
0xBB0014E0	RMT_INTR_INFO [3].RESERVED[31:10]	22
0xBB0014E0	RMT_INTR_INFO [3].RX_SEQ_ID[9:5]	5
0xBB0014E0	RMT_INTR_INFO [3].SEQ_ID[4:0]	5
0xBB0014E4	RMT_INTR_INFO [4].RESERVED[31:10]	22
0xBB0014E4	RMT_INTR_INFO [4].RX_SEQ_ID[9:5]	5
0xBB0014E4	RMT_INTR_INFO [4].SEQ_ID[4:0]	5
0xBB0014E8	RMT_INTR_INFO [5].RESERVED[31:10]	22
0xBB0014E8	RMT_INTR_INFO [5].RX_SEQ_ID[9:5]	5
0xBB0014E8	RMT_INTR_INFO [5].SEQ_ID[4:0]	5
0xBB0014EC	RMT_INTR_INFO [6].RESERVED[31:10]	22
0xBB0014EC	RMT_INTR_INFO [6].RX_SEQ_ID[9:5]	5
0xBB0014EC	RMT_INTR_INFO [6].SEQ_ID[4:0]	5
0xBB0014F0	RMT_INTR_INFO [7].RESERVED[31:10]	22
0xBB0014F0	RMT_INTR_INFO [7].RX_SEQ_ID[9:5]	5
0xBB0014F0	RMT_INTR_INFO [7].SEQ_ID[4:0]	5
0xBB0014F4	RMT_INTR_INFO [8].RESERVED[31:10]	22
0xBB0014F4	RMT_INTR_INFO [8].RX_SEQ_ID[9:5]	5
0xBB0014F4	RMT_INTR_INFO [8].SEQ_ID[4:0]	5
0xBB0014F8	RMT_INTR_INFO [9].RESERVED[31:10]	22

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0014F8	RMT_INTR_INFO [9].RX_SEQ_ID[9:5]	5
0xBB0014F8	RMT_INTR_INFO [9].SEQ_ID[4:0]	5
0xBB0014FC	RMT_INTR_INFO [10].RESERVED[31:10]	22
0xBB0014FC	RMT_INTR_INFO [10].RX_SEQ_ID[9:5]	5
0xBB0014FC	RMT_INTR_INFO [10].SEQ_ID[4:0]	5
0xBB001500	RMT_INTR_INFO [11].RESERVED[31:10]	22
0xBB001500	RMT_INTR_INFO [11].RX_SEQ_ID[9:5]	5
0xBB001500	RMT_INTR_INFO [11].SEQ_ID[4:0]	5
0xBB001504	RMT_INTR_INFO [12].RESERVED[31:10]	22
0xBB001504	RMT_INTR_INFO [12].RX_SEQ_ID[9:5]	5
0xBB001504	RMT_INTR_INFO [12].SEQ_ID[4:0]	5
0xBB001508	RMT_INTR_INFO [13].RESERVED[31:10]	22
0xBB001508	RMT_INTR_INFO [13].RX_SEQ_ID[9:5]	5
0xBB001508	RMT_INTR_INFO [13].SEQ_ID[4:0]	5
0xBB00150C	RMT_INTR_INFO [14].RESERVED[31:10]	22
0xBB00150C	RMT_INTR_INFO [14].RX_SEQ_ID[9:5]	5
0xBB00150C	RMT_INTR_INFO [14].SEQ_ID[4:0]	5
0xBB001510	RMT_INTR_INFO [15].RESERVED[31:10]	22
0xBB001510	RMT_INTR_INFO [15].RX_SEQ_ID[9:5]	5
0xBB001510	RMT_INTR_INFO [15].SEQ_ID[4:0]	5
0xBB000FB4	RMT_PHY_ACCESS_CFG.RESERVED[31:31]	1
0xBB000FB4	RMT_PHY_ACCESS_CFG.TIMEOUT_TIMER[30:23]	8
0xBB000FB4	RMT_PHY_ACCESS_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB000FB4	RMT_PHY_ACCESS_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB000FB4	RMT_PHY_ACCESS_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB000FB4	RMT_PHY_ACCESS_CFG.SEQ_ID[4:0]	5
0xBB000FAC	RMT_PHY_ACCESS_CTRL.RESERVED[31:11]	21
0xBB000FAC	RMT_PHY_ACCESS_CTRL.RESULT[10:10]	1
0xBB000FAC	RMT_PHY_ACCESS_CTRL.CMD[9:9]	1
0xBB000FAC	RMT_PHY_ACCESS_CTRL.ACCESS_SEL[8:8]	1
0xBB000FAC	RMT_PHY_ACCESS_CTRL.BC[7:7]	1
0xBB000FAC	RMT_PHY_ACCESS_CTRL.RWOP[6:6]	1
0xBB000FAC	RMT_PHY_ACCESS_CTRL.TYPE[5:4]	2
0xBB000FAC	RMT_PHY_ACCESS_CTRL.DEV_ID[3:0]	4
0xBB000FB0	RMT_PHY_ACCESS_DATA.RESERVED[31:16]	16
0xBB000FB0	RMT_PHY_ACCESS_DATA.DATA[15:0]	16
0xBB000FC8	RMT_PHY_ACCESS_MMD_PARAM.RESERVED[31:21]	11
0xBB000FC8	RMT_PHY_ACCESS_MMD_PARAM.MMD_DEV_ADDR[20:16]	5
0xBB000FC8	RMT_PHY_ACCESS_MMD_PARAM.MMD_REG[15:0]	16
0xBB000FC4	RMT_PHY_ACCESS_PARAM.RESERVED[31:27]	5
0xBB000FC4	RMT_PHY_ACCESS_PARAM.MAIN_PAGE[26:14]	13
0xBB000FC4	RMT_PHY_ACCESS_PARAM.EXT_PAGE[13:5]	9
0xBB000FC4	RMT_PHY_ACCESS_PARAM.REG[4:0]	5
0xBB000FB8	RMT_PHY_ACCESS_PHY_MSK_CTRL.RESERVED[31:24]	8
0xBB000FB8	RMT_PHY_ACCESS_PHY_MSK_CTRL.PHY_MSK_HI[23:0]	24
0xBB000FBC	RMT_PHY_ACCESS_PHY_MSK_CTRL.PHY_MSK_LO[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000FC0	RMT_PHY_ACCESS_PORT_ID_CTRL.RESERVED[31:6]	26
0xBB000FC0	RMT_PHY_ACCESS_PORT_ID_CTRL.PORT_ID[5:0]	6
0xBB0014A8	RMT_REG_ACCESS_CFG.RESERVED[31:31]	1
0xBB0014A8	RMT_REG_ACCESS_CFG.TIMEOUT_TIMER[30:23]	8
0xBB0014A8	RMT_REG_ACCESS_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB0014A8	RMT_REG_ACCESS_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB0014A8	RMT_REG_ACCESS_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB0014A8	RMT_REG_ACCESS_CFG.SEQ_ID[4:0]	5
0xBB0014A0	RMT_REG_ACCESS_CTRL.RESERVED[31:31]	1
0xBB0014A0	RMT_REG_ACCESS_CTRL.RESULT[30:30]	1
0xBB0014A0	RMT_REG_ACCESS_CTRL.EXEC[29:29]	1
0xBB0014A0	RMT_REG_ACCESS_CTRL.RWOP[28:28]	1
0xBB0014A0	RMT_REG_ACCESS_CTRL.DEV[27:24]	4
0xBB0014A0	RMT_REG_ACCESS_CTRL.ADDR[23:0]	24
0xBB0014A4	RMT_REG_ACCESS_DATA.DATA[31:0]	32
0xBB00856C	RMT_TBL_ACCESS_SET_0_CFG.RESERVED[31:31]	1
0xBB00856C	RMT_TBL_ACCESS_SET_0_CFG.TIMEOUT_TIMER[30:23]	8
0xBB00856C	RMT_TBL_ACCESS_SET_0_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB00856C	RMT_TBL_ACCESS_SET_0_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB00856C	RMT_TBL_ACCESS_SET_0_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB00856C	RMT_TBL_ACCESS_SET_0_CFG.SEQ_ID[4:0]	5
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.RESERVED[31:26]	6
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.RESULT[25:25]	1
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.EXEC[24:24]	1
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.CMD[23:23]	1
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.DEV_ID[22:19]	4
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.TBL[18:15]	4
0xBB008548	RMT_TBL_ACCESS_SET_0_CTRL.ADDR[14:0]	15
0xBB00854C	RMT_TBL_ACCESS_SET_0_DATA[0].DATA[31:0]	32
0xBB008550	RMT_TBL_ACCESS_SET_0_DATA[1].DATA[31:0]	32
0xBB008554	RMT_TBL_ACCESS_SET_0_DATA[2].DATA[31:0]	32
0xBB008558	RMT_TBL_ACCESS_SET_0_DATA[3].DATA[31:0]	32
0xBB00855C	RMT_TBL_ACCESS_SET_0_DATA[4].DATA[31:0]	32
0xBB008560	RMT_TBL_ACCESS_SET_0_DATA[5].DATA[31:0]	32
0xBB008564	RMT_TBL_ACCESS_SET_0_DATA[6].DATA[31:0]	32
0xBB008568	RMT_TBL_ACCESS_SET_0_DATA[7].DATA[31:0]	32
0xBB008570	RMT_TBL_ACCESS_SET_0_L2_READ_MTHD_CTRL.RESERVED[31:19]	13
0xBB008570	RMT_TBL_ACCESS_SET_0_L2_READ_MTHD_CTRL.METHOD[18:16]	3
0xBB008570	RMT_TBL_ACCESS_SET_0_L2_READ_MTHD_CTRL.HIT[15:15]	1
0xBB008570	RMT_TBL_ACCESS_SET_0_L2_READ_MTHD_CTRL.HIT_ADDR[14:0]	15
0xBB004714	RMT_TBL_ACCESS_SET_1_CFG.RESERVED[31:31]	1
0xBB004714	RMT_TBL_ACCESS_SET_1_CFG.TIMEOUT_TIMER[30:23]	8
0xBB004714	RMT_TBL_ACCESS_SET_1_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB004714	RMT_TBL_ACCESS_SET_1_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB004714	RMT_TBL_ACCESS_SET_1_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB004714	RMT_TBL_ACCESS_SET_1_CFG.SEQ_ID[4:0]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.RESERVED[31:23]	9
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.RESULT[22:22]	1
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.EXEC[21:21]	1
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.CMD[20:20]	1
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.DEV_ID[19:16]	4
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.TBL[15:14]	2
0xBB0046B8	RMT_TBL_ACCESS_SET_1_CTRL.ADDR[13:0]	14
0xBB0046BC	RMT_TBL_ACCESS_SET_1_DATA [0].DATA[31:0]	32
0xBB0046C0	RMT_TBL_ACCESS_SET_1_DATA [1].DATA[31:0]	32
0xBB0046C4	RMT_TBL_ACCESS_SET_1_DATA [2].DATA[31:0]	32
0xBB0046C8	RMT_TBL_ACCESS_SET_1_DATA [3].DATA[31:0]	32
0xBB0046CC	RMT_TBL_ACCESS_SET_1_DATA [4].DATA[31:0]	32
0xBB0046D0	RMT_TBL_ACCESS_SET_1_DATA [5].DATA[31:0]	32
0xBB0046D4	RMT_TBL_ACCESS_SET_1_DATA [6].DATA[31:0]	32
0xBB0046D8	RMT_TBL_ACCESS_SET_1_DATA [7].DATA[31:0]	32
0xBB0046DC	RMT_TBL_ACCESS_SET_1_DATA [8].DATA[31:0]	32
0xBB0046E0	RMT_TBL_ACCESS_SET_1_DATA [9].DATA[31:0]	32
0xBB0046E4	RMT_TBL_ACCESS_SET_1_DATA [10].DATA[31:0]	32
0xBB0046E8	RMT_TBL_ACCESS_SET_1_DATA [11].DATA[31:0]	32
0xBB0046EC	RMT_TBL_ACCESS_SET_1_DATA [12].DATA[31:0]	32
0xBB0046F0	RMT_TBL_ACCESS_SET_1_DATA [13].DATA[31:0]	32
0xBB0046F4	RMT_TBL_ACCESS_SET_1_DATA [14].DATA[31:0]	32
0xBB0046F8	RMT_TBL_ACCESS_SET_1_DATA [15].DATA[31:0]	32
0xBB0046FC	RMT_TBL_ACCESS_SET_1_DATA [16].DATA[31:0]	32
0xBB004700	RMT_TBL_ACCESS_SET_1_DATA [17].DATA[31:0]	32
0xBB004704	RMT_TBL_ACCESS_SET_1_DATA [18].DATA[31:0]	32
0xBB004708	RMT_TBL_ACCESS_SET_1_DATA [19].DATA[31:0]	32
0xBB00470C	RMT_TBL_ACCESS_SET_1_DATA [20].DATA[31:0]	32
0xBB004710	RMT_TBL_ACCESS_SET_1_DATA [21].DATA[31:0]	32
0xBB008590	RMT_TBL_ACCESS_SET_2_CFG.RESERVED[31:31]	1
0xBB008590	RMT_TBL_ACCESS_SET_2_CFG.TIMEOUT_TIMER[30:23]	8
0xBB008590	RMT_TBL_ACCESS_SET_2_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB008590	RMT_TBL_ACCESS_SET_2_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB008590	RMT_TBL_ACCESS_SET_2_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB008590	RMT_TBL_ACCESS_SET_2_CFG.SEQ_ID[4:0]	5
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.RESERVED[31:25]	7
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.RESULT[24:24]	1
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.EXEC[23:23]	1
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.CMD[22:22]	1
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.DEV_ID[21:18]	4
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.TBL[17:14]	4
0xBB008574	RMT_TBL_ACCESS_SET_2_CTRL.ADDR[13:0]	14
0xBB008578	RMT_TBL_ACCESS_SET_2_DATA [0].DATA[31:0]	32
0xBB00857C	RMT_TBL_ACCESS_SET_2_DATA [1].DATA[31:0]	32
0xBB008580	RMT_TBL_ACCESS_SET_2_DATA [2].DATA[31:0]	32
0xBB008584	RMT_TBL_ACCESS_SET_2_DATA [3].DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008588	RMT_TBL_ACCESS_SET_2_DATA [4].DATA[31:0]	32
0xBB00858C	RMT_TBL_ACCESS_SET_2_DATA [5].DATA[31:0]	32
0xBB004FC8	RMT_TBL_ACCESS_SET_3_CFG.RESERVED[31:31]	1
0xBB004FC8	RMT_TBL_ACCESS_SET_3_CFG.TIMEOUT_TIMER[30:23]	8
0xBB004FC8	RMT_TBL_ACCESS_SET_3_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB004FC8	RMT_TBL_ACCESS_SET_3_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB004FC8	RMT_TBL_ACCESS_SET_3_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB004FC8	RMT_TBL_ACCESS_SET_3_CFG.SEQ_ID[4:0]	5
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.RESERVED[31:22]	10
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.RESULT[21:21]	1
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.EXEC[20:20]	1
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.CMD[19:19]	1
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.DEV_ID[18:15]	4
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.TBL[14:12]	3
0xBB004FA0	RMT_TBL_ACCESS_SET_3_CTRL.ADDR[11:0]	12
0xBB004FA4	RMT_TBL_ACCESS_SET_3_DATA [0].DATA[31:0]	32
0xBB004FA8	RMT_TBL_ACCESS_SET_3_DATA [1].DATA[31:0]	32
0xBB004FAC	RMT_TBL_ACCESS_SET_3_DATA [2].DATA[31:0]	32
0xBB004FB0	RMT_TBL_ACCESS_SET_3_DATA [3].DATA[31:0]	32
0xBB004FB4	RMT_TBL_ACCESS_SET_3_DATA [4].DATA[31:0]	32
0xBB004FB8	RMT_TBL_ACCESS_SET_3_DATA [5].DATA[31:0]	32
0xBB004FBC	RMT_TBL_ACCESS_SET_3_DATA [6].DATA[31:0]	32
0xBB004FC0	RMT_TBL_ACCESS_SET_3_DATA [7].DATA[31:0]	32
0xBB004FC4	RMT_TBL_ACCESS_SET_3_DATA [8].DATA[31:0]	32
0xBB0030D8	RMT_TBL_ACCESS_SET_4_CFG.RESERVED[31:31]	1
0xBB0030D8	RMT_TBL_ACCESS_SET_4_CFG.TIMEOUT_TIMER[30:23]	8
0xBB0030D8	RMT_TBL_ACCESS_SET_4_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB0030D8	RMT_TBL_ACCESS_SET_4_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB0030D8	RMT_TBL_ACCESS_SET_4_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB0030D8	RMT_TBL_ACCESS_SET_4_CFG.SEQ_ID[4:0]	5
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.RESERVED[31:14]	18
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.RESULT[13:13]	1
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.EXEC[12:12]	1
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.CMD[11:11]	1
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.DEV_ID[10:7]	4
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.TBL[6:6]	1
0xBB003060	RMT_TBL_ACCESS_SET_4_CTRL.ADDR[5:0]	6
0xBB003064	RMT_TBL_ACCESS_SET_4_DATA [0].DATA[31:0]	32
0xBB003068	RMT_TBL_ACCESS_SET_4_DATA [1].DATA[31:0]	32
0xBB00306C	RMT_TBL_ACCESS_SET_4_DATA [2].DATA[31:0]	32
0xBB003070	RMT_TBL_ACCESS_SET_4_DATA [3].DATA[31:0]	32
0xBB003074	RMT_TBL_ACCESS_SET_4_DATA [4].DATA[31:0]	32
0xBB003078	RMT_TBL_ACCESS_SET_4_DATA [5].DATA[31:0]	32
0xBB00307C	RMT_TBL_ACCESS_SET_4_DATA [6].DATA[31:0]	32
0xBB003080	RMT_TBL_ACCESS_SET_4_DATA [7].DATA[31:0]	32
0xBB003084	RMT_TBL_ACCESS_SET_4_DATA [8].DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB003088	RMT_TBL_ACCESS_SET_4_DATA [9].DATA[31:0]	32
0xBB00308C	RMT_TBL_ACCESS_SET_4_DATA [10].DATA[31:0]	32
0xBB003090	RMT_TBL_ACCESS_SET_4_DATA [11].DATA[31:0]	32
0xBB003094	RMT_TBL_ACCESS_SET_4_DATA [12].DATA[31:0]	32
0xBB003098	RMT_TBL_ACCESS_SET_4_DATA [13].DATA[31:0]	32
0xBB00309C	RMT_TBL_ACCESS_SET_4_DATA [14].DATA[31:0]	32
0xBB0030A0	RMT_TBL_ACCESS_SET_4_DATA [15].DATA[31:0]	32
0xBB0030A4	RMT_TBL_ACCESS_SET_4_DATA [16].DATA[31:0]	32
0xBB0030A8	RMT_TBL_ACCESS_SET_4_DATA [17].DATA[31:0]	32
0xBB0030AC	RMT_TBL_ACCESS_SET_4_DATA [18].DATA[31:0]	32
0xBB0030B0	RMT_TBL_ACCESS_SET_4_DATA [19].DATA[31:0]	32
0xBB0030B4	RMT_TBL_ACCESS_SET_4_DATA [20].DATA[31:0]	32
0xBB0030B8	RMT_TBL_ACCESS_SET_4_DATA [21].DATA[31:0]	32
0xBB0030BC	RMT_TBL_ACCESS_SET_4_DATA [22].DATA[31:0]	32
0xBB0030C0	RMT_TBL_ACCESS_SET_4_DATA [23].DATA[31:0]	32
0xBB0030C4	RMT_TBL_ACCESS_SET_4_DATA [24].DATA[31:0]	32
0xBB0030C8	RMT_TBL_ACCESS_SET_4_DATA [25].DATA[31:0]	32
0xBB0030CC	RMT_TBL_ACCESS_SET_4_DATA [26].DATA[31:0]	32
0xBB0030D0	RMT_TBL_ACCESS_SET_4_DATA [27].DATA[31:0]	32
0xBB0030D4	RMT_TBL_ACCESS_SET_4_DATA [28].DATA[31:0]	32
0xBB007FF0	RMT_TBL_ACCESS_SET_5_CFG.RESERVED[31:31]	1
0xBB007FF0	RMT_TBL_ACCESS_SET_5_CFG.TIMEOUT_TIMER[30:23]	8
0xBB007FF0	RMT_TBL_ACCESS_SET_5_CFG.TIMEOUT_PERIOD[22:15]	8
0xBB007FF0	RMT_TBL_ACCESS_SET_5_CFG.RX_REQ_SEQ_ID[14:10]	5
0xBB007FF0	RMT_TBL_ACCESS_SET_5_CFG.RX_ACK_SEQ_ID[9:5]	5
0xBB007FF0	RMT_TBL_ACCESS_SET_5_CFG.SEQ_ID[4:0]	5
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.RESERVED[31:15]	17
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.RESULT[14:14]	1
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.EXEC[13:13]	1
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.CMD[12:12]	1
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.DEV_ID[11:8]	4
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.TBL[7:6]	2
0xBB007F18	RMT_TBL_ACCESS_SET_5_CTRL.ADDR[5:0]	6
0xBB007F1C	RMT_TBL_ACCESS_SET_5_DATA [0].DATA[31:0]	32
0xBB007F20	RMT_TBL_ACCESS_SET_5_DATA [1].DATA[31:0]	32
0xBB007F24	RMT_TBL_ACCESS_SET_5_DATA [2].DATA[31:0]	32
0xBB007F28	RMT_TBL_ACCESS_SET_5_DATA [3].DATA[31:0]	32
0xBB007F2C	RMT_TBL_ACCESS_SET_5_DATA [4].DATA[31:0]	32
0xBB007F30	RMT_TBL_ACCESS_SET_5_DATA [5].DATA[31:0]	32
0xBB007F34	RMT_TBL_ACCESS_SET_5_DATA [6].DATA[31:0]	32
0xBB007F38	RMT_TBL_ACCESS_SET_5_DATA [7].DATA[31:0]	32
0xBB007F3C	RMT_TBL_ACCESS_SET_5_DATA [8].DATA[31:0]	32
0xBB007F40	RMT_TBL_ACCESS_SET_5_DATA [9].DATA[31:0]	32
0xBB007F44	RMT_TBL_ACCESS_SET_5_DATA [10].DATA[31:0]	32
0xBB007F48	RMT_TBL_ACCESS_SET_5_DATA [11].DATA[31:0]	32
0xBB007F4C	RMT_TBL_ACCESS_SET_5_DATA [12].DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007F50	RMT_TBL_ACCESS_SET_5_DATA [13].DATA[31:0]	32
0xBB007F54	RMT_TBL_ACCESS_SET_5_DATA [14].DATA[31:0]	32
0xBB007F58	RMT_TBL_ACCESS_SET_5_DATA [15].DATA[31:0]	32
0xBB007F5C	RMT_TBL_ACCESS_SET_5_DATA [16].DATA[31:0]	32
0xBB007F60	RMT_TBL_ACCESS_SET_5_DATA [17].DATA[31:0]	32
0xBB007F64	RMT_TBL_ACCESS_SET_5_DATA [18].DATA[31:0]	32
0xBB007F68	RMT_TBL_ACCESS_SET_5_DATA [19].DATA[31:0]	32
0xBB007F6C	RMT_TBL_ACCESS_SET_5_DATA [20].DATA[31:0]	32
0xBB007F70	RMT_TBL_ACCESS_SET_5_DATA [21].DATA[31:0]	32
0xBB007F74	RMT_TBL_ACCESS_SET_5_DATA [22].DATA[31:0]	32
0xBB007F78	RMT_TBL_ACCESS_SET_5_DATA [23].DATA[31:0]	32
0xBB007F7C	RMT_TBL_ACCESS_SET_5_DATA [24].DATA[31:0]	32
0xBB007F80	RMT_TBL_ACCESS_SET_5_DATA [25].DATA[31:0]	32
0xBB007F84	RMT_TBL_ACCESS_SET_5_DATA [26].DATA[31:0]	32
0xBB007F88	RMT_TBL_ACCESS_SET_5_DATA [27].DATA[31:0]	32
0xBB007F8C	RMT_TBL_ACCESS_SET_5_DATA [28].DATA[31:0]	32
0xBB007F90	RMT_TBL_ACCESS_SET_5_DATA [29].DATA[31:0]	32
0xBB007F94	RMT_TBL_ACCESS_SET_5_DATA [30].DATA[31:0]	32
0xBB007F98	RMT_TBL_ACCESS_SET_5_DATA [31].DATA[31:0]	32
0xBB007F9C	RMT_TBL_ACCESS_SET_5_DATA [32].DATA[31:0]	32
0xBB007FA0	RMT_TBL_ACCESS_SET_5_DATA [33].DATA[31:0]	32
0xBB007FA4	RMT_TBL_ACCESS_SET_5_DATA [34].DATA[31:0]	32
0xBB007FA8	RMT_TBL_ACCESS_SET_5_DATA [35].DATA[31:0]	32
0xBB007FAC	RMT_TBL_ACCESS_SET_5_DATA [36].DATA[31:0]	32
0xBB007FB0	RMT_TBL_ACCESS_SET_5_DATA [37].DATA[31:0]	32
0xBB007FB4	RMT_TBL_ACCESS_SET_5_DATA [38].DATA[31:0]	32
0xBB007FB8	RMT_TBL_ACCESS_SET_5_DATA [39].DATA[31:0]	32
0xBB007FBC	RMT_TBL_ACCESS_SET_5_DATA [40].DATA[31:0]	32
0xBB007FC0	RMT_TBL_ACCESS_SET_5_DATA [41].DATA[31:0]	32
0xBB007FC4	RMT_TBL_ACCESS_SET_5_DATA [42].DATA[31:0]	32
0xBB007FC8	RMT_TBL_ACCESS_SET_5_DATA [43].DATA[31:0]	32
0xBB007FCC	RMT_TBL_ACCESS_SET_5_DATA [44].DATA[31:0]	32
0xBB007FD0	RMT_TBL_ACCESS_SET_5_DATA [45].DATA[31:0]	32
0xBB007FD4	RMT_TBL_ACCESS_SET_5_DATA [46].DATA[31:0]	32
0xBB007FD8	RMT_TBL_ACCESS_SET_5_DATA [47].DATA[31:0]	32
0xBB007FDC	RMT_TBL_ACCESS_SET_5_DATA [48].DATA[31:0]	32
0xBB007FE0	RMT_TBL_ACCESS_SET_5_DATA [49].DATA[31:0]	32
0xBB007FE4	RMT_TBL_ACCESS_SET_5_DATA [50].DATA[31:0]	32
0xBB007FE8	RMT_TBL_ACCESS_SET_5_DATA [51].DATA[31:0]	32
0xBB007FEC	RMT_TBL_ACCESS_SET_5_DATA [52].DATA[31:0]	32
0xBB0045D8	RNG_CHK_CTRL [0].RESERVED[31:3]	29
0xBB0045D8	RNG_CHK_CTRL [0].TYPE[2:0]	3
0xBB0045DC	RNG_CHK_CTRL [0].UPPER[31:16]	16
0xBB0045DC	RNG_CHK_CTRL [0].LOWER[15:0]	16
0xBB0045E0	RNG_CHK_CTRL [1].RESERVED[31:3]	29
0xBB0045E0	RNG_CHK_CTRL [1].TYPE[2:0]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0045E4	RNG_CHK_CTRL [1].UPPER[31:16]	16
0xBB0045E4	RNG_CHK_CTRL [1].LOWER[15:0]	16
0xBB0045E8	RNG_CHK_CTRL [2].RESERVED[31:3]	29
0xBB0045E8	RNG_CHK_CTRL [2].TYPE[2:0]	3
0xBB0045EC	RNG_CHK_CTRL [2].UPPER[31:16]	16
0xBB0045EC	RNG_CHK_CTRL [2].LOWER[15:0]	16
0xBB0045F0	RNG_CHK_CTRL [3].RESERVED[31:3]	29
0xBB0045F0	RNG_CHK_CTRL [3].TYPE[2:0]	3
0xBB0045F4	RNG_CHK_CTRL [3].UPPER[31:16]	16
0xBB0045F4	RNG_CHK_CTRL [3].LOWER[15:0]	16
0xBB0045F8	RNG_CHK_CTRL [4].RESERVED[31:3]	29
0xBB0045F8	RNG_CHK_CTRL [4].TYPE[2:0]	3
0xBB0045FC	RNG_CHK_CTRL [4].UPPER[31:16]	16
0xBB0045FC	RNG_CHK_CTRL [4].LOWER[15:0]	16
0xBB004600	RNG_CHK_CTRL [5].RESERVED[31:3]	29
0xBB004600	RNG_CHK_CTRL [5].TYPE[2:0]	3
0xBB004604	RNG_CHK_CTRL [5].UPPER[31:16]	16
0xBB004604	RNG_CHK_CTRL [5].LOWER[15:0]	16
0xBB004608	RNG_CHK_CTRL [6].RESERVED[31:3]	29
0xBB004608	RNG_CHK_CTRL [6].TYPE[2:0]	3
0xBB00460C	RNG_CHK_CTRL [6].UPPER[31:16]	16
0xBB00460C	RNG_CHK_CTRL [6].LOWER[15:0]	16
0xBB004610	RNG_CHK_CTRL [7].RESERVED[31:3]	29
0xBB004610	RNG_CHK_CTRL [7].TYPE[2:0]	3
0xBB004614	RNG_CHK_CTRL [7].UPPER[31:16]	16
0xBB004614	RNG_CHK_CTRL [7].LOWER[15:0]	16
0xBB004618	RNG_CHK_CTRL [8].RESERVED[31:3]	29
0xBB004618	RNG_CHK_CTRL [8].TYPE[2:0]	3
0xBB00461C	RNG_CHK_CTRL [8].UPPER[31:16]	16
0xBB00461C	RNG_CHK_CTRL [8].LOWER[15:0]	16
0xBB004620	RNG_CHK_CTRL [9].RESERVED[31:3]	29
0xBB004620	RNG_CHK_CTRL [9].TYPE[2:0]	3
0xBB004624	RNG_CHK_CTRL [9].UPPER[31:16]	16
0xBB004624	RNG_CHK_CTRL [9].LOWER[15:0]	16
0xBB004628	RNG_CHK_CTRL [10].RESERVED[31:3]	29
0xBB004628	RNG_CHK_CTRL [10].TYPE[2:0]	3
0xBB00462C	RNG_CHK_CTRL [10].UPPER[31:16]	16
0xBB00462C	RNG_CHK_CTRL [10].LOWER[15:0]	16
0xBB004630	RNG_CHK_CTRL [11].RESERVED[31:3]	29
0xBB004630	RNG_CHK_CTRL [11].TYPE[2:0]	3
0xBB004634	RNG_CHK_CTRL [11].UPPER[31:16]	16
0xBB004634	RNG_CHK_CTRL [11].LOWER[15:0]	16
0xBB004638	RNG_CHK_CTRL [12].RESERVED[31:3]	29
0xBB004638	RNG_CHK_CTRL [12].TYPE[2:0]	3
0xBB00463C	RNG_CHK_CTRL [12].UPPER[31:16]	16
0xBB00463C	RNG_CHK_CTRL [12].LOWER[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004640	RNG_CHK_CTRL [13].RESERVED[31:3]	29
0xBB004640	RNG_CHK_CTRL [13].TYPE[2:0]	3
0xBB004644	RNG_CHK_CTRL [13].UPPER[31:16]	16
0xBB004644	RNG_CHK_CTRL [13].LOWER[15:0]	16
0xBB004648	RNG_CHK_CTRL [14].RESERVED[31:3]	29
0xBB004648	RNG_CHK_CTRL [14].TYPE[2:0]	3
0xBB00464C	RNG_CHK_CTRL [14].UPPER[31:16]	16
0xBB00464C	RNG_CHK_CTRL [14].LOWER[15:0]	16
0xBB004650	RNG_CHK_CTRL [15].RESERVED[31:3]	29
0xBB004650	RNG_CHK_CTRL [15].TYPE[2:0]	3
0xBB004654	RNG_CHK_CTRL [15].UPPER[31:16]	16
0xBB004654	RNG_CHK_CTRL [15].LOWER[15:0]	16
0xBB004658	RNG_CHK_IP_CTRL [0].RESERVED[31:3]	29
0xBB004658	RNG_CHK_IP_CTRL [0].TYPE[2:0]	3
0xBB00465C	RNG_CHK_IP_CTRL [1].RESERVED[31:3]	29
0xBB00465C	RNG_CHK_IP_CTRL [1].TYPE[2:0]	3
0xBB004660	RNG_CHK_IP_CTRL [2].RESERVED[31:3]	29
0xBB004660	RNG_CHK_IP_CTRL [2].TYPE[2:0]	3
0xBB004664	RNG_CHK_IP_CTRL [3].RESERVED[31:3]	29
0xBB004664	RNG_CHK_IP_CTRL [3].TYPE[2:0]	3
0xBB004668	RNG_CHK_IP_CTRL [4].RESERVED[31:3]	29
0xBB004668	RNG_CHK_IP_CTRL [4].TYPE[2:0]	3
0xBB00466C	RNG_CHK_IP_CTRL [5].RESERVED[31:3]	29
0xBB00466C	RNG_CHK_IP_CTRL [5].TYPE[2:0]	3
0xBB004670	RNG_CHK_IP_CTRL [6].RESERVED[31:3]	29
0xBB004670	RNG_CHK_IP_CTRL [6].TYPE[2:0]	3
0xBB004674	RNG_CHK_IP_CTRL [7].RESERVED[31:3]	29
0xBB004674	RNG_CHK_IP_CTRL [7].TYPE[2:0]	3
0xBB004678	RNG_CHK_IP_RNG [0].IP_UPPER[31:0]	32
0xBB00467C	RNG_CHK_IP_RNG [0].IP_LOWER[31:0]	32
0xBB004680	RNG_CHK_IP_RNG [1].IP_UPPER[31:0]	32
0xBB004684	RNG_CHK_IP_RNG [1].IP_LOWER[31:0]	32
0xBB004688	RNG_CHK_IP_RNG [2].IP_UPPER[31:0]	32
0xBB00468C	RNG_CHK_IP_RNG [2].IP_LOWER[31:0]	32
0xBB004690	RNG_CHK_IP_RNG [3].IP_UPPER[31:0]	32
0xBB004694	RNG_CHK_IP_RNG [3].IP_LOWER[31:0]	32
0xBB004698	RNG_CHK_IP_RNG [4].IP_UPPER[31:0]	32
0xBB00469C	RNG_CHK_IP_RNG [4].IP_LOWER[31:0]	32
0xBB0046A0	RNG_CHK_IP_RNG [5].IP_UPPER[31:0]	32
0xBB0046A4	RNG_CHK_IP_RNG [5].IP_LOWER[31:0]	32
0xBB0046A8	RNG_CHK_IP_RNG [6].IP_UPPER[31:0]	32
0xBB0046AC	RNG_CHK_IP_RNG [6].IP_LOWER[31:0]	32
0xBB0046B0	RNG_CHK_IP_RNG [7].IP_UPPER[31:0]	32
0xBB0046B4	RNG_CHK_IP_RNG [7].IP_LOWER[31:0]	32
0xBB000400	RST_GLB_CTRL.RESERVED[31:20]	12
0xBB000400	RST_GLB_CTRL.BYP_NIC_ACK_TIMEOUT[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000400	RST_GLB_CTRL.REG_POR_PULL_CTRL[18:17]	2
0xBB000400	RST_GLB_CTRL.REG_POR_LVDTCT[16:16]	1
0xBB000400	RST_GLB_CTRL.RST_PULSE_WIDTH[15:8]	8
0xBB000400	RST_GLB_CTRL.MDX_GLB_RST[7:7]	1
0xBB000400	RST_GLB_CTRL.CHIP_RST_EN[6:6]	1
0xBB000400	RST_GLB_CTRL.WDOG_RST_EN[5:5]	1
0xBB000400	RST_GLB_CTRL.CPU_MEM_RST[4:4]	1
0xBB000400	RST_GLB_CTRL.SW_SERDES_RST[3:3]	1
0xBB000400	RST_GLB_CTRL.SW_NIC_RST[2:2]	1
0xBB000400	RST_GLB_CTRL.SW_Q_RST[1:1]	1
0xBB000400	RST_GLB_CTRL.SW_CHIP_RST[0:0]	1
0xBB002FC8	SCHED_CPU_Q_CTRL [0].RESERVED[31:9]	23
0xBB002FC8	SCHED_CPU_Q_CTRL [0].STRICT_EN[8:8]	1
0xBB002FC8	SCHED_CPU_Q_CTRL [0].RESERVED[7:7]	1
0xBB002FC8	SCHED_CPU_Q_CTRL [0].WEIGHT[6:0]	7
0xBB002FCC	SCHED_CPU_Q_CTRL [1].RESERVED[31:9]	23
0xBB002FCC	SCHED_CPU_Q_CTRL [1].STRICT_EN[8:8]	1
0xBB002FCC	SCHED_CPU_Q_CTRL [1].RESERVED[7:7]	1
0xBB002FCC	SCHED_CPU_Q_CTRL [1].WEIGHT[6:0]	7
0xBB002FD0	SCHED_CPU_Q_CTRL [2].RESERVED[31:9]	23
0xBB002FD0	SCHED_CPU_Q_CTRL [2].STRICT_EN[8:8]	1
0xBB002FD0	SCHED_CPU_Q_CTRL [2].RESERVED[7:7]	1
0xBB002FD0	SCHED_CPU_Q_CTRL [2].WEIGHT[6:0]	7
0xBB002FD4	SCHED_CPU_Q_CTRL [3].RESERVED[31:9]	23
0xBB002FD4	SCHED_CPU_Q_CTRL [3].STRICT_EN[8:8]	1
0xBB002FD4	SCHED_CPU_Q_CTRL [3].RESERVED[7:7]	1
0xBB002FD4	SCHED_CPU_Q_CTRL [3].WEIGHT[6:0]	7
0xBB002FD8	SCHED_CPU_Q_CTRL [4].RESERVED[31:9]	23
0xBB002FD8	SCHED_CPU_Q_CTRL [4].STRICT_EN[8:8]	1
0xBB002FD8	SCHED_CPU_Q_CTRL [4].RESERVED[7:7]	1
0xBB002FD8	SCHED_CPU_Q_CTRL [4].WEIGHT[6:0]	7
0xBB002FDC	SCHED_CPU_Q_CTRL [5].RESERVED[31:9]	23
0xBB002FDC	SCHED_CPU_Q_CTRL [5].STRICT_EN[8:8]	1
0xBB002FDC	SCHED_CPU_Q_CTRL [5].RESERVED[7:7]	1
0xBB002FDC	SCHED_CPU_Q_CTRL [5].WEIGHT[6:0]	7
0xBB002FE0	SCHED_CPU_Q_CTRL [6].RESERVED[31:9]	23
0xBB002FE0	SCHED_CPU_Q_CTRL [6].STRICT_EN[8:8]	1
0xBB002FE0	SCHED_CPU_Q_CTRL [6].RESERVED[7:7]	1
0xBB002FE0	SCHED_CPU_Q_CTRL [6].WEIGHT[6:0]	7
0xBB002FE4	SCHED_CPU_Q_CTRL [7].RESERVED[31:9]	23
0xBB002FE4	SCHED_CPU_Q_CTRL [7].STRICT_EN[8:8]	1
0xBB002FE4	SCHED_CPU_Q_CTRL [7].RESERVED[7:7]	1
0xBB002FE4	SCHED_CPU_Q_CTRL [7].WEIGHT[6:0]	7
0xBB002FE8	SCHED_CPU_Q_CTRL [8].RESERVED[31:9]	23
0xBB002FE8	SCHED_CPU_Q_CTRL [8].STRICT_EN[8:8]	1
0xBB002FE8	SCHED_CPU_Q_CTRL [8].RESERVED[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002FE8	SCHED_CPU_Q_CTRL [8].WEIGHT[6:0]	7
0xBB002FEC	SCHED_CPU_Q_CTRL [9].RESERVED[31:9]	23
0xBB002FEC	SCHED_CPU_Q_CTRL [9].STRICT_EN[8:8]	1
0xBB002FEC	SCHED_CPU_Q_CTRL [9].RESERVED[7:7]	1
0xBB002FEC	SCHED_CPU_Q_CTRL [9].WEIGHT[6:0]	7
0xBB002FF0	SCHED_CPU_Q_CTRL [10].RESERVED[31:9]	23
0xBB002FF0	SCHED_CPU_Q_CTRL [10].STRICT_EN[8:8]	1
0xBB002FF0	SCHED_CPU_Q_CTRL [10].RESERVED[7:7]	1
0xBB002FF0	SCHED_CPU_Q_CTRL [10].WEIGHT[6:0]	7
0xBB002FF4	SCHED_CPU_Q_CTRL [11].RESERVED[31:9]	23
0xBB002FF4	SCHED_CPU_Q_CTRL [11].STRICT_EN[8:8]	1
0xBB002FF4	SCHED_CPU_Q_CTRL [11].RESERVED[7:7]	1
0xBB002FF4	SCHED_CPU_Q_CTRL [11].WEIGHT[6:0]	7
0xBB002FF8	SCHED_CPU_Q_CTRL [12].RESERVED[31:9]	23
0xBB002FF8	SCHED_CPU_Q_CTRL [12].STRICT_EN[8:8]	1
0xBB002FF8	SCHED_CPU_Q_CTRL [12].RESERVED[7:7]	1
0xBB002FF8	SCHED_CPU_Q_CTRL [12].WEIGHT[6:0]	7
0xBB002FFC	SCHED_CPU_Q_CTRL [13].RESERVED[31:9]	23
0xBB002FFC	SCHED_CPU_Q_CTRL [13].STRICT_EN[8:8]	1
0xBB002FFC	SCHED_CPU_Q_CTRL [13].RESERVED[7:7]	1
0xBB002FFC	SCHED_CPU_Q_CTRL [13].WEIGHT[6:0]	7
0xBB003000	SCHED_CPU_Q_CTRL [14].RESERVED[31:9]	23
0xBB003000	SCHED_CPU_Q_CTRL [14].STRICT_EN[8:8]	1
0xBB003000	SCHED_CPU_Q_CTRL [14].RESERVED[7:7]	1
0xBB003000	SCHED_CPU_Q_CTRL [14].WEIGHT[6:0]	7
0xBB003004	SCHED_CPU_Q_CTRL [15].RESERVED[31:9]	23
0xBB003004	SCHED_CPU_Q_CTRL [15].STRICT_EN[8:8]	1
0xBB003004	SCHED_CPU_Q_CTRL [15].RESERVED[7:7]	1
0xBB003004	SCHED_CPU_Q_CTRL [15].WEIGHT[6:0]	7
0xBB003008	SCHED_CPU_Q_CTRL [16].RESERVED[31:9]	23
0xBB003008	SCHED_CPU_Q_CTRL [16].STRICT_EN[8:8]	1
0xBB003008	SCHED_CPU_Q_CTRL [16].RESERVED[7:7]	1
0xBB003008	SCHED_CPU_Q_CTRL [16].WEIGHT[6:0]	7
0xBB00300C	SCHED_CPU_Q_CTRL [17].RESERVED[31:9]	23
0xBB00300C	SCHED_CPU_Q_CTRL [17].STRICT_EN[8:8]	1
0xBB00300C	SCHED_CPU_Q_CTRL [17].RESERVED[7:7]	1
0xBB00300C	SCHED_CPU_Q_CTRL [17].WEIGHT[6:0]	7
0xBB003010	SCHED_CPU_Q_CTRL [18].RESERVED[31:9]	23
0xBB003010	SCHED_CPU_Q_CTRL [18].STRICT_EN[8:8]	1
0xBB003010	SCHED_CPU_Q_CTRL [18].RESERVED[7:7]	1
0xBB003010	SCHED_CPU_Q_CTRL [18].WEIGHT[6:0]	7
0xBB003014	SCHED_CPU_Q_CTRL [19].RESERVED[31:9]	23
0xBB003014	SCHED_CPU_Q_CTRL [19].STRICT_EN[8:8]	1
0xBB003014	SCHED_CPU_Q_CTRL [19].RESERVED[7:7]	1
0xBB003014	SCHED_CPU_Q_CTRL [19].WEIGHT[6:0]	7
0xBB003018	SCHED_CPU_Q_CTRL [20].RESERVED[31:9]	23

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB003018	SCHED_CPU_Q_CTRL [20].STRICT_EN[8:8]	1
0xBB003018	SCHED_CPU_Q_CTRL [20].RESERVED[7:7]	1
0xBB003018	SCHED_CPU_Q_CTRL [20].WEIGHT[6:0]	7
0xBB00301C	SCHED_CPU_Q_CTRL [21].RESERVED[31:9]	23
0xBB00301C	SCHED_CPU_Q_CTRL [21].STRICT_EN[8:8]	1
0xBB00301C	SCHED_CPU_Q_CTRL [21].RESERVED[7:7]	1
0xBB00301C	SCHED_CPU_Q_CTRL [21].WEIGHT[6:0]	7
0xBB003020	SCHED_CPU_Q_CTRL [22].RESERVED[31:9]	23
0xBB003020	SCHED_CPU_Q_CTRL [22].STRICT_EN[8:8]	1
0xBB003020	SCHED_CPU_Q_CTRL [22].RESERVED[7:7]	1
0xBB003020	SCHED_CPU_Q_CTRL [22].WEIGHT[6:0]	7
0xBB003024	SCHED_CPU_Q_CTRL [23].RESERVED[31:9]	23
0xBB003024	SCHED_CPU_Q_CTRL [23].STRICT_EN[8:8]	1
0xBB003024	SCHED_CPU_Q_CTRL [23].RESERVED[7:7]	1
0xBB003024	SCHED_CPU_Q_CTRL [23].WEIGHT[6:0]	7
0xBB003028	SCHED_CPU_Q_CTRL [24].RESERVED[31:9]	23
0xBB003028	SCHED_CPU_Q_CTRL [24].STRICT_EN[8:8]	1
0xBB003028	SCHED_CPU_Q_CTRL [24].RESERVED[7:7]	1
0xBB003028	SCHED_CPU_Q_CTRL [24].WEIGHT[6:0]	7
0xBB00302C	SCHED_CPU_Q_CTRL [25].RESERVED[31:9]	23
0xBB00302C	SCHED_CPU_Q_CTRL [25].STRICT_EN[8:8]	1
0xBB00302C	SCHED_CPU_Q_CTRL [25].RESERVED[7:7]	1
0xBB00302C	SCHED_CPU_Q_CTRL [25].WEIGHT[6:0]	7
0xBB003030	SCHED_CPU_Q_CTRL [26].RESERVED[31:9]	23
0xBB003030	SCHED_CPU_Q_CTRL [26].STRICT_EN[8:8]	1
0xBB003030	SCHED_CPU_Q_CTRL [26].RESERVED[7:7]	1
0xBB003030	SCHED_CPU_Q_CTRL [26].WEIGHT[6:0]	7
0xBB003034	SCHED_CPU_Q_CTRL [27].RESERVED[31:9]	23
0xBB003034	SCHED_CPU_Q_CTRL [27].STRICT_EN[8:8]	1
0xBB003034	SCHED_CPU_Q_CTRL [27].RESERVED[7:7]	1
0xBB003034	SCHED_CPU_Q_CTRL [27].WEIGHT[6:0]	7
0xBB003038	SCHED_CPU_Q_CTRL [28].RESERVED[31:9]	23
0xBB003038	SCHED_CPU_Q_CTRL [28].STRICT_EN[8:8]	1
0xBB003038	SCHED_CPU_Q_CTRL [28].RESERVED[7:7]	1
0xBB003038	SCHED_CPU_Q_CTRL [28].WEIGHT[6:0]	7
0xBB00303C	SCHED_CPU_Q_CTRL [29].RESERVED[31:9]	23
0xBB00303C	SCHED_CPU_Q_CTRL [29].STRICT_EN[8:8]	1
0xBB00303C	SCHED_CPU_Q_CTRL [29].RESERVED[7:7]	1
0xBB00303C	SCHED_CPU_Q_CTRL [29].WEIGHT[6:0]	7
0xBB003040	SCHED_CPU_Q_CTRL [30].RESERVED[31:9]	23
0xBB003040	SCHED_CPU_Q_CTRL [30].STRICT_EN[8:8]	1
0xBB003040	SCHED_CPU_Q_CTRL [30].RESERVED[7:7]	1
0xBB003040	SCHED_CPU_Q_CTRL [30].WEIGHT[6:0]	7
0xBB003044	SCHED_CPU_Q_CTRL [31].RESERVED[31:9]	23
0xBB003044	SCHED_CPU_Q_CTRL [31].STRICT_EN[8:8]	1
0xBB003044	SCHED_CPU_Q_CTRL [31].RESERVED[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB003044	SCHED_CPU_Q_CTRL [31].WEIGHT[6:0]	7
0xBB003050	SCHED_CTRL.RESERVED[31:4]	28
0xBB003050	SCHED_CTRL.MAX_Q_PKT_TH[3:0]	4
0xBB003048	SCHED_PORT_ALGO_CTRL [0].SCHED_TYPE[0:0]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [1].SCHED_TYPE[1:1]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [2].SCHED_TYPE[2:2]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [3].SCHED_TYPE[3:3]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [4].SCHED_TYPE[4:4]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [5].SCHED_TYPE[5:5]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [6].SCHED_TYPE[6:6]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [7].SCHED_TYPE[7:7]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [8].SCHED_TYPE[8:8]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [9].SCHED_TYPE[9:9]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [10].SCHED_TYPE[10:10]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [11].SCHED_TYPE[11:11]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [12].SCHED_TYPE[12:12]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [13].SCHED_TYPE[13:13]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [14].SCHED_TYPE[14:14]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [15].SCHED_TYPE[15:15]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [16].SCHED_TYPE[16:16]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [17].SCHED_TYPE[17:17]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [18].SCHED_TYPE[18:18]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [19].SCHED_TYPE[19:19]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [20].SCHED_TYPE[20:20]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [21].SCHED_TYPE[21:21]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [22].SCHED_TYPE[22:22]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [23].SCHED_TYPE[23:23]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [24].SCHED_TYPE[24:24]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [25].SCHED_TYPE[25:25]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [26].SCHED_TYPE[26:26]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [27].SCHED_TYPE[27:27]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [28].SCHED_TYPE[28:28]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [29].SCHED_TYPE[29:29]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [30].SCHED_TYPE[30:30]	1
0xBB003048	SCHED_PORT_ALGO_CTRL [31].SCHED_TYPE[31:31]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [32].SCHED_TYPE[0:0]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [33].SCHED_TYPE[1:1]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [34].SCHED_TYPE[2:2]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [35].SCHED_TYPE[3:3]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [36].SCHED_TYPE[4:4]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [37].SCHED_TYPE[5:5]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [38].SCHED_TYPE[6:6]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [39].SCHED_TYPE[7:7]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [40].SCHED_TYPE[8:8]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [41].SCHED_TYPE[9:9]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [42].SCHED_TYPE[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00304C	SCHED_PORT_ALGO_CTRL [43].SCHED_TYPE[11:11]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [44].SCHED_TYPE[12:12]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [45].SCHED_TYPE[13:13]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [46].SCHED_TYPE[14:14]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [47].SCHED_TYPE[15:15]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [48].SCHED_TYPE[16:16]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [49].SCHED_TYPE[17:17]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [50].SCHED_TYPE[18:18]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [51].SCHED_TYPE[19:19]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [52].SCHED_TYPE[20:20]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [53].SCHED_TYPE[21:21]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [54].SCHED_TYPE[22:22]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [55].SCHED_TYPE[23:23]	1
0xBB00304C	SCHED_PORT_ALGO_CTRL [56].SCHED_TYPE[24:24]	1
0xBB002888	SCHED_PORT_Q_CTRL_SET0 [0][0].RESERVED[31:9]	23
0xBB002888	SCHED_PORT_Q_CTRL_SET0 [0][0].STRICT_EN[8:8]	1
0xBB002888	SCHED_PORT_Q_CTRL_SET0 [0][0].RESERVED[7:7]	1
0xBB002888	SCHED_PORT_Q_CTRL_SET0 [0][0].WEIGHT[6:0]	7
0xBB00288C	SCHED_PORT_Q_CTRL_SET0 [0][1].RESERVED[31:9]	23
0xBB00288C	SCHED_PORT_Q_CTRL_SET0 [0][1].STRICT_EN[8:8]	1
0xBB00288C	SCHED_PORT_Q_CTRL_SET0 [0][1].RESERVED[7:7]	1
0xBB00288C	SCHED_PORT_Q_CTRL_SET0 [0][1].WEIGHT[6:0]	7
0xBB002890	SCHED_PORT_Q_CTRL_SET0 [0][2].RESERVED[31:9]	23
0xBB002890	SCHED_PORT_Q_CTRL_SET0 [0][2].STRICT_EN[8:8]	1
0xBB002890	SCHED_PORT_Q_CTRL_SET0 [0][2].RESERVED[7:7]	1
0xBB002890	SCHED_PORT_Q_CTRL_SET0 [0][2].WEIGHT[6:0]	7
0xBB002894	SCHED_PORT_Q_CTRL_SET0 [0][3].RESERVED[31:9]	23
0xBB002894	SCHED_PORT_Q_CTRL_SET0 [0][3].STRICT_EN[8:8]	1
0xBB002894	SCHED_PORT_Q_CTRL_SET0 [0][3].RESERVED[7:7]	1
0xBB002894	SCHED_PORT_Q_CTRL_SET0 [0][3].WEIGHT[6:0]	7
0xBB002898	SCHED_PORT_Q_CTRL_SET0 [0][4].RESERVED[31:9]	23
0xBB002898	SCHED_PORT_Q_CTRL_SET0 [0][4].STRICT_EN[8:8]	1
0xBB002898	SCHED_PORT_Q_CTRL_SET0 [0][4].RESERVED[7:7]	1
0xBB002898	SCHED_PORT_Q_CTRL_SET0 [0][4].WEIGHT[6:0]	7
0xBB00289C	SCHED_PORT_Q_CTRL_SET0 [0][5].RESERVED[31:9]	23
0xBB00289C	SCHED_PORT_Q_CTRL_SET0 [0][5].STRICT_EN[8:8]	1
0xBB00289C	SCHED_PORT_Q_CTRL_SET0 [0][5].RESERVED[7:7]	1
0xBB00289C	SCHED_PORT_Q_CTRL_SET0 [0][5].WEIGHT[6:0]	7
0xBB0028A0	SCHED_PORT_Q_CTRL_SET0 [0][6].RESERVED[31:9]	23
0xBB0028A0	SCHED_PORT_Q_CTRL_SET0 [0][6].STRICT_EN[8:8]	1
0xBB0028A0	SCHED_PORT_Q_CTRL_SET0 [0][6].RESERVED[7:7]	1
0xBB0028A0	SCHED_PORT_Q_CTRL_SET0 [0][6].WEIGHT[6:0]	7
0xBB0028A4	SCHED_PORT_Q_CTRL_SET0 [0][7].RESERVED[31:9]	23
0xBB0028A4	SCHED_PORT_Q_CTRL_SET0 [0][7].STRICT_EN[8:8]	1
0xBB0028A4	SCHED_PORT_Q_CTRL_SET0 [0][7].RESERVED[7:7]	1
0xBB0028A4	SCHED_PORT_Q_CTRL_SET0 [0][7].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0028A8	SCHED_PORT_Q_CTRL_SET0 [1][0].RESERVED[31:9]	23
0xBB0028A8	SCHED_PORT_Q_CTRL_SET0 [1][0].STRICT_EN[8:8]	1
0xBB0028A8	SCHED_PORT_Q_CTRL_SET0 [1][0].RESERVED[7:7]	1
0xBB0028A8	SCHED_PORT_Q_CTRL_SET0 [1][0].WEIGHT[6:0]	7
0xBB0028AC	SCHED_PORT_Q_CTRL_SET0 [1][1].RESERVED[31:9]	23
0xBB0028AC	SCHED_PORT_Q_CTRL_SET0 [1][1].STRICT_EN[8:8]	1
0xBB0028AC	SCHED_PORT_Q_CTRL_SET0 [1][1].RESERVED[7:7]	1
0xBB0028AC	SCHED_PORT_Q_CTRL_SET0 [1][1].WEIGHT[6:0]	7
0xBB0028B0	SCHED_PORT_Q_CTRL_SET0 [1][2].RESERVED[31:9]	23
0xBB0028B0	SCHED_PORT_Q_CTRL_SET0 [1][2].STRICT_EN[8:8]	1
0xBB0028B0	SCHED_PORT_Q_CTRL_SET0 [1][2].RESERVED[7:7]	1
0xBB0028B0	SCHED_PORT_Q_CTRL_SET0 [1][2].WEIGHT[6:0]	7
0xBB0028B4	SCHED_PORT_Q_CTRL_SET0 [1][3].RESERVED[31:9]	23
0xBB0028B4	SCHED_PORT_Q_CTRL_SET0 [1][3].STRICT_EN[8:8]	1
0xBB0028B4	SCHED_PORT_Q_CTRL_SET0 [1][3].RESERVED[7:7]	1
0xBB0028B4	SCHED_PORT_Q_CTRL_SET0 [1][3].WEIGHT[6:0]	7
0xBB0028B8	SCHED_PORT_Q_CTRL_SET0 [1][4].RESERVED[31:9]	23
0xBB0028B8	SCHED_PORT_Q_CTRL_SET0 [1][4].STRICT_EN[8:8]	1
0xBB0028B8	SCHED_PORT_Q_CTRL_SET0 [1][4].RESERVED[7:7]	1
0xBB0028B8	SCHED_PORT_Q_CTRL_SET0 [1][4].WEIGHT[6:0]	7
0xBB0028BC	SCHED_PORT_Q_CTRL_SET0 [1][5].RESERVED[31:9]	23
0xBB0028BC	SCHED_PORT_Q_CTRL_SET0 [1][5].STRICT_EN[8:8]	1
0xBB0028BC	SCHED_PORT_Q_CTRL_SET0 [1][5].RESERVED[7:7]	1
0xBB0028BC	SCHED_PORT_Q_CTRL_SET0 [1][5].WEIGHT[6:0]	7
0xBB0028C0	SCHED_PORT_Q_CTRL_SET0 [1][6].RESERVED[31:9]	23
0xBB0028C0	SCHED_PORT_Q_CTRL_SET0 [1][6].STRICT_EN[8:8]	1
0xBB0028C0	SCHED_PORT_Q_CTRL_SET0 [1][6].RESERVED[7:7]	1
0xBB0028C0	SCHED_PORT_Q_CTRL_SET0 [1][6].WEIGHT[6:0]	7
0xBB0028C4	SCHED_PORT_Q_CTRL_SET0 [1][7].RESERVED[31:9]	23
0xBB0028C4	SCHED_PORT_Q_CTRL_SET0 [1][7].STRICT_EN[8:8]	1
0xBB0028C4	SCHED_PORT_Q_CTRL_SET0 [1][7].RESERVED[7:7]	1
0xBB0028C4	SCHED_PORT_Q_CTRL_SET0 [1][7].WEIGHT[6:0]	7
0xBB0028C8	SCHED_PORT_Q_CTRL_SET0 [2][0].RESERVED[31:9]	23
0xBB0028C8	SCHED_PORT_Q_CTRL_SET0 [2][0].STRICT_EN[8:8]	1
0xBB0028C8	SCHED_PORT_Q_CTRL_SET0 [2][0].RESERVED[7:7]	1
0xBB0028C8	SCHED_PORT_Q_CTRL_SET0 [2][0].WEIGHT[6:0]	7
0xBB0028CC	SCHED_PORT_Q_CTRL_SET0 [2][1].RESERVED[31:9]	23
0xBB0028CC	SCHED_PORT_Q_CTRL_SET0 [2][1].STRICT_EN[8:8]	1
0xBB0028CC	SCHED_PORT_Q_CTRL_SET0 [2][1].RESERVED[7:7]	1
0xBB0028CC	SCHED_PORT_Q_CTRL_SET0 [2][1].WEIGHT[6:0]	7
0xBB0028D0	SCHED_PORT_Q_CTRL_SET0 [2][2].RESERVED[31:9]	23
0xBB0028D0	SCHED_PORT_Q_CTRL_SET0 [2][2].STRICT_EN[8:8]	1
0xBB0028D0	SCHED_PORT_Q_CTRL_SET0 [2][2].RESERVED[7:7]	1
0xBB0028D0	SCHED_PORT_Q_CTRL_SET0 [2][2].WEIGHT[6:0]	7
0xBB0028D4	SCHED_PORT_Q_CTRL_SET0 [2][3].RESERVED[31:9]	23
0xBB0028D4	SCHED_PORT_Q_CTRL_SET0 [2][3].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0028D4	SCHED_PORT_Q_CTRL_SET0 [2][3].RESERVED[7:7]	1
0xBB0028D4	SCHED_PORT_Q_CTRL_SET0 [2][3].WEIGHT[6:0]	7
0xBB0028D8	SCHED_PORT_Q_CTRL_SET0 [2][4].RESERVED[31:9]	23
0xBB0028D8	SCHED_PORT_Q_CTRL_SET0 [2][4].STRICT_EN[8:8]	1
0xBB0028D8	SCHED_PORT_Q_CTRL_SET0 [2][4].RESERVED[7:7]	1
0xBB0028D8	SCHED_PORT_Q_CTRL_SET0 [2][4].WEIGHT[6:0]	7
0xBB0028DC	SCHED_PORT_Q_CTRL_SET0 [2][5].RESERVED[31:9]	23
0xBB0028DC	SCHED_PORT_Q_CTRL_SET0 [2][5].STRICT_EN[8:8]	1
0xBB0028DC	SCHED_PORT_Q_CTRL_SET0 [2][5].RESERVED[7:7]	1
0xBB0028DC	SCHED_PORT_Q_CTRL_SET0 [2][5].WEIGHT[6:0]	7
0xBB0028E0	SCHED_PORT_Q_CTRL_SET0 [2][6].RESERVED[31:9]	23
0xBB0028E0	SCHED_PORT_Q_CTRL_SET0 [2][6].STRICT_EN[8:8]	1
0xBB0028E0	SCHED_PORT_Q_CTRL_SET0 [2][6].RESERVED[7:7]	1
0xBB0028E0	SCHED_PORT_Q_CTRL_SET0 [2][6].WEIGHT[6:0]	7
0xBB0028E4	SCHED_PORT_Q_CTRL_SET0 [2][7].RESERVED[31:9]	23
0xBB0028E4	SCHED_PORT_Q_CTRL_SET0 [2][7].STRICT_EN[8:8]	1
0xBB0028E4	SCHED_PORT_Q_CTRL_SET0 [2][7].RESERVED[7:7]	1
0xBB0028E4	SCHED_PORT_Q_CTRL_SET0 [2][7].WEIGHT[6:0]	7
0xBB0028E8	SCHED_PORT_Q_CTRL_SET0 [3][0].RESERVED[31:9]	23
0xBB0028E8	SCHED_PORT_Q_CTRL_SET0 [3][0].STRICT_EN[8:8]	1
0xBB0028E8	SCHED_PORT_Q_CTRL_SET0 [3][0].RESERVED[7:7]	1
0xBB0028E8	SCHED_PORT_Q_CTRL_SET0 [3][0].WEIGHT[6:0]	7
0xBB0028EC	SCHED_PORT_Q_CTRL_SET0 [3][1].RESERVED[31:9]	23
0xBB0028EC	SCHED_PORT_Q_CTRL_SET0 [3][1].STRICT_EN[8:8]	1
0xBB0028EC	SCHED_PORT_Q_CTRL_SET0 [3][1].RESERVED[7:7]	1
0xBB0028EC	SCHED_PORT_Q_CTRL_SET0 [3][1].WEIGHT[6:0]	7
0xBB0028F0	SCHED_PORT_Q_CTRL_SET0 [3][2].RESERVED[31:9]	23
0xBB0028F0	SCHED_PORT_Q_CTRL_SET0 [3][2].STRICT_EN[8:8]	1
0xBB0028F0	SCHED_PORT_Q_CTRL_SET0 [3][2].RESERVED[7:7]	1
0xBB0028F0	SCHED_PORT_Q_CTRL_SET0 [3][2].WEIGHT[6:0]	7
0xBB0028F4	SCHED_PORT_Q_CTRL_SET0 [3][3].RESERVED[31:9]	23
0xBB0028F4	SCHED_PORT_Q_CTRL_SET0 [3][3].STRICT_EN[8:8]	1
0xBB0028F4	SCHED_PORT_Q_CTRL_SET0 [3][3].RESERVED[7:7]	1
0xBB0028F4	SCHED_PORT_Q_CTRL_SET0 [3][3].WEIGHT[6:0]	7
0xBB0028F8	SCHED_PORT_Q_CTRL_SET0 [3][4].RESERVED[31:9]	23
0xBB0028F8	SCHED_PORT_Q_CTRL_SET0 [3][4].STRICT_EN[8:8]	1
0xBB0028F8	SCHED_PORT_Q_CTRL_SET0 [3][4].RESERVED[7:7]	1
0xBB0028F8	SCHED_PORT_Q_CTRL_SET0 [3][4].WEIGHT[6:0]	7
0xBB0028FC	SCHED_PORT_Q_CTRL_SET0 [3][5].RESERVED[31:9]	23
0xBB0028FC	SCHED_PORT_Q_CTRL_SET0 [3][5].STRICT_EN[8:8]	1
0xBB0028FC	SCHED_PORT_Q_CTRL_SET0 [3][5].RESERVED[7:7]	1
0xBB0028FC	SCHED_PORT_Q_CTRL_SET0 [3][5].WEIGHT[6:0]	7
0xBB002900	SCHED_PORT_Q_CTRL_SET0 [3][6].RESERVED[31:9]	23
0xBB002900	SCHED_PORT_Q_CTRL_SET0 [3][6].STRICT_EN[8:8]	1
0xBB002900	SCHED_PORT_Q_CTRL_SET0 [3][6].RESERVED[7:7]	1
0xBB002900	SCHED_PORT_Q_CTRL_SET0 [3][6].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002904	SCHED_PORT_Q_CTRL_SET0 [3][7].RESERVED[31:9]	23
0xBB002904	SCHED_PORT_Q_CTRL_SET0 [3][7].STRICT_EN[8:8]	1
0xBB002904	SCHED_PORT_Q_CTRL_SET0 [3][7].RESERVED[7:7]	1
0xBB002904	SCHED_PORT_Q_CTRL_SET0 [3][7].WEIGHT[6:0]	7
0xBB002908	SCHED_PORT_Q_CTRL_SET0 [4][0].RESERVED[31:9]	23
0xBB002908	SCHED_PORT_Q_CTRL_SET0 [4][0].STRICT_EN[8:8]	1
0xBB002908	SCHED_PORT_Q_CTRL_SET0 [4][0].RESERVED[7:7]	1
0xBB002908	SCHED_PORT_Q_CTRL_SET0 [4][0].WEIGHT[6:0]	7
0xBB00290C	SCHED_PORT_Q_CTRL_SET0 [4][1].RESERVED[31:9]	23
0xBB00290C	SCHED_PORT_Q_CTRL_SET0 [4][1].STRICT_EN[8:8]	1
0xBB00290C	SCHED_PORT_Q_CTRL_SET0 [4][1].RESERVED[7:7]	1
0xBB00290C	SCHED_PORT_Q_CTRL_SET0 [4][1].WEIGHT[6:0]	7
0xBB002910	SCHED_PORT_Q_CTRL_SET0 [4][2].RESERVED[31:9]	23
0xBB002910	SCHED_PORT_Q_CTRL_SET0 [4][2].STRICT_EN[8:8]	1
0xBB002910	SCHED_PORT_Q_CTRL_SET0 [4][2].RESERVED[7:7]	1
0xBB002910	SCHED_PORT_Q_CTRL_SET0 [4][2].WEIGHT[6:0]	7
0xBB002914	SCHED_PORT_Q_CTRL_SET0 [4][3].RESERVED[31:9]	23
0xBB002914	SCHED_PORT_Q_CTRL_SET0 [4][3].STRICT_EN[8:8]	1
0xBB002914	SCHED_PORT_Q_CTRL_SET0 [4][3].RESERVED[7:7]	1
0xBB002914	SCHED_PORT_Q_CTRL_SET0 [4][3].WEIGHT[6:0]	7
0xBB002918	SCHED_PORT_Q_CTRL_SET0 [4][4].RESERVED[31:9]	23
0xBB002918	SCHED_PORT_Q_CTRL_SET0 [4][4].STRICT_EN[8:8]	1
0xBB002918	SCHED_PORT_Q_CTRL_SET0 [4][4].RESERVED[7:7]	1
0xBB002918	SCHED_PORT_Q_CTRL_SET0 [4][4].WEIGHT[6:0]	7
0xBB00291C	SCHED_PORT_Q_CTRL_SET0 [4][5].RESERVED[31:9]	23
0xBB00291C	SCHED_PORT_Q_CTRL_SET0 [4][5].STRICT_EN[8:8]	1
0xBB00291C	SCHED_PORT_Q_CTRL_SET0 [4][5].RESERVED[7:7]	1
0xBB00291C	SCHED_PORT_Q_CTRL_SET0 [4][5].WEIGHT[6:0]	7
0xBB002920	SCHED_PORT_Q_CTRL_SET0 [4][6].RESERVED[31:9]	23
0xBB002920	SCHED_PORT_Q_CTRL_SET0 [4][6].STRICT_EN[8:8]	1
0xBB002920	SCHED_PORT_Q_CTRL_SET0 [4][6].RESERVED[7:7]	1
0xBB002920	SCHED_PORT_Q_CTRL_SET0 [4][6].WEIGHT[6:0]	7
0xBB002924	SCHED_PORT_Q_CTRL_SET0 [4][7].RESERVED[31:9]	23
0xBB002924	SCHED_PORT_Q_CTRL_SET0 [4][7].STRICT_EN[8:8]	1
0xBB002924	SCHED_PORT_Q_CTRL_SET0 [4][7].RESERVED[7:7]	1
0xBB002924	SCHED_PORT_Q_CTRL_SET0 [4][7].WEIGHT[6:0]	7
0xBB002928	SCHED_PORT_Q_CTRL_SET0 [5][0].RESERVED[31:9]	23
0xBB002928	SCHED_PORT_Q_CTRL_SET0 [5][0].STRICT_EN[8:8]	1
0xBB002928	SCHED_PORT_Q_CTRL_SET0 [5][0].RESERVED[7:7]	1
0xBB002928	SCHED_PORT_Q_CTRL_SET0 [5][0].WEIGHT[6:0]	7
0xBB00292C	SCHED_PORT_Q_CTRL_SET0 [5][1].RESERVED[31:9]	23
0xBB00292C	SCHED_PORT_Q_CTRL_SET0 [5][1].STRICT_EN[8:8]	1
0xBB00292C	SCHED_PORT_Q_CTRL_SET0 [5][1].RESERVED[7:7]	1
0xBB00292C	SCHED_PORT_Q_CTRL_SET0 [5][1].WEIGHT[6:0]	7
0xBB002930	SCHED_PORT_Q_CTRL_SET0 [5][2].RESERVED[31:9]	23
0xBB002930	SCHED_PORT_Q_CTRL_SET0 [5][2].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002930	SCHED_PORT_Q_CTRL_SET0 [5][2].RESERVED[7:7]	1
0xBB002930	SCHED_PORT_Q_CTRL_SET0 [5][2].WEIGHT[6:0]	7
0xBB002934	SCHED_PORT_Q_CTRL_SET0 [5][3].RESERVED[31:9]	23
0xBB002934	SCHED_PORT_Q_CTRL_SET0 [5][3].STRICT_EN[8:8]	1
0xBB002934	SCHED_PORT_Q_CTRL_SET0 [5][3].RESERVED[7:7]	1
0xBB002934	SCHED_PORT_Q_CTRL_SET0 [5][3].WEIGHT[6:0]	7
0xBB002938	SCHED_PORT_Q_CTRL_SET0 [5][4].RESERVED[31:9]	23
0xBB002938	SCHED_PORT_Q_CTRL_SET0 [5][4].STRICT_EN[8:8]	1
0xBB002938	SCHED_PORT_Q_CTRL_SET0 [5][4].RESERVED[7:7]	1
0xBB002938	SCHED_PORT_Q_CTRL_SET0 [5][4].WEIGHT[6:0]	7
0xBB00293C	SCHED_PORT_Q_CTRL_SET0 [5][5].RESERVED[31:9]	23
0xBB00293C	SCHED_PORT_Q_CTRL_SET0 [5][5].STRICT_EN[8:8]	1
0xBB00293C	SCHED_PORT_Q_CTRL_SET0 [5][5].RESERVED[7:7]	1
0xBB00293C	SCHED_PORT_Q_CTRL_SET0 [5][5].WEIGHT[6:0]	7
0xBB002940	SCHED_PORT_Q_CTRL_SET0 [5][6].RESERVED[31:9]	23
0xBB002940	SCHED_PORT_Q_CTRL_SET0 [5][6].STRICT_EN[8:8]	1
0xBB002940	SCHED_PORT_Q_CTRL_SET0 [5][6].RESERVED[7:7]	1
0xBB002940	SCHED_PORT_Q_CTRL_SET0 [5][6].WEIGHT[6:0]	7
0xBB002944	SCHED_PORT_Q_CTRL_SET0 [5][7].RESERVED[31:9]	23
0xBB002944	SCHED_PORT_Q_CTRL_SET0 [5][7].STRICT_EN[8:8]	1
0xBB002944	SCHED_PORT_Q_CTRL_SET0 [5][7].RESERVED[7:7]	1
0xBB002944	SCHED_PORT_Q_CTRL_SET0 [5][7].WEIGHT[6:0]	7
0xBB002948	SCHED_PORT_Q_CTRL_SET0 [6][0].RESERVED[31:9]	23
0xBB002948	SCHED_PORT_Q_CTRL_SET0 [6][0].STRICT_EN[8:8]	1
0xBB002948	SCHED_PORT_Q_CTRL_SET0 [6][0].RESERVED[7:7]	1
0xBB002948	SCHED_PORT_Q_CTRL_SET0 [6][0].WEIGHT[6:0]	7
0xBB00294C	SCHED_PORT_Q_CTRL_SET0 [6][1].RESERVED[31:9]	23
0xBB00294C	SCHED_PORT_Q_CTRL_SET0 [6][1].STRICT_EN[8:8]	1
0xBB00294C	SCHED_PORT_Q_CTRL_SET0 [6][1].RESERVED[7:7]	1
0xBB00294C	SCHED_PORT_Q_CTRL_SET0 [6][1].WEIGHT[6:0]	7
0xBB002950	SCHED_PORT_Q_CTRL_SET0 [6][2].RESERVED[31:9]	23
0xBB002950	SCHED_PORT_Q_CTRL_SET0 [6][2].STRICT_EN[8:8]	1
0xBB002950	SCHED_PORT_Q_CTRL_SET0 [6][2].RESERVED[7:7]	1
0xBB002950	SCHED_PORT_Q_CTRL_SET0 [6][2].WEIGHT[6:0]	7
0xBB002954	SCHED_PORT_Q_CTRL_SET0 [6][3].RESERVED[31:9]	23
0xBB002954	SCHED_PORT_Q_CTRL_SET0 [6][3].STRICT_EN[8:8]	1
0xBB002954	SCHED_PORT_Q_CTRL_SET0 [6][3].RESERVED[7:7]	1
0xBB002954	SCHED_PORT_Q_CTRL_SET0 [6][3].WEIGHT[6:0]	7
0xBB002958	SCHED_PORT_Q_CTRL_SET0 [6][4].RESERVED[31:9]	23
0xBB002958	SCHED_PORT_Q_CTRL_SET0 [6][4].STRICT_EN[8:8]	1
0xBB002958	SCHED_PORT_Q_CTRL_SET0 [6][4].RESERVED[7:7]	1
0xBB002958	SCHED_PORT_Q_CTRL_SET0 [6][4].WEIGHT[6:0]	7
0xBB00295C	SCHED_PORT_Q_CTRL_SET0 [6][5].RESERVED[31:9]	23
0xBB00295C	SCHED_PORT_Q_CTRL_SET0 [6][5].STRICT_EN[8:8]	1
0xBB00295C	SCHED_PORT_Q_CTRL_SET0 [6][5].RESERVED[7:7]	1
0xBB00295C	SCHED_PORT_Q_CTRL_SET0 [6][5].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002960	SCHED_PORT_Q_CTRL_SET0 [6][6].RESERVED[31:9]	23
0xBB002960	SCHED_PORT_Q_CTRL_SET0 [6][6].STRICT_EN[8:8]	1
0xBB002960	SCHED_PORT_Q_CTRL_SET0 [6][6].RESERVED[7:7]	1
0xBB002960	SCHED_PORT_Q_CTRL_SET0 [6][6].WEIGHT[6:0]	7
0xBB002964	SCHED_PORT_Q_CTRL_SET0 [6][7].RESERVED[31:9]	23
0xBB002964	SCHED_PORT_Q_CTRL_SET0 [6][7].STRICT_EN[8:8]	1
0xBB002964	SCHED_PORT_Q_CTRL_SET0 [6][7].RESERVED[7:7]	1
0xBB002964	SCHED_PORT_Q_CTRL_SET0 [6][7].WEIGHT[6:0]	7
0xBB002968	SCHED_PORT_Q_CTRL_SET0 [7][0].RESERVED[31:9]	23
0xBB002968	SCHED_PORT_Q_CTRL_SET0 [7][0].STRICT_EN[8:8]	1
0xBB002968	SCHED_PORT_Q_CTRL_SET0 [7][0].RESERVED[7:7]	1
0xBB002968	SCHED_PORT_Q_CTRL_SET0 [7][0].WEIGHT[6:0]	7
0xBB00296C	SCHED_PORT_Q_CTRL_SET0 [7][1].RESERVED[31:9]	23
0xBB00296C	SCHED_PORT_Q_CTRL_SET0 [7][1].STRICT_EN[8:8]	1
0xBB00296C	SCHED_PORT_Q_CTRL_SET0 [7][1].RESERVED[7:7]	1
0xBB00296C	SCHED_PORT_Q_CTRL_SET0 [7][1].WEIGHT[6:0]	7
0xBB002970	SCHED_PORT_Q_CTRL_SET0 [7][2].RESERVED[31:9]	23
0xBB002970	SCHED_PORT_Q_CTRL_SET0 [7][2].STRICT_EN[8:8]	1
0xBB002970	SCHED_PORT_Q_CTRL_SET0 [7][2].RESERVED[7:7]	1
0xBB002970	SCHED_PORT_Q_CTRL_SET0 [7][2].WEIGHT[6:0]	7
0xBB002974	SCHED_PORT_Q_CTRL_SET0 [7][3].RESERVED[31:9]	23
0xBB002974	SCHED_PORT_Q_CTRL_SET0 [7][3].STRICT_EN[8:8]	1
0xBB002974	SCHED_PORT_Q_CTRL_SET0 [7][3].RESERVED[7:7]	1
0xBB002974	SCHED_PORT_Q_CTRL_SET0 [7][3].WEIGHT[6:0]	7
0xBB002978	SCHED_PORT_Q_CTRL_SET0 [7][4].RESERVED[31:9]	23
0xBB002978	SCHED_PORT_Q_CTRL_SET0 [7][4].STRICT_EN[8:8]	1
0xBB002978	SCHED_PORT_Q_CTRL_SET0 [7][4].RESERVED[7:7]	1
0xBB002978	SCHED_PORT_Q_CTRL_SET0 [7][4].WEIGHT[6:0]	7
0xBB00297C	SCHED_PORT_Q_CTRL_SET0 [7][5].RESERVED[31:9]	23
0xBB00297C	SCHED_PORT_Q_CTRL_SET0 [7][5].STRICT_EN[8:8]	1
0xBB00297C	SCHED_PORT_Q_CTRL_SET0 [7][5].RESERVED[7:7]	1
0xBB00297C	SCHED_PORT_Q_CTRL_SET0 [7][5].WEIGHT[6:0]	7
0xBB002980	SCHED_PORT_Q_CTRL_SET0 [7][6].RESERVED[31:9]	23
0xBB002980	SCHED_PORT_Q_CTRL_SET0 [7][6].STRICT_EN[8:8]	1
0xBB002980	SCHED_PORT_Q_CTRL_SET0 [7][6].RESERVED[7:7]	1
0xBB002980	SCHED_PORT_Q_CTRL_SET0 [7][6].WEIGHT[6:0]	7
0xBB002984	SCHED_PORT_Q_CTRL_SET0 [7][7].RESERVED[31:9]	23
0xBB002984	SCHED_PORT_Q_CTRL_SET0 [7][7].STRICT_EN[8:8]	1
0xBB002984	SCHED_PORT_Q_CTRL_SET0 [7][7].RESERVED[7:7]	1
0xBB002984	SCHED_PORT_Q_CTRL_SET0 [7][7].WEIGHT[6:0]	7
0xBB002988	SCHED_PORT_Q_CTRL_SET0 [8][0].RESERVED[31:9]	23
0xBB002988	SCHED_PORT_Q_CTRL_SET0 [8][0].STRICT_EN[8:8]	1
0xBB002988	SCHED_PORT_Q_CTRL_SET0 [8][0].RESERVED[7:7]	1
0xBB002988	SCHED_PORT_Q_CTRL_SET0 [8][0].WEIGHT[6:0]	7
0xBB00298C	SCHED_PORT_Q_CTRL_SET0 [8][1].RESERVED[31:9]	23
0xBB00298C	SCHED_PORT_Q_CTRL_SET0 [8][1].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00298C	SCHED_PORT_Q_CTRL_SET0 [8][1].RESERVED[7:7]	1
0xBB00298C	SCHED_PORT_Q_CTRL_SET0 [8][1].WEIGHT[6:0]	7
0xBB002990	SCHED_PORT_Q_CTRL_SET0 [8][2].RESERVED[31:9]	23
0xBB002990	SCHED_PORT_Q_CTRL_SET0 [8][2].STRICT_EN[8:8]	1
0xBB002990	SCHED_PORT_Q_CTRL_SET0 [8][2].RESERVED[7:7]	1
0xBB002990	SCHED_PORT_Q_CTRL_SET0 [8][2].WEIGHT[6:0]	7
0xBB002994	SCHED_PORT_Q_CTRL_SET0 [8][3].RESERVED[31:9]	23
0xBB002994	SCHED_PORT_Q_CTRL_SET0 [8][3].STRICT_EN[8:8]	1
0xBB002994	SCHED_PORT_Q_CTRL_SET0 [8][3].RESERVED[7:7]	1
0xBB002994	SCHED_PORT_Q_CTRL_SET0 [8][3].WEIGHT[6:0]	7
0xBB002998	SCHED_PORT_Q_CTRL_SET0 [8][4].RESERVED[31:9]	23
0xBB002998	SCHED_PORT_Q_CTRL_SET0 [8][4].STRICT_EN[8:8]	1
0xBB002998	SCHED_PORT_Q_CTRL_SET0 [8][4].RESERVED[7:7]	1
0xBB002998	SCHED_PORT_Q_CTRL_SET0 [8][4].WEIGHT[6:0]	7
0xBB00299C	SCHED_PORT_Q_CTRL_SET0 [8][5].RESERVED[31:9]	23
0xBB00299C	SCHED_PORT_Q_CTRL_SET0 [8][5].STRICT_EN[8:8]	1
0xBB00299C	SCHED_PORT_Q_CTRL_SET0 [8][5].RESERVED[7:7]	1
0xBB00299C	SCHED_PORT_Q_CTRL_SET0 [8][5].WEIGHT[6:0]	7
0xBB0029A0	SCHED_PORT_Q_CTRL_SET0 [8][6].RESERVED[31:9]	23
0xBB0029A0	SCHED_PORT_Q_CTRL_SET0 [8][6].STRICT_EN[8:8]	1
0xBB0029A0	SCHED_PORT_Q_CTRL_SET0 [8][6].RESERVED[7:7]	1
0xBB0029A0	SCHED_PORT_Q_CTRL_SET0 [8][6].WEIGHT[6:0]	7
0xBB0029A4	SCHED_PORT_Q_CTRL_SET0 [8][7].RESERVED[31:9]	23
0xBB0029A4	SCHED_PORT_Q_CTRL_SET0 [8][7].STRICT_EN[8:8]	1
0xBB0029A4	SCHED_PORT_Q_CTRL_SET0 [8][7].RESERVED[7:7]	1
0xBB0029A4	SCHED_PORT_Q_CTRL_SET0 [8][7].WEIGHT[6:0]	7
0xBB0029A8	SCHED_PORT_Q_CTRL_SET0 [9][0].RESERVED[31:9]	23
0xBB0029A8	SCHED_PORT_Q_CTRL_SET0 [9][0].STRICT_EN[8:8]	1
0xBB0029A8	SCHED_PORT_Q_CTRL_SET0 [9][0].RESERVED[7:7]	1
0xBB0029A8	SCHED_PORT_Q_CTRL_SET0 [9][0].WEIGHT[6:0]	7
0xBB0029AC	SCHED_PORT_Q_CTRL_SET0 [9][1].RESERVED[31:9]	23
0xBB0029AC	SCHED_PORT_Q_CTRL_SET0 [9][1].STRICT_EN[8:8]	1
0xBB0029AC	SCHED_PORT_Q_CTRL_SET0 [9][1].RESERVED[7:7]	1
0xBB0029AC	SCHED_PORT_Q_CTRL_SET0 [9][1].WEIGHT[6:0]	7
0xBB0029B0	SCHED_PORT_Q_CTRL_SET0 [9][2].RESERVED[31:9]	23
0xBB0029B0	SCHED_PORT_Q_CTRL_SET0 [9][2].STRICT_EN[8:8]	1
0xBB0029B0	SCHED_PORT_Q_CTRL_SET0 [9][2].RESERVED[7:7]	1
0xBB0029B0	SCHED_PORT_Q_CTRL_SET0 [9][2].WEIGHT[6:0]	7
0xBB0029B4	SCHED_PORT_Q_CTRL_SET0 [9][3].RESERVED[31:9]	23
0xBB0029B4	SCHED_PORT_Q_CTRL_SET0 [9][3].STRICT_EN[8:8]	1
0xBB0029B4	SCHED_PORT_Q_CTRL_SET0 [9][3].RESERVED[7:7]	1
0xBB0029B4	SCHED_PORT_Q_CTRL_SET0 [9][3].WEIGHT[6:0]	7
0xBB0029B8	SCHED_PORT_Q_CTRL_SET0 [9][4].RESERVED[31:9]	23
0xBB0029B8	SCHED_PORT_Q_CTRL_SET0 [9][4].STRICT_EN[8:8]	1
0xBB0029B8	SCHED_PORT_Q_CTRL_SET0 [9][4].RESERVED[7:7]	1
0xBB0029B8	SCHED_PORT_Q_CTRL_SET0 [9][4].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0029BC	SCHED_PORT_Q_CTRL_SET0 [9][5].RESERVED[31:9]	23
0xBB0029BC	SCHED_PORT_Q_CTRL_SET0 [9][5].STRICT_EN[8:8]	1
0xBB0029BC	SCHED_PORT_Q_CTRL_SET0 [9][5].RESERVED[7:7]	1
0xBB0029BC	SCHED_PORT_Q_CTRL_SET0 [9][5].WEIGHT[6:0]	7
0xBB0029C0	SCHED_PORT_Q_CTRL_SET0 [9][6].RESERVED[31:9]	23
0xBB0029C0	SCHED_PORT_Q_CTRL_SET0 [9][6].STRICT_EN[8:8]	1
0xBB0029C0	SCHED_PORT_Q_CTRL_SET0 [9][6].RESERVED[7:7]	1
0xBB0029C0	SCHED_PORT_Q_CTRL_SET0 [9][6].WEIGHT[6:0]	7
0xBB0029C4	SCHED_PORT_Q_CTRL_SET0 [9][7].RESERVED[31:9]	23
0xBB0029C4	SCHED_PORT_Q_CTRL_SET0 [9][7].STRICT_EN[8:8]	1
0xBB0029C4	SCHED_PORT_Q_CTRL_SET0 [9][7].RESERVED[7:7]	1
0xBB0029C4	SCHED_PORT_Q_CTRL_SET0 [9][7].WEIGHT[6:0]	7
0xBB0029C8	SCHED_PORT_Q_CTRL_SET0 [10][0].RESERVED[31:9]	23
0xBB0029C8	SCHED_PORT_Q_CTRL_SET0 [10][0].STRICT_EN[8:8]	1
0xBB0029C8	SCHED_PORT_Q_CTRL_SET0 [10][0].RESERVED[7:7]	1
0xBB0029C8	SCHED_PORT_Q_CTRL_SET0 [10][0].WEIGHT[6:0]	7
0xBB0029CC	SCHED_PORT_Q_CTRL_SET0 [10][1].RESERVED[31:9]	23
0xBB0029CC	SCHED_PORT_Q_CTRL_SET0 [10][1].STRICT_EN[8:8]	1
0xBB0029CC	SCHED_PORT_Q_CTRL_SET0 [10][1].RESERVED[7:7]	1
0xBB0029CC	SCHED_PORT_Q_CTRL_SET0 [10][1].WEIGHT[6:0]	7
0xBB0029D0	SCHED_PORT_Q_CTRL_SET0 [10][2].RESERVED[31:9]	23
0xBB0029D0	SCHED_PORT_Q_CTRL_SET0 [10][2].STRICT_EN[8:8]	1
0xBB0029D0	SCHED_PORT_Q_CTRL_SET0 [10][2].RESERVED[7:7]	1
0xBB0029D0	SCHED_PORT_Q_CTRL_SET0 [10][2].WEIGHT[6:0]	7
0xBB0029D4	SCHED_PORT_Q_CTRL_SET0 [10][3].RESERVED[31:9]	23
0xBB0029D4	SCHED_PORT_Q_CTRL_SET0 [10][3].STRICT_EN[8:8]	1
0xBB0029D4	SCHED_PORT_Q_CTRL_SET0 [10][3].RESERVED[7:7]	1
0xBB0029D4	SCHED_PORT_Q_CTRL_SET0 [10][3].WEIGHT[6:0]	7
0xBB0029D8	SCHED_PORT_Q_CTRL_SET0 [10][4].RESERVED[31:9]	23
0xBB0029D8	SCHED_PORT_Q_CTRL_SET0 [10][4].STRICT_EN[8:8]	1
0xBB0029D8	SCHED_PORT_Q_CTRL_SET0 [10][4].RESERVED[7:7]	1
0xBB0029D8	SCHED_PORT_Q_CTRL_SET0 [10][4].WEIGHT[6:0]	7
0xBB0029DC	SCHED_PORT_Q_CTRL_SET0 [10][5].RESERVED[31:9]	23
0xBB0029DC	SCHED_PORT_Q_CTRL_SET0 [10][5].STRICT_EN[8:8]	1
0xBB0029DC	SCHED_PORT_Q_CTRL_SET0 [10][5].RESERVED[7:7]	1
0xBB0029DC	SCHED_PORT_Q_CTRL_SET0 [10][5].WEIGHT[6:0]	7
0xBB0029E0	SCHED_PORT_Q_CTRL_SET0 [10][6].RESERVED[31:9]	23
0xBB0029E0	SCHED_PORT_Q_CTRL_SET0 [10][6].STRICT_EN[8:8]	1
0xBB0029E0	SCHED_PORT_Q_CTRL_SET0 [10][6].RESERVED[7:7]	1
0xBB0029E0	SCHED_PORT_Q_CTRL_SET0 [10][6].WEIGHT[6:0]	7
0xBB0029E4	SCHED_PORT_Q_CTRL_SET0 [10][7].RESERVED[31:9]	23
0xBB0029E4	SCHED_PORT_Q_CTRL_SET0 [10][7].STRICT_EN[8:8]	1
0xBB0029E4	SCHED_PORT_Q_CTRL_SET0 [10][7].RESERVED[7:7]	1
0xBB0029E4	SCHED_PORT_Q_CTRL_SET0 [10][7].WEIGHT[6:0]	7
0xBB0029E8	SCHED_PORT_Q_CTRL_SET0 [11][0].RESERVED[31:9]	23
0xBB0029E8	SCHED_PORT_Q_CTRL_SET0 [11][0].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0029E8	SCHED_PORT_Q_CTRL_SET0 [11][0].RESERVED[7:7]	1
0xBB0029E8	SCHED_PORT_Q_CTRL_SET0 [11][0].WEIGHT[6:0]	7
0xBB0029EC	SCHED_PORT_Q_CTRL_SET0 [11][1].RESERVED[31:9]	23
0xBB0029EC	SCHED_PORT_Q_CTRL_SET0 [11][1].STRICT_EN[8:8]	1
0xBB0029EC	SCHED_PORT_Q_CTRL_SET0 [11][1].RESERVED[7:7]	1
0xBB0029EC	SCHED_PORT_Q_CTRL_SET0 [11][1].WEIGHT[6:0]	7
0xBB0029F0	SCHED_PORT_Q_CTRL_SET0 [11][2].RESERVED[31:9]	23
0xBB0029F0	SCHED_PORT_Q_CTRL_SET0 [11][2].STRICT_EN[8:8]	1
0xBB0029F0	SCHED_PORT_Q_CTRL_SET0 [11][2].RESERVED[7:7]	1
0xBB0029F0	SCHED_PORT_Q_CTRL_SET0 [11][2].WEIGHT[6:0]	7
0xBB0029F4	SCHED_PORT_Q_CTRL_SET0 [11][3].RESERVED[31:9]	23
0xBB0029F4	SCHED_PORT_Q_CTRL_SET0 [11][3].STRICT_EN[8:8]	1
0xBB0029F4	SCHED_PORT_Q_CTRL_SET0 [11][3].RESERVED[7:7]	1
0xBB0029F4	SCHED_PORT_Q_CTRL_SET0 [11][3].WEIGHT[6:0]	7
0xBB0029F8	SCHED_PORT_Q_CTRL_SET0 [11][4].RESERVED[31:9]	23
0xBB0029F8	SCHED_PORT_Q_CTRL_SET0 [11][4].STRICT_EN[8:8]	1
0xBB0029F8	SCHED_PORT_Q_CTRL_SET0 [11][4].RESERVED[7:7]	1
0xBB0029F8	SCHED_PORT_Q_CTRL_SET0 [11][4].WEIGHT[6:0]	7
0xBB0029FC	SCHED_PORT_Q_CTRL_SET0 [11][5].RESERVED[31:9]	23
0xBB0029FC	SCHED_PORT_Q_CTRL_SET0 [11][5].STRICT_EN[8:8]	1
0xBB0029FC	SCHED_PORT_Q_CTRL_SET0 [11][5].RESERVED[7:7]	1
0xBB0029FC	SCHED_PORT_Q_CTRL_SET0 [11][5].WEIGHT[6:0]	7
0xBB002A00	SCHED_PORT_Q_CTRL_SET0 [11][6].RESERVED[31:9]	23
0xBB002A00	SCHED_PORT_Q_CTRL_SET0 [11][6].STRICT_EN[8:8]	1
0xBB002A00	SCHED_PORT_Q_CTRL_SET0 [11][6].RESERVED[7:7]	1
0xBB002A00	SCHED_PORT_Q_CTRL_SET0 [11][6].WEIGHT[6:0]	7
0xBB002A04	SCHED_PORT_Q_CTRL_SET0 [11][7].RESERVED[31:9]	23
0xBB002A04	SCHED_PORT_Q_CTRL_SET0 [11][7].STRICT_EN[8:8]	1
0xBB002A04	SCHED_PORT_Q_CTRL_SET0 [11][7].RESERVED[7:7]	1
0xBB002A04	SCHED_PORT_Q_CTRL_SET0 [11][7].WEIGHT[6:0]	7
0xBB002A08	SCHED_PORT_Q_CTRL_SET0 [12][0].RESERVED[31:9]	23
0xBB002A08	SCHED_PORT_Q_CTRL_SET0 [12][0].STRICT_EN[8:8]	1
0xBB002A08	SCHED_PORT_Q_CTRL_SET0 [12][0].RESERVED[7:7]	1
0xBB002A08	SCHED_PORT_Q_CTRL_SET0 [12][0].WEIGHT[6:0]	7
0xBB002A0C	SCHED_PORT_Q_CTRL_SET0 [12][1].RESERVED[31:9]	23
0xBB002A0C	SCHED_PORT_Q_CTRL_SET0 [12][1].STRICT_EN[8:8]	1
0xBB002A0C	SCHED_PORT_Q_CTRL_SET0 [12][1].RESERVED[7:7]	1
0xBB002A0C	SCHED_PORT_Q_CTRL_SET0 [12][1].WEIGHT[6:0]	7
0xBB002A10	SCHED_PORT_Q_CTRL_SET0 [12][2].RESERVED[31:9]	23
0xBB002A10	SCHED_PORT_Q_CTRL_SET0 [12][2].STRICT_EN[8:8]	1
0xBB002A10	SCHED_PORT_Q_CTRL_SET0 [12][2].RESERVED[7:7]	1
0xBB002A10	SCHED_PORT_Q_CTRL_SET0 [12][2].WEIGHT[6:0]	7
0xBB002A14	SCHED_PORT_Q_CTRL_SET0 [12][3].RESERVED[31:9]	23
0xBB002A14	SCHED_PORT_Q_CTRL_SET0 [12][3].STRICT_EN[8:8]	1
0xBB002A14	SCHED_PORT_Q_CTRL_SET0 [12][3].RESERVED[7:7]	1
0xBB002A14	SCHED_PORT_Q_CTRL_SET0 [12][3].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002A18	SCHED_PORT_Q_CTRL_SET0 [12][4].RESERVED[31:9]	23
0xBB002A18	SCHED_PORT_Q_CTRL_SET0 [12][4].STRICT_EN[8:8]	1
0xBB002A18	SCHED_PORT_Q_CTRL_SET0 [12][4].RESERVED[7:7]	1
0xBB002A18	SCHED_PORT_Q_CTRL_SET0 [12][4].WEIGHT[6:0]	7
0xBB002A1C	SCHED_PORT_Q_CTRL_SET0 [12][5].RESERVED[31:9]	23
0xBB002A1C	SCHED_PORT_Q_CTRL_SET0 [12][5].STRICT_EN[8:8]	1
0xBB002A1C	SCHED_PORT_Q_CTRL_SET0 [12][5].RESERVED[7:7]	1
0xBB002A1C	SCHED_PORT_Q_CTRL_SET0 [12][5].WEIGHT[6:0]	7
0xBB002A20	SCHED_PORT_Q_CTRL_SET0 [12][6].RESERVED[31:9]	23
0xBB002A20	SCHED_PORT_Q_CTRL_SET0 [12][6].STRICT_EN[8:8]	1
0xBB002A20	SCHED_PORT_Q_CTRL_SET0 [12][6].RESERVED[7:7]	1
0xBB002A20	SCHED_PORT_Q_CTRL_SET0 [12][6].WEIGHT[6:0]	7
0xBB002A24	SCHED_PORT_Q_CTRL_SET0 [12][7].RESERVED[31:9]	23
0xBB002A24	SCHED_PORT_Q_CTRL_SET0 [12][7].STRICT_EN[8:8]	1
0xBB002A24	SCHED_PORT_Q_CTRL_SET0 [12][7].RESERVED[7:7]	1
0xBB002A24	SCHED_PORT_Q_CTRL_SET0 [12][7].WEIGHT[6:0]	7
0xBB002A28	SCHED_PORT_Q_CTRL_SET0 [13][0].RESERVED[31:9]	23
0xBB002A28	SCHED_PORT_Q_CTRL_SET0 [13][0].STRICT_EN[8:8]	1
0xBB002A28	SCHED_PORT_Q_CTRL_SET0 [13][0].RESERVED[7:7]	1
0xBB002A28	SCHED_PORT_Q_CTRL_SET0 [13][0].WEIGHT[6:0]	7
0xBB002A2C	SCHED_PORT_Q_CTRL_SET0 [13][1].RESERVED[31:9]	23
0xBB002A2C	SCHED_PORT_Q_CTRL_SET0 [13][1].STRICT_EN[8:8]	1
0xBB002A2C	SCHED_PORT_Q_CTRL_SET0 [13][1].RESERVED[7:7]	1
0xBB002A2C	SCHED_PORT_Q_CTRL_SET0 [13][1].WEIGHT[6:0]	7
0xBB002A30	SCHED_PORT_Q_CTRL_SET0 [13][2].RESERVED[31:9]	23
0xBB002A30	SCHED_PORT_Q_CTRL_SET0 [13][2].STRICT_EN[8:8]	1
0xBB002A30	SCHED_PORT_Q_CTRL_SET0 [13][2].RESERVED[7:7]	1
0xBB002A30	SCHED_PORT_Q_CTRL_SET0 [13][2].WEIGHT[6:0]	7
0xBB002A34	SCHED_PORT_Q_CTRL_SET0 [13][3].RESERVED[31:9]	23
0xBB002A34	SCHED_PORT_Q_CTRL_SET0 [13][3].STRICT_EN[8:8]	1
0xBB002A34	SCHED_PORT_Q_CTRL_SET0 [13][3].RESERVED[7:7]	1
0xBB002A34	SCHED_PORT_Q_CTRL_SET0 [13][3].WEIGHT[6:0]	7
0xBB002A38	SCHED_PORT_Q_CTRL_SET0 [13][4].RESERVED[31:9]	23
0xBB002A38	SCHED_PORT_Q_CTRL_SET0 [13][4].STRICT_EN[8:8]	1
0xBB002A38	SCHED_PORT_Q_CTRL_SET0 [13][4].RESERVED[7:7]	1
0xBB002A38	SCHED_PORT_Q_CTRL_SET0 [13][4].WEIGHT[6:0]	7
0xBB002A3C	SCHED_PORT_Q_CTRL_SET0 [13][5].RESERVED[31:9]	23
0xBB002A3C	SCHED_PORT_Q_CTRL_SET0 [13][5].STRICT_EN[8:8]	1
0xBB002A3C	SCHED_PORT_Q_CTRL_SET0 [13][5].RESERVED[7:7]	1
0xBB002A3C	SCHED_PORT_Q_CTRL_SET0 [13][5].WEIGHT[6:0]	7
0xBB002A40	SCHED_PORT_Q_CTRL_SET0 [13][6].RESERVED[31:9]	23
0xBB002A40	SCHED_PORT_Q_CTRL_SET0 [13][6].STRICT_EN[8:8]	1
0xBB002A40	SCHED_PORT_Q_CTRL_SET0 [13][6].RESERVED[7:7]	1
0xBB002A40	SCHED_PORT_Q_CTRL_SET0 [13][6].WEIGHT[6:0]	7
0xBB002A44	SCHED_PORT_Q_CTRL_SET0 [13][7].RESERVED[31:9]	23
0xBB002A44	SCHED_PORT_Q_CTRL_SET0 [13][7].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002A44	SCHED_PORT_Q_CTRL_SET0 [13][7].RESERVED[7:7]	1
0xBB002A44	SCHED_PORT_Q_CTRL_SET0 [13][7].WEIGHT[6:0]	7
0xBB002A48	SCHED_PORT_Q_CTRL_SET0 [14][0].RESERVED[31:9]	23
0xBB002A48	SCHED_PORT_Q_CTRL_SET0 [14][0].STRICT_EN[8:8]	1
0xBB002A48	SCHED_PORT_Q_CTRL_SET0 [14][0].RESERVED[7:7]	1
0xBB002A48	SCHED_PORT_Q_CTRL_SET0 [14][0].WEIGHT[6:0]	7
0xBB002A4C	SCHED_PORT_Q_CTRL_SET0 [14][1].RESERVED[31:9]	23
0xBB002A4C	SCHED_PORT_Q_CTRL_SET0 [14][1].STRICT_EN[8:8]	1
0xBB002A4C	SCHED_PORT_Q_CTRL_SET0 [14][1].RESERVED[7:7]	1
0xBB002A4C	SCHED_PORT_Q_CTRL_SET0 [14][1].WEIGHT[6:0]	7
0xBB002A50	SCHED_PORT_Q_CTRL_SET0 [14][2].RESERVED[31:9]	23
0xBB002A50	SCHED_PORT_Q_CTRL_SET0 [14][2].STRICT_EN[8:8]	1
0xBB002A50	SCHED_PORT_Q_CTRL_SET0 [14][2].RESERVED[7:7]	1
0xBB002A50	SCHED_PORT_Q_CTRL_SET0 [14][2].WEIGHT[6:0]	7
0xBB002A54	SCHED_PORT_Q_CTRL_SET0 [14][3].RESERVED[31:9]	23
0xBB002A54	SCHED_PORT_Q_CTRL_SET0 [14][3].STRICT_EN[8:8]	1
0xBB002A54	SCHED_PORT_Q_CTRL_SET0 [14][3].RESERVED[7:7]	1
0xBB002A54	SCHED_PORT_Q_CTRL_SET0 [14][3].WEIGHT[6:0]	7
0xBB002A58	SCHED_PORT_Q_CTRL_SET0 [14][4].RESERVED[31:9]	23
0xBB002A58	SCHED_PORT_Q_CTRL_SET0 [14][4].STRICT_EN[8:8]	1
0xBB002A58	SCHED_PORT_Q_CTRL_SET0 [14][4].RESERVED[7:7]	1
0xBB002A58	SCHED_PORT_Q_CTRL_SET0 [14][4].WEIGHT[6:0]	7
0xBB002A5C	SCHED_PORT_Q_CTRL_SET0 [14][5].RESERVED[31:9]	23
0xBB002A5C	SCHED_PORT_Q_CTRL_SET0 [14][5].STRICT_EN[8:8]	1
0xBB002A5C	SCHED_PORT_Q_CTRL_SET0 [14][5].RESERVED[7:7]	1
0xBB002A5C	SCHED_PORT_Q_CTRL_SET0 [14][5].WEIGHT[6:0]	7
0xBB002A60	SCHED_PORT_Q_CTRL_SET0 [14][6].RESERVED[31:9]	23
0xBB002A60	SCHED_PORT_Q_CTRL_SET0 [14][6].STRICT_EN[8:8]	1
0xBB002A60	SCHED_PORT_Q_CTRL_SET0 [14][6].RESERVED[7:7]	1
0xBB002A60	SCHED_PORT_Q_CTRL_SET0 [14][6].WEIGHT[6:0]	7
0xBB002A64	SCHED_PORT_Q_CTRL_SET0 [14][7].RESERVED[31:9]	23
0xBB002A64	SCHED_PORT_Q_CTRL_SET0 [14][7].STRICT_EN[8:8]	1
0xBB002A64	SCHED_PORT_Q_CTRL_SET0 [14][7].RESERVED[7:7]	1
0xBB002A64	SCHED_PORT_Q_CTRL_SET0 [14][7].WEIGHT[6:0]	7
0xBB002A68	SCHED_PORT_Q_CTRL_SET0 [15][0].RESERVED[31:9]	23
0xBB002A68	SCHED_PORT_Q_CTRL_SET0 [15][0].STRICT_EN[8:8]	1
0xBB002A68	SCHED_PORT_Q_CTRL_SET0 [15][0].RESERVED[7:7]	1
0xBB002A68	SCHED_PORT_Q_CTRL_SET0 [15][0].WEIGHT[6:0]	7
0xBB002A6C	SCHED_PORT_Q_CTRL_SET0 [15][1].RESERVED[31:9]	23
0xBB002A6C	SCHED_PORT_Q_CTRL_SET0 [15][1].STRICT_EN[8:8]	1
0xBB002A6C	SCHED_PORT_Q_CTRL_SET0 [15][1].RESERVED[7:7]	1
0xBB002A6C	SCHED_PORT_Q_CTRL_SET0 [15][1].WEIGHT[6:0]	7
0xBB002A70	SCHED_PORT_Q_CTRL_SET0 [15][2].RESERVED[31:9]	23
0xBB002A70	SCHED_PORT_Q_CTRL_SET0 [15][2].STRICT_EN[8:8]	1
0xBB002A70	SCHED_PORT_Q_CTRL_SET0 [15][2].RESERVED[7:7]	1
0xBB002A70	SCHED_PORT_Q_CTRL_SET0 [15][2].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002A74	SCHED_PORT_Q_CTRL_SET0 [15][3].RESERVED[31:9]	23
0xBB002A74	SCHED_PORT_Q_CTRL_SET0 [15][3].STRICT_EN[8:8]	1
0xBB002A74	SCHED_PORT_Q_CTRL_SET0 [15][3].RESERVED[7:7]	1
0xBB002A74	SCHED_PORT_Q_CTRL_SET0 [15][3].WEIGHT[6:0]	7
0xBB002A78	SCHED_PORT_Q_CTRL_SET0 [15][4].RESERVED[31:9]	23
0xBB002A78	SCHED_PORT_Q_CTRL_SET0 [15][4].STRICT_EN[8:8]	1
0xBB002A78	SCHED_PORT_Q_CTRL_SET0 [15][4].RESERVED[7:7]	1
0xBB002A78	SCHED_PORT_Q_CTRL_SET0 [15][4].WEIGHT[6:0]	7
0xBB002A7C	SCHED_PORT_Q_CTRL_SET0 [15][5].RESERVED[31:9]	23
0xBB002A7C	SCHED_PORT_Q_CTRL_SET0 [15][5].STRICT_EN[8:8]	1
0xBB002A7C	SCHED_PORT_Q_CTRL_SET0 [15][5].RESERVED[7:7]	1
0xBB002A7C	SCHED_PORT_Q_CTRL_SET0 [15][5].WEIGHT[6:0]	7
0xBB002A80	SCHED_PORT_Q_CTRL_SET0 [15][6].RESERVED[31:9]	23
0xBB002A80	SCHED_PORT_Q_CTRL_SET0 [15][6].STRICT_EN[8:8]	1
0xBB002A80	SCHED_PORT_Q_CTRL_SET0 [15][6].RESERVED[7:7]	1
0xBB002A80	SCHED_PORT_Q_CTRL_SET0 [15][6].WEIGHT[6:0]	7
0xBB002A84	SCHED_PORT_Q_CTRL_SET0 [15][7].RESERVED[31:9]	23
0xBB002A84	SCHED_PORT_Q_CTRL_SET0 [15][7].STRICT_EN[8:8]	1
0xBB002A84	SCHED_PORT_Q_CTRL_SET0 [15][7].RESERVED[7:7]	1
0xBB002A84	SCHED_PORT_Q_CTRL_SET0 [15][7].WEIGHT[6:0]	7
0xBB002A88	SCHED_PORT_Q_CTRL_SET0 [16][0].RESERVED[31:9]	23
0xBB002A88	SCHED_PORT_Q_CTRL_SET0 [16][0].STRICT_EN[8:8]	1
0xBB002A88	SCHED_PORT_Q_CTRL_SET0 [16][0].RESERVED[7:7]	1
0xBB002A88	SCHED_PORT_Q_CTRL_SET0 [16][0].WEIGHT[6:0]	7
0xBB002A8C	SCHED_PORT_Q_CTRL_SET0 [16][1].RESERVED[31:9]	23
0xBB002A8C	SCHED_PORT_Q_CTRL_SET0 [16][1].STRICT_EN[8:8]	1
0xBB002A8C	SCHED_PORT_Q_CTRL_SET0 [16][1].RESERVED[7:7]	1
0xBB002A8C	SCHED_PORT_Q_CTRL_SET0 [16][1].WEIGHT[6:0]	7
0xBB002A90	SCHED_PORT_Q_CTRL_SET0 [16][2].RESERVED[31:9]	23
0xBB002A90	SCHED_PORT_Q_CTRL_SET0 [16][2].STRICT_EN[8:8]	1
0xBB002A90	SCHED_PORT_Q_CTRL_SET0 [16][2].RESERVED[7:7]	1
0xBB002A90	SCHED_PORT_Q_CTRL_SET0 [16][2].WEIGHT[6:0]	7
0xBB002A94	SCHED_PORT_Q_CTRL_SET0 [16][3].RESERVED[31:9]	23
0xBB002A94	SCHED_PORT_Q_CTRL_SET0 [16][3].STRICT_EN[8:8]	1
0xBB002A94	SCHED_PORT_Q_CTRL_SET0 [16][3].RESERVED[7:7]	1
0xBB002A94	SCHED_PORT_Q_CTRL_SET0 [16][3].WEIGHT[6:0]	7
0xBB002A98	SCHED_PORT_Q_CTRL_SET0 [16][4].RESERVED[31:9]	23
0xBB002A98	SCHED_PORT_Q_CTRL_SET0 [16][4].STRICT_EN[8:8]	1
0xBB002A98	SCHED_PORT_Q_CTRL_SET0 [16][4].RESERVED[7:7]	1
0xBB002A98	SCHED_PORT_Q_CTRL_SET0 [16][4].WEIGHT[6:0]	7
0xBB002A9C	SCHED_PORT_Q_CTRL_SET0 [16][5].RESERVED[31:9]	23
0xBB002A9C	SCHED_PORT_Q_CTRL_SET0 [16][5].STRICT_EN[8:8]	1
0xBB002A9C	SCHED_PORT_Q_CTRL_SET0 [16][5].RESERVED[7:7]	1
0xBB002A9C	SCHED_PORT_Q_CTRL_SET0 [16][5].WEIGHT[6:0]	7
0xBB002AA0	SCHED_PORT_Q_CTRL_SET0 [16][6].RESERVED[31:9]	23
0xBB002AA0	SCHED_PORT_Q_CTRL_SET0 [16][6].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002AA0	SCHED_PORT_Q_CTRL_SET0 [16][6].RESERVED[7:7]	1
0xBB002AA0	SCHED_PORT_Q_CTRL_SET0 [16][6].WEIGHT[6:0]	7
0xBB002AA4	SCHED_PORT_Q_CTRL_SET0 [16][7].RESERVED[31:9]	23
0xBB002AA4	SCHED_PORT_Q_CTRL_SET0 [16][7].STRICT_EN[8:8]	1
0xBB002AA4	SCHED_PORT_Q_CTRL_SET0 [16][7].RESERVED[7:7]	1
0xBB002AA4	SCHED_PORT_Q_CTRL_SET0 [16][7].WEIGHT[6:0]	7
0xBB002AA8	SCHED_PORT_Q_CTRL_SET0 [17][0].RESERVED[31:9]	23
0xBB002AA8	SCHED_PORT_Q_CTRL_SET0 [17][0].STRICT_EN[8:8]	1
0xBB002AA8	SCHED_PORT_Q_CTRL_SET0 [17][0].RESERVED[7:7]	1
0xBB002AA8	SCHED_PORT_Q_CTRL_SET0 [17][0].WEIGHT[6:0]	7
0xBB002AAC	SCHED_PORT_Q_CTRL_SET0 [17][1].RESERVED[31:9]	23
0xBB002AAC	SCHED_PORT_Q_CTRL_SET0 [17][1].STRICT_EN[8:8]	1
0xBB002AAC	SCHED_PORT_Q_CTRL_SET0 [17][1].RESERVED[7:7]	1
0xBB002AAC	SCHED_PORT_Q_CTRL_SET0 [17][1].WEIGHT[6:0]	7
0xBB002AB0	SCHED_PORT_Q_CTRL_SET0 [17][2].RESERVED[31:9]	23
0xBB002AB0	SCHED_PORT_Q_CTRL_SET0 [17][2].STRICT_EN[8:8]	1
0xBB002AB0	SCHED_PORT_Q_CTRL_SET0 [17][2].RESERVED[7:7]	1
0xBB002AB0	SCHED_PORT_Q_CTRL_SET0 [17][2].WEIGHT[6:0]	7
0xBB002AB4	SCHED_PORT_Q_CTRL_SET0 [17][3].RESERVED[31:9]	23
0xBB002AB4	SCHED_PORT_Q_CTRL_SET0 [17][3].STRICT_EN[8:8]	1
0xBB002AB4	SCHED_PORT_Q_CTRL_SET0 [17][3].RESERVED[7:7]	1
0xBB002AB4	SCHED_PORT_Q_CTRL_SET0 [17][3].WEIGHT[6:0]	7
0xBB002AB8	SCHED_PORT_Q_CTRL_SET0 [17][4].RESERVED[31:9]	23
0xBB002AB8	SCHED_PORT_Q_CTRL_SET0 [17][4].STRICT_EN[8:8]	1
0xBB002AB8	SCHED_PORT_Q_CTRL_SET0 [17][4].RESERVED[7:7]	1
0xBB002AB8	SCHED_PORT_Q_CTRL_SET0 [17][4].WEIGHT[6:0]	7
0xBB002ABC	SCHED_PORT_Q_CTRL_SET0 [17][5].RESERVED[31:9]	23
0xBB002ABC	SCHED_PORT_Q_CTRL_SET0 [17][5].STRICT_EN[8:8]	1
0xBB002ABC	SCHED_PORT_Q_CTRL_SET0 [17][5].RESERVED[7:7]	1
0xBB002ABC	SCHED_PORT_Q_CTRL_SET0 [17][5].WEIGHT[6:0]	7
0xBB002AC0	SCHED_PORT_Q_CTRL_SET0 [17][6].RESERVED[31:9]	23
0xBB002AC0	SCHED_PORT_Q_CTRL_SET0 [17][6].STRICT_EN[8:8]	1
0xBB002AC0	SCHED_PORT_Q_CTRL_SET0 [17][6].RESERVED[7:7]	1
0xBB002AC0	SCHED_PORT_Q_CTRL_SET0 [17][6].WEIGHT[6:0]	7
0xBB002AC4	SCHED_PORT_Q_CTRL_SET0 [17][7].RESERVED[31:9]	23
0xBB002AC4	SCHED_PORT_Q_CTRL_SET0 [17][7].STRICT_EN[8:8]	1
0xBB002AC4	SCHED_PORT_Q_CTRL_SET0 [17][7].RESERVED[7:7]	1
0xBB002AC4	SCHED_PORT_Q_CTRL_SET0 [17][7].WEIGHT[6:0]	7
0xBB002AC8	SCHED_PORT_Q_CTRL_SET0 [18][0].RESERVED[31:9]	23
0xBB002AC8	SCHED_PORT_Q_CTRL_SET0 [18][0].STRICT_EN[8:8]	1
0xBB002AC8	SCHED_PORT_Q_CTRL_SET0 [18][0].RESERVED[7:7]	1
0xBB002AC8	SCHED_PORT_Q_CTRL_SET0 [18][0].WEIGHT[6:0]	7
0xBB002ACC	SCHED_PORT_Q_CTRL_SET0 [18][1].RESERVED[31:9]	23
0xBB002ACC	SCHED_PORT_Q_CTRL_SET0 [18][1].STRICT_EN[8:8]	1
0xBB002ACC	SCHED_PORT_Q_CTRL_SET0 [18][1].RESERVED[7:7]	1
0xBB002ACC	SCHED_PORT_Q_CTRL_SET0 [18][1].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002AD0	SCHED_PORT_Q_CTRL_SET0 [18][2].RESERVED[31:9]	23
0xBB002AD0	SCHED_PORT_Q_CTRL_SET0 [18][2].STRICT_EN[8:8]	1
0xBB002AD0	SCHED_PORT_Q_CTRL_SET0 [18][2].RESERVED[7:7]	1
0xBB002AD0	SCHED_PORT_Q_CTRL_SET0 [18][2].WEIGHT[6:0]	7
0xBB002AD4	SCHED_PORT_Q_CTRL_SET0 [18][3].RESERVED[31:9]	23
0xBB002AD4	SCHED_PORT_Q_CTRL_SET0 [18][3].STRICT_EN[8:8]	1
0xBB002AD4	SCHED_PORT_Q_CTRL_SET0 [18][3].RESERVED[7:7]	1
0xBB002AD4	SCHED_PORT_Q_CTRL_SET0 [18][3].WEIGHT[6:0]	7
0xBB002AD8	SCHED_PORT_Q_CTRL_SET0 [18][4].RESERVED[31:9]	23
0xBB002AD8	SCHED_PORT_Q_CTRL_SET0 [18][4].STRICT_EN[8:8]	1
0xBB002AD8	SCHED_PORT_Q_CTRL_SET0 [18][4].RESERVED[7:7]	1
0xBB002AD8	SCHED_PORT_Q_CTRL_SET0 [18][4].WEIGHT[6:0]	7
0xBB002ADC	SCHED_PORT_Q_CTRL_SET0 [18][5].RESERVED[31:9]	23
0xBB002ADC	SCHED_PORT_Q_CTRL_SET0 [18][5].STRICT_EN[8:8]	1
0xBB002ADC	SCHED_PORT_Q_CTRL_SET0 [18][5].RESERVED[7:7]	1
0xBB002ADC	SCHED_PORT_Q_CTRL_SET0 [18][5].WEIGHT[6:0]	7
0xBB002AE0	SCHED_PORT_Q_CTRL_SET0 [18][6].RESERVED[31:9]	23
0xBB002AE0	SCHED_PORT_Q_CTRL_SET0 [18][6].STRICT_EN[8:8]	1
0xBB002AE0	SCHED_PORT_Q_CTRL_SET0 [18][6].RESERVED[7:7]	1
0xBB002AE0	SCHED_PORT_Q_CTRL_SET0 [18][6].WEIGHT[6:0]	7
0xBB002AE4	SCHED_PORT_Q_CTRL_SET0 [18][7].RESERVED[31:9]	23
0xBB002AE4	SCHED_PORT_Q_CTRL_SET0 [18][7].STRICT_EN[8:8]	1
0xBB002AE4	SCHED_PORT_Q_CTRL_SET0 [18][7].RESERVED[7:7]	1
0xBB002AE4	SCHED_PORT_Q_CTRL_SET0 [18][7].WEIGHT[6:0]	7
0xBB002AE8	SCHED_PORT_Q_CTRL_SET0 [19][0].RESERVED[31:9]	23
0xBB002AE8	SCHED_PORT_Q_CTRL_SET0 [19][0].STRICT_EN[8:8]	1
0xBB002AE8	SCHED_PORT_Q_CTRL_SET0 [19][0].RESERVED[7:7]	1
0xBB002AE8	SCHED_PORT_Q_CTRL_SET0 [19][0].WEIGHT[6:0]	7
0xBB002AEC	SCHED_PORT_Q_CTRL_SET0 [19][1].RESERVED[31:9]	23
0xBB002AEC	SCHED_PORT_Q_CTRL_SET0 [19][1].STRICT_EN[8:8]	1
0xBB002AFC	SCHED_PORT_Q_CTRL_SET0 [19][1].RESERVED[7:7]	1
0xBB002AEC	SCHED_PORT_Q_CTRL_SET0 [19][1].WEIGHT[6:0]	7
0xBB002AF0	SCHED_PORT_Q_CTRL_SET0 [19][2].RESERVED[31:9]	23
0xBB002AF0	SCHED_PORT_Q_CTRL_SET0 [19][2].STRICT_EN[8:8]	1
0xBB002AF0	SCHED_PORT_Q_CTRL_SET0 [19][2].RESERVED[7:7]	1
0xBB002AF0	SCHED_PORT_Q_CTRL_SET0 [19][2].WEIGHT[6:0]	7
0xBB002AF4	SCHED_PORT_Q_CTRL_SET0 [19][3].RESERVED[31:9]	23
0xBB002AF4	SCHED_PORT_Q_CTRL_SET0 [19][3].STRICT_EN[8:8]	1
0xBB002AF4	SCHED_PORT_Q_CTRL_SET0 [19][3].RESERVED[7:7]	1
0xBB002AF4	SCHED_PORT_Q_CTRL_SET0 [19][3].WEIGHT[6:0]	7
0xBB002AF8	SCHED_PORT_Q_CTRL_SET0 [19][4].RESERVED[31:9]	23
0xBB002AF8	SCHED_PORT_Q_CTRL_SET0 [19][4].STRICT_EN[8:8]	1
0xBB002AF8	SCHED_PORT_Q_CTRL_SET0 [19][4].RESERVED[7:7]	1
0xBB002AF8	SCHED_PORT_Q_CTRL_SET0 [19][4].WEIGHT[6:0]	7
0xBB002AFC	SCHED_PORT_Q_CTRL_SET0 [19][5].RESERVED[31:9]	23
0xBB002AFC	SCHED_PORT_Q_CTRL_SET0 [19][5].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002AFC	SCHED_PORT_Q_CTRL_SET0 [19][5].RESERVED[7:7]	1
0xBB002AFC	SCHED_PORT_Q_CTRL_SET0 [19][5].WEIGHT[6:0]	7
0xBB002B00	SCHED_PORT_Q_CTRL_SET0 [19][6].RESERVED[31:9]	23
0xBB002B00	SCHED_PORT_Q_CTRL_SET0 [19][6].STRICT_EN[8:8]	1
0xBB002B00	SCHED_PORT_Q_CTRL_SET0 [19][6].RESERVED[7:7]	1
0xBB002B00	SCHED_PORT_Q_CTRL_SET0 [19][6].WEIGHT[6:0]	7
0xBB002B04	SCHED_PORT_Q_CTRL_SET0 [19][7].RESERVED[31:9]	23
0xBB002B04	SCHED_PORT_Q_CTRL_SET0 [19][7].STRICT_EN[8:8]	1
0xBB002B04	SCHED_PORT_Q_CTRL_SET0 [19][7].RESERVED[7:7]	1
0xBB002B04	SCHED_PORT_Q_CTRL_SET0 [19][7].WEIGHT[6:0]	7
0xBB002B08	SCHED_PORT_Q_CTRL_SET0 [20][0].RESERVED[31:9]	23
0xBB002B08	SCHED_PORT_Q_CTRL_SET0 [20][0].STRICT_EN[8:8]	1
0xBB002B08	SCHED_PORT_Q_CTRL_SET0 [20][0].RESERVED[7:7]	1
0xBB002B08	SCHED_PORT_Q_CTRL_SET0 [20][0].WEIGHT[6:0]	7
0xBB002B0C	SCHED_PORT_Q_CTRL_SET0 [20][1].RESERVED[31:9]	23
0xBB002B0C	SCHED_PORT_Q_CTRL_SET0 [20][1].STRICT_EN[8:8]	1
0xBB002B0C	SCHED_PORT_Q_CTRL_SET0 [20][1].RESERVED[7:7]	1
0xBB002B0C	SCHED_PORT_Q_CTRL_SET0 [20][1].WEIGHT[6:0]	7
0xBB002B10	SCHED_PORT_Q_CTRL_SET0 [20][2].RESERVED[31:9]	23
0xBB002B10	SCHED_PORT_Q_CTRL_SET0 [20][2].STRICT_EN[8:8]	1
0xBB002B10	SCHED_PORT_Q_CTRL_SET0 [20][2].RESERVED[7:7]	1
0xBB002B10	SCHED_PORT_Q_CTRL_SET0 [20][2].WEIGHT[6:0]	7
0xBB002B14	SCHED_PORT_Q_CTRL_SET0 [20][3].RESERVED[31:9]	23
0xBB002B14	SCHED_PORT_Q_CTRL_SET0 [20][3].STRICT_EN[8:8]	1
0xBB002B14	SCHED_PORT_Q_CTRL_SET0 [20][3].RESERVED[7:7]	1
0xBB002B14	SCHED_PORT_Q_CTRL_SET0 [20][3].WEIGHT[6:0]	7
0xBB002B18	SCHED_PORT_Q_CTRL_SET0 [20][4].RESERVED[31:9]	23
0xBB002B18	SCHED_PORT_Q_CTRL_SET0 [20][4].STRICT_EN[8:8]	1
0xBB002B18	SCHED_PORT_Q_CTRL_SET0 [20][4].RESERVED[7:7]	1
0xBB002B18	SCHED_PORT_Q_CTRL_SET0 [20][4].WEIGHT[6:0]	7
0xBB002B1C	SCHED_PORT_Q_CTRL_SET0 [20][5].RESERVED[31:9]	23
0xBB002B1C	SCHED_PORT_Q_CTRL_SET0 [20][5].STRICT_EN[8:8]	1
0xBB002B1C	SCHED_PORT_Q_CTRL_SET0 [20][5].RESERVED[7:7]	1
0xBB002B1C	SCHED_PORT_Q_CTRL_SET0 [20][5].WEIGHT[6:0]	7
0xBB002B20	SCHED_PORT_Q_CTRL_SET0 [20][6].RESERVED[31:9]	23
0xBB002B20	SCHED_PORT_Q_CTRL_SET0 [20][6].STRICT_EN[8:8]	1
0xBB002B20	SCHED_PORT_Q_CTRL_SET0 [20][6].RESERVED[7:7]	1
0xBB002B20	SCHED_PORT_Q_CTRL_SET0 [20][6].WEIGHT[6:0]	7
0xBB002B24	SCHED_PORT_Q_CTRL_SET0 [20][7].RESERVED[31:9]	23
0xBB002B24	SCHED_PORT_Q_CTRL_SET0 [20][7].STRICT_EN[8:8]	1
0xBB002B24	SCHED_PORT_Q_CTRL_SET0 [20][7].RESERVED[7:7]	1
0xBB002B24	SCHED_PORT_Q_CTRL_SET0 [20][7].WEIGHT[6:0]	7
0xBB002B28	SCHED_PORT_Q_CTRL_SET0 [21][0].RESERVED[31:9]	23
0xBB002B28	SCHED_PORT_Q_CTRL_SET0 [21][0].STRICT_EN[8:8]	1
0xBB002B28	SCHED_PORT_Q_CTRL_SET0 [21][0].RESERVED[7:7]	1
0xBB002B28	SCHED_PORT_Q_CTRL_SET0 [21][0].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002B2C	SCHED_PORT_Q_CTRL_SET0 [21][1].RESERVED[31:9]	23
0xBB002B2C	SCHED_PORT_Q_CTRL_SET0 [21][1].STRICT_EN[8:8]	1
0xBB002B2C	SCHED_PORT_Q_CTRL_SET0 [21][1].RESERVED[7:7]	1
0xBB002B2C	SCHED_PORT_Q_CTRL_SET0 [21][1].WEIGHT[6:0]	7
0xBB002B30	SCHED_PORT_Q_CTRL_SET0 [21][2].RESERVED[31:9]	23
0xBB002B30	SCHED_PORT_Q_CTRL_SET0 [21][2].STRICT_EN[8:8]	1
0xBB002B30	SCHED_PORT_Q_CTRL_SET0 [21][2].RESERVED[7:7]	1
0xBB002B30	SCHED_PORT_Q_CTRL_SET0 [21][2].WEIGHT[6:0]	7
0xBB002B34	SCHED_PORT_Q_CTRL_SET0 [21][3].RESERVED[31:9]	23
0xBB002B34	SCHED_PORT_Q_CTRL_SET0 [21][3].STRICT_EN[8:8]	1
0xBB002B34	SCHED_PORT_Q_CTRL_SET0 [21][3].RESERVED[7:7]	1
0xBB002B34	SCHED_PORT_Q_CTRL_SET0 [21][3].WEIGHT[6:0]	7
0xBB002B38	SCHED_PORT_Q_CTRL_SET0 [21][4].RESERVED[31:9]	23
0xBB002B38	SCHED_PORT_Q_CTRL_SET0 [21][4].STRICT_EN[8:8]	1
0xBB002B38	SCHED_PORT_Q_CTRL_SET0 [21][4].RESERVED[7:7]	1
0xBB002B38	SCHED_PORT_Q_CTRL_SET0 [21][4].WEIGHT[6:0]	7
0xBB002B3C	SCHED_PORT_Q_CTRL_SET0 [21][5].RESERVED[31:9]	23
0xBB002B3C	SCHED_PORT_Q_CTRL_SET0 [21][5].STRICT_EN[8:8]	1
0xBB002B3C	SCHED_PORT_Q_CTRL_SET0 [21][5].RESERVED[7:7]	1
0xBB002B3C	SCHED_PORT_Q_CTRL_SET0 [21][5].WEIGHT[6:0]	7
0xBB002B40	SCHED_PORT_Q_CTRL_SET0 [21][6].RESERVED[31:9]	23
0xBB002B40	SCHED_PORT_Q_CTRL_SET0 [21][6].STRICT_EN[8:8]	1
0xBB002B40	SCHED_PORT_Q_CTRL_SET0 [21][6].RESERVED[7:7]	1
0xBB002B40	SCHED_PORT_Q_CTRL_SET0 [21][6].WEIGHT[6:0]	7
0xBB002B44	SCHED_PORT_Q_CTRL_SET0 [21][7].RESERVED[31:9]	23
0xBB002B44	SCHED_PORT_Q_CTRL_SET0 [21][7].STRICT_EN[8:8]	1
0xBB002B44	SCHED_PORT_Q_CTRL_SET0 [21][7].RESERVED[7:7]	1
0xBB002B44	SCHED_PORT_Q_CTRL_SET0 [21][7].WEIGHT[6:0]	7
0xBB002B48	SCHED_PORT_Q_CTRL_SET0 [22][0].RESERVED[31:9]	23
0xBB002B48	SCHED_PORT_Q_CTRL_SET0 [22][0].STRICT_EN[8:8]	1
0xBB002B48	SCHED_PORT_Q_CTRL_SET0 [22][0].RESERVED[7:7]	1
0xBB002B48	SCHED_PORT_Q_CTRL_SET0 [22][0].WEIGHT[6:0]	7
0xBB002B4C	SCHED_PORT_Q_CTRL_SET0 [22][1].RESERVED[31:9]	23
0xBB002B4C	SCHED_PORT_Q_CTRL_SET0 [22][1].STRICT_EN[8:8]	1
0xBB002B4C	SCHED_PORT_Q_CTRL_SET0 [22][1].RESERVED[7:7]	1
0xBB002B4C	SCHED_PORT_Q_CTRL_SET0 [22][1].WEIGHT[6:0]	7
0xBB002B50	SCHED_PORT_Q_CTRL_SET0 [22][2].RESERVED[31:9]	23
0xBB002B50	SCHED_PORT_Q_CTRL_SET0 [22][2].STRICT_EN[8:8]	1
0xBB002B50	SCHED_PORT_Q_CTRL_SET0 [22][2].RESERVED[7:7]	1
0xBB002B50	SCHED_PORT_Q_CTRL_SET0 [22][2].WEIGHT[6:0]	7
0xBB002B54	SCHED_PORT_Q_CTRL_SET0 [22][3].RESERVED[31:9]	23
0xBB002B54	SCHED_PORT_Q_CTRL_SET0 [22][3].STRICT_EN[8:8]	1
0xBB002B54	SCHED_PORT_Q_CTRL_SET0 [22][3].RESERVED[7:7]	1
0xBB002B54	SCHED_PORT_Q_CTRL_SET0 [22][3].WEIGHT[6:0]	7
0xBB002B58	SCHED_PORT_Q_CTRL_SET0 [22][4].RESERVED[31:9]	23
0xBB002B58	SCHED_PORT_Q_CTRL_SET0 [22][4].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002B58	SCHED_PORT_Q_CTRL_SET0 [22][4].RESERVED[7:7]	1
0xBB002B58	SCHED_PORT_Q_CTRL_SET0 [22][4].WEIGHT[6:0]	7
0xBB002B5C	SCHED_PORT_Q_CTRL_SET0 [22][5].RESERVED[31:9]	23
0xBB002B5C	SCHED_PORT_Q_CTRL_SET0 [22][5].STRICT_EN[8:8]	1
0xBB002B5C	SCHED_PORT_Q_CTRL_SET0 [22][5].RESERVED[7:7]	1
0xBB002B5C	SCHED_PORT_Q_CTRL_SET0 [22][5].WEIGHT[6:0]	7
0xBB002B60	SCHED_PORT_Q_CTRL_SET0 [22][6].RESERVED[31:9]	23
0xBB002B60	SCHED_PORT_Q_CTRL_SET0 [22][6].STRICT_EN[8:8]	1
0xBB002B60	SCHED_PORT_Q_CTRL_SET0 [22][6].RESERVED[7:7]	1
0xBB002B60	SCHED_PORT_Q_CTRL_SET0 [22][6].WEIGHT[6:0]	7
0xBB002B64	SCHED_PORT_Q_CTRL_SET0 [22][7].RESERVED[31:9]	23
0xBB002B64	SCHED_PORT_Q_CTRL_SET0 [22][7].STRICT_EN[8:8]	1
0xBB002B64	SCHED_PORT_Q_CTRL_SET0 [22][7].RESERVED[7:7]	1
0xBB002B64	SCHED_PORT_Q_CTRL_SET0 [22][7].WEIGHT[6:0]	7
0xBB002B68	SCHED_PORT_Q_CTRL_SET0 [23][0].RESERVED[31:9]	23
0xBB002B68	SCHED_PORT_Q_CTRL_SET0 [23][0].STRICT_EN[8:8]	1
0xBB002B68	SCHED_PORT_Q_CTRL_SET0 [23][0].RESERVED[7:7]	1
0xBB002B68	SCHED_PORT_Q_CTRL_SET0 [23][0].WEIGHT[6:0]	7
0xBB002B6C	SCHED_PORT_Q_CTRL_SET0 [23][1].RESERVED[31:9]	23
0xBB002B6C	SCHED_PORT_Q_CTRL_SET0 [23][1].STRICT_EN[8:8]	1
0xBB002B6C	SCHED_PORT_Q_CTRL_SET0 [23][1].RESERVED[7:7]	1
0xBB002B6C	SCHED_PORT_Q_CTRL_SET0 [23][1].WEIGHT[6:0]	7
0xBB002B70	SCHED_PORT_Q_CTRL_SET0 [23][2].RESERVED[31:9]	23
0xBB002B70	SCHED_PORT_Q_CTRL_SET0 [23][2].STRICT_EN[8:8]	1
0xBB002B70	SCHED_PORT_Q_CTRL_SET0 [23][2].RESERVED[7:7]	1
0xBB002B70	SCHED_PORT_Q_CTRL_SET0 [23][2].WEIGHT[6:0]	7
0xBB002B74	SCHED_PORT_Q_CTRL_SET0 [23][3].RESERVED[31:9]	23
0xBB002B74	SCHED_PORT_Q_CTRL_SET0 [23][3].STRICT_EN[8:8]	1
0xBB002B74	SCHED_PORT_Q_CTRL_SET0 [23][3].RESERVED[7:7]	1
0xBB002B74	SCHED_PORT_Q_CTRL_SET0 [23][3].WEIGHT[6:0]	7
0xBB002B78	SCHED_PORT_Q_CTRL_SET0 [23][4].RESERVED[31:9]	23
0xBB002B78	SCHED_PORT_Q_CTRL_SET0 [23][4].STRICT_EN[8:8]	1
0xBB002B78	SCHED_PORT_Q_CTRL_SET0 [23][4].RESERVED[7:7]	1
0xBB002B78	SCHED_PORT_Q_CTRL_SET0 [23][4].WEIGHT[6:0]	7
0xBB002B7C	SCHED_PORT_Q_CTRL_SET0 [23][5].RESERVED[31:9]	23
0xBB002B7C	SCHED_PORT_Q_CTRL_SET0 [23][5].STRICT_EN[8:8]	1
0xBB002B7C	SCHED_PORT_Q_CTRL_SET0 [23][5].RESERVED[7:7]	1
0xBB002B7C	SCHED_PORT_Q_CTRL_SET0 [23][5].WEIGHT[6:0]	7
0xBB002B80	SCHED_PORT_Q_CTRL_SET0 [23][6].RESERVED[31:9]	23
0xBB002B80	SCHED_PORT_Q_CTRL_SET0 [23][6].STRICT_EN[8:8]	1
0xBB002B80	SCHED_PORT_Q_CTRL_SET0 [23][6].RESERVED[7:7]	1
0xBB002B80	SCHED_PORT_Q_CTRL_SET0 [23][6].WEIGHT[6:0]	7
0xBB002B84	SCHED_PORT_Q_CTRL_SET0 [23][7].RESERVED[31:9]	23
0xBB002B84	SCHED_PORT_Q_CTRL_SET0 [23][7].STRICT_EN[8:8]	1
0xBB002B84	SCHED_PORT_Q_CTRL_SET0 [23][7].RESERVED[7:7]	1
0xBB002B84	SCHED_PORT_Q_CTRL_SET0 [23][7].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002B88	SCHED_PORT_Q_CTRL_SET0 [24][0].RESERVED[31:9]	23
0xBB002B88	SCHED_PORT_Q_CTRL_SET0 [24][0].STRICT_EN[8:8]	1
0xBB002B88	SCHED_PORT_Q_CTRL_SET0 [24][0].RESERVED[7:7]	1
0xBB002B88	SCHED_PORT_Q_CTRL_SET0 [24][0].WEIGHT[6:0]	7
0xBB002B8C	SCHED_PORT_Q_CTRL_SET0 [24][1].RESERVED[31:9]	23
0xBB002B8C	SCHED_PORT_Q_CTRL_SET0 [24][1].STRICT_EN[8:8]	1
0xBB002B8C	SCHED_PORT_Q_CTRL_SET0 [24][1].RESERVED[7:7]	1
0xBB002B8C	SCHED_PORT_Q_CTRL_SET0 [24][1].WEIGHT[6:0]	7
0xBB002B90	SCHED_PORT_Q_CTRL_SET0 [24][2].RESERVED[31:9]	23
0xBB002B90	SCHED_PORT_Q_CTRL_SET0 [24][2].STRICT_EN[8:8]	1
0xBB002B90	SCHED_PORT_Q_CTRL_SET0 [24][2].RESERVED[7:7]	1
0xBB002B90	SCHED_PORT_Q_CTRL_SET0 [24][2].WEIGHT[6:0]	7
0xBB002B94	SCHED_PORT_Q_CTRL_SET0 [24][3].RESERVED[31:9]	23
0xBB002B94	SCHED_PORT_Q_CTRL_SET0 [24][3].STRICT_EN[8:8]	1
0xBB002B94	SCHED_PORT_Q_CTRL_SET0 [24][3].RESERVED[7:7]	1
0xBB002B94	SCHED_PORT_Q_CTRL_SET0 [24][3].WEIGHT[6:0]	7
0xBB002B98	SCHED_PORT_Q_CTRL_SET0 [24][4].RESERVED[31:9]	23
0xBB002B98	SCHED_PORT_Q_CTRL_SET0 [24][4].STRICT_EN[8:8]	1
0xBB002B98	SCHED_PORT_Q_CTRL_SET0 [24][4].RESERVED[7:7]	1
0xBB002B98	SCHED_PORT_Q_CTRL_SET0 [24][4].WEIGHT[6:0]	7
0xBB002B9C	SCHED_PORT_Q_CTRL_SET0 [24][5].RESERVED[31:9]	23
0xBB002B9C	SCHED_PORT_Q_CTRL_SET0 [24][5].STRICT_EN[8:8]	1
0xBB002B9C	SCHED_PORT_Q_CTRL_SET0 [24][5].RESERVED[7:7]	1
0xBB002B9C	SCHED_PORT_Q_CTRL_SET0 [24][5].WEIGHT[6:0]	7
0xBB002BA0	SCHED_PORT_Q_CTRL_SET0 [24][6].RESERVED[31:9]	23
0xBB002BA0	SCHED_PORT_Q_CTRL_SET0 [24][6].STRICT_EN[8:8]	1
0xBB002BA0	SCHED_PORT_Q_CTRL_SET0 [24][6].RESERVED[7:7]	1
0xBB002BA0	SCHED_PORT_Q_CTRL_SET0 [24][6].WEIGHT[6:0]	7
0xBB002BA4	SCHED_PORT_Q_CTRL_SET0 [24][7].RESERVED[31:9]	23
0xBB002BA4	SCHED_PORT_Q_CTRL_SET0 [24][7].STRICT_EN[8:8]	1
0xBB002BA4	SCHED_PORT_Q_CTRL_SET0 [24][7].RESERVED[7:7]	1
0xBB002BA4	SCHED_PORT_Q_CTRL_SET0 [24][7].WEIGHT[6:0]	7
0xBB002BA8	SCHED_PORT_Q_CTRL_SET0 [25][0].RESERVED[31:9]	23
0xBB002BA8	SCHED_PORT_Q_CTRL_SET0 [25][0].STRICT_EN[8:8]	1
0xBB002BA8	SCHED_PORT_Q_CTRL_SET0 [25][0].RESERVED[7:7]	1
0xBB002BA8	SCHED_PORT_Q_CTRL_SET0 [25][0].WEIGHT[6:0]	7
0xBB002BAC	SCHED_PORT_Q_CTRL_SET0 [25][1].RESERVED[31:9]	23
0xBB002BAC	SCHED_PORT_Q_CTRL_SET0 [25][1].STRICT_EN[8:8]	1
0xBB002BAC	SCHED_PORT_Q_CTRL_SET0 [25][1].RESERVED[7:7]	1
0xBB002BAC	SCHED_PORT_Q_CTRL_SET0 [25][1].WEIGHT[6:0]	7
0xBB002BB0	SCHED_PORT_Q_CTRL_SET0 [25][2].RESERVED[31:9]	23
0xBB002BB0	SCHED_PORT_Q_CTRL_SET0 [25][2].STRICT_EN[8:8]	1
0xBB002BB0	SCHED_PORT_Q_CTRL_SET0 [25][2].RESERVED[7:7]	1
0xBB002BB0	SCHED_PORT_Q_CTRL_SET0 [25][2].WEIGHT[6:0]	7
0xBB002BB4	SCHED_PORT_Q_CTRL_SET0 [25][3].RESERVED[31:9]	23
0xBB002BB4	SCHED_PORT_Q_CTRL_SET0 [25][3].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002BB4	SCHED_PORT_Q_CTRL_SET0 [25][3].RESERVED[7:7]	1
0xBB002BB4	SCHED_PORT_Q_CTRL_SET0 [25][3].WEIGHT[6:0]	7
0xBB002BB8	SCHED_PORT_Q_CTRL_SET0 [25][4].RESERVED[31:9]	23
0xBB002BB8	SCHED_PORT_Q_CTRL_SET0 [25][4].STRICT_EN[8:8]	1
0xBB002BB8	SCHED_PORT_Q_CTRL_SET0 [25][4].RESERVED[7:7]	1
0xBB002BB8	SCHED_PORT_Q_CTRL_SET0 [25][4].WEIGHT[6:0]	7
0xBB002BBC	SCHED_PORT_Q_CTRL_SET0 [25][5].RESERVED[31:9]	23
0xBB002BBC	SCHED_PORT_Q_CTRL_SET0 [25][5].STRICT_EN[8:8]	1
0xBB002BBC	SCHED_PORT_Q_CTRL_SET0 [25][5].RESERVED[7:7]	1
0xBB002BBC	SCHED_PORT_Q_CTRL_SET0 [25][5].WEIGHT[6:0]	7
0xBB002BC0	SCHED_PORT_Q_CTRL_SET0 [25][6].RESERVED[31:9]	23
0xBB002BC0	SCHED_PORT_Q_CTRL_SET0 [25][6].STRICT_EN[8:8]	1
0xBB002BC0	SCHED_PORT_Q_CTRL_SET0 [25][6].RESERVED[7:7]	1
0xBB002BC0	SCHED_PORT_Q_CTRL_SET0 [25][6].WEIGHT[6:0]	7
0xBB002BC4	SCHED_PORT_Q_CTRL_SET0 [25][7].RESERVED[31:9]	23
0xBB002BC4	SCHED_PORT_Q_CTRL_SET0 [25][7].STRICT_EN[8:8]	1
0xBB002BC4	SCHED_PORT_Q_CTRL_SET0 [25][7].RESERVED[7:7]	1
0xBB002BC4	SCHED_PORT_Q_CTRL_SET0 [25][7].WEIGHT[6:0]	7
0xBB002BC8	SCHED_PORT_Q_CTRL_SET0 [26][0].RESERVED[31:9]	23
0xBB002BC8	SCHED_PORT_Q_CTRL_SET0 [26][0].STRICT_EN[8:8]	1
0xBB002BC8	SCHED_PORT_Q_CTRL_SET0 [26][0].RESERVED[7:7]	1
0xBB002BC8	SCHED_PORT_Q_CTRL_SET0 [26][0].WEIGHT[6:0]	7
0xBB002BCC	SCHED_PORT_Q_CTRL_SET0 [26][1].RESERVED[31:9]	23
0xBB002BCC	SCHED_PORT_Q_CTRL_SET0 [26][1].STRICT_EN[8:8]	1
0xBB002BCC	SCHED_PORT_Q_CTRL_SET0 [26][1].RESERVED[7:7]	1
0xBB002BCC	SCHED_PORT_Q_CTRL_SET0 [26][1].WEIGHT[6:0]	7
0xBB002BD0	SCHED_PORT_Q_CTRL_SET0 [26][2].RESERVED[31:9]	23
0xBB002BD0	SCHED_PORT_Q_CTRL_SET0 [26][2].STRICT_EN[8:8]	1
0xBB002BD0	SCHED_PORT_Q_CTRL_SET0 [26][2].RESERVED[7:7]	1
0xBB002BD0	SCHED_PORT_Q_CTRL_SET0 [26][2].WEIGHT[6:0]	7
0xBB002BD4	SCHED_PORT_Q_CTRL_SET0 [26][3].RESERVED[31:9]	23
0xBB002BD4	SCHED_PORT_Q_CTRL_SET0 [26][3].STRICT_EN[8:8]	1
0xBB002BD4	SCHED_PORT_Q_CTRL_SET0 [26][3].RESERVED[7:7]	1
0xBB002BD4	SCHED_PORT_Q_CTRL_SET0 [26][3].WEIGHT[6:0]	7
0xBB002BD8	SCHED_PORT_Q_CTRL_SET0 [26][4].RESERVED[31:9]	23
0xBB002BD8	SCHED_PORT_Q_CTRL_SET0 [26][4].STRICT_EN[8:8]	1
0xBB002BD8	SCHED_PORT_Q_CTRL_SET0 [26][4].RESERVED[7:7]	1
0xBB002BD8	SCHED_PORT_Q_CTRL_SET0 [26][4].WEIGHT[6:0]	7
0xBB002BDC	SCHED_PORT_Q_CTRL_SET0 [26][5].RESERVED[31:9]	23
0xBB002BDC	SCHED_PORT_Q_CTRL_SET0 [26][5].STRICT_EN[8:8]	1
0xBB002BDC	SCHED_PORT_Q_CTRL_SET0 [26][5].RESERVED[7:7]	1
0xBB002BDC	SCHED_PORT_Q_CTRL_SET0 [26][5].WEIGHT[6:0]	7
0xBB002BE0	SCHED_PORT_Q_CTRL_SET0 [26][6].RESERVED[31:9]	23
0xBB002BE0	SCHED_PORT_Q_CTRL_SET0 [26][6].STRICT_EN[8:8]	1
0xBB002BE0	SCHED_PORT_Q_CTRL_SET0 [26][6].RESERVED[7:7]	1
0xBB002BE0	SCHED_PORT_Q_CTRL_SET0 [26][6].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002BE4	SCHED_PORT_Q_CTRL_SET0 [26][7].RESERVED[31:9]	23
0xBB002BE4	SCHED_PORT_Q_CTRL_SET0 [26][7].STRICT_EN[8:8]	1
0xBB002BE4	SCHED_PORT_Q_CTRL_SET0 [26][7].RESERVED[7:7]	1
0xBB002BE4	SCHED_PORT_Q_CTRL_SET0 [26][7].WEIGHT[6:0]	7
0xBB002BE8	SCHED_PORT_Q_CTRL_SET0 [27][0].RESERVED[31:9]	23
0xBB002BE8	SCHED_PORT_Q_CTRL_SET0 [27][0].STRICT_EN[8:8]	1
0xBB002BE8	SCHED_PORT_Q_CTRL_SET0 [27][0].RESERVED[7:7]	1
0xBB002BE8	SCHED_PORT_Q_CTRL_SET0 [27][0].WEIGHT[6:0]	7
0xBB002BEC	SCHED_PORT_Q_CTRL_SET0 [27][1].RESERVED[31:9]	23
0xBB002BEC	SCHED_PORT_Q_CTRL_SET0 [27][1].STRICT_EN[8:8]	1
0xBB002BEC	SCHED_PORT_Q_CTRL_SET0 [27][1].RESERVED[7:7]	1
0xBB002BEC	SCHED_PORT_Q_CTRL_SET0 [27][1].WEIGHT[6:0]	7
0xBB002BF0	SCHED_PORT_Q_CTRL_SET0 [27][2].RESERVED[31:9]	23
0xBB002BF0	SCHED_PORT_Q_CTRL_SET0 [27][2].STRICT_EN[8:8]	1
0xBB002BF0	SCHED_PORT_Q_CTRL_SET0 [27][2].RESERVED[7:7]	1
0xBB002BF0	SCHED_PORT_Q_CTRL_SET0 [27][2].WEIGHT[6:0]	7
0xBB002BF4	SCHED_PORT_Q_CTRL_SET0 [27][3].RESERVED[31:9]	23
0xBB002BF4	SCHED_PORT_Q_CTRL_SET0 [27][3].STRICT_EN[8:8]	1
0xBB002BF4	SCHED_PORT_Q_CTRL_SET0 [27][3].RESERVED[7:7]	1
0xBB002BF4	SCHED_PORT_Q_CTRL_SET0 [27][3].WEIGHT[6:0]	7
0xBB002BF8	SCHED_PORT_Q_CTRL_SET0 [27][4].RESERVED[31:9]	23
0xBB002BF8	SCHED_PORT_Q_CTRL_SET0 [27][4].STRICT_EN[8:8]	1
0xBB002BF8	SCHED_PORT_Q_CTRL_SET0 [27][4].RESERVED[7:7]	1
0xBB002BF8	SCHED_PORT_Q_CTRL_SET0 [27][4].WEIGHT[6:0]	7
0xBB002BFC	SCHED_PORT_Q_CTRL_SET0 [27][5].RESERVED[31:9]	23
0xBB002BFC	SCHED_PORT_Q_CTRL_SET0 [27][5].STRICT_EN[8:8]	1
0xBB002BFC	SCHED_PORT_Q_CTRL_SET0 [27][5].RESERVED[7:7]	1
0xBB002BFC	SCHED_PORT_Q_CTRL_SET0 [27][5].WEIGHT[6:0]	7
0xBB002C00	SCHED_PORT_Q_CTRL_SET0 [27][6].RESERVED[31:9]	23
0xBB002C00	SCHED_PORT_Q_CTRL_SET0 [27][6].STRICT_EN[8:8]	1
0xBB002C00	SCHED_PORT_Q_CTRL_SET0 [27][6].RESERVED[7:7]	1
0xBB002C00	SCHED_PORT_Q_CTRL_SET0 [27][6].WEIGHT[6:0]	7
0xBB002C04	SCHED_PORT_Q_CTRL_SET0 [27][7].RESERVED[31:9]	23
0xBB002C04	SCHED_PORT_Q_CTRL_SET0 [27][7].STRICT_EN[8:8]	1
0xBB002C04	SCHED_PORT_Q_CTRL_SET0 [27][7].RESERVED[7:7]	1
0xBB002C04	SCHED_PORT_Q_CTRL_SET0 [27][7].WEIGHT[6:0]	7
0xBB002C08	SCHED_PORT_Q_CTRL_SET0 [28][0].RESERVED[31:9]	23
0xBB002C08	SCHED_PORT_Q_CTRL_SET0 [28][0].STRICT_EN[8:8]	1
0xBB002C08	SCHED_PORT_Q_CTRL_SET0 [28][0].RESERVED[7:7]	1
0xBB002C08	SCHED_PORT_Q_CTRL_SET0 [28][0].WEIGHT[6:0]	7
0xBB002C0C	SCHED_PORT_Q_CTRL_SET0 [28][1].RESERVED[31:9]	23
0xBB002C0C	SCHED_PORT_Q_CTRL_SET0 [28][1].STRICT_EN[8:8]	1
0xBB002C0C	SCHED_PORT_Q_CTRL_SET0 [28][1].RESERVED[7:7]	1
0xBB002C0C	SCHED_PORT_Q_CTRL_SET0 [28][1].WEIGHT[6:0]	7
0xBB002C10	SCHED_PORT_Q_CTRL_SET0 [28][2].RESERVED[31:9]	23
0xBB002C10	SCHED_PORT_Q_CTRL_SET0 [28][2].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002C10	SCHED_PORT_Q_CTRL_SET0 [28][2].RESERVED[7:7]	1
0xBB002C10	SCHED_PORT_Q_CTRL_SET0 [28][2].WEIGHT[6:0]	7
0xBB002C14	SCHED_PORT_Q_CTRL_SET0 [28][3].RESERVED[31:9]	23
0xBB002C14	SCHED_PORT_Q_CTRL_SET0 [28][3].STRICT_EN[8:8]	1
0xBB002C14	SCHED_PORT_Q_CTRL_SET0 [28][3].RESERVED[7:7]	1
0xBB002C14	SCHED_PORT_Q_CTRL_SET0 [28][3].WEIGHT[6:0]	7
0xBB002C18	SCHED_PORT_Q_CTRL_SET0 [28][4].RESERVED[31:9]	23
0xBB002C18	SCHED_PORT_Q_CTRL_SET0 [28][4].STRICT_EN[8:8]	1
0xBB002C18	SCHED_PORT_Q_CTRL_SET0 [28][4].RESERVED[7:7]	1
0xBB002C18	SCHED_PORT_Q_CTRL_SET0 [28][4].WEIGHT[6:0]	7
0xBB002C1C	SCHED_PORT_Q_CTRL_SET0 [28][5].RESERVED[31:9]	23
0xBB002C1C	SCHED_PORT_Q_CTRL_SET0 [28][5].STRICT_EN[8:8]	1
0xBB002C1C	SCHED_PORT_Q_CTRL_SET0 [28][5].RESERVED[7:7]	1
0xBB002C1C	SCHED_PORT_Q_CTRL_SET0 [28][5].WEIGHT[6:0]	7
0xBB002C20	SCHED_PORT_Q_CTRL_SET0 [28][6].RESERVED[31:9]	23
0xBB002C20	SCHED_PORT_Q_CTRL_SET0 [28][6].STRICT_EN[8:8]	1
0xBB002C20	SCHED_PORT_Q_CTRL_SET0 [28][6].RESERVED[7:7]	1
0xBB002C20	SCHED_PORT_Q_CTRL_SET0 [28][6].WEIGHT[6:0]	7
0xBB002C24	SCHED_PORT_Q_CTRL_SET0 [28][7].RESERVED[31:9]	23
0xBB002C24	SCHED_PORT_Q_CTRL_SET0 [28][7].STRICT_EN[8:8]	1
0xBB002C24	SCHED_PORT_Q_CTRL_SET0 [28][7].RESERVED[7:7]	1
0xBB002C24	SCHED_PORT_Q_CTRL_SET0 [28][7].WEIGHT[6:0]	7
0xBB002C28	SCHED_PORT_Q_CTRL_SET0 [29][0].RESERVED[31:9]	23
0xBB002C28	SCHED_PORT_Q_CTRL_SET0 [29][0].STRICT_EN[8:8]	1
0xBB002C28	SCHED_PORT_Q_CTRL_SET0 [29][0].RESERVED[7:7]	1
0xBB002C28	SCHED_PORT_Q_CTRL_SET0 [29][0].WEIGHT[6:0]	7
0xBB002C2C	SCHED_PORT_Q_CTRL_SET0 [29][1].RESERVED[31:9]	23
0xBB002C2C	SCHED_PORT_Q_CTRL_SET0 [29][1].STRICT_EN[8:8]	1
0xBB002C2C	SCHED_PORT_Q_CTRL_SET0 [29][1].RESERVED[7:7]	1
0xBB002C2C	SCHED_PORT_Q_CTRL_SET0 [29][1].WEIGHT[6:0]	7
0xBB002C30	SCHED_PORT_Q_CTRL_SET0 [29][2].RESERVED[31:9]	23
0xBB002C30	SCHED_PORT_Q_CTRL_SET0 [29][2].STRICT_EN[8:8]	1
0xBB002C30	SCHED_PORT_Q_CTRL_SET0 [29][2].RESERVED[7:7]	1
0xBB002C30	SCHED_PORT_Q_CTRL_SET0 [29][2].WEIGHT[6:0]	7
0xBB002C34	SCHED_PORT_Q_CTRL_SET0 [29][3].RESERVED[31:9]	23
0xBB002C34	SCHED_PORT_Q_CTRL_SET0 [29][3].STRICT_EN[8:8]	1
0xBB002C34	SCHED_PORT_Q_CTRL_SET0 [29][3].RESERVED[7:7]	1
0xBB002C34	SCHED_PORT_Q_CTRL_SET0 [29][3].WEIGHT[6:0]	7
0xBB002C38	SCHED_PORT_Q_CTRL_SET0 [29][4].RESERVED[31:9]	23
0xBB002C38	SCHED_PORT_Q_CTRL_SET0 [29][4].STRICT_EN[8:8]	1
0xBB002C38	SCHED_PORT_Q_CTRL_SET0 [29][4].RESERVED[7:7]	1
0xBB002C38	SCHED_PORT_Q_CTRL_SET0 [29][4].WEIGHT[6:0]	7
0xBB002C3C	SCHED_PORT_Q_CTRL_SET0 [29][5].RESERVED[31:9]	23
0xBB002C3C	SCHED_PORT_Q_CTRL_SET0 [29][5].STRICT_EN[8:8]	1
0xBB002C3C	SCHED_PORT_Q_CTRL_SET0 [29][5].RESERVED[7:7]	1
0xBB002C3C	SCHED_PORT_Q_CTRL_SET0 [29][5].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002C40	SCHED_PORT_Q_CTRL_SET0 [29][6].RESERVED[31:9]	23
0xBB002C40	SCHED_PORT_Q_CTRL_SET0 [29][6].STRICT_EN[8:8]	1
0xBB002C40	SCHED_PORT_Q_CTRL_SET0 [29][6].RESERVED[7:7]	1
0xBB002C40	SCHED_PORT_Q_CTRL_SET0 [29][6].WEIGHT[6:0]	7
0xBB002C44	SCHED_PORT_Q_CTRL_SET0 [29][7].RESERVED[31:9]	23
0xBB002C44	SCHED_PORT_Q_CTRL_SET0 [29][7].STRICT_EN[8:8]	1
0xBB002C44	SCHED_PORT_Q_CTRL_SET0 [29][7].RESERVED[7:7]	1
0xBB002C44	SCHED_PORT_Q_CTRL_SET0 [29][7].WEIGHT[6:0]	7
0xBB002C48	SCHED_PORT_Q_CTRL_SET0 [30][0].RESERVED[31:9]	23
0xBB002C48	SCHED_PORT_Q_CTRL_SET0 [30][0].STRICT_EN[8:8]	1
0xBB002C48	SCHED_PORT_Q_CTRL_SET0 [30][0].RESERVED[7:7]	1
0xBB002C48	SCHED_PORT_Q_CTRL_SET0 [30][0].WEIGHT[6:0]	7
0xBB002C4C	SCHED_PORT_Q_CTRL_SET0 [30][1].RESERVED[31:9]	23
0xBB002C4C	SCHED_PORT_Q_CTRL_SET0 [30][1].STRICT_EN[8:8]	1
0xBB002C4C	SCHED_PORT_Q_CTRL_SET0 [30][1].RESERVED[7:7]	1
0xBB002C4C	SCHED_PORT_Q_CTRL_SET0 [30][1].WEIGHT[6:0]	7
0xBB002C50	SCHED_PORT_Q_CTRL_SET0 [30][2].RESERVED[31:9]	23
0xBB002C50	SCHED_PORT_Q_CTRL_SET0 [30][2].STRICT_EN[8:8]	1
0xBB002C50	SCHED_PORT_Q_CTRL_SET0 [30][2].RESERVED[7:7]	1
0xBB002C50	SCHED_PORT_Q_CTRL_SET0 [30][2].WEIGHT[6:0]	7
0xBB002C54	SCHED_PORT_Q_CTRL_SET0 [30][3].RESERVED[31:9]	23
0xBB002C54	SCHED_PORT_Q_CTRL_SET0 [30][3].STRICT_EN[8:8]	1
0xBB002C54	SCHED_PORT_Q_CTRL_SET0 [30][3].RESERVED[7:7]	1
0xBB002C54	SCHED_PORT_Q_CTRL_SET0 [30][3].WEIGHT[6:0]	7
0xBB002C58	SCHED_PORT_Q_CTRL_SET0 [30][4].RESERVED[31:9]	23
0xBB002C58	SCHED_PORT_Q_CTRL_SET0 [30][4].STRICT_EN[8:8]	1
0xBB002C58	SCHED_PORT_Q_CTRL_SET0 [30][4].RESERVED[7:7]	1
0xBB002C58	SCHED_PORT_Q_CTRL_SET0 [30][4].WEIGHT[6:0]	7
0xBB002C5C	SCHED_PORT_Q_CTRL_SET0 [30][5].RESERVED[31:9]	23
0xBB002C5C	SCHED_PORT_Q_CTRL_SET0 [30][5].STRICT_EN[8:8]	1
0xBB002C5C	SCHED_PORT_Q_CTRL_SET0 [30][5].RESERVED[7:7]	1
0xBB002C5C	SCHED_PORT_Q_CTRL_SET0 [30][5].WEIGHT[6:0]	7
0xBB002C60	SCHED_PORT_Q_CTRL_SET0 [30][6].RESERVED[31:9]	23
0xBB002C60	SCHED_PORT_Q_CTRL_SET0 [30][6].STRICT_EN[8:8]	1
0xBB002C60	SCHED_PORT_Q_CTRL_SET0 [30][6].RESERVED[7:7]	1
0xBB002C60	SCHED_PORT_Q_CTRL_SET0 [30][6].WEIGHT[6:0]	7
0xBB002C64	SCHED_PORT_Q_CTRL_SET0 [30][7].RESERVED[31:9]	23
0xBB002C64	SCHED_PORT_Q_CTRL_SET0 [30][7].STRICT_EN[8:8]	1
0xBB002C64	SCHED_PORT_Q_CTRL_SET0 [30][7].RESERVED[7:7]	1
0xBB002C64	SCHED_PORT_Q_CTRL_SET0 [30][7].WEIGHT[6:0]	7
0xBB002C68	SCHED_PORT_Q_CTRL_SET0 [31][0].RESERVED[31:9]	23
0xBB002C68	SCHED_PORT_Q_CTRL_SET0 [31][0].STRICT_EN[8:8]	1
0xBB002C68	SCHED_PORT_Q_CTRL_SET0 [31][0].RESERVED[7:7]	1
0xBB002C68	SCHED_PORT_Q_CTRL_SET0 [31][0].WEIGHT[6:0]	7
0xBB002C6C	SCHED_PORT_Q_CTRL_SET0 [31][1].RESERVED[31:9]	23
0xBB002C6C	SCHED_PORT_Q_CTRL_SET0 [31][1].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002C6C	SCHED_PORT_Q_CTRL_SET0 [31][1].RESERVED[7:7]	1
0xBB002C6C	SCHED_PORT_Q_CTRL_SET0 [31][1].WEIGHT[6:0]	7
0xBB002C70	SCHED_PORT_Q_CTRL_SET0 [31][2].RESERVED[31:9]	23
0xBB002C70	SCHED_PORT_Q_CTRL_SET0 [31][2].STRICT_EN[8:8]	1
0xBB002C70	SCHED_PORT_Q_CTRL_SET0 [31][2].RESERVED[7:7]	1
0xBB002C70	SCHED_PORT_Q_CTRL_SET0 [31][2].WEIGHT[6:0]	7
0xBB002C74	SCHED_PORT_Q_CTRL_SET0 [31][3].RESERVED[31:9]	23
0xBB002C74	SCHED_PORT_Q_CTRL_SET0 [31][3].STRICT_EN[8:8]	1
0xBB002C74	SCHED_PORT_Q_CTRL_SET0 [31][3].RESERVED[7:7]	1
0xBB002C74	SCHED_PORT_Q_CTRL_SET0 [31][3].WEIGHT[6:0]	7
0xBB002C78	SCHED_PORT_Q_CTRL_SET0 [31][4].RESERVED[31:9]	23
0xBB002C78	SCHED_PORT_Q_CTRL_SET0 [31][4].STRICT_EN[8:8]	1
0xBB002C78	SCHED_PORT_Q_CTRL_SET0 [31][4].RESERVED[7:7]	1
0xBB002C78	SCHED_PORT_Q_CTRL_SET0 [31][4].WEIGHT[6:0]	7
0xBB002C7C	SCHED_PORT_Q_CTRL_SET0 [31][5].RESERVED[31:9]	23
0xBB002C7C	SCHED_PORT_Q_CTRL_SET0 [31][5].STRICT_EN[8:8]	1
0xBB002C7C	SCHED_PORT_Q_CTRL_SET0 [31][5].RESERVED[7:7]	1
0xBB002C7C	SCHED_PORT_Q_CTRL_SET0 [31][5].WEIGHT[6:0]	7
0xBB002C80	SCHED_PORT_Q_CTRL_SET0 [31][6].RESERVED[31:9]	23
0xBB002C80	SCHED_PORT_Q_CTRL_SET0 [31][6].STRICT_EN[8:8]	1
0xBB002C80	SCHED_PORT_Q_CTRL_SET0 [31][6].RESERVED[7:7]	1
0xBB002C80	SCHED_PORT_Q_CTRL_SET0 [31][6].WEIGHT[6:0]	7
0xBB002C84	SCHED_PORT_Q_CTRL_SET0 [31][7].RESERVED[31:9]	23
0xBB002C84	SCHED_PORT_Q_CTRL_SET0 [31][7].STRICT_EN[8:8]	1
0xBB002C84	SCHED_PORT_Q_CTRL_SET0 [31][7].RESERVED[7:7]	1
0xBB002C84	SCHED_PORT_Q_CTRL_SET0 [31][7].WEIGHT[6:0]	7
0xBB002C88	SCHED_PORT_Q_CTRL_SET0 [32][0].RESERVED[31:9]	23
0xBB002C88	SCHED_PORT_Q_CTRL_SET0 [32][0].STRICT_EN[8:8]	1
0xBB002C88	SCHED_PORT_Q_CTRL_SET0 [32][0].RESERVED[7:7]	1
0xBB002C88	SCHED_PORT_Q_CTRL_SET0 [32][0].WEIGHT[6:0]	7
0xBB002C8C	SCHED_PORT_Q_CTRL_SET0 [32][1].RESERVED[31:9]	23
0xBB002C8C	SCHED_PORT_Q_CTRL_SET0 [32][1].STRICT_EN[8:8]	1
0xBB002C8C	SCHED_PORT_Q_CTRL_SET0 [32][1].RESERVED[7:7]	1
0xBB002C8C	SCHED_PORT_Q_CTRL_SET0 [32][1].WEIGHT[6:0]	7
0xBB002C90	SCHED_PORT_Q_CTRL_SET0 [32][2].RESERVED[31:9]	23
0xBB002C90	SCHED_PORT_Q_CTRL_SET0 [32][2].STRICT_EN[8:8]	1
0xBB002C90	SCHED_PORT_Q_CTRL_SET0 [32][2].RESERVED[7:7]	1
0xBB002C90	SCHED_PORT_Q_CTRL_SET0 [32][2].WEIGHT[6:0]	7
0xBB002C94	SCHED_PORT_Q_CTRL_SET0 [32][3].RESERVED[31:9]	23
0xBB002C94	SCHED_PORT_Q_CTRL_SET0 [32][3].STRICT_EN[8:8]	1
0xBB002C94	SCHED_PORT_Q_CTRL_SET0 [32][3].RESERVED[7:7]	1
0xBB002C94	SCHED_PORT_Q_CTRL_SET0 [32][3].WEIGHT[6:0]	7
0xBB002C98	SCHED_PORT_Q_CTRL_SET0 [32][4].RESERVED[31:9]	23
0xBB002C98	SCHED_PORT_Q_CTRL_SET0 [32][4].STRICT_EN[8:8]	1
0xBB002C98	SCHED_PORT_Q_CTRL_SET0 [32][4].RESERVED[7:7]	1
0xBB002C98	SCHED_PORT_Q_CTRL_SET0 [32][4].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002C9C	SCHED_PORT_Q_CTRL_SET0 [32][5].RESERVED[31:9]	23
0xBB002C9C	SCHED_PORT_Q_CTRL_SET0 [32][5].STRICT_EN[8:8]	1
0xBB002C9C	SCHED_PORT_Q_CTRL_SET0 [32][5].RESERVED[7:7]	1
0xBB002C9C	SCHED_PORT_Q_CTRL_SET0 [32][5].WEIGHT[6:0]	7
0xBB002CA0	SCHED_PORT_Q_CTRL_SET0 [32][6].RESERVED[31:9]	23
0xBB002CA0	SCHED_PORT_Q_CTRL_SET0 [32][6].STRICT_EN[8:8]	1
0xBB002CA0	SCHED_PORT_Q_CTRL_SET0 [32][6].RESERVED[7:7]	1
0xBB002CA0	SCHED_PORT_Q_CTRL_SET0 [32][6].WEIGHT[6:0]	7
0xBB002CA4	SCHED_PORT_Q_CTRL_SET0 [32][7].RESERVED[31:9]	23
0xBB002CA4	SCHED_PORT_Q_CTRL_SET0 [32][7].STRICT_EN[8:8]	1
0xBB002CA4	SCHED_PORT_Q_CTRL_SET0 [32][7].RESERVED[7:7]	1
0xBB002CA4	SCHED_PORT_Q_CTRL_SET0 [32][7].WEIGHT[6:0]	7
0xBB002CA8	SCHED_PORT_Q_CTRL_SET0 [33][0].RESERVED[31:9]	23
0xBB002CA8	SCHED_PORT_Q_CTRL_SET0 [33][0].STRICT_EN[8:8]	1
0xBB002CA8	SCHED_PORT_Q_CTRL_SET0 [33][0].RESERVED[7:7]	1
0xBB002CA8	SCHED_PORT_Q_CTRL_SET0 [33][0].WEIGHT[6:0]	7
0xBB002CAC	SCHED_PORT_Q_CTRL_SET0 [33][1].RESERVED[31:9]	23
0xBB002CAC	SCHED_PORT_Q_CTRL_SET0 [33][1].STRICT_EN[8:8]	1
0xBB002CAC	SCHED_PORT_Q_CTRL_SET0 [33][1].RESERVED[7:7]	1
0xBB002CAC	SCHED_PORT_Q_CTRL_SET0 [33][1].WEIGHT[6:0]	7
0xBB002CB0	SCHED_PORT_Q_CTRL_SET0 [33][2].RESERVED[31:9]	23
0xBB002CB0	SCHED_PORT_Q_CTRL_SET0 [33][2].STRICT_EN[8:8]	1
0xBB002CB0	SCHED_PORT_Q_CTRL_SET0 [33][2].RESERVED[7:7]	1
0xBB002CB0	SCHED_PORT_Q_CTRL_SET0 [33][2].WEIGHT[6:0]	7
0xBB002CB4	SCHED_PORT_Q_CTRL_SET0 [33][3].RESERVED[31:9]	23
0xBB002CB4	SCHED_PORT_Q_CTRL_SET0 [33][3].STRICT_EN[8:8]	1
0xBB002CB4	SCHED_PORT_Q_CTRL_SET0 [33][3].RESERVED[7:7]	1
0xBB002CB4	SCHED_PORT_Q_CTRL_SET0 [33][3].WEIGHT[6:0]	7
0xBB002CB8	SCHED_PORT_Q_CTRL_SET0 [33][4].RESERVED[31:9]	23
0xBB002CB8	SCHED_PORT_Q_CTRL_SET0 [33][4].STRICT_EN[8:8]	1
0xBB002CB8	SCHED_PORT_Q_CTRL_SET0 [33][4].RESERVED[7:7]	1
0xBB002CB8	SCHED_PORT_Q_CTRL_SET0 [33][4].WEIGHT[6:0]	7
0xBB002CBC	SCHED_PORT_Q_CTRL_SET0 [33][5].RESERVED[31:9]	23
0xBB002CBC	SCHED_PORT_Q_CTRL_SET0 [33][5].STRICT_EN[8:8]	1
0xBB002CBC	SCHED_PORT_Q_CTRL_SET0 [33][5].RESERVED[7:7]	1
0xBB002CBC	SCHED_PORT_Q_CTRL_SET0 [33][5].WEIGHT[6:0]	7
0xBB002CC0	SCHED_PORT_Q_CTRL_SET0 [33][6].RESERVED[31:9]	23
0xBB002CC0	SCHED_PORT_Q_CTRL_SET0 [33][6].STRICT_EN[8:8]	1
0xBB002CC0	SCHED_PORT_Q_CTRL_SET0 [33][6].RESERVED[7:7]	1
0xBB002CC0	SCHED_PORT_Q_CTRL_SET0 [33][6].WEIGHT[6:0]	7
0xBB002CC4	SCHED_PORT_Q_CTRL_SET0 [33][7].RESERVED[31:9]	23
0xBB002CC4	SCHED_PORT_Q_CTRL_SET0 [33][7].STRICT_EN[8:8]	1
0xBB002CC4	SCHED_PORT_Q_CTRL_SET0 [33][7].RESERVED[7:7]	1
0xBB002CC4	SCHED_PORT_Q_CTRL_SET0 [33][7].WEIGHT[6:0]	7
0xBB002CC8	SCHED_PORT_Q_CTRL_SET0 [34][0].RESERVED[31:9]	23
0xBB002CC8	SCHED_PORT_Q_CTRL_SET0 [34][0].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002CC8	SCHED_PORT_Q_CTRL_SET0 [34][0].RESERVED[7:7]	1
0xBB002CC8	SCHED_PORT_Q_CTRL_SET0 [34][0].WEIGHT[6:0]	7
0xBB002CCC	SCHED_PORT_Q_CTRL_SET0 [34][1].RESERVED[31:9]	23
0xBB002CCC	SCHED_PORT_Q_CTRL_SET0 [34][1].STRICT_EN[8:8]	1
0xBB002CCC	SCHED_PORT_Q_CTRL_SET0 [34][1].RESERVED[7:7]	1
0xBB002CCC	SCHED_PORT_Q_CTRL_SET0 [34][1].WEIGHT[6:0]	7
0xBB002CD0	SCHED_PORT_Q_CTRL_SET0 [34][2].RESERVED[31:9]	23
0xBB002CD0	SCHED_PORT_Q_CTRL_SET0 [34][2].STRICT_EN[8:8]	1
0xBB002CD0	SCHED_PORT_Q_CTRL_SET0 [34][2].RESERVED[7:7]	1
0xBB002CD0	SCHED_PORT_Q_CTRL_SET0 [34][2].WEIGHT[6:0]	7
0xBB002CD4	SCHED_PORT_Q_CTRL_SET0 [34][3].RESERVED[31:9]	23
0xBB002CD4	SCHED_PORT_Q_CTRL_SET0 [34][3].STRICT_EN[8:8]	1
0xBB002CD4	SCHED_PORT_Q_CTRL_SET0 [34][3].RESERVED[7:7]	1
0xBB002CD4	SCHED_PORT_Q_CTRL_SET0 [34][3].WEIGHT[6:0]	7
0xBB002CD8	SCHED_PORT_Q_CTRL_SET0 [34][4].RESERVED[31:9]	23
0xBB002CD8	SCHED_PORT_Q_CTRL_SET0 [34][4].STRICT_EN[8:8]	1
0xBB002CD8	SCHED_PORT_Q_CTRL_SET0 [34][4].RESERVED[7:7]	1
0xBB002CD8	SCHED_PORT_Q_CTRL_SET0 [34][4].WEIGHT[6:0]	7
0xBB002CDC	SCHED_PORT_Q_CTRL_SET0 [34][5].RESERVED[31:9]	23
0xBB002CDC	SCHED_PORT_Q_CTRL_SET0 [34][5].STRICT_EN[8:8]	1
0xBB002CDC	SCHED_PORT_Q_CTRL_SET0 [34][5].RESERVED[7:7]	1
0xBB002CDC	SCHED_PORT_Q_CTRL_SET0 [34][5].WEIGHT[6:0]	7
0xBB002CE0	SCHED_PORT_Q_CTRL_SET0 [34][6].RESERVED[31:9]	23
0xBB002CE0	SCHED_PORT_Q_CTRL_SET0 [34][6].STRICT_EN[8:8]	1
0xBB002CE0	SCHED_PORT_Q_CTRL_SET0 [34][6].RESERVED[7:7]	1
0xBB002CE0	SCHED_PORT_Q_CTRL_SET0 [34][6].WEIGHT[6:0]	7
0xBB002CE4	SCHED_PORT_Q_CTRL_SET0 [34][7].RESERVED[31:9]	23
0xBB002CE4	SCHED_PORT_Q_CTRL_SET0 [34][7].STRICT_EN[8:8]	1
0xBB002CE4	SCHED_PORT_Q_CTRL_SET0 [34][7].RESERVED[7:7]	1
0xBB002CE4	SCHED_PORT_Q_CTRL_SET0 [34][7].WEIGHT[6:0]	7
0xBB002CE8	SCHED_PORT_Q_CTRL_SET0 [35][0].RESERVED[31:9]	23
0xBB002CE8	SCHED_PORT_Q_CTRL_SET0 [35][0].STRICT_EN[8:8]	1
0xBB002CE8	SCHED_PORT_Q_CTRL_SET0 [35][0].RESERVED[7:7]	1
0xBB002CE8	SCHED_PORT_Q_CTRL_SET0 [35][0].WEIGHT[6:0]	7
0xBB002CEC	SCHED_PORT_Q_CTRL_SET0 [35][1].RESERVED[31:9]	23
0xBB002CEC	SCHED_PORT_Q_CTRL_SET0 [35][1].STRICT_EN[8:8]	1
0xBB002CEC	SCHED_PORT_Q_CTRL_SET0 [35][1].RESERVED[7:7]	1
0xBB002CEC	SCHED_PORT_Q_CTRL_SET0 [35][1].WEIGHT[6:0]	7
0xBB002CF0	SCHED_PORT_Q_CTRL_SET0 [35][2].RESERVED[31:9]	23
0xBB002CF0	SCHED_PORT_Q_CTRL_SET0 [35][2].STRICT_EN[8:8]	1
0xBB002CF0	SCHED_PORT_Q_CTRL_SET0 [35][2].RESERVED[7:7]	1
0xBB002CF0	SCHED_PORT_Q_CTRL_SET0 [35][2].WEIGHT[6:0]	7
0xBB002CF4	SCHED_PORT_Q_CTRL_SET0 [35][3].RESERVED[31:9]	23
0xBB002CF4	SCHED_PORT_Q_CTRL_SET0 [35][3].STRICT_EN[8:8]	1
0xBB002CF4	SCHED_PORT_Q_CTRL_SET0 [35][3].RESERVED[7:7]	1
0xBB002CF4	SCHED_PORT_Q_CTRL_SET0 [35][3].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002CF8	SCHED_PORT_Q_CTRL_SET0 [35][4].RESERVED[31:9]	23
0xBB002CF8	SCHED_PORT_Q_CTRL_SET0 [35][4].STRICT_EN[8:8]	1
0xBB002CF8	SCHED_PORT_Q_CTRL_SET0 [35][4].RESERVED[7:7]	1
0xBB002CF8	SCHED_PORT_Q_CTRL_SET0 [35][4].WEIGHT[6:0]	7
0xBB002CFC	SCHED_PORT_Q_CTRL_SET0 [35][5].RESERVED[31:9]	23
0xBB002CFC	SCHED_PORT_Q_CTRL_SET0 [35][5].STRICT_EN[8:8]	1
0xBB002CFC	SCHED_PORT_Q_CTRL_SET0 [35][5].RESERVED[7:7]	1
0xBB002CFC	SCHED_PORT_Q_CTRL_SET0 [35][5].WEIGHT[6:0]	7
0xBB002D00	SCHED_PORT_Q_CTRL_SET0 [35][6].RESERVED[31:9]	23
0xBB002D00	SCHED_PORT_Q_CTRL_SET0 [35][6].STRICT_EN[8:8]	1
0xBB002D00	SCHED_PORT_Q_CTRL_SET0 [35][6].RESERVED[7:7]	1
0xBB002D00	SCHED_PORT_Q_CTRL_SET0 [35][6].WEIGHT[6:0]	7
0xBB002D04	SCHED_PORT_Q_CTRL_SET0 [35][7].RESERVED[31:9]	23
0xBB002D04	SCHED_PORT_Q_CTRL_SET0 [35][7].STRICT_EN[8:8]	1
0xBB002D04	SCHED_PORT_Q_CTRL_SET0 [35][7].RESERVED[7:7]	1
0xBB002D04	SCHED_PORT_Q_CTRL_SET0 [35][7].WEIGHT[6:0]	7
0xBB002D08	SCHED_PORT_Q_CTRL_SET0 [36][0].RESERVED[31:9]	23
0xBB002D08	SCHED_PORT_Q_CTRL_SET0 [36][0].STRICT_EN[8:8]	1
0xBB002D08	SCHED_PORT_Q_CTRL_SET0 [36][0].RESERVED[7:7]	1
0xBB002D08	SCHED_PORT_Q_CTRL_SET0 [36][0].WEIGHT[6:0]	7
0xBB002D0C	SCHED_PORT_Q_CTRL_SET0 [36][1].RESERVED[31:9]	23
0xBB002D0C	SCHED_PORT_Q_CTRL_SET0 [36][1].STRICT_EN[8:8]	1
0xBB002D0C	SCHED_PORT_Q_CTRL_SET0 [36][1].RESERVED[7:7]	1
0xBB002D0C	SCHED_PORT_Q_CTRL_SET0 [36][1].WEIGHT[6:0]	7
0xBB002D10	SCHED_PORT_Q_CTRL_SET0 [36][2].RESERVED[31:9]	23
0xBB002D10	SCHED_PORT_Q_CTRL_SET0 [36][2].STRICT_EN[8:8]	1
0xBB002D10	SCHED_PORT_Q_CTRL_SET0 [36][2].RESERVED[7:7]	1
0xBB002D10	SCHED_PORT_Q_CTRL_SET0 [36][2].WEIGHT[6:0]	7
0xBB002D14	SCHED_PORT_Q_CTRL_SET0 [36][3].RESERVED[31:9]	23
0xBB002D14	SCHED_PORT_Q_CTRL_SET0 [36][3].STRICT_EN[8:8]	1
0xBB002D14	SCHED_PORT_Q_CTRL_SET0 [36][3].RESERVED[7:7]	1
0xBB002D14	SCHED_PORT_Q_CTRL_SET0 [36][3].WEIGHT[6:0]	7
0xBB002D18	SCHED_PORT_Q_CTRL_SET0 [36][4].RESERVED[31:9]	23
0xBB002D18	SCHED_PORT_Q_CTRL_SET0 [36][4].STRICT_EN[8:8]	1
0xBB002D18	SCHED_PORT_Q_CTRL_SET0 [36][4].RESERVED[7:7]	1
0xBB002D18	SCHED_PORT_Q_CTRL_SET0 [36][4].WEIGHT[6:0]	7
0xBB002D1C	SCHED_PORT_Q_CTRL_SET0 [36][5].RESERVED[31:9]	23
0xBB002D1C	SCHED_PORT_Q_CTRL_SET0 [36][5].STRICT_EN[8:8]	1
0xBB002D1C	SCHED_PORT_Q_CTRL_SET0 [36][5].RESERVED[7:7]	1
0xBB002D1C	SCHED_PORT_Q_CTRL_SET0 [36][5].WEIGHT[6:0]	7
0xBB002D20	SCHED_PORT_Q_CTRL_SET0 [36][6].RESERVED[31:9]	23
0xBB002D20	SCHED_PORT_Q_CTRL_SET0 [36][6].STRICT_EN[8:8]	1
0xBB002D20	SCHED_PORT_Q_CTRL_SET0 [36][6].RESERVED[7:7]	1
0xBB002D20	SCHED_PORT_Q_CTRL_SET0 [36][6].WEIGHT[6:0]	7
0xBB002D24	SCHED_PORT_Q_CTRL_SET0 [36][7].RESERVED[31:9]	23
0xBB002D24	SCHED_PORT_Q_CTRL_SET0 [36][7].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002D24	SCHED_PORT_Q_CTRL_SET0 [36][7].RESERVED[7:7]	1
0xBB002D24	SCHED_PORT_Q_CTRL_SET0 [36][7].WEIGHT[6:0]	7
0xBB002D28	SCHED_PORT_Q_CTRL_SET0 [37][0].RESERVED[31:9]	23
0xBB002D28	SCHED_PORT_Q_CTRL_SET0 [37][0].STRICT_EN[8:8]	1
0xBB002D28	SCHED_PORT_Q_CTRL_SET0 [37][0].RESERVED[7:7]	1
0xBB002D28	SCHED_PORT_Q_CTRL_SET0 [37][0].WEIGHT[6:0]	7
0xBB002D2C	SCHED_PORT_Q_CTRL_SET0 [37][1].RESERVED[31:9]	23
0xBB002D2C	SCHED_PORT_Q_CTRL_SET0 [37][1].STRICT_EN[8:8]	1
0xBB002D2C	SCHED_PORT_Q_CTRL_SET0 [37][1].RESERVED[7:7]	1
0xBB002D2C	SCHED_PORT_Q_CTRL_SET0 [37][1].WEIGHT[6:0]	7
0xBB002D30	SCHED_PORT_Q_CTRL_SET0 [37][2].RESERVED[31:9]	23
0xBB002D30	SCHED_PORT_Q_CTRL_SET0 [37][2].STRICT_EN[8:8]	1
0xBB002D30	SCHED_PORT_Q_CTRL_SET0 [37][2].RESERVED[7:7]	1
0xBB002D30	SCHED_PORT_Q_CTRL_SET0 [37][2].WEIGHT[6:0]	7
0xBB002D34	SCHED_PORT_Q_CTRL_SET0 [37][3].RESERVED[31:9]	23
0xBB002D34	SCHED_PORT_Q_CTRL_SET0 [37][3].STRICT_EN[8:8]	1
0xBB002D34	SCHED_PORT_Q_CTRL_SET0 [37][3].RESERVED[7:7]	1
0xBB002D34	SCHED_PORT_Q_CTRL_SET0 [37][3].WEIGHT[6:0]	7
0xBB002D38	SCHED_PORT_Q_CTRL_SET0 [37][4].RESERVED[31:9]	23
0xBB002D38	SCHED_PORT_Q_CTRL_SET0 [37][4].STRICT_EN[8:8]	1
0xBB002D38	SCHED_PORT_Q_CTRL_SET0 [37][4].RESERVED[7:7]	1
0xBB002D38	SCHED_PORT_Q_CTRL_SET0 [37][4].WEIGHT[6:0]	7
0xBB002D3C	SCHED_PORT_Q_CTRL_SET0 [37][5].RESERVED[31:9]	23
0xBB002D3C	SCHED_PORT_Q_CTRL_SET0 [37][5].STRICT_EN[8:8]	1
0xBB002D3C	SCHED_PORT_Q_CTRL_SET0 [37][5].RESERVED[7:7]	1
0xBB002D3C	SCHED_PORT_Q_CTRL_SET0 [37][5].WEIGHT[6:0]	7
0xBB002D40	SCHED_PORT_Q_CTRL_SET0 [37][6].RESERVED[31:9]	23
0xBB002D40	SCHED_PORT_Q_CTRL_SET0 [37][6].STRICT_EN[8:8]	1
0xBB002D40	SCHED_PORT_Q_CTRL_SET0 [37][6].RESERVED[7:7]	1
0xBB002D40	SCHED_PORT_Q_CTRL_SET0 [37][6].WEIGHT[6:0]	7
0xBB002D44	SCHED_PORT_Q_CTRL_SET0 [37][7].RESERVED[31:9]	23
0xBB002D44	SCHED_PORT_Q_CTRL_SET0 [37][7].STRICT_EN[8:8]	1
0xBB002D44	SCHED_PORT_Q_CTRL_SET0 [37][7].RESERVED[7:7]	1
0xBB002D44	SCHED_PORT_Q_CTRL_SET0 [37][7].WEIGHT[6:0]	7
0xBB002D48	SCHED_PORT_Q_CTRL_SET0 [38][0].RESERVED[31:9]	23
0xBB002D48	SCHED_PORT_Q_CTRL_SET0 [38][0].STRICT_EN[8:8]	1
0xBB002D48	SCHED_PORT_Q_CTRL_SET0 [38][0].RESERVED[7:7]	1
0xBB002D48	SCHED_PORT_Q_CTRL_SET0 [38][0].WEIGHT[6:0]	7
0xBB002D4C	SCHED_PORT_Q_CTRL_SET0 [38][1].RESERVED[31:9]	23
0xBB002D4C	SCHED_PORT_Q_CTRL_SET0 [38][1].STRICT_EN[8:8]	1
0xBB002D4C	SCHED_PORT_Q_CTRL_SET0 [38][1].RESERVED[7:7]	1
0xBB002D4C	SCHED_PORT_Q_CTRL_SET0 [38][1].WEIGHT[6:0]	7
0xBB002D50	SCHED_PORT_Q_CTRL_SET0 [38][2].RESERVED[31:9]	23
0xBB002D50	SCHED_PORT_Q_CTRL_SET0 [38][2].STRICT_EN[8:8]	1
0xBB002D50	SCHED_PORT_Q_CTRL_SET0 [38][2].RESERVED[7:7]	1
0xBB002D50	SCHED_PORT_Q_CTRL_SET0 [38][2].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002D54	SCHED_PORT_Q_CTRL_SET0 [38][3].RESERVED[31:9]	23
0xBB002D54	SCHED_PORT_Q_CTRL_SET0 [38][3].STRICT_EN[8:8]	1
0xBB002D54	SCHED_PORT_Q_CTRL_SET0 [38][3].RESERVED[7:7]	1
0xBB002D54	SCHED_PORT_Q_CTRL_SET0 [38][3].WEIGHT[6:0]	7
0xBB002D58	SCHED_PORT_Q_CTRL_SET0 [38][4].RESERVED[31:9]	23
0xBB002D58	SCHED_PORT_Q_CTRL_SET0 [38][4].STRICT_EN[8:8]	1
0xBB002D58	SCHED_PORT_Q_CTRL_SET0 [38][4].RESERVED[7:7]	1
0xBB002D58	SCHED_PORT_Q_CTRL_SET0 [38][4].WEIGHT[6:0]	7
0xBB002D5C	SCHED_PORT_Q_CTRL_SET0 [38][5].RESERVED[31:9]	23
0xBB002D5C	SCHED_PORT_Q_CTRL_SET0 [38][5].STRICT_EN[8:8]	1
0xBB002D5C	SCHED_PORT_Q_CTRL_SET0 [38][5].RESERVED[7:7]	1
0xBB002D5C	SCHED_PORT_Q_CTRL_SET0 [38][5].WEIGHT[6:0]	7
0xBB002D60	SCHED_PORT_Q_CTRL_SET0 [38][6].RESERVED[31:9]	23
0xBB002D60	SCHED_PORT_Q_CTRL_SET0 [38][6].STRICT_EN[8:8]	1
0xBB002D60	SCHED_PORT_Q_CTRL_SET0 [38][6].RESERVED[7:7]	1
0xBB002D60	SCHED_PORT_Q_CTRL_SET0 [38][6].WEIGHT[6:0]	7
0xBB002D64	SCHED_PORT_Q_CTRL_SET0 [38][7].RESERVED[31:9]	23
0xBB002D64	SCHED_PORT_Q_CTRL_SET0 [38][7].STRICT_EN[8:8]	1
0xBB002D64	SCHED_PORT_Q_CTRL_SET0 [38][7].RESERVED[7:7]	1
0xBB002D64	SCHED_PORT_Q_CTRL_SET0 [38][7].WEIGHT[6:0]	7
0xBB002D68	SCHED_PORT_Q_CTRL_SET0 [39][0].RESERVED[31:9]	23
0xBB002D68	SCHED_PORT_Q_CTRL_SET0 [39][0].STRICT_EN[8:8]	1
0xBB002D68	SCHED_PORT_Q_CTRL_SET0 [39][0].RESERVED[7:7]	1
0xBB002D68	SCHED_PORT_Q_CTRL_SET0 [39][0].WEIGHT[6:0]	7
0xBB002D6C	SCHED_PORT_Q_CTRL_SET0 [39][1].RESERVED[31:9]	23
0xBB002D6C	SCHED_PORT_Q_CTRL_SET0 [39][1].STRICT_EN[8:8]	1
0xBB002D6C	SCHED_PORT_Q_CTRL_SET0 [39][1].RESERVED[7:7]	1
0xBB002D6C	SCHED_PORT_Q_CTRL_SET0 [39][1].WEIGHT[6:0]	7
0xBB002D70	SCHED_PORT_Q_CTRL_SET0 [39][2].RESERVED[31:9]	23
0xBB002D70	SCHED_PORT_Q_CTRL_SET0 [39][2].STRICT_EN[8:8]	1
0xBB002D70	SCHED_PORT_Q_CTRL_SET0 [39][2].RESERVED[7:7]	1
0xBB002D70	SCHED_PORT_Q_CTRL_SET0 [39][2].WEIGHT[6:0]	7
0xBB002D74	SCHED_PORT_Q_CTRL_SET0 [39][3].RESERVED[31:9]	23
0xBB002D74	SCHED_PORT_Q_CTRL_SET0 [39][3].STRICT_EN[8:8]	1
0xBB002D74	SCHED_PORT_Q_CTRL_SET0 [39][3].RESERVED[7:7]	1
0xBB002D74	SCHED_PORT_Q_CTRL_SET0 [39][3].WEIGHT[6:0]	7
0xBB002D78	SCHED_PORT_Q_CTRL_SET0 [39][4].RESERVED[31:9]	23
0xBB002D78	SCHED_PORT_Q_CTRL_SET0 [39][4].STRICT_EN[8:8]	1
0xBB002D78	SCHED_PORT_Q_CTRL_SET0 [39][4].RESERVED[7:7]	1
0xBB002D78	SCHED_PORT_Q_CTRL_SET0 [39][4].WEIGHT[6:0]	7
0xBB002D7C	SCHED_PORT_Q_CTRL_SET0 [39][5].RESERVED[31:9]	23
0xBB002D7C	SCHED_PORT_Q_CTRL_SET0 [39][5].STRICT_EN[8:8]	1
0xBB002D7C	SCHED_PORT_Q_CTRL_SET0 [39][5].RESERVED[7:7]	1
0xBB002D7C	SCHED_PORT_Q_CTRL_SET0 [39][5].WEIGHT[6:0]	7
0xBB002D80	SCHED_PORT_Q_CTRL_SET0 [39][6].RESERVED[31:9]	23
0xBB002D80	SCHED_PORT_Q_CTRL_SET0 [39][6].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002D80	SCHED_PORT_Q_CTRL_SET0 [39][6].RESERVED[7:7]	1
0xBB002D80	SCHED_PORT_Q_CTRL_SET0 [39][6].WEIGHT[6:0]	7
0xBB002D84	SCHED_PORT_Q_CTRL_SET0 [39][7].RESERVED[31:9]	23
0xBB002D84	SCHED_PORT_Q_CTRL_SET0 [39][7].STRICT_EN[8:8]	1
0xBB002D84	SCHED_PORT_Q_CTRL_SET0 [39][7].RESERVED[7:7]	1
0xBB002D84	SCHED_PORT_Q_CTRL_SET0 [39][7].WEIGHT[6:0]	7
0xBB002D88	SCHED_PORT_Q_CTRL_SET0 [40][0].RESERVED[31:9]	23
0xBB002D88	SCHED_PORT_Q_CTRL_SET0 [40][0].STRICT_EN[8:8]	1
0xBB002D88	SCHED_PORT_Q_CTRL_SET0 [40][0].RESERVED[7:7]	1
0xBB002D88	SCHED_PORT_Q_CTRL_SET0 [40][0].WEIGHT[6:0]	7
0xBB002D8C	SCHED_PORT_Q_CTRL_SET0 [40][1].RESERVED[31:9]	23
0xBB002D8C	SCHED_PORT_Q_CTRL_SET0 [40][1].STRICT_EN[8:8]	1
0xBB002D8C	SCHED_PORT_Q_CTRL_SET0 [40][1].RESERVED[7:7]	1
0xBB002D8C	SCHED_PORT_Q_CTRL_SET0 [40][1].WEIGHT[6:0]	7
0xBB002D90	SCHED_PORT_Q_CTRL_SET0 [40][2].RESERVED[31:9]	23
0xBB002D90	SCHED_PORT_Q_CTRL_SET0 [40][2].STRICT_EN[8:8]	1
0xBB002D90	SCHED_PORT_Q_CTRL_SET0 [40][2].RESERVED[7:7]	1
0xBB002D90	SCHED_PORT_Q_CTRL_SET0 [40][2].WEIGHT[6:0]	7
0xBB002D94	SCHED_PORT_Q_CTRL_SET0 [40][3].RESERVED[31:9]	23
0xBB002D94	SCHED_PORT_Q_CTRL_SET0 [40][3].STRICT_EN[8:8]	1
0xBB002D94	SCHED_PORT_Q_CTRL_SET0 [40][3].RESERVED[7:7]	1
0xBB002D94	SCHED_PORT_Q_CTRL_SET0 [40][3].WEIGHT[6:0]	7
0xBB002D98	SCHED_PORT_Q_CTRL_SET0 [40][4].RESERVED[31:9]	23
0xBB002D98	SCHED_PORT_Q_CTRL_SET0 [40][4].STRICT_EN[8:8]	1
0xBB002D98	SCHED_PORT_Q_CTRL_SET0 [40][4].RESERVED[7:7]	1
0xBB002D98	SCHED_PORT_Q_CTRL_SET0 [40][4].WEIGHT[6:0]	7
0xBB002D9C	SCHED_PORT_Q_CTRL_SET0 [40][5].RESERVED[31:9]	23
0xBB002D9C	SCHED_PORT_Q_CTRL_SET0 [40][5].STRICT_EN[8:8]	1
0xBB002D9C	SCHED_PORT_Q_CTRL_SET0 [40][5].RESERVED[7:7]	1
0xBB002D9C	SCHED_PORT_Q_CTRL_SET0 [40][5].WEIGHT[6:0]	7
0xBB002DA0	SCHED_PORT_Q_CTRL_SET0 [40][6].RESERVED[31:9]	23
0xBB002DA0	SCHED_PORT_Q_CTRL_SET0 [40][6].STRICT_EN[8:8]	1
0xBB002DA0	SCHED_PORT_Q_CTRL_SET0 [40][6].RESERVED[7:7]	1
0xBB002DA0	SCHED_PORT_Q_CTRL_SET0 [40][6].WEIGHT[6:0]	7
0xBB002DA4	SCHED_PORT_Q_CTRL_SET0 [40][7].RESERVED[31:9]	23
0xBB002DA4	SCHED_PORT_Q_CTRL_SET0 [40][7].STRICT_EN[8:8]	1
0xBB002DA4	SCHED_PORT_Q_CTRL_SET0 [40][7].RESERVED[7:7]	1
0xBB002DA4	SCHED_PORT_Q_CTRL_SET0 [40][7].WEIGHT[6:0]	7
0xBB002DA8	SCHED_PORT_Q_CTRL_SET0 [41][0].RESERVED[31:9]	23
0xBB002DA8	SCHED_PORT_Q_CTRL_SET0 [41][0].STRICT_EN[8:8]	1
0xBB002DA8	SCHED_PORT_Q_CTRL_SET0 [41][0].RESERVED[7:7]	1
0xBB002DA8	SCHED_PORT_Q_CTRL_SET0 [41][0].WEIGHT[6:0]	7
0xBB002DAC	SCHED_PORT_Q_CTRL_SET0 [41][1].RESERVED[31:9]	23
0xBB002DAC	SCHED_PORT_Q_CTRL_SET0 [41][1].STRICT_EN[8:8]	1
0xBB002DAC	SCHED_PORT_Q_CTRL_SET0 [41][1].RESERVED[7:7]	1
0xBB002DAC	SCHED_PORT_Q_CTRL_SET0 [41][1].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002DB0	SCHED_PORT_Q_CTRL_SET0 [41][2].RESERVED[31:9]	23
0xBB002DB0	SCHED_PORT_Q_CTRL_SET0 [41][2].STRICT_EN[8:8]	1
0xBB002DB0	SCHED_PORT_Q_CTRL_SET0 [41][2].RESERVED[7:7]	1
0xBB002DB0	SCHED_PORT_Q_CTRL_SET0 [41][2].WEIGHT[6:0]	7
0xBB002DB4	SCHED_PORT_Q_CTRL_SET0 [41][3].RESERVED[31:9]	23
0xBB002DB4	SCHED_PORT_Q_CTRL_SET0 [41][3].STRICT_EN[8:8]	1
0xBB002DB4	SCHED_PORT_Q_CTRL_SET0 [41][3].RESERVED[7:7]	1
0xBB002DB4	SCHED_PORT_Q_CTRL_SET0 [41][3].WEIGHT[6:0]	7
0xBB002DB8	SCHED_PORT_Q_CTRL_SET0 [41][4].RESERVED[31:9]	23
0xBB002DB8	SCHED_PORT_Q_CTRL_SET0 [41][4].STRICT_EN[8:8]	1
0xBB002DB8	SCHED_PORT_Q_CTRL_SET0 [41][4].RESERVED[7:7]	1
0xBB002DB8	SCHED_PORT_Q_CTRL_SET0 [41][4].WEIGHT[6:0]	7
0xBB002DBC	SCHED_PORT_Q_CTRL_SET0 [41][5].RESERVED[31:9]	23
0xBB002DBC	SCHED_PORT_Q_CTRL_SET0 [41][5].STRICT_EN[8:8]	1
0xBB002DBC	SCHED_PORT_Q_CTRL_SET0 [41][5].RESERVED[7:7]	1
0xBB002DBC	SCHED_PORT_Q_CTRL_SET0 [41][5].WEIGHT[6:0]	7
0xBB002DC0	SCHED_PORT_Q_CTRL_SET0 [41][6].RESERVED[31:9]	23
0xBB002DC0	SCHED_PORT_Q_CTRL_SET0 [41][6].STRICT_EN[8:8]	1
0xBB002DC0	SCHED_PORT_Q_CTRL_SET0 [41][6].RESERVED[7:7]	1
0xBB002DC0	SCHED_PORT_Q_CTRL_SET0 [41][6].WEIGHT[6:0]	7
0xBB002DC4	SCHED_PORT_Q_CTRL_SET0 [41][7].RESERVED[31:9]	23
0xBB002DC4	SCHED_PORT_Q_CTRL_SET0 [41][7].STRICT_EN[8:8]	1
0xBB002DC4	SCHED_PORT_Q_CTRL_SET0 [41][7].RESERVED[7:7]	1
0xBB002DC4	SCHED_PORT_Q_CTRL_SET0 [41][7].WEIGHT[6:0]	7
0xBB002DC8	SCHED_PORT_Q_CTRL_SET0 [42][0].RESERVED[31:9]	23
0xBB002DC8	SCHED_PORT_Q_CTRL_SET0 [42][0].STRICT_EN[8:8]	1
0xBB002DC8	SCHED_PORT_Q_CTRL_SET0 [42][0].RESERVED[7:7]	1
0xBB002DC8	SCHED_PORT_Q_CTRL_SET0 [42][0].WEIGHT[6:0]	7
0xBB002DCC	SCHED_PORT_Q_CTRL_SET0 [42][1].RESERVED[31:9]	23
0xBB002DCC	SCHED_PORT_Q_CTRL_SET0 [42][1].STRICT_EN[8:8]	1
0xBB002DCC	SCHED_PORT_Q_CTRL_SET0 [42][1].RESERVED[7:7]	1
0xBB002DCC	SCHED_PORT_Q_CTRL_SET0 [42][1].WEIGHT[6:0]	7
0xBB002DD0	SCHED_PORT_Q_CTRL_SET0 [42][2].RESERVED[31:9]	23
0xBB002DD0	SCHED_PORT_Q_CTRL_SET0 [42][2].STRICT_EN[8:8]	1
0xBB002DD0	SCHED_PORT_Q_CTRL_SET0 [42][2].RESERVED[7:7]	1
0xBB002DD0	SCHED_PORT_Q_CTRL_SET0 [42][2].WEIGHT[6:0]	7
0xBB002DD4	SCHED_PORT_Q_CTRL_SET0 [42][3].RESERVED[31:9]	23
0xBB002DD4	SCHED_PORT_Q_CTRL_SET0 [42][3].STRICT_EN[8:8]	1
0xBB002DD4	SCHED_PORT_Q_CTRL_SET0 [42][3].RESERVED[7:7]	1
0xBB002DD4	SCHED_PORT_Q_CTRL_SET0 [42][3].WEIGHT[6:0]	7
0xBB002DD8	SCHED_PORT_Q_CTRL_SET0 [42][4].RESERVED[31:9]	23
0xBB002DD8	SCHED_PORT_Q_CTRL_SET0 [42][4].STRICT_EN[8:8]	1
0xBB002DD8	SCHED_PORT_Q_CTRL_SET0 [42][4].RESERVED[7:7]	1
0xBB002DD8	SCHED_PORT_Q_CTRL_SET0 [42][4].WEIGHT[6:0]	7
0xBB002DDC	SCHED_PORT_Q_CTRL_SET0 [42][5].RESERVED[31:9]	23
0xBB002DDC	SCHED_PORT_Q_CTRL_SET0 [42][5].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002DDC	SCHED_PORT_Q_CTRL_SET0 [42][5].RESERVED[7:7]	1
0xBB002DDC	SCHED_PORT_Q_CTRL_SET0 [42][5].WEIGHT[6:0]	7
0xBB002DE0	SCHED_PORT_Q_CTRL_SET0 [42][6].RESERVED[31:9]	23
0xBB002DE0	SCHED_PORT_Q_CTRL_SET0 [42][6].STRICT_EN[8:8]	1
0xBB002DE0	SCHED_PORT_Q_CTRL_SET0 [42][6].RESERVED[7:7]	1
0xBB002DE0	SCHED_PORT_Q_CTRL_SET0 [42][6].WEIGHT[6:0]	7
0xBB002DE4	SCHED_PORT_Q_CTRL_SET0 [42][7].RESERVED[31:9]	23
0xBB002DE4	SCHED_PORT_Q_CTRL_SET0 [42][7].STRICT_EN[8:8]	1
0xBB002DE4	SCHED_PORT_Q_CTRL_SET0 [42][7].RESERVED[7:7]	1
0xBB002DE4	SCHED_PORT_Q_CTRL_SET0 [42][7].WEIGHT[6:0]	7
0xBB002DE8	SCHED_PORT_Q_CTRL_SET0 [43][0].RESERVED[31:9]	23
0xBB002DE8	SCHED_PORT_Q_CTRL_SET0 [43][0].STRICT_EN[8:8]	1
0xBB002DE8	SCHED_PORT_Q_CTRL_SET0 [43][0].RESERVED[7:7]	1
0xBB002DE8	SCHED_PORT_Q_CTRL_SET0 [43][0].WEIGHT[6:0]	7
0xBB002DEC	SCHED_PORT_Q_CTRL_SET0 [43][1].RESERVED[31:9]	23
0xBB002DEC	SCHED_PORT_Q_CTRL_SET0 [43][1].STRICT_EN[8:8]	1
0xBB002DEC	SCHED_PORT_Q_CTRL_SET0 [43][1].RESERVED[7:7]	1
0xBB002DEC	SCHED_PORT_Q_CTRL_SET0 [43][1].WEIGHT[6:0]	7
0xBB002DF0	SCHED_PORT_Q_CTRL_SET0 [43][2].RESERVED[31:9]	23
0xBB002DF0	SCHED_PORT_Q_CTRL_SET0 [43][2].STRICT_EN[8:8]	1
0xBB002DF0	SCHED_PORT_Q_CTRL_SET0 [43][2].RESERVED[7:7]	1
0xBB002DF0	SCHED_PORT_Q_CTRL_SET0 [43][2].WEIGHT[6:0]	7
0xBB002DF4	SCHED_PORT_Q_CTRL_SET0 [43][3].RESERVED[31:9]	23
0xBB002DF4	SCHED_PORT_Q_CTRL_SET0 [43][3].STRICT_EN[8:8]	1
0xBB002DF4	SCHED_PORT_Q_CTRL_SET0 [43][3].RESERVED[7:7]	1
0xBB002DF4	SCHED_PORT_Q_CTRL_SET0 [43][3].WEIGHT[6:0]	7
0xBB002DF8	SCHED_PORT_Q_CTRL_SET0 [43][4].RESERVED[31:9]	23
0xBB002DF8	SCHED_PORT_Q_CTRL_SET0 [43][4].STRICT_EN[8:8]	1
0xBB002DF8	SCHED_PORT_Q_CTRL_SET0 [43][4].RESERVED[7:7]	1
0xBB002DF8	SCHED_PORT_Q_CTRL_SET0 [43][4].WEIGHT[6:0]	7
0xBB002DFC	SCHED_PORT_Q_CTRL_SET0 [43][5].RESERVED[31:9]	23
0xBB002DFC	SCHED_PORT_Q_CTRL_SET0 [43][5].STRICT_EN[8:8]	1
0xBB002DFC	SCHED_PORT_Q_CTRL_SET0 [43][5].RESERVED[7:7]	1
0xBB002DFC	SCHED_PORT_Q_CTRL_SET0 [43][5].WEIGHT[6:0]	7
0xBB002E00	SCHED_PORT_Q_CTRL_SET0 [43][6].RESERVED[31:9]	23
0xBB002E00	SCHED_PORT_Q_CTRL_SET0 [43][6].STRICT_EN[8:8]	1
0xBB002E00	SCHED_PORT_Q_CTRL_SET0 [43][6].RESERVED[7:7]	1
0xBB002E00	SCHED_PORT_Q_CTRL_SET0 [43][6].WEIGHT[6:0]	7
0xBB002E04	SCHED_PORT_Q_CTRL_SET0 [43][7].RESERVED[31:9]	23
0xBB002E04	SCHED_PORT_Q_CTRL_SET0 [43][7].STRICT_EN[8:8]	1
0xBB002E04	SCHED_PORT_Q_CTRL_SET0 [43][7].RESERVED[7:7]	1
0xBB002E04	SCHED_PORT_Q_CTRL_SET0 [43][7].WEIGHT[6:0]	7
0xBB002E08	SCHED_PORT_Q_CTRL_SET0 [44][0].RESERVED[31:9]	23
0xBB002E08	SCHED_PORT_Q_CTRL_SET0 [44][0].STRICT_EN[8:8]	1
0xBB002E08	SCHED_PORT_Q_CTRL_SET0 [44][0].RESERVED[7:7]	1
0xBB002E08	SCHED_PORT_Q_CTRL_SET0 [44][0].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002E0C	SCHED_PORT_Q_CTRL_SET0 [44][1].RESERVED[31:9]	23
0xBB002E0C	SCHED_PORT_Q_CTRL_SET0 [44][1].STRICT_EN[8:8]	1
0xBB002E0C	SCHED_PORT_Q_CTRL_SET0 [44][1].RESERVED[7:7]	1
0xBB002E0C	SCHED_PORT_Q_CTRL_SET0 [44][1].WEIGHT[6:0]	7
0xBB002E10	SCHED_PORT_Q_CTRL_SET0 [44][2].RESERVED[31:9]	23
0xBB002E10	SCHED_PORT_Q_CTRL_SET0 [44][2].STRICT_EN[8:8]	1
0xBB002E10	SCHED_PORT_Q_CTRL_SET0 [44][2].RESERVED[7:7]	1
0xBB002E10	SCHED_PORT_Q_CTRL_SET0 [44][2].WEIGHT[6:0]	7
0xBB002E14	SCHED_PORT_Q_CTRL_SET0 [44][3].RESERVED[31:9]	23
0xBB002E14	SCHED_PORT_Q_CTRL_SET0 [44][3].STRICT_EN[8:8]	1
0xBB002E14	SCHED_PORT_Q_CTRL_SET0 [44][3].RESERVED[7:7]	1
0xBB002E14	SCHED_PORT_Q_CTRL_SET0 [44][3].WEIGHT[6:0]	7
0xBB002E18	SCHED_PORT_Q_CTRL_SET0 [44][4].RESERVED[31:9]	23
0xBB002E18	SCHED_PORT_Q_CTRL_SET0 [44][4].STRICT_EN[8:8]	1
0xBB002E18	SCHED_PORT_Q_CTRL_SET0 [44][4].RESERVED[7:7]	1
0xBB002E18	SCHED_PORT_Q_CTRL_SET0 [44][4].WEIGHT[6:0]	7
0xBB002E1C	SCHED_PORT_Q_CTRL_SET0 [44][5].RESERVED[31:9]	23
0xBB002E1C	SCHED_PORT_Q_CTRL_SET0 [44][5].STRICT_EN[8:8]	1
0xBB002E1C	SCHED_PORT_Q_CTRL_SET0 [44][5].RESERVED[7:7]	1
0xBB002E1C	SCHED_PORT_Q_CTRL_SET0 [44][5].WEIGHT[6:0]	7
0xBB002E20	SCHED_PORT_Q_CTRL_SET0 [44][6].RESERVED[31:9]	23
0xBB002E20	SCHED_PORT_Q_CTRL_SET0 [44][6].STRICT_EN[8:8]	1
0xBB002E20	SCHED_PORT_Q_CTRL_SET0 [44][6].RESERVED[7:7]	1
0xBB002E20	SCHED_PORT_Q_CTRL_SET0 [44][6].WEIGHT[6:0]	7
0xBB002E24	SCHED_PORT_Q_CTRL_SET0 [44][7].RESERVED[31:9]	23
0xBB002E24	SCHED_PORT_Q_CTRL_SET0 [44][7].STRICT_EN[8:8]	1
0xBB002E24	SCHED_PORT_Q_CTRL_SET0 [44][7].RESERVED[7:7]	1
0xBB002E24	SCHED_PORT_Q_CTRL_SET0 [44][7].WEIGHT[6:0]	7
0xBB002E28	SCHED_PORT_Q_CTRL_SET0 [45][0].RESERVED[31:9]	23
0xBB002E28	SCHED_PORT_Q_CTRL_SET0 [45][0].STRICT_EN[8:8]	1
0xBB002E28	SCHED_PORT_Q_CTRL_SET0 [45][0].RESERVED[7:7]	1
0xBB002E28	SCHED_PORT_Q_CTRL_SET0 [45][0].WEIGHT[6:0]	7
0xBB002E2C	SCHED_PORT_Q_CTRL_SET0 [45][1].RESERVED[31:9]	23
0xBB002E2C	SCHED_PORT_Q_CTRL_SET0 [45][1].STRICT_EN[8:8]	1
0xBB002E2C	SCHED_PORT_Q_CTRL_SET0 [45][1].RESERVED[7:7]	1
0xBB002E2C	SCHED_PORT_Q_CTRL_SET0 [45][1].WEIGHT[6:0]	7
0xBB002E30	SCHED_PORT_Q_CTRL_SET0 [45][2].RESERVED[31:9]	23
0xBB002E30	SCHED_PORT_Q_CTRL_SET0 [45][2].STRICT_EN[8:8]	1
0xBB002E30	SCHED_PORT_Q_CTRL_SET0 [45][2].RESERVED[7:7]	1
0xBB002E30	SCHED_PORT_Q_CTRL_SET0 [45][2].WEIGHT[6:0]	7
0xBB002E34	SCHED_PORT_Q_CTRL_SET0 [45][3].RESERVED[31:9]	23
0xBB002E34	SCHED_PORT_Q_CTRL_SET0 [45][3].STRICT_EN[8:8]	1
0xBB002E34	SCHED_PORT_Q_CTRL_SET0 [45][3].RESERVED[7:7]	1
0xBB002E34	SCHED_PORT_Q_CTRL_SET0 [45][3].WEIGHT[6:0]	7
0xBB002E38	SCHED_PORT_Q_CTRL_SET0 [45][4].RESERVED[31:9]	23
0xBB002E38	SCHED_PORT_Q_CTRL_SET0 [45][4].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002E38	SCHED_PORT_Q_CTRL_SET0 [45][4].RESERVED[7:7]	1
0xBB002E38	SCHED_PORT_Q_CTRL_SET0 [45][4].WEIGHT[6:0]	7
0xBB002E3C	SCHED_PORT_Q_CTRL_SET0 [45][5].RESERVED[31:9]	23
0xBB002E3C	SCHED_PORT_Q_CTRL_SET0 [45][5].STRICT_EN[8:8]	1
0xBB002E3C	SCHED_PORT_Q_CTRL_SET0 [45][5].RESERVED[7:7]	1
0xBB002E3C	SCHED_PORT_Q_CTRL_SET0 [45][5].WEIGHT[6:0]	7
0xBB002E40	SCHED_PORT_Q_CTRL_SET0 [45][6].RESERVED[31:9]	23
0xBB002E40	SCHED_PORT_Q_CTRL_SET0 [45][6].STRICT_EN[8:8]	1
0xBB002E40	SCHED_PORT_Q_CTRL_SET0 [45][6].RESERVED[7:7]	1
0xBB002E40	SCHED_PORT_Q_CTRL_SET0 [45][6].WEIGHT[6:0]	7
0xBB002E44	SCHED_PORT_Q_CTRL_SET0 [45][7].RESERVED[31:9]	23
0xBB002E44	SCHED_PORT_Q_CTRL_SET0 [45][7].STRICT_EN[8:8]	1
0xBB002E44	SCHED_PORT_Q_CTRL_SET0 [45][7].RESERVED[7:7]	1
0xBB002E44	SCHED_PORT_Q_CTRL_SET0 [45][7].WEIGHT[6:0]	7
0xBB002E48	SCHED_PORT_Q_CTRL_SET0 [46][0].RESERVED[31:9]	23
0xBB002E48	SCHED_PORT_Q_CTRL_SET0 [46][0].STRICT_EN[8:8]	1
0xBB002E48	SCHED_PORT_Q_CTRL_SET0 [46][0].RESERVED[7:7]	1
0xBB002E48	SCHED_PORT_Q_CTRL_SET0 [46][0].WEIGHT[6:0]	7
0xBB002E4C	SCHED_PORT_Q_CTRL_SET0 [46][1].RESERVED[31:9]	23
0xBB002E4C	SCHED_PORT_Q_CTRL_SET0 [46][1].STRICT_EN[8:8]	1
0xBB002E4C	SCHED_PORT_Q_CTRL_SET0 [46][1].RESERVED[7:7]	1
0xBB002E4C	SCHED_PORT_Q_CTRL_SET0 [46][1].WEIGHT[6:0]	7
0xBB002E50	SCHED_PORT_Q_CTRL_SET0 [46][2].RESERVED[31:9]	23
0xBB002E50	SCHED_PORT_Q_CTRL_SET0 [46][2].STRICT_EN[8:8]	1
0xBB002E50	SCHED_PORT_Q_CTRL_SET0 [46][2].RESERVED[7:7]	1
0xBB002E50	SCHED_PORT_Q_CTRL_SET0 [46][2].WEIGHT[6:0]	7
0xBB002E54	SCHED_PORT_Q_CTRL_SET0 [46][3].RESERVED[31:9]	23
0xBB002E54	SCHED_PORT_Q_CTRL_SET0 [46][3].STRICT_EN[8:8]	1
0xBB002E54	SCHED_PORT_Q_CTRL_SET0 [46][3].RESERVED[7:7]	1
0xBB002E54	SCHED_PORT_Q_CTRL_SET0 [46][3].WEIGHT[6:0]	7
0xBB002E58	SCHED_PORT_Q_CTRL_SET0 [46][4].RESERVED[31:9]	23
0xBB002E58	SCHED_PORT_Q_CTRL_SET0 [46][4].STRICT_EN[8:8]	1
0xBB002E58	SCHED_PORT_Q_CTRL_SET0 [46][4].RESERVED[7:7]	1
0xBB002E58	SCHED_PORT_Q_CTRL_SET0 [46][4].WEIGHT[6:0]	7
0xBB002E5C	SCHED_PORT_Q_CTRL_SET0 [46][5].RESERVED[31:9]	23
0xBB002E5C	SCHED_PORT_Q_CTRL_SET0 [46][5].STRICT_EN[8:8]	1
0xBB002E5C	SCHED_PORT_Q_CTRL_SET0 [46][5].RESERVED[7:7]	1
0xBB002E5C	SCHED_PORT_Q_CTRL_SET0 [46][5].WEIGHT[6:0]	7
0xBB002E60	SCHED_PORT_Q_CTRL_SET0 [46][6].RESERVED[31:9]	23
0xBB002E60	SCHED_PORT_Q_CTRL_SET0 [46][6].STRICT_EN[8:8]	1
0xBB002E60	SCHED_PORT_Q_CTRL_SET0 [46][6].RESERVED[7:7]	1
0xBB002E60	SCHED_PORT_Q_CTRL_SET0 [46][6].WEIGHT[6:0]	7
0xBB002E64	SCHED_PORT_Q_CTRL_SET0 [46][7].RESERVED[31:9]	23
0xBB002E64	SCHED_PORT_Q_CTRL_SET0 [46][7].STRICT_EN[8:8]	1
0xBB002E64	SCHED_PORT_Q_CTRL_SET0 [46][7].RESERVED[7:7]	1
0xBB002E64	SCHED_PORT_Q_CTRL_SET0 [46][7].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002E68	SCHED_PORT_Q_CTRL_SET0 [47][0].RESERVED[31:9]	23
0xBB002E68	SCHED_PORT_Q_CTRL_SET0 [47][0].STRICT_EN[8:8]	1
0xBB002E68	SCHED_PORT_Q_CTRL_SET0 [47][0].RESERVED[7:7]	1
0xBB002E68	SCHED_PORT_Q_CTRL_SET0 [47][0].WEIGHT[6:0]	7
0xBB002E6C	SCHED_PORT_Q_CTRL_SET0 [47][1].RESERVED[31:9]	23
0xBB002E6C	SCHED_PORT_Q_CTRL_SET0 [47][1].STRICT_EN[8:8]	1
0xBB002E6C	SCHED_PORT_Q_CTRL_SET0 [47][1].RESERVED[7:7]	1
0xBB002E6C	SCHED_PORT_Q_CTRL_SET0 [47][1].WEIGHT[6:0]	7
0xBB002E70	SCHED_PORT_Q_CTRL_SET0 [47][2].RESERVED[31:9]	23
0xBB002E70	SCHED_PORT_Q_CTRL_SET0 [47][2].STRICT_EN[8:8]	1
0xBB002E70	SCHED_PORT_Q_CTRL_SET0 [47][2].RESERVED[7:7]	1
0xBB002E70	SCHED_PORT_Q_CTRL_SET0 [47][2].WEIGHT[6:0]	7
0xBB002E74	SCHED_PORT_Q_CTRL_SET0 [47][3].RESERVED[31:9]	23
0xBB002E74	SCHED_PORT_Q_CTRL_SET0 [47][3].STRICT_EN[8:8]	1
0xBB002E74	SCHED_PORT_Q_CTRL_SET0 [47][3].RESERVED[7:7]	1
0xBB002E74	SCHED_PORT_Q_CTRL_SET0 [47][3].WEIGHT[6:0]	7
0xBB002E78	SCHED_PORT_Q_CTRL_SET0 [47][4].RESERVED[31:9]	23
0xBB002E78	SCHED_PORT_Q_CTRL_SET0 [47][4].STRICT_EN[8:8]	1
0xBB002E78	SCHED_PORT_Q_CTRL_SET0 [47][4].RESERVED[7:7]	1
0xBB002E78	SCHED_PORT_Q_CTRL_SET0 [47][4].WEIGHT[6:0]	7
0xBB002E7C	SCHED_PORT_Q_CTRL_SET0 [47][5].RESERVED[31:9]	23
0xBB002E7C	SCHED_PORT_Q_CTRL_SET0 [47][5].STRICT_EN[8:8]	1
0xBB002E7C	SCHED_PORT_Q_CTRL_SET0 [47][5].RESERVED[7:7]	1
0xBB002E7C	SCHED_PORT_Q_CTRL_SET0 [47][5].WEIGHT[6:0]	7
0xBB002E80	SCHED_PORT_Q_CTRL_SET0 [47][6].RESERVED[31:9]	23
0xBB002E80	SCHED_PORT_Q_CTRL_SET0 [47][6].STRICT_EN[8:8]	1
0xBB002E80	SCHED_PORT_Q_CTRL_SET0 [47][6].RESERVED[7:7]	1
0xBB002E80	SCHED_PORT_Q_CTRL_SET0 [47][6].WEIGHT[6:0]	7
0xBB002E84	SCHED_PORT_Q_CTRL_SET0 [47][7].RESERVED[31:9]	23
0xBB002E84	SCHED_PORT_Q_CTRL_SET0 [47][7].STRICT_EN[8:8]	1
0xBB002E84	SCHED_PORT_Q_CTRL_SET0 [47][7].RESERVED[7:7]	1
0xBB002E84	SCHED_PORT_Q_CTRL_SET0 [47][7].WEIGHT[6:0]	7
0xBB002E88	SCHED_PORT_Q_CTRL_SET0 [48][0].RESERVED[31:9]	23
0xBB002E88	SCHED_PORT_Q_CTRL_SET0 [48][0].STRICT_EN[8:8]	1
0xBB002E88	SCHED_PORT_Q_CTRL_SET0 [48][0].RESERVED[7:7]	1
0xBB002E88	SCHED_PORT_Q_CTRL_SET0 [48][0].WEIGHT[6:0]	7
0xBB002E8C	SCHED_PORT_Q_CTRL_SET0 [48][1].RESERVED[31:9]	23
0xBB002E8C	SCHED_PORT_Q_CTRL_SET0 [48][1].STRICT_EN[8:8]	1
0xBB002E8C	SCHED_PORT_Q_CTRL_SET0 [48][1].RESERVED[7:7]	1
0xBB002E8C	SCHED_PORT_Q_CTRL_SET0 [48][1].WEIGHT[6:0]	7
0xBB002E90	SCHED_PORT_Q_CTRL_SET0 [48][2].RESERVED[31:9]	23
0xBB002E90	SCHED_PORT_Q_CTRL_SET0 [48][2].STRICT_EN[8:8]	1
0xBB002E90	SCHED_PORT_Q_CTRL_SET0 [48][2].RESERVED[7:7]	1
0xBB002E90	SCHED_PORT_Q_CTRL_SET0 [48][2].WEIGHT[6:0]	7
0xBB002E94	SCHED_PORT_Q_CTRL_SET0 [48][3].RESERVED[31:9]	23
0xBB002E94	SCHED_PORT_Q_CTRL_SET0 [48][3].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002E94	SCHED_PORT_Q_CTRL_SET0 [48][3].RESERVED[7:7]	1
0xBB002E94	SCHED_PORT_Q_CTRL_SET0 [48][3].WEIGHT[6:0]	7
0xBB002E98	SCHED_PORT_Q_CTRL_SET0 [48][4].RESERVED[31:9]	23
0xBB002E98	SCHED_PORT_Q_CTRL_SET0 [48][4].STRICT_EN[8:8]	1
0xBB002E98	SCHED_PORT_Q_CTRL_SET0 [48][4].RESERVED[7:7]	1
0xBB002E98	SCHED_PORT_Q_CTRL_SET0 [48][4].WEIGHT[6:0]	7
0xBB002E9C	SCHED_PORT_Q_CTRL_SET0 [48][5].RESERVED[31:9]	23
0xBB002E9C	SCHED_PORT_Q_CTRL_SET0 [48][5].STRICT_EN[8:8]	1
0xBB002E9C	SCHED_PORT_Q_CTRL_SET0 [48][5].RESERVED[7:7]	1
0xBB002E9C	SCHED_PORT_Q_CTRL_SET0 [48][5].WEIGHT[6:0]	7
0xBB002EA0	SCHED_PORT_Q_CTRL_SET0 [48][6].RESERVED[31:9]	23
0xBB002EA0	SCHED_PORT_Q_CTRL_SET0 [48][6].STRICT_EN[8:8]	1
0xBB002EA0	SCHED_PORT_Q_CTRL_SET0 [48][6].RESERVED[7:7]	1
0xBB002EA0	SCHED_PORT_Q_CTRL_SET0 [48][6].WEIGHT[6:0]	7
0xBB002EA4	SCHED_PORT_Q_CTRL_SET0 [48][7].RESERVED[31:9]	23
0xBB002EA4	SCHED_PORT_Q_CTRL_SET0 [48][7].STRICT_EN[8:8]	1
0xBB002EA4	SCHED_PORT_Q_CTRL_SET0 [48][7].RESERVED[7:7]	1
0xBB002EA4	SCHED_PORT_Q_CTRL_SET0 [48][7].WEIGHT[6:0]	7
0xBB002EA8	SCHED_PORT_Q_CTRL_SET0 [49][0].RESERVED[31:9]	23
0xBB002EA8	SCHED_PORT_Q_CTRL_SET0 [49][0].STRICT_EN[8:8]	1
0xBB002EA8	SCHED_PORT_Q_CTRL_SET0 [49][0].RESERVED[7:7]	1
0xBB002EA8	SCHED_PORT_Q_CTRL_SET0 [49][0].WEIGHT[6:0]	7
0xBB002EAC	SCHED_PORT_Q_CTRL_SET0 [49][1].RESERVED[31:9]	23
0xBB002EAC	SCHED_PORT_Q_CTRL_SET0 [49][1].STRICT_EN[8:8]	1
0xBB002EAC	SCHED_PORT_Q_CTRL_SET0 [49][1].RESERVED[7:7]	1
0xBB002EAC	SCHED_PORT_Q_CTRL_SET0 [49][1].WEIGHT[6:0]	7
0xBB002EB0	SCHED_PORT_Q_CTRL_SET0 [49][2].RESERVED[31:9]	23
0xBB002EB0	SCHED_PORT_Q_CTRL_SET0 [49][2].STRICT_EN[8:8]	1
0xBB002EB0	SCHED_PORT_Q_CTRL_SET0 [49][2].RESERVED[7:7]	1
0xBB002EB0	SCHED_PORT_Q_CTRL_SET0 [49][2].WEIGHT[6:0]	7
0xBB002EB4	SCHED_PORT_Q_CTRL_SET0 [49][3].RESERVED[31:9]	23
0xBB002EB4	SCHED_PORT_Q_CTRL_SET0 [49][3].STRICT_EN[8:8]	1
0xBB002EB4	SCHED_PORT_Q_CTRL_SET0 [49][3].RESERVED[7:7]	1
0xBB002EB4	SCHED_PORT_Q_CTRL_SET0 [49][3].WEIGHT[6:0]	7
0xBB002EB8	SCHED_PORT_Q_CTRL_SET0 [49][4].RESERVED[31:9]	23
0xBB002EB8	SCHED_PORT_Q_CTRL_SET0 [49][4].STRICT_EN[8:8]	1
0xBB002EB8	SCHED_PORT_Q_CTRL_SET0 [49][4].RESERVED[7:7]	1
0xBB002EB8	SCHED_PORT_Q_CTRL_SET0 [49][4].WEIGHT[6:0]	7
0xBB002EBC	SCHED_PORT_Q_CTRL_SET0 [49][5].RESERVED[31:9]	23
0xBB002EBC	SCHED_PORT_Q_CTRL_SET0 [49][5].STRICT_EN[8:8]	1
0xBB002EBC	SCHED_PORT_Q_CTRL_SET0 [49][5].RESERVED[7:7]	1
0xBB002EBC	SCHED_PORT_Q_CTRL_SET0 [49][5].WEIGHT[6:0]	7
0xBB002EC0	SCHED_PORT_Q_CTRL_SET0 [49][6].RESERVED[31:9]	23
0xBB002EC0	SCHED_PORT_Q_CTRL_SET0 [49][6].STRICT_EN[8:8]	1
0xBB002EC0	SCHED_PORT_Q_CTRL_SET0 [49][6].RESERVED[7:7]	1
0xBB002EC0	SCHED_PORT_Q_CTRL_SET0 [49][6].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002EC4	SCHED_PORT_Q_CTRL_SET0 [49][7].RESERVED[31:9]	23
0xBB002EC4	SCHED_PORT_Q_CTRL_SET0 [49][7].STRICT_EN[8:8]	1
0xBB002EC4	SCHED_PORT_Q_CTRL_SET0 [49][7].RESERVED[7:7]	1
0xBB002EC4	SCHED_PORT_Q_CTRL_SET0 [49][7].WEIGHT[6:0]	7
0xBB002EC8	SCHED_PORT_Q_CTRL_SET0 [50][0].RESERVED[31:9]	23
0xBB002EC8	SCHED_PORT_Q_CTRL_SET0 [50][0].STRICT_EN[8:8]	1
0xBB002EC8	SCHED_PORT_Q_CTRL_SET0 [50][0].RESERVED[7:7]	1
0xBB002EC8	SCHED_PORT_Q_CTRL_SET0 [50][0].WEIGHT[6:0]	7
0xBB002ECC	SCHED_PORT_Q_CTRL_SET0 [50][1].RESERVED[31:9]	23
0xBB002ECC	SCHED_PORT_Q_CTRL_SET0 [50][1].STRICT_EN[8:8]	1
0xBB002ECC	SCHED_PORT_Q_CTRL_SET0 [50][1].RESERVED[7:7]	1
0xBB002ECC	SCHED_PORT_Q_CTRL_SET0 [50][1].WEIGHT[6:0]	7
0xBB002ED0	SCHED_PORT_Q_CTRL_SET0 [50][2].RESERVED[31:9]	23
0xBB002ED0	SCHED_PORT_Q_CTRL_SET0 [50][2].STRICT_EN[8:8]	1
0xBB002ED0	SCHED_PORT_Q_CTRL_SET0 [50][2].RESERVED[7:7]	1
0xBB002ED0	SCHED_PORT_Q_CTRL_SET0 [50][2].WEIGHT[6:0]	7
0xBB002ED4	SCHED_PORT_Q_CTRL_SET0 [50][3].RESERVED[31:9]	23
0xBB002ED4	SCHED_PORT_Q_CTRL_SET0 [50][3].STRICT_EN[8:8]	1
0xBB002ED4	SCHED_PORT_Q_CTRL_SET0 [50][3].RESERVED[7:7]	1
0xBB002ED4	SCHED_PORT_Q_CTRL_SET0 [50][3].WEIGHT[6:0]	7
0xBB002ED8	SCHED_PORT_Q_CTRL_SET0 [50][4].RESERVED[31:9]	23
0xBB002ED8	SCHED_PORT_Q_CTRL_SET0 [50][4].STRICT_EN[8:8]	1
0xBB002ED8	SCHED_PORT_Q_CTRL_SET0 [50][4].RESERVED[7:7]	1
0xBB002ED8	SCHED_PORT_Q_CTRL_SET0 [50][4].WEIGHT[6:0]	7
0xBB002EDC	SCHED_PORT_Q_CTRL_SET0 [50][5].RESERVED[31:9]	23
0xBB002EDC	SCHED_PORT_Q_CTRL_SET0 [50][5].STRICT_EN[8:8]	1
0xBB002EDC	SCHED_PORT_Q_CTRL_SET0 [50][5].RESERVED[7:7]	1
0xBB002EDC	SCHED_PORT_Q_CTRL_SET0 [50][5].WEIGHT[6:0]	7
0xBB002EE0	SCHED_PORT_Q_CTRL_SET0 [50][6].RESERVED[31:9]	23
0xBB002EE0	SCHED_PORT_Q_CTRL_SET0 [50][6].STRICT_EN[8:8]	1
0xBB002EE0	SCHED_PORT_Q_CTRL_SET0 [50][6].RESERVED[7:7]	1
0xBB002EE0	SCHED_PORT_Q_CTRL_SET0 [50][6].WEIGHT[6:0]	7
0xBB002EE4	SCHED_PORT_Q_CTRL_SET0 [50][7].RESERVED[31:9]	23
0xBB002EE4	SCHED_PORT_Q_CTRL_SET0 [50][7].STRICT_EN[8:8]	1
0xBB002EE4	SCHED_PORT_Q_CTRL_SET0 [50][7].RESERVED[7:7]	1
0xBB002EE4	SCHED_PORT_Q_CTRL_SET0 [50][7].WEIGHT[6:0]	7
0xBB002EE8	SCHED_PORT_Q_CTRL_SET0 [51][0].RESERVED[31:9]	23
0xBB002EE8	SCHED_PORT_Q_CTRL_SET0 [51][0].STRICT_EN[8:8]	1
0xBB002EE8	SCHED_PORT_Q_CTRL_SET0 [51][0].RESERVED[7:7]	1
0xBB002EE8	SCHED_PORT_Q_CTRL_SET0 [51][0].WEIGHT[6:0]	7
0xBB002EEC	SCHED_PORT_Q_CTRL_SET0 [51][1].RESERVED[31:9]	23
0xBB002EEC	SCHED_PORT_Q_CTRL_SET0 [51][1].STRICT_EN[8:8]	1
0xBB002EEC	SCHED_PORT_Q_CTRL_SET0 [51][1].RESERVED[7:7]	1
0xBB002EEC	SCHED_PORT_Q_CTRL_SET0 [51][1].WEIGHT[6:0]	7
0xBB002EF0	SCHED_PORT_Q_CTRL_SET0 [51][2].RESERVED[31:9]	23
0xBB002EF0	SCHED_PORT_Q_CTRL_SET0 [51][2].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002EF0	SCHED_PORT_Q_CTRL_SET0 [51][2].RESERVED[7:7]	1
0xBB002EF0	SCHED_PORT_Q_CTRL_SET0 [51][2].WEIGHT[6:0]	7
0xBB002EF4	SCHED_PORT_Q_CTRL_SET0 [51][3].RESERVED[31:9]	23
0xBB002EF4	SCHED_PORT_Q_CTRL_SET0 [51][3].STRICT_EN[8:8]	1
0xBB002EF4	SCHED_PORT_Q_CTRL_SET0 [51][3].RESERVED[7:7]	1
0xBB002EF4	SCHED_PORT_Q_CTRL_SET0 [51][3].WEIGHT[6:0]	7
0xBB002EF8	SCHED_PORT_Q_CTRL_SET0 [51][4].RESERVED[31:9]	23
0xBB002EF8	SCHED_PORT_Q_CTRL_SET0 [51][4].STRICT_EN[8:8]	1
0xBB002EF8	SCHED_PORT_Q_CTRL_SET0 [51][4].RESERVED[7:7]	1
0xBB002EF8	SCHED_PORT_Q_CTRL_SET0 [51][4].WEIGHT[6:0]	7
0xBB002EFC	SCHED_PORT_Q_CTRL_SET0 [51][5].RESERVED[31:9]	23
0xBB002EFC	SCHED_PORT_Q_CTRL_SET0 [51][5].STRICT_EN[8:8]	1
0xBB002EFC	SCHED_PORT_Q_CTRL_SET0 [51][5].RESERVED[7:7]	1
0xBB002EFC	SCHED_PORT_Q_CTRL_SET0 [51][5].WEIGHT[6:0]	7
0xBB002F00	SCHED_PORT_Q_CTRL_SET0 [51][6].RESERVED[31:9]	23
0xBB002F00	SCHED_PORT_Q_CTRL_SET0 [51][6].STRICT_EN[8:8]	1
0xBB002F00	SCHED_PORT_Q_CTRL_SET0 [51][6].RESERVED[7:7]	1
0xBB002F00	SCHED_PORT_Q_CTRL_SET0 [51][6].WEIGHT[6:0]	7
0xBB002F04	SCHED_PORT_Q_CTRL_SET0 [51][7].RESERVED[31:9]	23
0xBB002F04	SCHED_PORT_Q_CTRL_SET0 [51][7].STRICT_EN[8:8]	1
0xBB002F04	SCHED_PORT_Q_CTRL_SET0 [51][7].RESERVED[7:7]	1
0xBB002F04	SCHED_PORT_Q_CTRL_SET0 [51][7].WEIGHT[6:0]	7
0xBB002F08	SCHED_PORT_Q_CTRL_SET1 [52][0].RESERVED[31:9]	23
0xBB002F08	SCHED_PORT_Q_CTRL_SET1 [52][0].STRICT_EN[8:8]	1
0xBB002F08	SCHED_PORT_Q_CTRL_SET1 [52][0].RESERVED[7:7]	1
0xBB002F08	SCHED_PORT_Q_CTRL_SET1 [52][0].WEIGHT[6:0]	7
0xBB002F0C	SCHED_PORT_Q_CTRL_SET1 [52][1].RESERVED[31:9]	23
0xBB002F0C	SCHED_PORT_Q_CTRL_SET1 [52][1].STRICT_EN[8:8]	1
0xBB002F0C	SCHED_PORT_Q_CTRL_SET1 [52][1].RESERVED[7:7]	1
0xBB002F0C	SCHED_PORT_Q_CTRL_SET1 [52][1].WEIGHT[6:0]	7
0xBB002F10	SCHED_PORT_Q_CTRL_SET1 [52][2].RESERVED[31:9]	23
0xBB002F10	SCHED_PORT_Q_CTRL_SET1 [52][2].STRICT_EN[8:8]	1
0xBB002F10	SCHED_PORT_Q_CTRL_SET1 [52][2].RESERVED[7:7]	1
0xBB002F10	SCHED_PORT_Q_CTRL_SET1 [52][2].WEIGHT[6:0]	7
0xBB002F14	SCHED_PORT_Q_CTRL_SET1 [52][3].RESERVED[31:9]	23
0xBB002F14	SCHED_PORT_Q_CTRL_SET1 [52][3].STRICT_EN[8:8]	1
0xBB002F14	SCHED_PORT_Q_CTRL_SET1 [52][3].RESERVED[7:7]	1
0xBB002F14	SCHED_PORT_Q_CTRL_SET1 [52][3].WEIGHT[6:0]	7
0xBB002F18	SCHED_PORT_Q_CTRL_SET1 [52][4].RESERVED[31:9]	23
0xBB002F18	SCHED_PORT_Q_CTRL_SET1 [52][4].STRICT_EN[8:8]	1
0xBB002F18	SCHED_PORT_Q_CTRL_SET1 [52][4].RESERVED[7:7]	1
0xBB002F18	SCHED_PORT_Q_CTRL_SET1 [52][4].WEIGHT[6:0]	7
0xBB002F1C	SCHED_PORT_Q_CTRL_SET1 [52][5].RESERVED[31:9]	23
0xBB002F1C	SCHED_PORT_Q_CTRL_SET1 [52][5].STRICT_EN[8:8]	1
0xBB002F1C	SCHED_PORT_Q_CTRL_SET1 [52][5].RESERVED[7:7]	1
0xBB002F1C	SCHED_PORT_Q_CTRL_SET1 [52][5].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002F20	SCHED_PORT_Q_CTRL_SET1 [52][6].RESERVED[31:9]	23
0xBB002F20	SCHED_PORT_Q_CTRL_SET1 [52][6].STRICT_EN[8:8]	1
0xBB002F20	SCHED_PORT_Q_CTRL_SET1 [52][6].RESERVED[7:7]	1
0xBB002F20	SCHED_PORT_Q_CTRL_SET1 [52][6].WEIGHT[6:0]	7
0xBB002F24	SCHED_PORT_Q_CTRL_SET1 [52][7].RESERVED[31:9]	23
0xBB002F24	SCHED_PORT_Q_CTRL_SET1 [52][7].STRICT_EN[8:8]	1
0xBB002F24	SCHED_PORT_Q_CTRL_SET1 [52][7].RESERVED[7:7]	1
0xBB002F24	SCHED_PORT_Q_CTRL_SET1 [52][7].WEIGHT[6:0]	7
0xBB002F28	SCHED_PORT_Q_CTRL_SET1 [52][8].RESERVED[31:9]	23
0xBB002F28	SCHED_PORT_Q_CTRL_SET1 [52][8].STRICT_EN[8:8]	1
0xBB002F28	SCHED_PORT_Q_CTRL_SET1 [52][8].RESERVED[7:7]	1
0xBB002F28	SCHED_PORT_Q_CTRL_SET1 [52][8].WEIGHT[6:0]	7
0xBB002F2C	SCHED_PORT_Q_CTRL_SET1 [52][9].RESERVED[31:9]	23
0xBB002F2C	SCHED_PORT_Q_CTRL_SET1 [52][9].STRICT_EN[8:8]	1
0xBB002F2C	SCHED_PORT_Q_CTRL_SET1 [52][9].RESERVED[7:7]	1
0xBB002F2C	SCHED_PORT_Q_CTRL_SET1 [52][9].WEIGHT[6:0]	7
0xBB002F30	SCHED_PORT_Q_CTRL_SET1 [52][10].RESERVED[31:9]	23
0xBB002F30	SCHED_PORT_Q_CTRL_SET1 [52][10].STRICT_EN[8:8]	1
0xBB002F30	SCHED_PORT_Q_CTRL_SET1 [52][10].RESERVED[7:7]	1
0xBB002F30	SCHED_PORT_Q_CTRL_SET1 [52][10].WEIGHT[6:0]	7
0xBB002F34	SCHED_PORT_Q_CTRL_SET1 [52][11].RESERVED[31:9]	23
0xBB002F34	SCHED_PORT_Q_CTRL_SET1 [52][11].STRICT_EN[8:8]	1
0xBB002F34	SCHED_PORT_Q_CTRL_SET1 [52][11].RESERVED[7:7]	1
0xBB002F34	SCHED_PORT_Q_CTRL_SET1 [52][11].WEIGHT[6:0]	7
0xBB002F38	SCHED_PORT_Q_CTRL_SET1 [53][0].RESERVED[31:9]	23
0xBB002F38	SCHED_PORT_Q_CTRL_SET1 [53][0].STRICT_EN[8:8]	1
0xBB002F38	SCHED_PORT_Q_CTRL_SET1 [53][0].RESERVED[7:7]	1
0xBB002F38	SCHED_PORT_Q_CTRL_SET1 [53][0].WEIGHT[6:0]	7
0xBB002F3C	SCHED_PORT_Q_CTRL_SET1 [53][1].RESERVED[31:9]	23
0xBB002F3C	SCHED_PORT_Q_CTRL_SET1 [53][1].STRICT_EN[8:8]	1
0xBB002F3C	SCHED_PORT_Q_CTRL_SET1 [53][1].RESERVED[7:7]	1
0xBB002F3C	SCHED_PORT_Q_CTRL_SET1 [53][1].WEIGHT[6:0]	7
0xBB002F40	SCHED_PORT_Q_CTRL_SET1 [53][2].RESERVED[31:9]	23
0xBB002F40	SCHED_PORT_Q_CTRL_SET1 [53][2].STRICT_EN[8:8]	1
0xBB002F40	SCHED_PORT_Q_CTRL_SET1 [53][2].RESERVED[7:7]	1
0xBB002F40	SCHED_PORT_Q_CTRL_SET1 [53][2].WEIGHT[6:0]	7
0xBB002F44	SCHED_PORT_Q_CTRL_SET1 [53][3].RESERVED[31:9]	23
0xBB002F44	SCHED_PORT_Q_CTRL_SET1 [53][3].STRICT_EN[8:8]	1
0xBB002F44	SCHED_PORT_Q_CTRL_SET1 [53][3].RESERVED[7:7]	1
0xBB002F44	SCHED_PORT_Q_CTRL_SET1 [53][3].WEIGHT[6:0]	7
0xBB002F48	SCHED_PORT_Q_CTRL_SET1 [53][4].RESERVED[31:9]	23
0xBB002F48	SCHED_PORT_Q_CTRL_SET1 [53][4].STRICT_EN[8:8]	1
0xBB002F48	SCHED_PORT_Q_CTRL_SET1 [53][4].RESERVED[7:7]	1
0xBB002F48	SCHED_PORT_Q_CTRL_SET1 [53][4].WEIGHT[6:0]	7
0xBB002F4C	SCHED_PORT_Q_CTRL_SET1 [53][5].RESERVED[31:9]	23
0xBB002F4C	SCHED_PORT_Q_CTRL_SET1 [53][5].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002F4C	SCHED_PORT_Q_CTRL_SET1 [53][5].RESERVED[7:7]	1
0xBB002F4C	SCHED_PORT_Q_CTRL_SET1 [53][5].WEIGHT[6:0]	7
0xBB002F50	SCHED_PORT_Q_CTRL_SET1 [53][6].RESERVED[31:9]	23
0xBB002F50	SCHED_PORT_Q_CTRL_SET1 [53][6].STRICT_EN[8:8]	1
0xBB002F50	SCHED_PORT_Q_CTRL_SET1 [53][6].RESERVED[7:7]	1
0xBB002F50	SCHED_PORT_Q_CTRL_SET1 [53][6].WEIGHT[6:0]	7
0xBB002F54	SCHED_PORT_Q_CTRL_SET1 [53][7].RESERVED[31:9]	23
0xBB002F54	SCHED_PORT_Q_CTRL_SET1 [53][7].STRICT_EN[8:8]	1
0xBB002F54	SCHED_PORT_Q_CTRL_SET1 [53][7].RESERVED[7:7]	1
0xBB002F54	SCHED_PORT_Q_CTRL_SET1 [53][7].WEIGHT[6:0]	7
0xBB002F58	SCHED_PORT_Q_CTRL_SET1 [53][8].RESERVED[31:9]	23
0xBB002F58	SCHED_PORT_Q_CTRL_SET1 [53][8].STRICT_EN[8:8]	1
0xBB002F58	SCHED_PORT_Q_CTRL_SET1 [53][8].RESERVED[7:7]	1
0xBB002F58	SCHED_PORT_Q_CTRL_SET1 [53][8].WEIGHT[6:0]	7
0xBB002F5C	SCHED_PORT_Q_CTRL_SET1 [53][9].RESERVED[31:9]	23
0xBB002F5C	SCHED_PORT_Q_CTRL_SET1 [53][9].STRICT_EN[8:8]	1
0xBB002F5C	SCHED_PORT_Q_CTRL_SET1 [53][9].RESERVED[7:7]	1
0xBB002F5C	SCHED_PORT_Q_CTRL_SET1 [53][9].WEIGHT[6:0]	7
0xBB002F60	SCHED_PORT_Q_CTRL_SET1 [53][10].RESERVED[31:9]	23
0xBB002F60	SCHED_PORT_Q_CTRL_SET1 [53][10].STRICT_EN[8:8]	1
0xBB002F60	SCHED_PORT_Q_CTRL_SET1 [53][10].RESERVED[7:7]	1
0xBB002F60	SCHED_PORT_Q_CTRL_SET1 [53][10].WEIGHT[6:0]	7
0xBB002F64	SCHED_PORT_Q_CTRL_SET1 [53][11].RESERVED[31:9]	23
0xBB002F64	SCHED_PORT_Q_CTRL_SET1 [53][11].STRICT_EN[8:8]	1
0xBB002F64	SCHED_PORT_Q_CTRL_SET1 [53][11].RESERVED[7:7]	1
0xBB002F64	SCHED_PORT_Q_CTRL_SET1 [53][11].WEIGHT[6:0]	7
0xBB002F68	SCHED_PORT_Q_CTRL_SET1 [54][0].RESERVED[31:9]	23
0xBB002F68	SCHED_PORT_Q_CTRL_SET1 [54][0].STRICT_EN[8:8]	1
0xBB002F68	SCHED_PORT_Q_CTRL_SET1 [54][0].RESERVED[7:7]	1
0xBB002F68	SCHED_PORT_Q_CTRL_SET1 [54][0].WEIGHT[6:0]	7
0xBB002F6C	SCHED_PORT_Q_CTRL_SET1 [54][1].RESERVED[31:9]	23
0xBB002F6C	SCHED_PORT_Q_CTRL_SET1 [54][1].STRICT_EN[8:8]	1
0xBB002F6C	SCHED_PORT_Q_CTRL_SET1 [54][1].RESERVED[7:7]	1
0xBB002F6C	SCHED_PORT_Q_CTRL_SET1 [54][1].WEIGHT[6:0]	7
0xBB002F70	SCHED_PORT_Q_CTRL_SET1 [54][2].RESERVED[31:9]	23
0xBB002F70	SCHED_PORT_Q_CTRL_SET1 [54][2].STRICT_EN[8:8]	1
0xBB002F70	SCHED_PORT_Q_CTRL_SET1 [54][2].RESERVED[7:7]	1
0xBB002F70	SCHED_PORT_Q_CTRL_SET1 [54][2].WEIGHT[6:0]	7
0xBB002F74	SCHED_PORT_Q_CTRL_SET1 [54][3].RESERVED[31:9]	23
0xBB002F74	SCHED_PORT_Q_CTRL_SET1 [54][3].STRICT_EN[8:8]	1
0xBB002F74	SCHED_PORT_Q_CTRL_SET1 [54][3].RESERVED[7:7]	1
0xBB002F74	SCHED_PORT_Q_CTRL_SET1 [54][3].WEIGHT[6:0]	7
0xBB002F78	SCHED_PORT_Q_CTRL_SET1 [54][4].RESERVED[31:9]	23
0xBB002F78	SCHED_PORT_Q_CTRL_SET1 [54][4].STRICT_EN[8:8]	1
0xBB002F78	SCHED_PORT_Q_CTRL_SET1 [54][4].RESERVED[7:7]	1
0xBB002F78	SCHED_PORT_Q_CTRL_SET1 [54][4].WEIGHT[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002F7C	SCHED_PORT_Q_CTRL_SET1 [54][5].RESERVED[31:9]	23
0xBB002F7C	SCHED_PORT_Q_CTRL_SET1 [54][5].STRICT_EN[8:8]	1
0xBB002F7C	SCHED_PORT_Q_CTRL_SET1 [54][5].RESERVED[7:7]	1
0xBB002F7C	SCHED_PORT_Q_CTRL_SET1 [54][5].WEIGHT[6:0]	7
0xBB002F80	SCHED_PORT_Q_CTRL_SET1 [54][6].RESERVED[31:9]	23
0xBB002F80	SCHED_PORT_Q_CTRL_SET1 [54][6].STRICT_EN[8:8]	1
0xBB002F80	SCHED_PORT_Q_CTRL_SET1 [54][6].RESERVED[7:7]	1
0xBB002F80	SCHED_PORT_Q_CTRL_SET1 [54][6].WEIGHT[6:0]	7
0xBB002F84	SCHED_PORT_Q_CTRL_SET1 [54][7].RESERVED[31:9]	23
0xBB002F84	SCHED_PORT_Q_CTRL_SET1 [54][7].STRICT_EN[8:8]	1
0xBB002F84	SCHED_PORT_Q_CTRL_SET1 [54][7].RESERVED[7:7]	1
0xBB002F84	SCHED_PORT_Q_CTRL_SET1 [54][7].WEIGHT[6:0]	7
0xBB002F88	SCHED_PORT_Q_CTRL_SET1 [54][8].RESERVED[31:9]	23
0xBB002F88	SCHED_PORT_Q_CTRL_SET1 [54][8].STRICT_EN[8:8]	1
0xBB002F88	SCHED_PORT_Q_CTRL_SET1 [54][8].RESERVED[7:7]	1
0xBB002F88	SCHED_PORT_Q_CTRL_SET1 [54][8].WEIGHT[6:0]	7
0xBB002F8C	SCHED_PORT_Q_CTRL_SET1 [54][9].RESERVED[31:9]	23
0xBB002F8C	SCHED_PORT_Q_CTRL_SET1 [54][9].STRICT_EN[8:8]	1
0xBB002F8C	SCHED_PORT_Q_CTRL_SET1 [54][9].RESERVED[7:7]	1
0xBB002F8C	SCHED_PORT_Q_CTRL_SET1 [54][9].WEIGHT[6:0]	7
0xBB002F90	SCHED_PORT_Q_CTRL_SET1 [54][10].RESERVED[31:9]	23
0xBB002F90	SCHED_PORT_Q_CTRL_SET1 [54][10].STRICT_EN[8:8]	1
0xBB002F90	SCHED_PORT_Q_CTRL_SET1 [54][10].RESERVED[7:7]	1
0xBB002F90	SCHED_PORT_Q_CTRL_SET1 [54][10].WEIGHT[6:0]	7
0xBB002F94	SCHED_PORT_Q_CTRL_SET1 [54][11].RESERVED[31:9]	23
0xBB002F94	SCHED_PORT_Q_CTRL_SET1 [54][11].STRICT_EN[8:8]	1
0xBB002F94	SCHED_PORT_Q_CTRL_SET1 [54][11].RESERVED[7:7]	1
0xBB002F94	SCHED_PORT_Q_CTRL_SET1 [54][11].WEIGHT[6:0]	7
0xBB002F98	SCHED_PORT_Q_CTRL_SET1 [55][0].RESERVED[31:9]	23
0xBB002F98	SCHED_PORT_Q_CTRL_SET1 [55][0].STRICT_EN[8:8]	1
0xBB002F98	SCHED_PORT_Q_CTRL_SET1 [55][0].RESERVED[7:7]	1
0xBB002F98	SCHED_PORT_Q_CTRL_SET1 [55][0].WEIGHT[6:0]	7
0xBB002F9C	SCHED_PORT_Q_CTRL_SET1 [55][1].RESERVED[31:9]	23
0xBB002F9C	SCHED_PORT_Q_CTRL_SET1 [55][1].STRICT_EN[8:8]	1
0xBB002F9C	SCHED_PORT_Q_CTRL_SET1 [55][1].RESERVED[7:7]	1
0xBB002F9C	SCHED_PORT_Q_CTRL_SET1 [55][1].WEIGHT[6:0]	7
0xBB002FA0	SCHED_PORT_Q_CTRL_SET1 [55][2].RESERVED[31:9]	23
0xBB002FA0	SCHED_PORT_Q_CTRL_SET1 [55][2].STRICT_EN[8:8]	1
0xBB002FA0	SCHED_PORT_Q_CTRL_SET1 [55][2].RESERVED[7:7]	1
0xBB002FA0	SCHED_PORT_Q_CTRL_SET1 [55][2].WEIGHT[6:0]	7
0xBB002FA4	SCHED_PORT_Q_CTRL_SET1 [55][3].RESERVED[31:9]	23
0xBB002FA4	SCHED_PORT_Q_CTRL_SET1 [55][3].STRICT_EN[8:8]	1
0xBB002FA4	SCHED_PORT_Q_CTRL_SET1 [55][3].RESERVED[7:7]	1
0xBB002FA4	SCHED_PORT_Q_CTRL_SET1 [55][3].WEIGHT[6:0]	7
0xBB002FA8	SCHED_PORT_Q_CTRL_SET1 [55][4].RESERVED[31:9]	23
0xBB002FA8	SCHED_PORT_Q_CTRL_SET1 [55][4].STRICT_EN[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002FA8	SCHED_PORT_Q_CTRL_SET1 [55][4].RESERVED[7:7]	1
0xBB002FA8	SCHED_PORT_Q_CTRL_SET1 [55][4].WEIGHT[6:0]	7
0xBB002FAC	SCHED_PORT_Q_CTRL_SET1 [55][5].RESERVED[31:9]	23
0xBB002FAC	SCHED_PORT_Q_CTRL_SET1 [55][5].STRICT_EN[8:8]	1
0xBB002FAC	SCHED_PORT_Q_CTRL_SET1 [55][5].RESERVED[7:7]	1
0xBB002FAC	SCHED_PORT_Q_CTRL_SET1 [55][5].WEIGHT[6:0]	7
0xBB002FB0	SCHED_PORT_Q_CTRL_SET1 [55][6].RESERVED[31:9]	23
0xBB002FB0	SCHED_PORT_Q_CTRL_SET1 [55][6].STRICT_EN[8:8]	1
0xBB002FB0	SCHED_PORT_Q_CTRL_SET1 [55][6].RESERVED[7:7]	1
0xBB002FB0	SCHED_PORT_Q_CTRL_SET1 [55][6].WEIGHT[6:0]	7
0xBB002FB4	SCHED_PORT_Q_CTRL_SET1 [55][7].RESERVED[31:9]	23
0xBB002FB4	SCHED_PORT_Q_CTRL_SET1 [55][7].STRICT_EN[8:8]	1
0xBB002FB4	SCHED_PORT_Q_CTRL_SET1 [55][7].RESERVED[7:7]	1
0xBB002FB4	SCHED_PORT_Q_CTRL_SET1 [55][7].WEIGHT[6:0]	7
0xBB002FB8	SCHED_PORT_Q_CTRL_SET1 [55][8].RESERVED[31:9]	23
0xBB002FB8	SCHED_PORT_Q_CTRL_SET1 [55][8].STRICT_EN[8:8]	1
0xBB002FB8	SCHED_PORT_Q_CTRL_SET1 [55][8].RESERVED[7:7]	1
0xBB002FB8	SCHED_PORT_Q_CTRL_SET1 [55][8].WEIGHT[6:0]	7
0xBB002FBC	SCHED_PORT_Q_CTRL_SET1 [55][9].RESERVED[31:9]	23
0xBB002FBC	SCHED_PORT_Q_CTRL_SET1 [55][9].STRICT_EN[8:8]	1
0xBB002FBC	SCHED_PORT_Q_CTRL_SET1 [55][9].RESERVED[7:7]	1
0xBB002FBC	SCHED_PORT_Q_CTRL_SET1 [55][9].WEIGHT[6:0]	7
0xBB002FC0	SCHED_PORT_Q_CTRL_SET1 [55][10].RESERVED[31:9]	23
0xBB002FC0	SCHED_PORT_Q_CTRL_SET1 [55][10].STRICT_EN[8:8]	1
0xBB002FC0	SCHED_PORT_Q_CTRL_SET1 [55][10].RESERVED[7:7]	1
0xBB002FC0	SCHED_PORT_Q_CTRL_SET1 [55][10].WEIGHT[6:0]	7
0xBB002FC4	SCHED_PORT_Q_CTRL_SET1 [55][11].RESERVED[31:9]	23
0xBB002FC4	SCHED_PORT_Q_CTRL_SET1 [55][11].STRICT_EN[8:8]	1
0xBB002FC4	SCHED_PORT_Q_CTRL_SET1 [55][11].RESERVED[7:7]	1
0xBB002FC4	SCHED_PORT_Q_CTRL_SET1 [55][11].WEIGHT[6:0]	7
0xBB002884	SC_DRAIN_OUT_THR.RESERVED[31:29]	3
0xBB002884	SC_DRAIN_OUT_THR.DRAIN_OUT_THR_H[28:16]	13
0xBB002884	SC_DRAIN_OUT_THR.RESERVED[15:13]	3
0xBB002884	SC_DRAIN_OUT_THR.DRAIN_OUT_THR[12:0]	13
0xBB006014	SC_PORT_TIMER [0].RESERVED[31:24]	8
0xBB006014	SC_PORT_TIMER [0].CNGST_TMR_H[23:20]	4
0xBB006014	SC_PORT_TIMER [0].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006014	SC_PORT_TIMER [0].RESERVED[15:8]	8
0xBB006014	SC_PORT_TIMER [0].CNGST_TMR[7:4]	4
0xBB006014	SC_PORT_TIMER [0].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006094	SC_PORT_TIMER [1].RESERVED[31:24]	8
0xBB006094	SC_PORT_TIMER [1].CNGST_TMR_H[23:20]	4
0xBB006094	SC_PORT_TIMER [1].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006094	SC_PORT_TIMER [1].RESERVED[15:8]	8
0xBB006094	SC_PORT_TIMER [1].CNGST_TMR[7:4]	4
0xBB006094	SC_PORT_TIMER [1].CNGST_SUST_TMR_LMT[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006114	SC_PORT_TIMER [2].RESERVED[31:24]	8
0xBB006114	SC_PORT_TIMER [2].CNGST_TMR_H[23:20]	4
0xBB006114	SC_PORT_TIMER [2].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006114	SC_PORT_TIMER [2].RESERVED[15:8]	8
0xBB006114	SC_PORT_TIMER [2].CNGST_TMR[7:4]	4
0xBB006114	SC_PORT_TIMER [2].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006194	SC_PORT_TIMER [3].RESERVED[31:24]	8
0xBB006194	SC_PORT_TIMER [3].CNGST_TMR_H[23:20]	4
0xBB006194	SC_PORT_TIMER [3].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006194	SC_PORT_TIMER [3].RESERVED[15:8]	8
0xBB006194	SC_PORT_TIMER [3].CNGST_TMR[7:4]	4
0xBB006194	SC_PORT_TIMER [3].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006214	SC_PORT_TIMER [4].RESERVED[31:24]	8
0xBB006214	SC_PORT_TIMER [4].CNGST_TMR_H[23:20]	4
0xBB006214	SC_PORT_TIMER [4].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006214	SC_PORT_TIMER [4].RESERVED[15:8]	8
0xBB006214	SC_PORT_TIMER [4].CNGST_TMR[7:4]	4
0xBB006214	SC_PORT_TIMER [4].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006294	SC_PORT_TIMER [5].RESERVED[31:24]	8
0xBB006294	SC_PORT_TIMER [5].CNGST_TMR_H[23:20]	4
0xBB006294	SC_PORT_TIMER [5].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006294	SC_PORT_TIMER [5].RESERVED[15:8]	8
0xBB006294	SC_PORT_TIMER [5].CNGST_TMR[7:4]	4
0xBB006294	SC_PORT_TIMER [5].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006314	SC_PORT_TIMER [6].RESERVED[31:24]	8
0xBB006314	SC_PORT_TIMER [6].CNGST_TMR_H[23:20]	4
0xBB006314	SC_PORT_TIMER [6].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006314	SC_PORT_TIMER [6].RESERVED[15:8]	8
0xBB006314	SC_PORT_TIMER [6].CNGST_TMR[7:4]	4
0xBB006314	SC_PORT_TIMER [6].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006394	SC_PORT_TIMER [7].RESERVED[31:24]	8
0xBB006394	SC_PORT_TIMER [7].CNGST_TMR_H[23:20]	4
0xBB006394	SC_PORT_TIMER [7].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006394	SC_PORT_TIMER [7].RESERVED[15:8]	8
0xBB006394	SC_PORT_TIMER [7].CNGST_TMR[7:4]	4
0xBB006394	SC_PORT_TIMER [7].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006414	SC_PORT_TIMER [8].RESERVED[31:24]	8
0xBB006414	SC_PORT_TIMER [8].CNGST_TMR_H[23:20]	4
0xBB006414	SC_PORT_TIMER [8].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006414	SC_PORT_TIMER [8].RESERVED[15:8]	8
0xBB006414	SC_PORT_TIMER [8].CNGST_TMR[7:4]	4
0xBB006414	SC_PORT_TIMER [8].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006494	SC_PORT_TIMER [9].RESERVED[31:24]	8
0xBB006494	SC_PORT_TIMER [9].CNGST_TMR_H[23:20]	4
0xBB006494	SC_PORT_TIMER [9].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006494	SC_PORT_TIMER [9].RESERVED[15:8]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006494	SC_PORT_TIMER [9].CNGST_TMR[7:4]	4
0xBB006494	SC_PORT_TIMER [9].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006514	SC_PORT_TIMER [10].RESERVED[31:24]	8
0xBB006514	SC_PORT_TIMER [10].CNGST_TMR_H[23:20]	4
0xBB006514	SC_PORT_TIMER [10].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006514	SC_PORT_TIMER [10].RESERVED[15:8]	8
0xBB006514	SC_PORT_TIMER [10].CNGST_TMR[7:4]	4
0xBB006514	SC_PORT_TIMER [10].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006594	SC_PORT_TIMER [11].RESERVED[31:24]	8
0xBB006594	SC_PORT_TIMER [11].CNGST_TMR_H[23:20]	4
0xBB006594	SC_PORT_TIMER [11].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006594	SC_PORT_TIMER [11].RESERVED[15:8]	8
0xBB006594	SC_PORT_TIMER [11].CNGST_TMR[7:4]	4
0xBB006594	SC_PORT_TIMER [11].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006614	SC_PORT_TIMER [12].RESERVED[31:24]	8
0xBB006614	SC_PORT_TIMER [12].CNGST_TMR_H[23:20]	4
0xBB006614	SC_PORT_TIMER [12].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006614	SC_PORT_TIMER [12].RESERVED[15:8]	8
0xBB006614	SC_PORT_TIMER [12].CNGST_TMR[7:4]	4
0xBB006614	SC_PORT_TIMER [12].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006694	SC_PORT_TIMER [13].RESERVED[31:24]	8
0xBB006694	SC_PORT_TIMER [13].CNGST_TMR_H[23:20]	4
0xBB006694	SC_PORT_TIMER [13].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006694	SC_PORT_TIMER [13].RESERVED[15:8]	8
0xBB006694	SC_PORT_TIMER [13].CNGST_TMR[7:4]	4
0xBB006694	SC_PORT_TIMER [13].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006714	SC_PORT_TIMER [14].RESERVED[31:24]	8
0xBB006714	SC_PORT_TIMER [14].CNGST_TMR_H[23:20]	4
0xBB006714	SC_PORT_TIMER [14].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006714	SC_PORT_TIMER [14].RESERVED[15:8]	8
0xBB006714	SC_PORT_TIMER [14].CNGST_TMR[7:4]	4
0xBB006714	SC_PORT_TIMER [14].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006794	SC_PORT_TIMER [15].RESERVED[31:24]	8
0xBB006794	SC_PORT_TIMER [15].CNGST_TMR_H[23:20]	4
0xBB006794	SC_PORT_TIMER [15].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006794	SC_PORT_TIMER [15].RESERVED[15:8]	8
0xBB006794	SC_PORT_TIMER [15].CNGST_TMR[7:4]	4
0xBB006794	SC_PORT_TIMER [15].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006814	SC_PORT_TIMER [16].RESERVED[31:24]	8
0xBB006814	SC_PORT_TIMER [16].CNGST_TMR_H[23:20]	4
0xBB006814	SC_PORT_TIMER [16].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006814	SC_PORT_TIMER [16].RESERVED[15:8]	8
0xBB006814	SC_PORT_TIMER [16].CNGST_TMR[7:4]	4
0xBB006814	SC_PORT_TIMER [16].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006894	SC_PORT_TIMER [17].RESERVED[31:24]	8
0xBB006894	SC_PORT_TIMER [17].CNGST_TMR_H[23:20]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006894	SC_PORT_TIMER [17].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006894	SC_PORT_TIMER [17].RESERVED[15:8]	8
0xBB006894	SC_PORT_TIMER [17].CNGST_TMR[7:4]	4
0xBB006894	SC_PORT_TIMER [17].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006914	SC_PORT_TIMER [18].RESERVED[31:24]	8
0xBB006914	SC_PORT_TIMER [18].CNGST_TMR_H[23:20]	4
0xBB006914	SC_PORT_TIMER [18].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006914	SC_PORT_TIMER [18].RESERVED[15:8]	8
0xBB006914	SC_PORT_TIMER [18].CNGST_TMR[7:4]	4
0xBB006914	SC_PORT_TIMER [18].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006994	SC_PORT_TIMER [19].RESERVED[31:24]	8
0xBB006994	SC_PORT_TIMER [19].CNGST_TMR_H[23:20]	4
0xBB006994	SC_PORT_TIMER [19].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006994	SC_PORT_TIMER [19].RESERVED[15:8]	8
0xBB006994	SC_PORT_TIMER [19].CNGST_TMR[7:4]	4
0xBB006994	SC_PORT_TIMER [19].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006A14	SC_PORT_TIMER [20].RESERVED[31:24]	8
0xBB006A14	SC_PORT_TIMER [20].CNGST_TMR_H[23:20]	4
0xBB006A14	SC_PORT_TIMER [20].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006A14	SC_PORT_TIMER [20].RESERVED[15:8]	8
0xBB006A14	SC_PORT_TIMER [20].CNGST_TMR[7:4]	4
0xBB006A14	SC_PORT_TIMER [20].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006A94	SC_PORT_TIMER [21].RESERVED[31:24]	8
0xBB006A94	SC_PORT_TIMER [21].CNGST_TMR_H[23:20]	4
0xBB006A94	SC_PORT_TIMER [21].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006A94	SC_PORT_TIMER [21].RESERVED[15:8]	8
0xBB006A94	SC_PORT_TIMER [21].CNGST_TMR[7:4]	4
0xBB006A94	SC_PORT_TIMER [21].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006B14	SC_PORT_TIMER [22].RESERVED[31:24]	8
0xBB006B14	SC_PORT_TIMER [22].CNGST_TMR_H[23:20]	4
0xBB006B14	SC_PORT_TIMER [22].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006B14	SC_PORT_TIMER [22].RESERVED[15:8]	8
0xBB006B14	SC_PORT_TIMER [22].CNGST_TMR[7:4]	4
0xBB006B14	SC_PORT_TIMER [22].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006B94	SC_PORT_TIMER [23].RESERVED[31:24]	8
0xBB006B94	SC_PORT_TIMER [23].CNGST_TMR_H[23:20]	4
0xBB006B94	SC_PORT_TIMER [23].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006B94	SC_PORT_TIMER [23].RESERVED[15:8]	8
0xBB006B94	SC_PORT_TIMER [23].CNGST_TMR[7:4]	4
0xBB006B94	SC_PORT_TIMER [23].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006C14	SC_PORT_TIMER [24].RESERVED[31:24]	8
0xBB006C14	SC_PORT_TIMER [24].CNGST_TMR_H[23:20]	4
0xBB006C14	SC_PORT_TIMER [24].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006C14	SC_PORT_TIMER [24].RESERVED[15:8]	8
0xBB006C14	SC_PORT_TIMER [24].CNGST_TMR[7:4]	4
0xBB006C14	SC_PORT_TIMER [24].CNGST_SUST_TMR_LMT[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006C94	SC_PORT_TIMER [25].RESERVED[31:24]	8
0xBB006C94	SC_PORT_TIMER [25].CNGST_TMR_H[23:20]	4
0xBB006C94	SC_PORT_TIMER [25].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006C94	SC_PORT_TIMER [25].RESERVED[15:8]	8
0xBB006C94	SC_PORT_TIMER [25].CNGST_TMR[7:4]	4
0xBB006C94	SC_PORT_TIMER [25].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006D14	SC_PORT_TIMER [26].RESERVED[31:24]	8
0xBB006D14	SC_PORT_TIMER [26].CNGST_TMR_H[23:20]	4
0xBB006D14	SC_PORT_TIMER [26].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006D14	SC_PORT_TIMER [26].RESERVED[15:8]	8
0xBB006D14	SC_PORT_TIMER [26].CNGST_TMR[7:4]	4
0xBB006D14	SC_PORT_TIMER [26].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006D94	SC_PORT_TIMER [27].RESERVED[31:24]	8
0xBB006D94	SC_PORT_TIMER [27].CNGST_TMR_H[23:20]	4
0xBB006D94	SC_PORT_TIMER [27].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006D94	SC_PORT_TIMER [27].RESERVED[15:8]	8
0xBB006D94	SC_PORT_TIMER [27].CNGST_TMR[7:4]	4
0xBB006D94	SC_PORT_TIMER [27].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006E14	SC_PORT_TIMER [28].RESERVED[31:24]	8
0xBB006E14	SC_PORT_TIMER [28].CNGST_TMR_H[23:20]	4
0xBB006E14	SC_PORT_TIMER [28].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006E14	SC_PORT_TIMER [28].RESERVED[15:8]	8
0xBB006E14	SC_PORT_TIMER [28].CNGST_TMR[7:4]	4
0xBB006E14	SC_PORT_TIMER [28].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006E94	SC_PORT_TIMER [29] RESERVED[31:24]	8
0xBB006E94	SC_PORT_TIMER [29].CNGST_TMR_H[23:20]	4
0xBB006E94	SC_PORT_TIMER [29].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006E94	SC_PORT_TIMER [29].RESERVED[15:8]	8
0xBB006E94	SC_PORT_TIMER [29].CNGST_TMR[7:4]	4
0xBB006E94	SC_PORT_TIMER [29].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006F14	SC_PORT_TIMER [30].RESERVED[31:24]	8
0xBB006F14	SC_PORT_TIMER [30].CNGST_TMR_H[23:20]	4
0xBB006F14	SC_PORT_TIMER [30].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006F14	SC_PORT_TIMER [30].RESERVED[15:8]	8
0xBB006F14	SC_PORT_TIMER [30].CNGST_TMR[7:4]	4
0xBB006F14	SC_PORT_TIMER [30].CNGST_SUST_TMR_LMT[3:0]	4
0xBB006F94	SC_PORT_TIMER [31].RESERVED[31:24]	8
0xBB006F94	SC_PORT_TIMER [31].CNGST_TMR_H[23:20]	4
0xBB006F94	SC_PORT_TIMER [31].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB006F94	SC_PORT_TIMER [31].RESERVED[15:8]	8
0xBB006F94	SC_PORT_TIMER [31].CNGST_TMR[7:4]	4
0xBB006F94	SC_PORT_TIMER [31].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007014	SC_PORT_TIMER [32].RESERVED[31:24]	8
0xBB007014	SC_PORT_TIMER [32].CNGST_TMR_H[23:20]	4
0xBB007014	SC_PORT_TIMER [32].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007014	SC_PORT_TIMER [32].RESERVED[15:8]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007014	SC_PORT_TIMER [32].CNGST_TMR[7:4]	4
0xBB007014	SC_PORT_TIMER [32].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007094	SC_PORT_TIMER [33].RESERVED[31:24]	8
0xBB007094	SC_PORT_TIMER [33].CNGST_TMR_H[23:20]	4
0xBB007094	SC_PORT_TIMER [33].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007094	SC_PORT_TIMER [33].RESERVED[15:8]	8
0xBB007094	SC_PORT_TIMER [33].CNGST_TMR[7:4]	4
0xBB007094	SC_PORT_TIMER [33].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007114	SC_PORT_TIMER [34].RESERVED[31:24]	8
0xBB007114	SC_PORT_TIMER [34].CNGST_TMR_H[23:20]	4
0xBB007114	SC_PORT_TIMER [34].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007114	SC_PORT_TIMER [34].RESERVED[15:8]	8
0xBB007114	SC_PORT_TIMER [34].CNGST_TMR[7:4]	4
0xBB007114	SC_PORT_TIMER [34].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007194	SC_PORT_TIMER [35].RESERVED[31:24]	8
0xBB007194	SC_PORT_TIMER [35].CNGST_TMR_H[23:20]	4
0xBB007194	SC_PORT_TIMER [35].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007194	SC_PORT_TIMER [35].RESERVED[15:8]	8
0xBB007194	SC_PORT_TIMER [35].CNGST_TMR[7:4]	4
0xBB007194	SC_PORT_TIMER [35].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007214	SC_PORT_TIMER [36].RESERVED[31:24]	8
0xBB007214	SC_PORT_TIMER [36].CNGST_TMR_H[23:20]	4
0xBB007214	SC_PORT_TIMER [36].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007214	SC_PORT_TIMER [36].RESERVED[15:8]	8
0xBB007214	SC_PORT_TIMER [36].CNGST_TMR[7:4]	4
0xBB007214	SC_PORT_TIMER [36].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007294	SC_PORT_TIMER [37].RESERVED[31:24]	8
0xBB007294	SC_PORT_TIMER [37].CNGST_TMR_H[23:20]	4
0xBB007294	SC_PORT_TIMER [37].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007294	SC_PORT_TIMER [37].RESERVED[15:8]	8
0xBB007294	SC_PORT_TIMER [37].CNGST_TMR[7:4]	4
0xBB007294	SC_PORT_TIMER [37].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007314	SC_PORT_TIMER [38].RESERVED[31:24]	8
0xBB007314	SC_PORT_TIMER [38].CNGST_TMR_H[23:20]	4
0xBB007314	SC_PORT_TIMER [38].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007314	SC_PORT_TIMER [38].RESERVED[15:8]	8
0xBB007314	SC_PORT_TIMER [38].CNGST_TMR[7:4]	4
0xBB007314	SC_PORT_TIMER [38].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007394	SC_PORT_TIMER [39].RESERVED[31:24]	8
0xBB007394	SC_PORT_TIMER [39].CNGST_TMR_H[23:20]	4
0xBB007394	SC_PORT_TIMER [39].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007394	SC_PORT_TIMER [39].RESERVED[15:8]	8
0xBB007394	SC_PORT_TIMER [39].CNGST_TMR[7:4]	4
0xBB007394	SC_PORT_TIMER [39].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007414	SC_PORT_TIMER [40].RESERVED[31:24]	8
0xBB007414	SC_PORT_TIMER [40].CNGST_TMR_H[23:20]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007414	SC_PORT_TIMER [40].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007414	SC_PORT_TIMER [40].RESERVED[15:8]	8
0xBB007414	SC_PORT_TIMER [40].CNGST_TMR[7:4]	4
0xBB007414	SC_PORT_TIMER [40].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007494	SC_PORT_TIMER [41].RESERVED[31:24]	8
0xBB007494	SC_PORT_TIMER [41].CNGST_TMR_H[23:20]	4
0xBB007494	SC_PORT_TIMER [41].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007494	SC_PORT_TIMER [41].RESERVED[15:8]	8
0xBB007494	SC_PORT_TIMER [41].CNGST_TMR[7:4]	4
0xBB007494	SC_PORT_TIMER [41].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007514	SC_PORT_TIMER [42].RESERVED[31:24]	8
0xBB007514	SC_PORT_TIMER [42].CNGST_TMR_H[23:20]	4
0xBB007514	SC_PORT_TIMER [42].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007514	SC_PORT_TIMER [42].RESERVED[15:8]	8
0xBB007514	SC_PORT_TIMER [42].CNGST_TMR[7:4]	4
0xBB007514	SC_PORT_TIMER [42].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007594	SC_PORT_TIMER [43].RESERVED[31:24]	8
0xBB007594	SC_PORT_TIMER [43].CNGST_TMR_H[23:20]	4
0xBB007594	SC_PORT_TIMER [43].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007594	SC_PORT_TIMER [43].RESERVED[15:8]	8
0xBB007594	SC_PORT_TIMER [43].CNGST_TMR[7:4]	4
0xBB007594	SC_PORT_TIMER [43].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007614	SC_PORT_TIMER [44].RESERVED[31:24]	8
0xBB007614	SC_PORT_TIMER [44].CNGST_TMR_H[23:20]	4
0xBB007614	SC_PORT_TIMER [44].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007614	SC_PORT_TIMER [44].RESERVED[15:8]	8
0xBB007614	SC_PORT_TIMER [44].CNGST_TMR[7:4]	4
0xBB007614	SC_PORT_TIMER [44].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007694	SC_PORT_TIMER [45].RESERVED[31:24]	8
0xBB007694	SC_PORT_TIMER [45].CNGST_TMR_H[23:20]	4
0xBB007694	SC_PORT_TIMER [45].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007694	SC_PORT_TIMER [45].RESERVED[15:8]	8
0xBB007694	SC_PORT_TIMER [45].CNGST_TMR[7:4]	4
0xBB007694	SC_PORT_TIMER [45].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007714	SC_PORT_TIMER [46].RESERVED[31:24]	8
0xBB007714	SC_PORT_TIMER [46].CNGST_TMR_H[23:20]	4
0xBB007714	SC_PORT_TIMER [46].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007714	SC_PORT_TIMER [46].RESERVED[15:8]	8
0xBB007714	SC_PORT_TIMER [46].CNGST_TMR[7:4]	4
0xBB007714	SC_PORT_TIMER [46].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007794	SC_PORT_TIMER [47].RESERVED[31:24]	8
0xBB007794	SC_PORT_TIMER [47].CNGST_TMR_H[23:20]	4
0xBB007794	SC_PORT_TIMER [47].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007794	SC_PORT_TIMER [47].RESERVED[15:8]	8
0xBB007794	SC_PORT_TIMER [47].CNGST_TMR[7:4]	4
0xBB007794	SC_PORT_TIMER [47].CNGST_SUST_TMR_LMT[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007814	SC_PORT_TIMER [48].RESERVED[31:24]	8
0xBB007814	SC_PORT_TIMER [48].CNGST_TMR_H[23:20]	4
0xBB007814	SC_PORT_TIMER [48].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007814	SC_PORT_TIMER [48].RESERVED[15:8]	8
0xBB007814	SC_PORT_TIMER [48].CNGST_TMR[7:4]	4
0xBB007814	SC_PORT_TIMER [48].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007894	SC_PORT_TIMER [49].RESERVED[31:24]	8
0xBB007894	SC_PORT_TIMER [49].CNGST_TMR_H[23:20]	4
0xBB007894	SC_PORT_TIMER [49].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007894	SC_PORT_TIMER [49].RESERVED[15:8]	8
0xBB007894	SC_PORT_TIMER [49].CNGST_TMR[7:4]	4
0xBB007894	SC_PORT_TIMER [49].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007914	SC_PORT_TIMER [50].RESERVED[31:24]	8
0xBB007914	SC_PORT_TIMER [50].CNGST_TMR_H[23:20]	4
0xBB007914	SC_PORT_TIMER [50].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007914	SC_PORT_TIMER [50].RESERVED[15:8]	8
0xBB007914	SC_PORT_TIMER [50].CNGST_TMR[7:4]	4
0xBB007914	SC_PORT_TIMER [50].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007994	SC_PORT_TIMER [51].RESERVED[31:24]	8
0xBB007994	SC_PORT_TIMER [51].CNGST_TMR_H[23:20]	4
0xBB007994	SC_PORT_TIMER [51].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007994	SC_PORT_TIMER [51].RESERVED[15:8]	8
0xBB007994	SC_PORT_TIMER [51].CNGST_TMR[7:4]	4
0xBB007994	SC_PORT_TIMER [51].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007A14	SC_PORT_TIMER [52].RESERVED[31:24]	8
0xBB007A14	SC_PORT_TIMER [52].CNGST_TMR_H[23:20]	4
0xBB007A14	SC_PORT_TIMER [52].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007A14	SC_PORT_TIMER [52].RESERVED[15:8]	8
0xBB007A14	SC_PORT_TIMER [52].CNGST_TMR[7:4]	4
0xBB007A14	SC_PORT_TIMER [52].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007A94	SC_PORT_TIMER [53].RESERVED[31:24]	8
0xBB007A94	SC_PORT_TIMER [53].CNGST_TMR_H[23:20]	4
0xBB007A94	SC_PORT_TIMER [53].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007A94	SC_PORT_TIMER [53].RESERVED[15:8]	8
0xBB007A94	SC_PORT_TIMER [53].CNGST_TMR[7:4]	4
0xBB007A94	SC_PORT_TIMER [53].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007B14	SC_PORT_TIMER [54].RESERVED[31:24]	8
0xBB007B14	SC_PORT_TIMER [54].CNGST_TMR_H[23:20]	4
0xBB007B14	SC_PORT_TIMER [54].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007B14	SC_PORT_TIMER [54].RESERVED[15:8]	8
0xBB007B14	SC_PORT_TIMER [54].CNGST_TMR[7:4]	4
0xBB007B14	SC_PORT_TIMER [54].CNGST_SUST_TMR_LMT[3:0]	4
0xBB007B94	SC_PORT_TIMER [55].RESERVED[31:24]	8
0xBB007B94	SC_PORT_TIMER [55].CNGST_TMR_H[23:20]	4
0xBB007B94	SC_PORT_TIMER [55].CNGST_SUST_TMR_LMT_H[19:16]	4
0xBB007B94	SC_PORT_TIMER [55].RESERVED[15:8]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007B94	SC_PORT_TIMER [55].CNGST_TMR[7:4]	4
0xBB007B94	SC_PORT_TIMER [55].CNGST_SUST_TMR_LMT[3:0]	4
0xBB008C5C	SEC_IP_MAC_BIND_CTRL.RESERVED[31:9]	23
0xBB008C5C	SEC_IP_MAC_BIND_CTRL.LU_MIS_ACT[8:6]	3
0xBB008C5C	SEC_IP_MAC_BIND_CTRL.MISMATCH_ACT[5:3]	3
0xBB008C5C	SEC_IP_MAC_BIND_CTRL.MATCH_ACT[2:0]	3
0xBB008C60	SEC_PORT_IP_MAC_BIND_CTRL [0].RESERVED[31:2]	30
0xBB008C60	SEC_PORT_IP_MAC_BIND_CTRL [0].ARP_CHK_EN[1:1]	1
0xBB008C60	SEC_PORT_IP_MAC_BIND_CTRL [0].CHK_EN[0:0]	1
0xBB008C64	SEC_PORT_IP_MAC_BIND_CTRL [1].RESERVED[31:2]	30
0xBB008C64	SEC_PORT_IP_MAC_BIND_CTRL [1].ARP_CHK_EN[1:1]	1
0xBB008C64	SEC_PORT_IP_MAC_BIND_CTRL [1].CHK_EN[0:0]	1
0xBB008C68	SEC_PORT_IP_MAC_BIND_CTRL [2].RESERVED[31:2]	30
0xBB008C68	SEC_PORT_IP_MAC_BIND_CTRL [2].ARP_CHK_EN[1:1]	1
0xBB008C68	SEC_PORT_IP_MAC_BIND_CTRL [2].CHK_EN[0:0]	1
0xBB008C6C	SEC_PORT_IP_MAC_BIND_CTRL [3].RESERVED[31:2]	30
0xBB008C6C	SEC_PORT_IP_MAC_BIND_CTRL [3].ARP_CHK_EN[1:1]	1
0xBB008C6C	SEC_PORT_IP_MAC_BIND_CTRL [3].CHK_EN[0:0]	1
0xBB008C70	SEC_PORT_IP_MAC_BIND_CTRL [4].RESERVED[31:2]	30
0xBB008C70	SEC_PORT_IP_MAC_BIND_CTRL [4].ARP_CHK_EN[1:1]	1
0xBB008C70	SEC_PORT_IP_MAC_BIND_CTRL [4].CHK_EN[0:0]	1
0xBB008C74	SEC_PORT_IP_MAC_BIND_CTRL [5].RESERVED[31:2]	30
0xBB008C74	SEC_PORT_IP_MAC_BIND_CTRL [5].ARP_CHK_EN[1:1]	1
0xBB008C74	SEC_PORT_IP_MAC_BIND_CTRL [5].CHK_EN[0:0]	1
0xBB008C78	SEC_PORT_IP_MAC_BIND_CTRL [6].RESERVED[31:2]	30
0xBB008C78	SEC_PORT_IP_MAC_BIND_CTRL [6].ARP_CHK_EN[1:1]	1
0xBB008C78	SEC_PORT_IP_MAC_BIND_CTRL [6].CHK_EN[0:0]	1
0xBB008C7C	SEC_PORT_IP_MAC_BIND_CTRL [7].RESERVED[31:2]	30
0xBB008C7C	SEC_PORT_IP_MAC_BIND_CTRL [7].ARP_CHK_EN[1:1]	1
0xBB008C7C	SEC_PORT_IP_MAC_BIND_CTRL [7].CHK_EN[0:0]	1
0xBB008C80	SEC_PORT_IP_MAC_BIND_CTRL [8].RESERVED[31:2]	30
0xBB008C80	SEC_PORT_IP_MAC_BIND_CTRL [8].ARP_CHK_EN[1:1]	1
0xBB008C80	SEC_PORT_IP_MAC_BIND_CTRL [8].CHK_EN[0:0]	1
0xBB008C84	SEC_PORT_IP_MAC_BIND_CTRL [9].RESERVED[31:2]	30
0xBB008C84	SEC_PORT_IP_MAC_BIND_CTRL [9].ARP_CHK_EN[1:1]	1
0xBB008C84	SEC_PORT_IP_MAC_BIND_CTRL [9].CHK_EN[0:0]	1
0xBB008C88	SEC_PORT_IP_MAC_BIND_CTRL [10].RESERVED[31:2]	30
0xBB008C88	SEC_PORT_IP_MAC_BIND_CTRL [10].ARP_CHK_EN[1:1]	1
0xBB008C88	SEC_PORT_IP_MAC_BIND_CTRL [10].CHK_EN[0:0]	1
0xBB008C8C	SEC_PORT_IP_MAC_BIND_CTRL [11].RESERVED[31:2]	30
0xBB008C8C	SEC_PORT_IP_MAC_BIND_CTRL [11].ARP_CHK_EN[1:1]	1
0xBB008C8C	SEC_PORT_IP_MAC_BIND_CTRL [11].CHK_EN[0:0]	1
0xBB008C90	SEC_PORT_IP_MAC_BIND_CTRL [12].RESERVED[31:2]	30
0xBB008C90	SEC_PORT_IP_MAC_BIND_CTRL [12].ARP_CHK_EN[1:1]	1
0xBB008C90	SEC_PORT_IP_MAC_BIND_CTRL [12].CHK_EN[0:0]	1
0xBB008C94	SEC_PORT_IP_MAC_BIND_CTRL [13].RESERVED[31:2]	30

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C94	SEC_PORT_IP_MAC_BIND_CTRL [13].ARP_CHK_EN[1:1]	1
0xBB008C94	SEC_PORT_IP_MAC_BIND_CTRL [13].CHK_EN[0:0]	1
0xBB008C98	SEC_PORT_IP_MAC_BIND_CTRL [14].RESERVED[31:2]	30
0xBB008C98	SEC_PORT_IP_MAC_BIND_CTRL [14].ARP_CHK_EN[1:1]	1
0xBB008C98	SEC_PORT_IP_MAC_BIND_CTRL [14].CHK_EN[0:0]	1
0xBB008C9C	SEC_PORT_IP_MAC_BIND_CTRL [15].RESERVED[31:2]	30
0xBB008C9C	SEC_PORT_IP_MAC_BIND_CTRL [15].ARP_CHK_EN[1:1]	1
0xBB008C9C	SEC_PORT_IP_MAC_BIND_CTRL [15].CHK_EN[0:0]	1
0xBB008CA0	SEC_PORT_IP_MAC_BIND_CTRL [16].RESERVED[31:2]	30
0xBB008CA0	SEC_PORT_IP_MAC_BIND_CTRL [16].ARP_CHK_EN[1:1]	1
0xBB008CA0	SEC_PORT_IP_MAC_BIND_CTRL [16].CHK_EN[0:0]	1
0xBB008CA4	SEC_PORT_IP_MAC_BIND_CTRL [17].RESERVED[31:2]	30
0xBB008CA4	SEC_PORT_IP_MAC_BIND_CTRL [17].ARP_CHK_EN[1:1]	1
0xBB008CA4	SEC_PORT_IP_MAC_BIND_CTRL [17].CHK_EN[0:0]	1
0xBB008CA8	SEC_PORT_IP_MAC_BIND_CTRL [18].RESERVED[31:2]	30
0xBB008CA8	SEC_PORT_IP_MAC_BIND_CTRL [18].ARP_CHK_EN[1:1]	1
0xBB008CA8	SEC_PORT_IP_MAC_BIND_CTRL [18].CHK_EN[0:0]	1
0xBB008CAC	SEC_PORT_IP_MAC_BIND_CTRL [19].RESERVED[31:2]	30
0xBB008CAC	SEC_PORT_IP_MAC_BIND_CTRL [19].ARP_CHK_EN[1:1]	1
0xBB008CAC	SEC_PORT_IP_MAC_BIND_CTRL [19].CHK_EN[0:0]	1
0xBB008CB0	SEC_PORT_IP_MAC_BIND_CTRL [20].RESERVED[31:2]	30
0xBB008CB0	SEC_PORT_IP_MAC_BIND_CTRL [20].ARP_CHK_EN[1:1]	1
0xBB008CB0	SEC_PORT_IP_MAC_BIND_CTRL [20].CHK_EN[0:0]	1
0xBB008CB4	SEC_PORT_IP_MAC_BIND_CTRL [21].RESERVED[31:2]	30
0xBB008CB4	SEC_PORT_IP_MAC_BIND_CTRL [21].ARP_CHK_EN[1:1]	1
0xBB008CB4	SEC_PORT_IP_MAC_BIND_CTRL [21].CHK_EN[0:0]	1
0xBB008CB8	SEC_PORT_IP_MAC_BIND_CTRL [22].RESERVED[31:2]	30
0xBB008CB8	SEC_PORT_IP_MAC_BIND_CTRL [22].ARP_CHK_EN[1:1]	1
0xBB008CB8	SEC_PORT_IP_MAC_BIND_CTRL [22].CHK_EN[0:0]	1
0xBB008CBC	SEC_PORT_IP_MAC_BIND_CTRL [23].RESERVED[31:2]	30
0xBB008CBC	SEC_PORT_IP_MAC_BIND_CTRL [23].ARP_CHK_EN[1:1]	1
0xBB008CBC	SEC_PORT_IP_MAC_BIND_CTRL [23].CHK_EN[0:0]	1
0xBB008CC0	SEC_PORT_IP_MAC_BIND_CTRL [24].RESERVED[31:2]	30
0xBB008CC0	SEC_PORT_IP_MAC_BIND_CTRL [24].ARP_CHK_EN[1:1]	1
0xBB008CC0	SEC_PORT_IP_MAC_BIND_CTRL [24].CHK_EN[0:0]	1
0xBB008CC4	SEC_PORT_IP_MAC_BIND_CTRL [25].RESERVED[31:2]	30
0xBB008CC4	SEC_PORT_IP_MAC_BIND_CTRL [25].ARP_CHK_EN[1:1]	1
0xBB008CC4	SEC_PORT_IP_MAC_BIND_CTRL [25].CHK_EN[0:0]	1
0xBB008CC8	SEC_PORT_IP_MAC_BIND_CTRL [26].RESERVED[31:2]	30
0xBB008CC8	SEC_PORT_IP_MAC_BIND_CTRL [26].ARP_CHK_EN[1:1]	1
0xBB008CC8	SEC_PORT_IP_MAC_BIND_CTRL [26].CHK_EN[0:0]	1
0xBB008CCC	SEC_PORT_IP_MAC_BIND_CTRL [27].RESERVED[31:2]	30
0xBB008CCC	SEC_PORT_IP_MAC_BIND_CTRL [27].ARP_CHK_EN[1:1]	1
0xBB008CCC	SEC_PORT_IP_MAC_BIND_CTRL [27].CHK_EN[0:0]	1
0xBB008CD0	SEC_PORT_IP_MAC_BIND_CTRL [28].RESERVED[31:2]	30
0xBB008CD0	SEC_PORT_IP_MAC_BIND_CTRL [28].ARP_CHK_EN[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008CD0	SEC_PORT_IP_MAC_BIND_CTRL [28].CHK_EN[0:0]	1
0xBB008CD4	SEC_PORT_IP_MAC_BIND_CTRL [29].RESERVED[31:2]	30
0xBB008CD4	SEC_PORT_IP_MAC_BIND_CTRL [29].ARP_CHK_EN[1:1]	1
0xBB008CD4	SEC_PORT_IP_MAC_BIND_CTRL [29].CHK_EN[0:0]	1
0xBB008CD8	SEC_PORT_IP_MAC_BIND_CTRL [30].RESERVED[31:2]	30
0xBB008CD8	SEC_PORT_IP_MAC_BIND_CTRL [30].ARP_CHK_EN[1:1]	1
0xBB008CD8	SEC_PORT_IP_MAC_BIND_CTRL [30].CHK_EN[0:0]	1
0xBB008CDC	SEC_PORT_IP_MAC_BIND_CTRL [31].RESERVED[31:2]	30
0xBB008CDC	SEC_PORT_IP_MAC_BIND_CTRL [31].ARP_CHK_EN[1:1]	1
0xBB008CDC	SEC_PORT_IP_MAC_BIND_CTRL [31].CHK_EN[0:0]	1
0xBB008CE0	SEC_PORT_IP_MAC_BIND_CTRL [32].RESERVED[31:2]	30
0xBB008CE0	SEC_PORT_IP_MAC_BIND_CTRL [32].ARP_CHK_EN[1:1]	1
0xBB008CE0	SEC_PORT_IP_MAC_BIND_CTRL [32].CHK_EN[0:0]	1
0xBB008CE4	SEC_PORT_IP_MAC_BIND_CTRL [33].RESERVED[31:2]	30
0xBB008CE4	SEC_PORT_IP_MAC_BIND_CTRL [33].ARP_CHK_EN[1:1]	1
0xBB008CE4	SEC_PORT_IP_MAC_BIND_CTRL [33].CHK_EN[0:0]	1
0xBB008CE8	SEC_PORT_IP_MAC_BIND_CTRL [34].RESERVED[31:2]	30
0xBB008CE8	SEC_PORT_IP_MAC_BIND_CTRL [34].ARP_CHK_EN[1:1]	1
0xBB008CE8	SEC_PORT_IP_MAC_BIND_CTRL [34].CHK_EN[0:0]	1
0xBB008CEC	SEC_PORT_IP_MAC_BIND_CTRL [35].RESERVED[31:2]	30
0xBB008CEC	SEC_PORT_IP_MAC_BIND_CTRL [35].ARP_CHK_EN[1:1]	1
0xBB008CEC	SEC_PORT_IP_MAC_BIND_CTRL [35].CHK_EN[0:0]	1
0xBB008CF0	SEC_PORT_IP_MAC_BIND_CTRL [36].RESERVED[31:2]	30
0xBB008CF0	SEC_PORT_IP_MAC_BIND_CTRL [36].ARP_CHK_EN[1:1]	1
0xBB008CF0	SEC_PORT_IP_MAC_BIND_CTRL [36].CHK_EN[0:0]	1
0xBB008CF4	SEC_PORT_IP_MAC_BIND_CTRL [37].RESERVED[31:2]	30
0xBB008CF4	SEC_PORT_IP_MAC_BIND_CTRL [37].ARP_CHK_EN[1:1]	1
0xBB008CF4	SEC_PORT_IP_MAC_BIND_CTRL [37].CHK_EN[0:0]	1
0xBB008CF8	SEC_PORT_IP_MAC_BIND_CTRL [38].RESERVED[31:2]	30
0xBB008CF8	SEC_PORT_IP_MAC_BIND_CTRL [38].ARP_CHK_EN[1:1]	1
0xBB008CF8	SEC_PORT_IP_MAC_BIND_CTRL [38].CHK_EN[0:0]	1
0xBB008CFC	SEC_PORT_IP_MAC_BIND_CTRL [39].RESERVED[31:2]	30
0xBB008CFC	SEC_PORT_IP_MAC_BIND_CTRL [39].ARP_CHK_EN[1:1]	1
0xBB008CFC	SEC_PORT_IP_MAC_BIND_CTRL [39].CHK_EN[0:0]	1
0xBB008D00	SEC_PORT_IP_MAC_BIND_CTRL [40].RESERVED[31:2]	30
0xBB008D00	SEC_PORT_IP_MAC_BIND_CTRL [40].ARP_CHK_EN[1:1]	1
0xBB008D00	SEC_PORT_IP_MAC_BIND_CTRL [40].CHK_EN[0:0]	1
0xBB008D04	SEC_PORT_IP_MAC_BIND_CTRL [41].RESERVED[31:2]	30
0xBB008D04	SEC_PORT_IP_MAC_BIND_CTRL [41].ARP_CHK_EN[1:1]	1
0xBB008D04	SEC_PORT_IP_MAC_BIND_CTRL [41].CHK_EN[0:0]	1
0xBB008D08	SEC_PORT_IP_MAC_BIND_CTRL [42].RESERVED[31:2]	30
0xBB008D08	SEC_PORT_IP_MAC_BIND_CTRL [42].ARP_CHK_EN[1:1]	1
0xBB008D08	SEC_PORT_IP_MAC_BIND_CTRL [42].CHK_EN[0:0]	1
0xBB008D0C	SEC_PORT_IP_MAC_BIND_CTRL [43].RESERVED[31:2]	30
0xBB008D0C	SEC_PORT_IP_MAC_BIND_CTRL [43].ARP_CHK_EN[1:1]	1
0xBB008D0C	SEC_PORT_IP_MAC_BIND_CTRL [43].CHK_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008D10	SEC_PORT_IP_MAC_BIND_CTRL [44].RESERVED[31:2]	30
0xBB008D10	SEC_PORT_IP_MAC_BIND_CTRL [44].ARP_CHK_EN[1:1]	1
0xBB008D10	SEC_PORT_IP_MAC_BIND_CTRL [44].CHK_EN[0:0]	1
0xBB008D14	SEC_PORT_IP_MAC_BIND_CTRL [45].RESERVED[31:2]	30
0xBB008D14	SEC_PORT_IP_MAC_BIND_CTRL [45].ARP_CHK_EN[1:1]	1
0xBB008D14	SEC_PORT_IP_MAC_BIND_CTRL [45].CHK_EN[0:0]	1
0xBB008D18	SEC_PORT_IP_MAC_BIND_CTRL [46].RESERVED[31:2]	30
0xBB008D18	SEC_PORT_IP_MAC_BIND_CTRL [46].ARP_CHK_EN[1:1]	1
0xBB008D18	SEC_PORT_IP_MAC_BIND_CTRL [46].CHK_EN[0:0]	1
0xBB008D1C	SEC_PORT_IP_MAC_BIND_CTRL [47].RESERVED[31:2]	30
0xBB008D1C	SEC_PORT_IP_MAC_BIND_CTRL [47].ARP_CHK_EN[1:1]	1
0xBB008D1C	SEC_PORT_IP_MAC_BIND_CTRL [47].CHK_EN[0:0]	1
0xBB008D20	SEC_PORT_IP_MAC_BIND_CTRL [48].RESERVED[31:2]	30
0xBB008D20	SEC_PORT_IP_MAC_BIND_CTRL [48].ARP_CHK_EN[1:1]	1
0xBB008D20	SEC_PORT_IP_MAC_BIND_CTRL [48].CHK_EN[0:0]	1
0xBB008D24	SEC_PORT_IP_MAC_BIND_CTRL [49].RESERVED[31:2]	30
0xBB008D24	SEC_PORT_IP_MAC_BIND_CTRL [49].ARP_CHK_EN[1:1]	1
0xBB008D24	SEC_PORT_IP_MAC_BIND_CTRL [49].CHK_EN[0:0]	1
0xBB008D28	SEC_PORT_IP_MAC_BIND_CTRL [50].RESERVED[31:2]	30
0xBB008D28	SEC_PORT_IP_MAC_BIND_CTRL [50].ARP_CHK_EN[1:1]	1
0xBB008D28	SEC_PORT_IP_MAC_BIND_CTRL [50].CHK_EN[0:0]	1
0xBB008D2C	SEC_PORT_IP_MAC_BIND_CTRL [51].RESERVED[31:2]	30
0xBB008D2C	SEC_PORT_IP_MAC_BIND_CTRL [51].ARP_CHK_EN[1:1]	1
0xBB008D2C	SEC_PORT_IP_MAC_BIND_CTRL [51].CHK_EN[0:0]	1
0xBB008D30	SEC_PORT_IP_MAC_BIND_CTRL [52].RESERVED[31:2]	30
0xBB008D30	SEC_PORT_IP_MAC_BIND_CTRL [52].ARP_CHK_EN[1:1]	1
0xBB008D30	SEC_PORT_IP_MAC_BIND_CTRL [52].CHK_EN[0:0]	1
0xBB008D34	SEC_PORT_IP_MAC_BIND_CTRL [53].RESERVED[31:2]	30
0xBB008D34	SEC_PORT_IP_MAC_BIND_CTRL [53].ARP_CHK_EN[1:1]	1
0xBB008D34	SEC_PORT_IP_MAC_BIND_CTRL [53].CHK_EN[0:0]	1
0xBB008D38	SEC_PORT_IP_MAC_BIND_CTRL [54].RESERVED[31:2]	30
0xBB008D38	SEC_PORT_IP_MAC_BIND_CTRL [54].ARP_CHK_EN[1:1]	1
0xBB008D38	SEC_PORT_IP_MAC_BIND_CTRL [54].CHK_EN[0:0]	1
0xBB008D3C	SEC_PORT_IP_MAC_BIND_CTRL [55].RESERVED[31:2]	30
0xBB008D3C	SEC_PORT_IP_MAC_BIND_CTRL [55].ARP_CHK_EN[1:1]	1
0xBB008D3C	SEC_PORT_IP_MAC_BIND_CTRL [55].CHK_EN[0:0]	1
0xBB005640	SERDES_BC_CTRL.RESERVED[31:31]	1
0xBB005640	SERDES_BC_CTRL.SERDES_BC_EN[30:5]	26
0xBB005640	SERDES_BC_CTRL.SERDES_BC_ID[4:0]	5
0xBB005638	SERDES_INDRT_ACCESS_CTRL.RESERVED[31:18]	14
0xBB005638	SERDES_INDRT_ACCESS_CTRL.REG[17:13]	5
0xBB005638	SERDES_INDRT_ACCESS_CTRL.PAGE[12:7]	6
0xBB005638	SERDES_INDRT_ACCESS_CTRL.INDEX[6:2]	5
0xBB005638	SERDES_INDRT_ACCESS_CTRL.RWOP[1:1]	1
0xBB005638	SERDES_INDRT_ACCESS_CTRL.CMD[0:0]	1
0xBB00563C	SERDES_INDRT_DATA_CTRL.RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00563C	SERDES_INDRT_DATA_CTRL.DATA[15:0]	16
0xBB0013E0	SERDES_MISC_CTRL0.RESERVED[31:28]	4
0xBB0013E0	SERDES_MISC_CTRL0.SDS_FRC_LD[27:14]	14
0xBB0013E0	SERDES_MISC_CTRL0.SDS_ISO_ON[13:0]	14
0xBB0013E4	SERDES_MISC_CTRL1.RESERVED[31:12]	20
0xBB0013E4	SERDES_MISC_CTRL1.SEL_ORI_312P5[11:0]	12
0xBB0013CC	SERDES_MODE_CTRL [0].SERDES_IF[7:0]	8
0xBB0013CC	SERDES_MODE_CTRL [1].SERDES_IF[15:8]	8
0xBB0013CC	SERDES_MODE_CTRL [2].SERDES_IF[23:16]	8
0xBB0013CC	SERDES_MODE_CTRL [3].SERDES_IF[31:24]	8
0xBB0013D0	SERDES_MODE_CTRL [4].SERDES_IF[7:0]	8
0xBB0013D0	SERDES_MODE_CTRL [5].SERDES_IF[15:8]	8
0xBB0013D0	SERDES_MODE_CTRL [6].SERDES_IF[23:16]	8
0xBB0013D0	SERDES_MODE_CTRL [7].SERDES_IF[31:24]	8
0xBB0013D4	SERDES_MODE_CTRL [8].SERDES_IF[7:0]	8
0xBB0013D4	SERDES_MODE_CTRL [9].SERDES_IF[15:8]	8
0xBB0013D4	SERDES_MODE_CTRL [10].SERDES_IF[23:16]	8
0xBB0013D4	SERDES_MODE_CTRL [11].SERDES_IF[31:24]	8
0xBB0013D8	SERDES_MODE_CTRL [12].SERDES_IF[7:0]	8
0xBB0013D8	SERDES_MODE_CTRL [13].SERDES_IF[15:8]	8
0xBB0013DC	SERDES_UNIDIR_CTRL.RESERVED[31:26]	6
0xBB0013DC	SERDES_UNIDIR_CTRL.SDS_UNIDIR_TX_ABLE[25:12]	14
0xBB0013DC	SERDES_UNIDIR_CTRL.CFG_UNIDIR_EN_TGXR[11:0]	12
0xBB008400	SFLOW_CTRL.RESERVED[31:2]	30
0xBB008400	SFLOW_CTRL.CPU_SEL[1:1]	1
0xBB008400	SFLOW_CTRL.SMPL_SEL[0:0]	1
0xBB008404	SFLOW_PORT_RATE_CTRL [0].EGR_RATE[31:16]	16
0xBB008404	SFLOW_PORT_RATE_CTRL [0].IGR_RATE[15:0]	16
0xBB008408	SFLOW_PORT_RATE_CTRL [1].EGR_RATE[31:16]	16
0xBB008408	SFLOW_PORT_RATE_CTRL [1].IGR_RATE[15:0]	16
0xBB00840C	SFLOW_PORT_RATE_CTRL [2].EGR_RATE[31:16]	16
0xBB00840C	SFLOW_PORT_RATE_CTRL [2].IGR_RATE[15:0]	16
0xBB008410	SFLOW_PORT_RATE_CTRL [3].EGR_RATE[31:16]	16
0xBB008410	SFLOW_PORT_RATE_CTRL [3].IGR_RATE[15:0]	16
0xBB008414	SFLOW_PORT_RATE_CTRL [4].EGR_RATE[31:16]	16
0xBB008414	SFLOW_PORT_RATE_CTRL [4].IGR_RATE[15:0]	16
0xBB008418	SFLOW_PORT_RATE_CTRL [5].EGR_RATE[31:16]	16
0xBB008418	SFLOW_PORT_RATE_CTRL [5].IGR_RATE[15:0]	16
0xBB00841C	SFLOW_PORT_RATE_CTRL [6].EGR_RATE[31:16]	16
0xBB00841C	SFLOW_PORT_RATE_CTRL [6].IGR_RATE[15:0]	16
0xBB008420	SFLOW_PORT_RATE_CTRL [7].EGR_RATE[31:16]	16
0xBB008420	SFLOW_PORT_RATE_CTRL [7].IGR_RATE[15:0]	16
0xBB008424	SFLOW_PORT_RATE_CTRL [8].EGR_RATE[31:16]	16
0xBB008424	SFLOW_PORT_RATE_CTRL [8].IGR_RATE[15:0]	16
0xBB008428	SFLOW_PORT_RATE_CTRL [9].EGR_RATE[31:16]	16
0xBB008428	SFLOW_PORT_RATE_CTRL [9].IGR_RATE[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00842C	SFLOW_PORT_RATE_CTRL [10].EGR_RATE[31:16]	16
0xBB00842C	SFLOW_PORT_RATE_CTRL [10].IGR_RATE[15:0]	16
0xBB008430	SFLOW_PORT_RATE_CTRL [11].EGR_RATE[31:16]	16
0xBB008430	SFLOW_PORT_RATE_CTRL [11].IGR_RATE[15:0]	16
0xBB008434	SFLOW_PORT_RATE_CTRL [12].EGR_RATE[31:16]	16
0xBB008434	SFLOW_PORT_RATE_CTRL [12].IGR_RATE[15:0]	16
0xBB008438	SFLOW_PORT_RATE_CTRL [13].EGR_RATE[31:16]	16
0xBB008438	SFLOW_PORT_RATE_CTRL [13].IGR_RATE[15:0]	16
0xBB00843C	SFLOW_PORT_RATE_CTRL [14].EGR_RATE[31:16]	16
0xBB00843C	SFLOW_PORT_RATE_CTRL [14].IGR_RATE[15:0]	16
0xBB008440	SFLOW_PORT_RATE_CTRL [15].EGR_RATE[31:16]	16
0xBB008440	SFLOW_PORT_RATE_CTRL [15].IGR_RATE[15:0]	16
0xBB008444	SFLOW_PORT_RATE_CTRL [16].EGR_RATE[31:16]	16
0xBB008444	SFLOW_PORT_RATE_CTRL [16].IGR_RATE[15:0]	16
0xBB008448	SFLOW_PORT_RATE_CTRL [17].EGR_RATE[31:16]	16
0xBB008448	SFLOW_PORT_RATE_CTRL [17].IGR_RATE[15:0]	16
0xBB00844C	SFLOW_PORT_RATE_CTRL [18].EGR_RATE[31:16]	16
0xBB00844C	SFLOW_PORT_RATE_CTRL [18].IGR_RATE[15:0]	16
0xBB008450	SFLOW_PORT_RATE_CTRL [19].EGR_RATE[31:16]	16
0xBB008450	SFLOW_PORT_RATE_CTRL [19].IGR_RATE[15:0]	16
0xBB008454	SFLOW_PORT_RATE_CTRL [20].EGR_RATE[31:16]	16
0xBB008454	SFLOW_PORT_RATE_CTRL [20].IGR_RATE[15:0]	16
0xBB008458	SFLOW_PORT_RATE_CTRL [21].EGR_RATE[31:16]	16
0xBB008458	SFLOW_PORT_RATE_CTRL [21].IGR_RATE[15:0]	16
0xBB00845C	SFLOW_PORT_RATE_CTRL [22].EGR_RATE[31:16]	16
0xBB00845C	SFLOW_PORT_RATE_CTRL [22].IGR_RATE[15:0]	16
0xBB008460	SFLOW_PORT_RATE_CTRL [23].EGR_RATE[31:16]	16
0xBB008460	SFLOW_PORT_RATE_CTRL [23].IGR_RATE[15:0]	16
0xBB008464	SFLOW_PORT_RATE_CTRL [24].EGR_RATE[31:16]	16
0xBB008464	SFLOW_PORT_RATE_CTRL [24].IGR_RATE[15:0]	16
0xBB008468	SFLOW_PORT_RATE_CTRL [25].EGR_RATE[31:16]	16
0xBB008468	SFLOW_PORT_RATE_CTRL [25].IGR_RATE[15:0]	16
0xBB00846C	SFLOW_PORT_RATE_CTRL [26].EGR_RATE[31:16]	16
0xBB00846C	SFLOW_PORT_RATE_CTRL [26].IGR_RATE[15:0]	16
0xBB008470	SFLOW_PORT_RATE_CTRL [27].EGR_RATE[31:16]	16
0xBB008470	SFLOW_PORT_RATE_CTRL [27].IGR_RATE[15:0]	16
0xBB008474	SFLOW_PORT_RATE_CTRL [28].EGR_RATE[31:16]	16
0xBB008474	SFLOW_PORT_RATE_CTRL [28].IGR_RATE[15:0]	16
0xBB008478	SFLOW_PORT_RATE_CTRL [29].EGR_RATE[31:16]	16
0xBB008478	SFLOW_PORT_RATE_CTRL [29].IGR_RATE[15:0]	16
0xBB00847C	SFLOW_PORT_RATE_CTRL [30].EGR_RATE[31:16]	16
0xBB00847C	SFLOW_PORT_RATE_CTRL [30].IGR_RATE[15:0]	16
0xBB008480	SFLOW_PORT_RATE_CTRL [31].EGR_RATE[31:16]	16
0xBB008480	SFLOW_PORT_RATE_CTRL [31].IGR_RATE[15:0]	16
0xBB008484	SFLOW_PORT_RATE_CTRL [32].EGR_RATE[31:16]	16
0xBB008484	SFLOW_PORT_RATE_CTRL [32].IGR_RATE[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008488	SFLOW_PORT_RATE_CTRL [33].EGR_RATE[31:16]	16
0xBB008488	SFLOW_PORT_RATE_CTRL [33].IGR_RATE[15:0]	16
0xBB00848C	SFLOW_PORT_RATE_CTRL [34].EGR_RATE[31:16]	16
0xBB00848C	SFLOW_PORT_RATE_CTRL [34].IGR_RATE[15:0]	16
0xBB008490	SFLOW_PORT_RATE_CTRL [35].EGR_RATE[31:16]	16
0xBB008490	SFLOW_PORT_RATE_CTRL [35].IGR_RATE[15:0]	16
0xBB008494	SFLOW_PORT_RATE_CTRL [36].EGR_RATE[31:16]	16
0xBB008494	SFLOW_PORT_RATE_CTRL [36].IGR_RATE[15:0]	16
0xBB008498	SFLOW_PORT_RATE_CTRL [37].EGR_RATE[31:16]	16
0xBB008498	SFLOW_PORT_RATE_CTRL [37].IGR_RATE[15:0]	16
0xBB00849C	SFLOW_PORT_RATE_CTRL [38].EGR_RATE[31:16]	16
0xBB00849C	SFLOW_PORT_RATE_CTRL [38].IGR_RATE[15:0]	16
0xBB0084A0	SFLOW_PORT_RATE_CTRL [39].EGR_RATE[31:16]	16
0xBB0084A0	SFLOW_PORT_RATE_CTRL [39].IGR_RATE[15:0]	16
0xBB0084A4	SFLOW_PORT_RATE_CTRL [40].EGR_RATE[31:16]	16
0xBB0084A4	SFLOW_PORT_RATE_CTRL [40].IGR_RATE[15:0]	16
0xBB0084A8	SFLOW_PORT_RATE_CTRL [41].EGR_RATE[31:16]	16
0xBB0084A8	SFLOW_PORT_RATE_CTRL [41].IGR_RATE[15:0]	16
0xBB0084AC	SFLOW_PORT_RATE_CTRL [42].EGR_RATE[31:16]	16
0xBB0084AC	SFLOW_PORT_RATE_CTRL [42].IGR_RATE[15:0]	16
0xBB0084B0	SFLOW_PORT_RATE_CTRL [43].EGR_RATE[31:16]	16
0xBB0084B0	SFLOW_PORT_RATE_CTRL [43].IGR_RATE[15:0]	16
0xBB0084B4	SFLOW_PORT_RATE_CTRL [44].EGR_RATE[31:16]	16
0xBB0084B4	SFLOW_PORT_RATE_CTRL [44].IGR_RATE[15:0]	16
0xBB0084B8	SFLOW_PORT_RATE_CTRL [45].EGR_RATE[31:16]	16
0xBB0084B8	SFLOW_PORT_RATE_CTRL [45].IGR_RATE[15:0]	16
0xBB0084BC	SFLOW_PORT_RATE_CTRL [46].EGR_RATE[31:16]	16
0xBB0084BC	SFLOW_PORT_RATE_CTRL [46].IGR_RATE[15:0]	16
0xBB0084C0	SFLOW_PORT_RATE_CTRL [47].EGR_RATE[31:16]	16
0xBB0084C0	SFLOW_PORT_RATE_CTRL [47].IGR_RATE[15:0]	16
0xBB0084C4	SFLOW_PORT_RATE_CTRL [48].EGR_RATE[31:16]	16
0xBB0084C4	SFLOW_PORT_RATE_CTRL [48].IGR_RATE[15:0]	16
0xBB0084C8	SFLOW_PORT_RATE_CTRL [49].EGR_RATE[31:16]	16
0xBB0084C8	SFLOW_PORT_RATE_CTRL [49].IGR_RATE[15:0]	16
0xBB0084CC	SFLOW_PORT_RATE_CTRL [50].EGR_RATE[31:16]	16
0xBB0084CC	SFLOW_PORT_RATE_CTRL [50].IGR_RATE[15:0]	16
0xBB0084D0	SFLOW_PORT_RATE_CTRL [51].EGR_RATE[31:16]	16
0xBB0084D0	SFLOW_PORT_RATE_CTRL [51].IGR_RATE[15:0]	16
0xBB0084D4	SFLOW_PORT_RATE_CTRL [52].EGR_RATE[31:16]	16
0xBB0084D4	SFLOW_PORT_RATE_CTRL [52].IGR_RATE[15:0]	16
0xBB0084D8	SFLOW_PORT_RATE_CTRL [53].EGR_RATE[31:16]	16
0xBB0084D8	SFLOW_PORT_RATE_CTRL [53].IGR_RATE[15:0]	16
0xBB0084DC	SFLOW_PORT_RATE_CTRL [54].EGR_RATE[31:16]	16
0xBB0084DC	SFLOW_PORT_RATE_CTRL [54].IGR_RATE[15:0]	16
0xBB0084E0	SFLOW_PORT_RATE_CTRL [55].EGR_RATE[31:16]	16
0xBB0084E0	SFLOW_PORT_RATE_CTRL [55].IGR_RATE[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000CF0	SMI_10GPHY_POLLING_SEL0.RESERVED[31:21]	11
0xBB000CF0	SMI_10GPHY_POLLING_SEL0.INTDEV0_POLLING_10GPHY[20:16]	5
0xBB000CF0	SMI_10GPHY_POLLING_SEL0.INTREG0_POLLING_10GPHY[15:0]	16
0xBB000CF4	SMI_10GPHY_POLLING_SEL1.RESERVED[31:21]	11
0xBB000CF4	SMI_10GPHY_POLLING_SEL1.INTDEV1_POLLING_10GPHY[20:16]	5
0xBB000CF4	SMI_10GPHY_POLLING_SEL1.INTREG1_POLLING_10GPHY[15:0]	16
0xBB000CF8	SMI_10GPHY_POLLING_SEL2.RESERVED[31:25]	7
0xBB000CF8	SMI_10GPHY_POLLING_SEL2.REG0_BIT_POLLING_10GPHY[24:21]	4
0xBB000CF8	SMI_10GPHY_POLLING_SEL2.REG0_DEV_POLLING_10GPHY[20:16]	5
0xBB000CF8	SMI_10GPHY_POLLING_SEL2.REG0_REG_POLLING_10GPHY[15:0]	16
0xBB000CFC	SMI_10GPHY_POLLING_SEL3.RESERVED[31:25]	7
0xBB000CFC	SMI_10GPHY_POLLING_SEL3.REG9_BIT_POLLING_10GPHY[24:21]	4
0xBB000CFC	SMI_10GPHY_POLLING_SEL3.REG9_DEV_POLLING_10GPHY[20:16]	5
0xBB000CFC	SMI_10GPHY_POLLING_SEL3.REG9_REG_POLLING_10GPHY[15:0]	16
0xBB000D00	SMI_10GPHY_POLLING_SEL4.RESERVED[31:25]	7
0xBB000D00	SMI_10GPHY_POLLING_SEL4.REG10_BIT_POLLING_10GPHY[24:21]	4
0xBB000D00	SMI_10GPHY_POLLING_SEL4.REG10_DEV_POLLING_10GPHY[20:16]	5
0xBB000D00	SMI_10GPHY_POLLING_SEL4.REG10_REG_POLLING_10GPHY[15:0]	16
0xBB000CE8	SMI_10GPHY_RLFD_POLLING_SEL.RESERVED[31:21]	11
0xBB000CE8	SMI_10GPHY_RLFD_POLLING_SEL.RLFD_DEV_POLLING_10GPHY[20:16]	5
0xBB000CE8	SMI_10GPHY_RLFD_POLLING_SEL.RLFD_REG_POLLING_10GPHY[15:0]	16
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [0].BYPASS_ABLTY_LOCK[0:0]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [1].BYPASS_ABLTY_LOCK[1:1]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [2].BYPASS_ABLTY_LOCK[2:2]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [3].BYPASS_ABLTY_LOCK[3:3]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [4].BYPASS_ABLTY_LOCK[4:4]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [5].BYPASS_ABLTY_LOCK[5:5]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [6].BYPASS_ABLTY_LOCK[6:6]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [7].BYPASS_ABLTY_LOCK[7:7]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [8].BYPASS_ABLTY_LOCK[8:8]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [9].BYPASS_ABLTY_LOCK[9:9]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [10].BYPASS_ABLTY_LOCK[10:10]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [11].BYPASS_ABLTY_LOCK[11:11]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [12].BYPASS_ABLTY_LOCK[12:12]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [13].BYPASS_ABLTY_LOCK[13:13]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [14].BYPASS_ABLTY_LOCK[14:14]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [15].BYPASS_ABLTY_LOCK[15:15]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [16].BYPASS_ABLTY_LOCK[16:16]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [17].BYPASS_ABLTY_LOCK[17:17]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [18].BYPASS_ABLTY_LOCK[18:18]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [19].BYPASS_ABLTY_LOCK[19:19]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [20].BYPASS_ABLTY_LOCK[20:20]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [21].BYPASS_ABLTY_LOCK[21:21]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [22].BYPASS_ABLTY_LOCK[22:22]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [23].BYPASS_ABLTY_LOCK[23:23]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [24].BYPASS_ABLTY_LOCK[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [25].BYPASS_ABLTY_LOCK[25:25]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [26].BYPASS_ABLTY_LOCK[26:26]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [27].BYPASS_ABLTY_LOCK[27:27]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [28].BYPASS_ABLTY_LOCK[28:28]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [29].BYPASS_ABLTY_LOCK[29:29]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [30].BYPASS_ABLTY_LOCK[30:30]	1
0xBB000CC4	SMI_BYPASS_ABLTY_LOCK_CTRL [31].BYPASS_ABLTY_LOCK[31:31]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [32].BYPASS_ABLTY_LOCK[0:0]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [33].BYPASS_ABLTY_LOCK[1:1]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [34].BYPASS_ABLTY_LOCK[2:2]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [35].BYPASS_ABLTY_LOCK[3:3]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [36].BYPASS_ABLTY_LOCK[4:4]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [37].BYPASS_ABLTY_LOCK[5:5]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [38].BYPASS_ABLTY_LOCK[6:6]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [39].BYPASS_ABLTY_LOCK[7:7]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [40].BYPASS_ABLTY_LOCK[8:8]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [41].BYPASS_ABLTY_LOCK[9:9]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [42].BYPASS_ABLTY_LOCK[10:10]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [43].BYPASS_ABLTY_LOCK[11:11]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [44].BYPASS_ABLTY_LOCK[12:12]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [45].BYPASS_ABLTY_LOCK[13:13]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [46].BYPASS_ABLTY_LOCK[14:14]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [47].BYPASS_ABLTY_LOCK[15:15]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [48].BYPASS_ABLTY_LOCK[16:16]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [49].BYPASS_ABLTY_LOCK[17:17]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [50].BYPASS_ABLTY_LOCK[18:18]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [51].BYPASS_ABLTY_LOCK[19:19]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [52].BYPASS_ABLTY_LOCK[20:20]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [53].BYPASS_ABLTY_LOCK[21:21]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [54].BYPASS_ABLTY_LOCK[22:22]	1
0xBB000CC8	SMI_BYPASS_ABLTY_LOCK_CTRL [55].BYPASS_ABLTY_LOCK[23:23]	1
0xBB000CC0	SMI_GLB_CTRL0.RESERVED[31:28]	4
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET3_PRVTE1_POLLING[27:27]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET2_PRVTE1_POLLING[26:26]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET1_PRVTE1_POLLING[25:25]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET0_PRVTE1_POLLING[24:24]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET3_PRVTE0_POLLING[23:23]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET2_PRVTE0_POLLING[22:22]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET1_PRVTE0_POLLING[21:21]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET0_PRVTE0_POLLING[20:20]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET3_POLLING_PARK_SEL[19:19]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET2_POLLING_PARK_SEL[18:18]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET1_POLLING_PARK_SEL[17:17]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET0_POLLING_PARK_SEL[16:16]	1
0xBB000CC0	SMI_GLB_CTRL0.RESERVED[15:12]	4
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET3_FREQ_SEL[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET2_FREQ_SEL[9:8]	2
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET1_FREQ_SEL[7:6]	2
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET0_FREQ_SEL[5:4]	2
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET3_PREAMBLE_SEL[3:3]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET2_PREAMBLE_SEL[2:2]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET1_PREAMBLE_SEL[1:1]	1
0xBB000CC0	SMI_GLB_CTRL0.SMI_SET0_PREAMBLE_SEL[0:0]	1
0xBB000CBC	SMI_GLB_CTRL1.RESERVED[31:19]	13
0xBB000CBC	SMI_GLB_CTRL1.SMI_GATING_SEL[18:18]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET3_SMPLEDG_SEL[17:17]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET2_SMPLEDG_SEL[16:16]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET1_SMPLEDG_SEL[15:15]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET0_SMPLEDG_SEL[14:14]	1
0xBB000CBC	SMI_GLB_CTRL1.ABORT_ACT[13:13]	1
0xBB000CBC	SMI_GLB_CTRL1.TMOUT_ACT[12:12]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET3_WRITE_PHY_PAGE0[11:11]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET2_WRITE_PHY_PAGE0[10:10]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET1_WRITE_PHY_PAGE0[9:9]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET0_WRITE_PHY_PAGE0[8:8]	1
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET3_FMT_SEL[7:6]	2
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET2_FMT_SEL[5:4]	2
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET1_FMT_SEL[3:2]	2
0xBB000CBC	SMI_GLB_CTRL1.SMI_SET0_FMT_SEL[1:0]	2
0xBB000CEC	SMI_GPHY_POLLING_SEL.INTPAGE_POLLING_GPHY[31:16]	16
0xBB000CEC	SMI_GPHY_POLLING_SEL.INTREG_POLLING_GPHY[15:0]	16
0xBB000CE0	SMI_GPHY_RLFD_POLLING_SEL.RLFD_PAGE_POLLING_GPHY[31:16]	16
0xBB000CE0	SMI_GPHY_RLFD_POLLING_SEL.RLFD_REG_POLLING_GPHY[15:0]	16
0xBB000C14	SMI_INDRT_ACCESS_BC_PHYID_CTRL.RESERVED[31:11]	21
0xBB000C14	SMI_INDRT_ACCESS_BC_PHYID_CTRL.PORT_ID[10:5]	6
0xBB000C14	SMI_INDRT_ACCESS_BC_PHYID_CTRL.BC_PHYID[4:0]	5
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.RESERVED[31:25]	7
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.ACCESS[24:24]	1
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.MAIN_PAGE[23:11]	13
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.REG[10:6]	5
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.BROADCAST[5:5]	1
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.RWOP[4:4]	1
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.TYPE[3:2]	2
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.FAIL[1:1]	1
0xBB000C00	SMI_INDRT_ACCESS_CTRL_0.CMD[0:0]	1
0xBB000C04	SMI_INDRT_ACCESS_CTRL_1.RESERVED[31:9]	23
0xBB000C04	SMI_INDRT_ACCESS_CTRL_1.EXT_PAGE[8:0]	9
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2[0].PORT_MSK[0:0]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2[1].PORT_MSK[1:1]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2[2].PORT_MSK[2:2]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2[3].PORT_MSK[3:3]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2[4].PORT_MSK[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [5].PORT_MSK[5:5]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [6].PORT_MSK[6:6]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [7].PORT_MSK[7:7]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [8].PORT_MSK[8:8]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [9].PORT_MSK[9:9]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [10].PORT_MSK[10:10]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [11].PORT_MSK[11:11]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [12].PORT_MSK[12:12]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [13].PORT_MSK[13:13]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [14].PORT_MSK[14:14]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [15].PORT_MSK[15:15]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [16].PORT_MSK[16:16]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [17].PORT_MSK[17:17]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [18].PORT_MSK[18:18]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [19].PORT_MSK[19:19]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [20].PORT_MSK[20:20]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [21].PORT_MSK[21:21]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [22].PORT_MSK[22:22]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [23].PORT_MSK[23:23]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [24].PORT_MSK[24:24]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [25].PORT_MSK[25:25]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [26].PORT_MSK[26:26]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [27].PORT_MSK[27:27]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [28].PORT_MSK[28:28]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [29].PORT_MSK[29:29]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [30].PORT_MSK[30:30]	1
0xBB000C08	SMI_INDRT_ACCESS_CTRL_2 [31].PORT_MSK[31:31]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [32].PORT_MSK[0:0]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [33].PORT_MSK[1:1]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [34].PORT_MSK[2:2]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [35].PORT_MSK[3:3]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [36].PORT_MSK[4:4]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [37].PORT_MSK[5:5]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [38].PORT_MSK[6:6]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [39].PORT_MSK[7:7]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [40].PORT_MSK[8:8]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [41].PORT_MSK[9:9]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [42].PORT_MSK[10:10]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [43].PORT_MSK[11:11]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [44].PORT_MSK[12:12]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [45].PORT_MSK[13:13]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [46].PORT_MSK[14:14]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [47].PORT_MSK[15:15]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [48].PORT_MSK[16:16]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [49].PORT_MSK[17:17]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [50].PORT_MSK[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [51].PORT_MSK[19:19]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [52].PORT_MSK[20:20]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [53].PORT_MSK[21:21]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [54].PORT_MSK[22:22]	1
0xBB000C0C	SMI_INDRT_ACCESS_CTRL_2 [55].PORT_MSK[23:23]	1
0xBB000C10	SMI_INDRT_ACCESS_CTRL_3.READ_DATA[31:16]	16
0xBB000C10	SMI_INDRT_ACCESS_CTRL_3.WRTIE_DATA[15:0]	16
0xBB000C18	SMI_INDRT_ACCESS_MMD_CTRL.RESERVED[31:21]	11
0xBB000C18	SMI_INDRT_ACCESS_MMD_CTRL.MMD_DEVAD[20:16]	5
0xBB000C18	SMI_INDRT_ACCESS_MMD_CTRL.MMD_PEG[15:0]	16
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [0].PHY_ABLTY_GET_SEL[1:0]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [1].PHY_ABLTY_GET_SEL[3:2]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [2].PHY_ABLTY_GET_SEL[5:4]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [3].PHY_ABLTY_GET_SEL[7:6]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [4].PHY_ABLTY_GET_SEL[9:8]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [5].PHY_ABLTY_GET_SEL[11:10]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [6].PHY_ABLTY_GET_SEL[13:12]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [7].PHY_ABLTY_GET_SEL[15:14]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [8].PHY_ABLTY_GET_SEL[17:16]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [9].PHY_ABLTY_GET_SEL[19:18]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [10].PHY_ABLTY_GET_SEL[21:20]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [11].PHY_ABLTY_GET_SEL[23:22]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [12].PHY_ABLTY_GET_SEL[25:24]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [13].PHY_ABLTY_GET_SEL[27:26]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [14].PHY_ABLTY_GET_SEL[29:28]	2
0xBB000CAC	SMI_PHY_ABLTY_GET_SEL [15].PHY_ABLTY_GET_SEL[31:30]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [16].PHY_ABLTY_GET_SEL[1:0]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [17].PHY_ABLTY_GET_SEL[3:2]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [18].PHY_ABLTY_GET_SEL[5:4]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [19].PHY_ABLTY_GET_SEL[7:6]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [20].PHY_ABLTY_GET_SEL[9:8]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [21].PHY_ABLTY_GET_SEL[11:10]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [22].PHY_ABLTY_GET_SEL[13:12]	2
0xBB000CB0	SMI_PHY_ABLTY_CET_SEL [23].PHY_ABLTY_GET_SEL[15:14]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [24].PHY_ABLTY_GET_SEL[17:16]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [25].PHY_ABLTY_GET_SEL[19:18]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [26].PHY_ABLTY_GET_SEL[21:20]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [27].PHY_ABLTY_GET_SEL[23:22]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [28].PHY_ABLTY_GET_SEL[25:24]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [29].PHY_ABLTY_GET_SEL[27:26]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [30].PHY_ABLTY_GET_SEL[29:28]	2
0xBB000CB0	SMI_PHY_ABLTY_GET_SEL [31].PHY_ABLTY_GET_SEL[31:30]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [32].PHY_ABLTY_GET_SEL[1:0]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [33].PHY_ABLTY_GET_SEL[3:2]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [34].PHY_ABLTY_GET_SEL[5:4]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [35].PHY_ABLTY_GET_SEL[7:6]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [36].PHY_ABLTY_GET_SEL[9:8]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [37].PHY_ABLTY_GET_SEL[11:10]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [38].PHY_ABLTY_GET_SEL[13:12]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [39].PHY_ABLTY_GET_SEL[15:14]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [40].PHY_ABLTY_GET_SEL[17:16]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [41].PHY_ABLTY_GET_SEL[19:18]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [42].PHY_ABLTY_GET_SEL[21:20]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [43].PHY_ABLTY_GET_SEL[23:22]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [44].PHY_ABLTY_GET_SEL[25:24]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [45].PHY_ABLTY_GET_SEL[27:26]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [46].PHY_ABLTY_GET_SEL[29:28]	2
0xBB000CB4	SMI_PHY_ABLTY_GET_SEL [47].PHY_ABLTY_GET_SEL[31:30]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [48].PHY_ABLTY_GET_SEL[1:0]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [49].PHY_ABLTY_GET_SEL[3:2]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [50].PHY_ABLTY_GET_SEL[5:4]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [51].PHY_ABLTY_GET_SEL[7:6]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [52].PHY_ABLTY_GET_SEL[9:8]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [53].PHY_ABLTY_GET_SEL[11:10]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [54].PHY_ABLTY_GET_SEL[13:12]	2
0xBB000CB8	SMI_PHY_ABLTY_GET_SEL [55].PHY_ABLTY_GET_SEL[15:14]	2
0xBB000C74	SMI_PORT_ADDR_CTRL [0].PORT_ADDR[4:0]	5
0xBB000C74	SMI_PORT_ADDR_CTRL [1].PORT_ADDR[9:5]	5
0xBB000C74	SMI_PORT_ADDR_CTRL [2].PORT_ADDR[14:10]	5
0xBB000C74	SMI_PORT_ADDR_CTRL [3].PORT_ADDR[19:15]	5
0xBB000C74	SMI_PORT_ADDR_CTRL [4].PORT_ADDR[24:20]	5
0xBB000C74	SMI_PORT_ADDR_CTRL [5].PORT_ADDR[29:25]	5
0xBB000C78	SMI_PORT_ADDR_CTRL [6].PORT_ADDR[4:0]	5
0xBB000C78	SMI_PORT_ADDR_CTRL [7].PORT_ADDR[9:5]	5
0xBB000C78	SMI_PORT_ADDR_CTRL [8].PORT_ADDR[14:10]	5
0xBB000C78	SMI_PORT_ADDR_CTRL [9].PORT_ADDR[19:15]	5
0xBB000C78	SMI_PORT_ADDR_CTRL [10].PORT_ADDR[24:20]	5
0xBB000C78	SMI_PORT_ADDR_CTRL [11].PORT_ADDR[29:25]	5
0xBB000C7C	SMI_PORT_ADDR_CTRL [12].PORT_ADDR[4:0]	5
0xBB000C7C	SMI_PORT_ADDR_CTRL [13].PORT_ADDR[9:5]	5
0xBB000C7C	SMI_PORT_ADDR_CTRL [14].PORT_ADDR[14:10]	5
0xBB000C7C	SMI_PORT_ADDR_CTRL [15].PORT_ADDR[19:15]	5
0xBB000C7C	SMI_PORT_ADDR_CTRL [16].PORT_ADDR[24:20]	5
0xBB000C7C	SMI_PORT_ADDR_CTRL [17].PORT_ADDR[29:25]	5
0xBB000C80	SMI_PORT_ADDR_CTRL [18].PORT_ADDR[4:0]	5
0xBB000C80	SMI_PORT_ADDR_CTRL [19].PORT_ADDR[9:5]	5
0xBB000C80	SMI_PORT_ADDR_CTRL [20].PORT_ADDR[14:10]	5
0xBB000C80	SMI_PORT_ADDR_CTRL [21].PORT_ADDR[19:15]	5
0xBB000C80	SMI_PORT_ADDR_CTRL [22].PORT_ADDR[24:20]	5
0xBB000C80	SMI_PORT_ADDR_CTRL [23].PORT_ADDR[29:25]	5
0xBB000C84	SMI_PORT_ADDR_CTRL [24].PORT_ADDR[4:0]	5
0xBB000C84	SMI_PORT_ADDR_CTRL [25].PORT_ADDR[9:5]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000C84	SMI_PORT_ADDR_CTRL [26].PORT_ADDR[14:10]	5
0xBB000C84	SMI_PORT_ADDR_CTRL [27].PORT_ADDR[19:15]	5
0xBB000C84	SMI_PORT_ADDR_CTRL [28].PORT_ADDR[24:20]	5
0xBB000C84	SMI_PORT_ADDR_CTRL [29].PORT_ADDR[29:25]	5
0xBB000C88	SMI_PORT_ADDR_CTRL [30].PORT_ADDR[4:0]	5
0xBB000C88	SMI_PORT_ADDR_CTRL [31].PORT_ADDR[9:5]	5
0xBB000C88	SMI_PORT_ADDR_CTRL [32].PORT_ADDR[14:10]	5
0xBB000C88	SMI_PORT_ADDR_CTRL [33].PORT_ADDR[19:15]	5
0xBB000C88	SMI_PORT_ADDR_CTRL [34].PORT_ADDR[24:20]	5
0xBB000C88	SMI_PORT_ADDR_CTRL [35].PORT_ADDR[29:25]	5
0xBB000C8C	SMI_PORT_ADDR_CTRL [36].PORT_ADDR[4:0]	5
0xBB000C8C	SMI_PORT_ADDR_CTRL [37].PORT_ADDR[9:5]	5
0xBB000C8C	SMI_PORT_ADDR_CTRL [38].PORT_ADDR[14:10]	5
0xBB000C8C	SMI_PORT_ADDR_CTRL [39].PORT_ADDR[19:15]	5
0xBB000C8C	SMI_PORT_ADDR_CTRL [40].PORT_ADDR[24:20]	5
0xBB000C8C	SMI_PORT_ADDR_CTRL [41].PORT_ADDR[29:25]	5
0xBB000C90	SMI_PORT_ADDR_CTRL [42].PORT_ADDR[4:0]	5
0xBB000C90	SMI_PORT_ADDR_CTRL [43].PORT_ADDR[9:5]	5
0xBB000C90	SMI_PORT_ADDR_CTRL [44].PORT_ADDR[14:10]	5
0xBB000C90	SMI_PORT_ADDR_CTRL [45].PORT_ADDR[19:15]	5
0xBB000C90	SMI_PORT_ADDR_CTRL [46].PORT_ADDR[24:20]	5
0xBB000C90	SMI_PORT_ADDR_CTRL [47].PORT_ADDR[29:25]	5
0xBB000C94	SMI_PORT_ADDR_CTRL [48].PORT_ADDR[4:0]	5
0xBB000C94	SMI_PORT_ADDR_CTRL [49].PORT_ADDR[9:5]	5
0xBB000C94	SMI_PORT_ADDR_CTRL [50].PORT_ADDR[14:10]	5
0xBB000C94	SMI_PORT_ADDR_CTRL [51].PORT_ADDR[19:15]	5
0xBB000C94	SMI_PORT_ADDR_CTRL [52].PORT_ADDR[24:20]	5
0xBB000C94	SMI_PORT_ADDR_CTRL [53].PORT_ADDR[29:25]	5
0xBB000C98	SMI_PORT_ADDR_CTRL [54].PORT_ADDR[4:0]	5
0xBB000C98	SMI_PORT_ADDR_CTRL [55].PORT_ADDR[9:5]	5
0xBB000CCC	SMI_PORT_POLLING_CTRL [0].SMI_POLLING_PMSK[0:0]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [1].SMI_POLLING_PMSK[1:1]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [2].SMI_POLLING_PMSK[2:2]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [3].SMI_POLLING_PMSK[3:3]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [4].SMI_POLLING_PMSK[4:4]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [5].SMI_POLLING_PMSK[5:5]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [6].SMI_POLLING_PMSK[6:6]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [7].SMI_POLLING_PMSK[7:7]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [8].SMI_POLLING_PMSK[8:8]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [9].SMI_POLLING_PMSK[9:9]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [10].SMI_POLLING_PMSK[10:10]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [11].SMI_POLLING_PMSK[11:11]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [12].SMI_POLLING_PMSK[12:12]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [13].SMI_POLLING_PMSK[13:13]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [14].SMI_POLLING_PMSK[14:14]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [15].SMI_POLLING_PMSK[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000CCC	SMI_PORT_POLLING_CTRL [16].SMI_POLLING_PMSK[16:16]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [17].SMI_POLLING_PMSK[17:17]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [18].SMI_POLLING_PMSK[18:18]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [19].SMI_POLLING_PMSK[19:19]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [20].SMI_POLLING_PMSK[20:20]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [21].SMI_POLLING_PMSK[21:21]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [22].SMI_POLLING_PMSK[22:22]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [23].SMI_POLLING_PMSK[23:23]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [24].SMI_POLLING_PMSK[24:24]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [25].SMI_POLLING_PMSK[25:25]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [26].SMI_POLLING_PMSK[26:26]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [27].SMI_POLLING_PMSK[27:27]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [28].SMI_POLLING_PMSK[28:28]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [29].SMI_POLLING_PMSK[29:29]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [30].SMI_POLLING_PMSK[30:30]	1
0xBB000CCC	SMI_PORT_POLLING_CTRL [31].SMI_POLLING_PMSK[31:31]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [32].SMI_POLLING_PMSK[0:0]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [33].SMI_POLLING_PMSK[1:1]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [34].SMI_POLLING_PMSK[2:2]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [35].SMI_POLLING_PMSK[3:3]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [36].SMI_POLLING_PMSK[4:4]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [37].SMI_POLLING_PMSK[5:5]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [38].SMI_POLLING_PMSK[6:6]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [39].SMI_POLLING_PMSK[7:7]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [40].SMI_POLLING_PMSK[8:8]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [41].SMI_POLLING_PMSK[9:9]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [42].SMI_POLLING_PMSK[10:10]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [43].SMI_POLLING_PMSK[11:11]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [44].SMI_POLLING_PMSK[12:12]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [45].SMI_POLLING_PMSK[13:13]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [46].SMI_POLLING_PMSK[14:14]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [47].SMI_POLLING_PMSK[15:15]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [48].SMI_POLLING_PMSK[16:16]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [49].SMI_POLLING_PMSK[17:17]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [50].SMI_POLLING_PMSK[18:18]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [51].SMI_POLLING_PMSK[19:19]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [52].SMI_POLLING_PMSK[20:20]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [53].SMI_POLLING_PMSK[21:21]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [54].SMI_POLLING_PMSK[22:22]	1
0xBB000CD0	SMI_PORT_POLLING_CTRL [55].SMI_POLLING_PMSK[23:23]	1
0xBB000C9C	SMI_PORT_POLLING_SEL [0].PORT_POLLING_SEL[1:0]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [1].PORT_POLLING_SEL[3:2]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [2].PORT_POLLING_SEL[5:4]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [3].PORT_POLLING_SEL[7:6]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [4].PORT_POLLING_SEL[9:8]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [5].PORT_POLLING_SEL[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000C9C	SMI_PORT_POLLING_SEL [6].PORT_POLLING_SEL[13:12]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [7].PORT_POLLING_SEL[15:14]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [8].PORT_POLLING_SEL[17:16]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [9].PORT_POLLING_SEL[19:18]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [10].PORT_POLLING_SEL[21:20]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [11].PORT_POLLING_SEL[23:22]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [12].PORT_POLLING_SEL[25:24]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [13].PORT_POLLING_SEL[27:26]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [14].PORT_POLLING_SEL[29:28]	2
0xBB000C9C	SMI_PORT_POLLING_SEL [15].PORT_POLLING_SEL[31:30]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [16].PORT_POLLING_SEL[1:0]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [17].PORT_POLLING_SEL[3:2]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [18].PORT_POLLING_SEL[5:4]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [19].PORT_POLLING_SEL[7:6]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [20].PORT_POLLING_SEL[9:8]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [21].PORT_POLLING_SEL[11:10]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [22].PORT_POLLING_SEL[13:12]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [23].PORT_POLLING_SEL[15:14]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [24].PORT_POLLING_SEL[17:16]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [25].PORT_POLLING_SEL[19:18]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [26].PORT_POLLING_SEL[21:20]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [27].PORT_POLLING_SEL[23:22]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [28].PORT_POLLING_SEL[25:24]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [29].PORT_POLLING_SEL[27:26]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [30].PORT_POLLING_SEL[29:28]	2
0xBB000CA0	SMI_PORT_POLLING_SEL [31].PORT_POLLING_SEL[31:30]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [32].PORT_POLLING_SEL[1:0]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [33].PORT_POLLING_SEL[3:2]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [34].PORT_POLLING_SEL[5:4]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [35].PORT_POLLING_SEL[7:6]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [36].PORT_POLLING_SEL[9:8]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [37].PORT_POLLING_SEL[11:10]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [38].PORT_POLLING_SEL[13:12]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [39].PORT_POLLING_SEL[15:14]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [40].PORT_POLLING_SEL[17:16]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [41].PORT_POLLING_SEL[19:18]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [42].PORT_POLLING_SEL[21:20]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [43].PORT_POLLING_SEL[23:22]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [44].PORT_POLLING_SEL[25:24]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [45].PORT_POLLING_SEL[27:26]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [46].PORT_POLLING_SEL[29:28]	2
0xBB000CA4	SMI_PORT_POLLING_SEL [47].PORT_POLLING_SEL[31:30]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [48].PORT_POLLING_SEL[1:0]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [49].PORT_POLLING_SEL[3:2]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [50].PORT_POLLING_SEL[5:4]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [51].PORT_POLLING_SEL[7:6]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000CA8	SMI_PORT_POLLING_SEL [52].PORT_POLLING_SEL[9:8]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [53].PORT_POLLING_SEL[11:10]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [54].PORT_POLLING_SEL[13:12]	2
0xBB000CA8	SMI_PORT_POLLING_SEL [55].PORT_POLLING_SEL[15:14]	2
0xBB000CE4	SMI_RLFD_POLLING_BIT_SEL.RESERVED[31:8]	24
0xBB000CE4	SMI_RLFD_POLLING_BIT_SEL.RLFD_BIT_POLLING_10GPHY[7:4]	4
0xBB000CE4	SMI_RLFD_POLLING_BIT_SEL.RLFD_BIT_POLLING_GPHY[3:0]	4
0xBB000F28	SPD_FORCE_VAL [0].SPD_FORCE_VAL[3:0]	4
0xBB000F28	SPD_FORCE_VAL [1].SPD_FORCE_VAL[7:4]	4
0xBB000F28	SPD_FORCE_VAL [2].SPD_FORCE_VAL[11:8]	4
0xBB000F28	SPD_FORCE_VAL [3].SPD_FORCE_VAL[15:12]	4
0xBB000F28	SPD_FORCE_VAL [4].SPD_FORCE_VAL[19:16]	4
0xBB000F28	SPD_FORCE_VAL [5].SPD_FORCE_VAL[23:20]	4
0xBB000F28	SPD_FORCE_VAL [6].SPD_FORCE_VAL[27:24]	4
0xBB000F28	SPD_FORCE_VAL [7].SPD_FORCE_VAL[31:28]	4
0xBB000F2C	SPD_FORCE_VAL [8].SPD_FORCE_VAL[3:0]	4
0xBB000F2C	SPD_FORCE_VAL [9].SPD_FORCE_VAL[7:4]	4
0xBB000F2C	SPD_FORCE_VAL [10].SPD_FORCE_VAL[11:8]	4
0xBB000F2C	SPD_FORCE_VAL [11].SPD_FORCE_VAL[15:12]	4
0xBB000F2C	SPD_FORCE_VAL [12].SPD_FORCE_VAL[19:16]	4
0xBB000F2C	SPD_FORCE_VAL [13].SPD_FORCE_VAL[23:20]	4
0xBB000F2C	SPD_FORCE_VAL [14].SPD_FORCE_VAL[27:24]	4
0xBB000F2C	SPD_FORCE_VAL [15].SPD_FORCE_VAL[31:28]	4
0xBB000F30	SPD_FORCE_VAL [16].SPD_FORCE_VAL[3:0]	4
0xBB000F30	SPD_FORCE_VAL [17].SPD_FORCE_VAL[7:4]	4
0xBB000F30	SPD_FORCE_VAL [18].SPD_FORCE_VAL[11:8]	4
0xBB000F30	SPD_FORCE_VAL [19].SPD_FORCE_VAL[15:12]	4
0xBB000F30	SPD_FORCE_VAL [20].SPD_FORCE_VAL[19:16]	4
0xBB000F30	SPD_FORCE_VAL [21].SPD_FORCE_VAL[23:20]	4
0xBB000F30	SPD_FORCE_VAL [22].SPD_FORCE_VAL[27:24]	4
0xBB000F30	SPD_FORCE_VAL [23].SPD_FORCE_VAL[31:28]	4
0xBB000F34	SPD_FORCE_VAL [24].SPD_FORCE_VAL[3:0]	4
0xBB000F34	SPD_FORCE_VAL [25].SPD_FORCE_VAL[7:4]	4
0xBB000F34	SPD_FORCE_VAL [26].SPD_FORCE_VAL[11:8]	4
0xBB000F34	SPD_FORCE_VAL [27].SPD_FORCE_VAL[15:12]	4
0xBB000F34	SPD_FORCE_VAL [28].SPD_FORCE_VAL[19:16]	4
0xBB000F34	SPD_FORCE_VAL [29].SPD_FORCE_VAL[23:20]	4
0xBB000F34	SPD_FORCE_VAL [30].SPD_FORCE_VAL[27:24]	4
0xBB000F34	SPD_FORCE_VAL [31].SPD_FORCE_VAL[31:28]	4
0xBB000F38	SPD_FORCE_VAL [32].SPD_FORCE_VAL[3:0]	4
0xBB000F38	SPD_FORCE_VAL [33].SPD_FORCE_VAL[7:4]	4
0xBB000F38	SPD_FORCE_VAL [34].SPD_FORCE_VAL[11:8]	4
0xBB000F38	SPD_FORCE_VAL [35].SPD_FORCE_VAL[15:12]	4
0xBB000F38	SPD_FORCE_VAL [36].SPD_FORCE_VAL[19:16]	4
0xBB000F38	SPD_FORCE_VAL [37].SPD_FORCE_VAL[23:20]	4
0xBB000F38	SPD_FORCE_VAL [38].SPD_FORCE_VAL[27:24]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000F38	SPD_FORCE_VAL [39].SPD_FORCE_VAL[31:28]	4
0xBB000F3C	SPD_FORCE_VAL [40].SPD_FORCE_VAL[3:0]	4
0xBB000F3C	SPD_FORCE_VAL [41].SPD_FORCE_VAL[7:4]	4
0xBB000F3C	SPD_FORCE_VAL [42].SPD_FORCE_VAL[11:8]	4
0xBB000F3C	SPD_FORCE_VAL [43].SPD_FORCE_VAL[15:12]	4
0xBB000F3C	SPD_FORCE_VAL [44].SPD_FORCE_VAL[19:16]	4
0xBB000F3C	SPD_FORCE_VAL [45].SPD_FORCE_VAL[23:20]	4
0xBB000F3C	SPD_FORCE_VAL [46].SPD_FORCE_VAL[27:24]	4
0xBB000F3C	SPD_FORCE_VAL [47].SPD_FORCE_VAL[31:28]	4
0xBB000F40	SPD_FORCE_VAL [48].SPD_FORCE_VAL[3:0]	4
0xBB000F40	SPD_FORCE_VAL [49].SPD_FORCE_VAL[7:4]	4
0xBB000F40	SPD_FORCE_VAL [50].SPD_FORCE_VAL[11:8]	4
0xBB000F40	SPD_FORCE_VAL [51].SPD_FORCE_VAL[15:12]	4
0xBB000F40	SPD_FORCE_VAL [52].SPD_FORCE_VAL[19:16]	4
0xBB000F40	SPD_FORCE_VAL [53].SPD_FORCE_VAL[23:20]	4
0xBB000F40	SPD_FORCE_VAL [54].SPD_FORCE_VAL[27:24]	4
0xBB000F40	SPD_FORCE_VAL [55].SPD_FORCE_VAL[31:28]	4
0xBB000F44	SPD_FORCE_VAL [56].SPD_FORCE_VAL[3:0]	4
0xBB001248	SPI_ADDR.SPI_ADDR[31:0]	32
0xBB00103C	SPI_CTRL0.RESERVED[31:26]	6
0xBB00103C	SPI_CTRL0.DATA_WIDTH[25:17]	9
0xBB00103C	SPI_CTRL0.ADDR_WIDTH[16:14]	3
0xBB00103C	SPI_CTRL0.SPI_CLK_SEL[13:11]	3
0xBB00103C	SPI_CTRL0.GPIO_SPI_SEL[10:10]	1
0xBB00103C	SPI_CTRL0.GPIO12_CSB1_SEL[9:9]	1
0xBB00103C	SPI_CTRL0.GPIO11_CSBO_SEL[8:8]	1
0xBB00103C	SPI_CTRL0.SPI_CPHA[7:7]	1
0xBB00103C	SPI_CTRL0.SPI_CPOL[6:6]	1
0xBB00103C	SPI_CTRL0.SPI_TSLCH[5:3]	3
0xBB00103C	SPI_CTRL0.SPI_TCHSH[2:0]	3
0xBB001040	SPI_CTRL1.RESERVED[31:9]	23
0xBB001040	SPI_CTRL1.SPI_RX_DLY[8:6]	3
0xBB001040	SPI_CTRL1.SPI_CLK_DLY[5:3]	3
0xBB001040	SPI_CTRL1.SPI_TX_DLY[2:0]	3
0xBB001044	SPI_CTRL2.RESERVED[31:11]	21
0xBB001044	SPI_CTRL2.CSB_OUT_SEL[10:10]	1
0xBB001044	SPI_CTRL2.SPI_CMD_TYPE[9:9]	1
0xBB001044	SPI_CTRL2.SPI_CMD[8:1]	8
0xBB001044	SPI_CTRL2.SPI_TRIG[0:0]	1
0xBB001048	SPI_DATA [0].SPI_DATA[31:0]	32
0xBB00104C	SPI_DATA [1].SPI_DATA[31:0]	32
0xBB001050	SPI_DATA [2].SPI_DATA[31:0]	32
0xBB001054	SPI_DATA [3].SPI_DATA[31:0]	32
0xBB001058	SPI_DATA [4].SPI_DATA[31:0]	32
0xBB00105C	SPI_DATA [5].SPI_DATA[31:0]	32
0xBB001060	SPI_DATA [6].SPI_DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001064	SPI_DATA [7].SPI_DATA[31:0]	32
0xBB001068	SPI_DATA [8].SPI_DATA[31:0]	32
0xBB00106C	SPI_DATA [9].SPI_DATA[31:0]	32
0xBB001070	SPI_DATA [10].SPI_DATA[31:0]	32
0xBB001074	SPI_DATA [11].SPI_DATA[31:0]	32
0xBB001078	SPI_DATA [12].SPI_DATA[31:0]	32
0xBB00107C	SPI_DATA [13].SPI_DATA[31:0]	32
0xBB001080	SPI_DATA [14].SPI_DATA[31:0]	32
0xBB001084	SPI_DATA [15].SPI_DATA[31:0]	32
0xBB001088	SPI_DATA [16].SPI_DATA[31:0]	32
0xBB00108C	SPI_DATA [17].SPI_DATA[31:0]	32
0xBB001090	SPI_DATA [18].SPI_DATA[31:0]	32
0xBB001094	SPI_DATA [19].SPI_DATA[31:0]	32
0xBB001098	SPI_DATA [20].SPI_DATA[31:0]	32
0xBB00109C	SPI_DATA [21].SPI_DATA[31:0]	32
0xBB0010A0	SPI_DATA [22].SPI_DATA[31:0]	32
0xBB0010A4	SPI_DATA [23].SPI_DATA[31:0]	32
0xBB0010A8	SPI_DATA [24].SPI_DATA[31:0]	32
0xBB0010AC	SPI_DATA [25].SPI_DATA[31:0]	32
0xBB0010B0	SPI_DATA [26].SPI_DATA[31:0]	32
0xBB0010B4	SPI_DATA [27].SPI_DATA[31:0]	32
0xBB0010B8	SPI_DATA [28].SPI_DATA[31:0]	32
0xBB0010BC	SPI_DATA [29].SPI_DATA[31:0]	32
0xBB0010C0	SPI_DATA [30].SPI_DATA[31:0]	32
0xBB0010C4	SPI_DATA [31].SPI_DATA[31:0]	32
0xBB0010C8	SPI_DATA [32].SPI_DATA[31:0]	32
0xBB0010CC	SPI_DATA [33].SPI_DATA[31:0]	32
0xBB0010D0	SPI_DATA [34].SPI_DATA[31:0]	32
0xBB0010D4	SPI_DATA [35].SPI_DATA[31:0]	32
0xBB0010D8	SPI_DATA [36].SPI_DATA[31:0]	32
0xBB0010DC	SPI_DATA [37].SPI_DATA[31:0]	32
0xBB0010E0	SPI_DATA [38].SPI_DATA[31:0]	32
0xBB0010E4	SPI_DATA [39].SPI_DATA[31:0]	32
0xBB0010E8	SPI_DATA [40].SPI_DATA[31:0]	32
0xBB0010EC	SPI_DATA [41].SPI_DATA[31:0]	32
0xBB0010F0	SPI_DATA [42].SPI_DATA[31:0]	32
0xBB0010F4	SPI_DATA [43].SPI_DATA[31:0]	32
0xBB0010F8	SPI_DATA [44].SPI_DATA[31:0]	32
0xBB0010FC	SPI_DATA [45].SPI_DATA[31:0]	32
0xBB001100	SPI_DATA [46].SPI_DATA[31:0]	32
0xBB001104	SPI_DATA [47].SPI_DATA[31:0]	32
0xBB001108	SPI_DATA [48].SPI_DATA[31:0]	32
0xBB00110C	SPI_DATA [49].SPI_DATA[31:0]	32
0xBB001110	SPI_DATA [50].SPI_DATA[31:0]	32
0xBB001114	SPI_DATA [51].SPI_DATA[31:0]	32
0xBB001118	SPI_DATA [52].SPI_DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00111C	SPI_DATA [53].SPI_DATA[31:0]	32
0xBB001120	SPI_DATA [54].SPI_DATA[31:0]	32
0xBB001124	SPI_DATA [55].SPI_DATA[31:0]	32
0xBB001128	SPI_DATA [56].SPI_DATA[31:0]	32
0xBB00112C	SPI_DATA [57].SPI_DATA[31:0]	32
0xBB001130	SPI_DATA [58].SPI_DATA[31:0]	32
0xBB001134	SPI_DATA [59].SPI_DATA[31:0]	32
0xBB001138	SPI_DATA [60].SPI_DATA[31:0]	32
0xBB00113C	SPI_DATA [61].SPI_DATA[31:0]	32
0xBB001140	SPI_DATA [62].SPI_DATA[31:0]	32
0xBB001144	SPI_DATA [63].SPI_DATA[31:0]	32
0xBB001148	SPI_DATA [64].SPI_DATA[31:0]	32
0xBB00114C	SPI_DATA [65].SPI_DATA[31:0]	32
0xBB001150	SPI_DATA [66].SPI_DATA[31:0]	32
0xBB001154	SPI_DATA [67].SPI_DATA[31:0]	32
0xBB001158	SPI_DATA [68].SPI_DATA[31:0]	32
0xBB00115C	SPI_DATA [69].SPI_DATA[31:0]	32
0xBB001160	SPI_DATA [70].SPI_DATA[31:0]	32
0xBB001164	SPI_DATA [71].SPI_DATA[31:0]	32
0xBB001168	SPI_DATA [72].SPI_DATA[31:0]	32
0xBB00116C	SPI_DATA [73].SPI_DATA[31:0]	32
0xBB001170	SPI_DATA [74].SPI_DATA[31:0]	32
0xBB001174	SPI_DATA [75].SPI_DATA[31:0]	32
0xBB001178	SPI_DATA [76].SPI_DATA[31:0]	32
0xBB00117C	SPI_DATA [77].SPI_DATA[31:0]	32
0xBB001180	SPI_DATA [78].SPI_DATA[31:0]	32
0xBB001184	SPI_DATA [79].SPI_DATA[31:0]	32
0xBB001188	SPI_DATA [80].SPI_DATA[31:0]	32
0xBB00118C	SPI_DATA [81].SPI_DATA[31:0]	32
0xBB001190	SPI_DATA [82].SPI_DATA[31:0]	32
0xBB001194	SPI_DATA [83].SPI_DATA[31:0]	32
0xBB001198	SPI_DATA [84].SPI_DATA[31:0]	32
0xBB00119C	SPI_DATA [85].SPI_DATA[31:0]	32
0xBB0011A0	SPI_DATA [86].SPI_DATA[31:0]	32
0xBB0011A4	SPI_DATA [87].SPI_DATA[31:0]	32
0xBB0011A8	SPI_DATA [88].SPI_DATA[31:0]	32
0xBB0011AC	SPI_DATA [89].SPI_DATA[31:0]	32
0xBB0011B0	SPI_DATA [90].SPI_DATA[31:0]	32
0xBB0011B4	SPI_DATA [91].SPI_DATA[31:0]	32
0xBB0011B8	SPI_DATA [92].SPI_DATA[31:0]	32
0xBB0011BC	SPI_DATA [93].SPI_DATA[31:0]	32
0xBB0011C0	SPI_DATA [94].SPI_DATA[31:0]	32
0xBB0011C4	SPI_DATA [95].SPI_DATA[31:0]	32
0xBB0011C8	SPI_DATA [96].SPI_DATA[31:0]	32
0xBB0011CC	SPI_DATA [97].SPI_DATA[31:0]	32
0xBB0011D0	SPI_DATA [98].SPI_DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0011D4	SPI_DATA [99].SPI_DATA[31:0]	32
0xBB0011D8	SPI_DATA [100].SPI_DATA[31:0]	32
0xBB0011DC	SPI_DATA [101].SPI_DATA[31:0]	32
0xBB0011E0	SPI_DATA [102].SPI_DATA[31:0]	32
0xBB0011E4	SPI_DATA [103].SPI_DATA[31:0]	32
0xBB0011E8	SPI_DATA [104].SPI_DATA[31:0]	32
0xBB0011EC	SPI_DATA [105].SPI_DATA[31:0]	32
0xBB0011F0	SPI_DATA [106].SPI_DATA[31:0]	32
0xBB0011F4	SPI_DATA [107].SPI_DATA[31:0]	32
0xBB0011F8	SPI_DATA [108].SPI_DATA[31:0]	32
0xBB0011FC	SPI_DATA [109].SPI_DATA[31:0]	32
0xBB001200	SPI_DATA [110].SPI_DATA[31:0]	32
0xBB001204	SPI_DATA [111].SPI_DATA[31:0]	32
0xBB001208	SPI_DATA [112].SPI_DATA[31:0]	32
0xBB00120C	SPI_DATA [113].SPI_DATA[31:0]	32
0xBB001210	SPI_DATA [114].SPI_DATA[31:0]	32
0xBB001214	SPI_DATA [115].SPI_DATA[31:0]	32
0xBB001218	SPI_DATA [116].SPI_DATA[31:0]	32
0xBB00121C	SPI_DATA [117].SPI_DATA[31:0]	32
0xBB001220	SPI_DATA [118].SPI_DATA[31:0]	32
0xBB001224	SPI_DATA [119].SPI_DATA[31:0]	32
0xBB001228	SPI_DATA [120].SPI_DATA[31:0]	32
0xBB00122C	SPI_DATA [121].SPI_DATA[31:0]	32
0xBB001230	SPI_DATA [122].SPI_DATA[31:0]	32
0xBB001234	SPI_DATA [123].SPI_DATA[31:0]	32
0xBB001238	SPI_DATA [124].SPI_DATA[31:0]	32
0xBB00123C	SPI_DATA [125].SPI_DATA[31:0]	32
0xBB001240	SPI_DATA [126].SPI_DATA[31:0]	32
0xBB001244	SPI_DATA [127].SPI_DATA[31:0]	32
0xBB007F14	STAT_BRIDGE_DOT1DTPLEARNEDENTRYDISCARDS.dot1dTpLearnedEntryDiscards[31:0]	32
0xBB005728	STAT_CNT_SET0_CTRL.RESERVED[31:30]	2
0xBB005728	STAT_CNT_SET0_CTRL.CNT_SET0_LEN_MAX[29:16]	14
0xBB005728	STAT_CNT_SET0_CTRL.RESERVED[15:14]	2
0xBB005728	STAT_CNT_SET0_CTRL.CNT_SET0_LEN_MIN[13:0]	14
0xBB005724	STAT_CNT_SET1_CTRL.RESERVED[31:30]	2
0xBB005724	STAT_CNT_SET1_CTRL.CNT_SET1_LEN_MAX[29:16]	14
0xBB005724	STAT_CNT_SET1_CTRL.RESERVED[15:14]	2
0xBB005724	STAT_CNT_SET1_CTRL.CNT_SET1_LEN_MIN[13:0]	14
0xBB005720	STAT_CTRL.RESERVED[31:5]	27
0xBB005720	STAT_CTRL.TX_MULTIPKT_CNT_PAUSE[4:4]	1
0xBB005720	STAT_CTRL.TX_STACK_CNT_HDR[3:3]	1
0xBB005720	STAT_CTRL.RX_STACK_CNT_HDR[2:2]	1
0xBB005720	STAT_CTRL.TX_CNT_TAG[1:1]	1
0xBB005720	STAT_CTRL.RX_CNT_TAG[0:0]	1
0xBB007EF8	STAT_PORT_RST.RESERVED[31:7]	25
0xBB007EF8	STAT_PORT_RST.RST_PORT_FLAG[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007EF8	STAT_PORT_RST.RST_PORT_MIB[5:0]	6
0xBB00D800	STAT_PRVTE_DBG_CNTR0.RESERVED[31:16]	16
0xBB00D800	STAT_PRVTE_DBG_CNTR0.REASON_0[15:0]	16
0xBB00D804	STAT_PRVTE_DBG_CNTR1.RESERVED[31:16]	16
0xBB00D804	STAT_PRVTE_DBG_CNTR1.REASON_1[15:0]	16
0xBB00D828	STAT_PRVTE_DBG_CNTR10.RESERVED[31:16]	16
0xBB00D828	STAT_PRVTE_DBG_CNTR10.REASON_10[15:0]	16
0xBB00D82C	STAT_PRVTE_DBG_CNTR11.RESERVED[31:16]	16
0xBB00D82C	STAT_PRVTE_DBG_CNTR11.REASON_11[15:0]	16
0xBB00D830	STAT_PRVTE_DBG_CNTR12.RESERVED[31:16]	16
0xBB00D830	STAT_PRVTE_DBG_CNTR12.REASON_12[15:0]	16
0xBB00D834	STAT_PRVTE_DBG_CNTR13.RESERVED[31:16]	16
0xBB00D834	STAT_PRVTE_DBG_CNTR13.REASON_13[15:0]	16
0xBB00D838	STAT_PRVTE_DBG_CNTR14.RESERVED[31:16]	16
0xBB00D838	STAT_PRVTE_DBG_CNTR14.REASON_14[15:0]	16
0xBB00D83C	STAT_PRVTE_DBG_CNTR15.RESERVED[31:16]	16
0xBB00D83C	STAT_PRVTE_DBG_CNTR15.REASON_15[15:0]	16
0xBB00D840	STAT_PRVTE_DBG_CNTR16.RESERVED[31:16]	16
0xBB00D840	STAT_PRVTE_DBG_CNTR16.REASON_16[15:0]	16
0xBB00D844	STAT_PRVTE_DBG_CNTR17.RESERVED[31:16]	16
0xBB00D844	STAT_PRVTE_DBG_CNTR17.REASON_17[15:0]	16
0xBB00D848	STAT_PRVTE_DBG_CNTR18.RESERVED[31:16]	16
0xBB00D848	STAT_PRVTE_DBG_CNTR18.REASON_18[15:0]	16
0xBB00D84C	STAT_PRVTE_DBG_CNTR19.RESERVED[31:16]	16
0xBB00D84C	STAT_PRVTE_DBG_CNTR19.REASON_19[15:0]	16
0xBB00D808	STAT_PRVTE_DBG_CNTR2.RESERVED[31:16]	16
0xBB00D808	STAT_PRVTE_DBG_CNTR2.REASON_2[15:0]	16
0xBB00D850	STAT_PRVTE_DBG_CNTR20.RESERVED[31:16]	16
0xBB00D850	STAT_PRVTE_DBG_CNTR20.REASON_20[15:0]	16
0xBB00D854	STAT_PRVTE_DBG_CNTR21.RESERVED[31:16]	16
0xBB00D854	STAT_PRVTE_DBG_CNTR21.REASON_21[15:0]	16
0xBB00D858	STAT_PRVTE_DBG_CNTR22.RESERVED[31:16]	16
0xBB00D858	STAT_PRVTE_DBG_CNTR22.REASON_22[15:0]	16
0xBB00D85C	STAT_PRVTE_DBG_CNTR23.RESERVED[31:16]	16
0xBB00D85C	STAT_PRVTE_DBG_CNTR23.REASON_23[15:0]	16
0xBB00D860	STAT_PRVTE_DBG_CNTR24.RESERVED[31:16]	16
0xBB00D860	STAT_PRVTE_DBG_CNTR24.REASON_24[15:0]	16
0xBB00D864	STAT_PRVTE_DBG_CNTR25.RESERVED[31:16]	16
0xBB00D864	STAT_PRVTE_DBG_CNTR25.REASON_25[15:0]	16
0xBB00D868	STAT_PRVTE_DBG_CNTR26.RESERVED[31:16]	16
0xBB00D868	STAT_PRVTE_DBG_CNTR26.REASON_26[15:0]	16
0xBB00D86C	STAT_PRVTE_DBG_CNTR27.RESERVED[31:16]	16
0xBB00D86C	STAT_PRVTE_DBG_CNTR27.REASON_27[15:0]	16
0xBB00D870	STAT_PRVTE_DBG_CNTR28.RESERVED[31:16]	16
0xBB00D870	STAT_PRVTE_DBG_CNTR28.REASON_28[15:0]	16
0xBB00D874	STAT_PRVTE_DBG_CNTR29.RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D874	STAT_PRVTE_DBG_CNTR29.REASON_29[15:0]	16
0xBB00D80C	STAT_PRVTE_DBG_CNTR3.RESERVED[31:16]	16
0xBB00D80C	STAT_PRVTE_DBG_CNTR3.REASON_3[15:0]	16
0xBB00D878	STAT_PRVTE_DBG_CNTR30.RESERVED[31:16]	16
0xBB00D878	STAT_PRVTE_DBG_CNTR30.REASON_30[15:0]	16
0xBB00D87C	STAT_PRVTE_DBG_CNTR31.RESERVED[31:16]	16
0xBB00D87C	STAT_PRVTE_DBG_CNTR31.REASON_31[15:0]	16
0xBB00D880	STAT_PRVTE_DBG_CNTR32.RESERVED[31:16]	16
0xBB00D880	STAT_PRVTE_DBG_CNTR32.REASON_32[15:0]	16
0xBB00D884	STAT_PRVTE_DBG_CNTR33.RESERVED[31:16]	16
0xBB00D884	STAT_PRVTE_DBG_CNTR33.REASON_33[15:0]	16
0xBB00D888	STAT_PRVTE_DBG_CNTR34.RESERVED[31:16]	16
0xBB00D888	STAT_PRVTE_DBG_CNTR34.REASON_34[15:0]	16
0xBB00D88C	STAT_PRVTE_DBG_CNTR35.RESERVED[31:16]	16
0xBB00D88C	STAT_PRVTE_DBG_CNTR35.REASON_35[15:0]	16
0xBB00D890	STAT_PRVTE_DBG_CNTR36.RESERVED[31:16]	16
0xBB00D890	STAT_PRVTE_DBG_CNTR36.REASON_36[15:0]	16
0xBB00D894	STAT_PRVTE_DBG_CNTR37.RESERVED[31:16]	16
0xBB00D894	STAT_PRVTE_DBG_CNTR37.REASON_37[15:0]	16
0xBB00D898	STAT_PRVTE_DBG_CNTR38.RESERVED[31:16]	16
0xBB00D898	STAT_PRVTE_DBG_CNTR38.REASON_38[15:0]	16
0xBB00D89C	STAT_PRVTE_DBG_CNTR39.RESERVED[31:16]	16
0xBB00D89C	STAT_PRVTE_DBG_CNTR39.REASON_39[15:0]	16
0xBB00D8A0	STAT_PRVTE_DBG_CNTR4.RESERVED[31:16]	16
0xBB00D810	STAT_PRVTE_DBG_CNTR4.REASON_4[15:0]	16
0xBB00D8A0	STAT_PRVTE_DBG_CNTR40.RESERVED[31:16]	16
0xBB00D8A0	STAT_PRVTE_DBG_CNTR40.REASON_40[15:0]	16
0xBB00D8A4	STAT_PRVTE_DBG_CNTR41.RESERVED[31:16]	16
0xBB00D8A4	STAT_PRVTE_DBG_CNTR41.REASON_41[15:0]	16
0xBB00D8A8	STAT_PRVTE_DBG_CNTR42.RESERVED[31:16]	16
0xBB00D8A8	STAT_PRVTE_DBG_CNTR42.REASON_42[15:0]	16
0xBB00D8AC	STAT_PRVTE_DBG_CNTR43.RESERVED[31:16]	16
0xBB00D8AC	STAT_PRVTE_DBG_CNTR43.REASON_43[15:0]	16
0xBB00D8B0	STAT_PRVTE_DBG_CNTR44.RESERVED[31:16]	16
0xBB00D8B0	STAT_PRVTE_DBG_CNTR44.REASON_44[15:0]	16
0xBB00D8B4	STAT_PRVTE_DBG_CNTR45.RESERVED[31:16]	16
0xBB00D8B4	STAT_PRVTE_DBG_CNTR45.REASON_45[15:0]	16
0xBB00D8B8	STAT_PRVTE_DBG_CNTR46.RESERVED[31:16]	16
0xBB00D8B8	STAT_PRVTE_DBG_CNTR46.REASON_46[15:0]	16
0xBB00D8BC	STAT_PRVTE_DBG_CNTR47.RESERVED[31:16]	16
0xBB00D8BC	STAT_PRVTE_DBG_CNTR47.REASON_47[15:0]	16
0xBB00D8C0	STAT_PRVTE_DBG_CNTR48.RESERVED[31:16]	16
0xBB00D8C0	STAT_PRVTE_DBG_CNTR48.REASON_48[15:0]	16
0xBB00D8C4	STAT_PRVTE_DBG_CNTR49.RESERVED[31:16]	16
0xBB00D8C4	STAT_PRVTE_DBG_CNTR49.REASON_49[15:0]	16
0xBB00D814	STAT_PRVTE_DBG_CNTR5.RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D814	STAT_PRVTE_DBG_CNTR5.REASON_5[15:0]	16
0xBB00D8C8	STAT_PRVTE_DBG_CNTR50.RESERVED[31:16]	16
0xBB00D8C8	STAT_PRVTE_DBG_CNTR50.REASON_50[15:0]	16
0xBB00D8CC	STAT_PRVTE_DBG_CNTR51.RESERVED[31:16]	16
0xBB00D8CC	STAT_PRVTE_DBG_CNTR51.REASON_51[15:0]	16
0xBB00D8D0	STAT_PRVTE_DBG_CNTR52.RESERVED[31:16]	16
0xBB00D8D0	STAT_PRVTE_DBG_CNTR52.REASON_52[15:0]	16
0xBB00D8D4	STAT_PRVTE_DBG_CNTR53.RESERVED[31:16]	16
0xBB00D8D4	STAT_PRVTE_DBG_CNTR53.REASON_53[15:0]	16
0xBB00D8D8	STAT_PRVTE_DBG_CNTR54.RESERVED[31:16]	16
0xBB00D8DC	STAT_PRVTE_DBG_CNTR55.RESERVED[31:16]	16
0xBB00D8DC	STAT_PRVTE_DBG_CNTR55.REASON_55[15:0]	16
0xBB00D8E0	STAT_PRVTE_DBG_CNTR56.RESERVED[31:16]	16
0xBB00D8E0	STAT_PRVTE_DBG_CNTR56.REASON_56[15:0]	16
0xBB00D8E4	STAT_PRVTE_DBG_CNTR57.RESERVED[31:16]	16
0xBB00D8E4	STAT_PRVTE_DBG_CNTR57.REASON_57[15:0]	16
0xBB00D8E8	STAT_PRVTE_DBG_CNTR58.RESERVED[31:16]	16
0xBB00D8E8	STAT_PRVTE_DBG_CNTR58.REASON_58[15:0]	16
0xBB00D8EC	STAT_PRVTE_DBG_CNTR59.RESERVED[31:16]	16
0xBB00D8EC	STAT_PRVTE_DBG_CNTR59.REASON_59[15:0]	16
0xBB00D818	STAT_PRVTE_DBG_CNTR6.RESERVED[31:16]	16
0xBB00D818	STAT_PRVTE_DBG_CNTR6.REASON_6[15:0]	16
0xBB00D8F0	STAT_PRVTE_DBG_CNTR60.RESERVED[31:16]	16
0xBB00D8F0	STAT_PRVTE_DBG_CNTR60.REASON_60[15:0]	16
0xBB00D8F4	STAT_PRVTE_DBG_CNTR61.RESERVED[31:16]	16
0xBB00D8F4	STAT_PRVTE_DBG_CNTR61.REASON_61[15:0]	16
0xBB00D8F8	STAT_PRVTE_DBG_CNTR62.RESERVED[31:16]	16
0xBB00D8F8	STAT_PRVTE_DBG_CNTR62.REASON_62[15:0]	16
0xBB00D8FC	STAT_PRVTE_DBG_CNTR63.RESERVED[31:16]	16
0xBB00D8FC	STAT_PRVTE_DBG_CNTR63.REASON_63[15:0]	16
0xBB00D900	STAT_PRVTE_DBG_CNTR64.RESERVED[31:16]	16
0xBB00D900	STAT_PRVTE_DBG_CNTR64.REASON_64[15:0]	16
0xBB00D904	STAT_PRVTE_DBG_CNTR65.RESERVED[31:16]	16
0xBB00D904	STAT_PRVTE_DBG_CNTR65.REASON_65[15:0]	16
0xBB00D908	STAT_PRVTE_DBG_CNTR66.RESERVED[31:16]	16
0xBB00D908	STAT_PRVTE_DBG_CNTR66.REASON_66[15:0]	16
0xBB00D90C	STAT_PRVTE_DBG_CNTR67.RESERVED[31:16]	16
0xBB00D90C	STAT_PRVTE_DBG_CNTR67.REASON_67[15:0]	16
0xBB00D910	STAT_PRVTE_DBG_CNTR68.RESERVED[31:16]	16
0xBB00D910	STAT_PRVTE_DBG_CNTR68.REASON_68[15:0]	16
0xBB00D914	STAT_PRVTE_DBG_CNTR69.RESERVED[31:16]	16
0xBB00D914	STAT_PRVTE_DBG_CNTR69.REASON_69[15:0]	16
0xBB00D81C	STAT_PRVTE_DBG_CNTR7.RESERVED[31:16]	16
0xBB00D81C	STAT_PRVTE_DBG_CNTR7.REASON_7[15:0]	16
0xBB00D918	STAT_PRVTE_DBG_CNTR70.RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00D918	STAT_PRVTE_DBG_CNTR70.REASON_70[15:0]	16
0xBB00D91C	STAT_PRVTE_DBG_CNTR71.RESERVED[31:16]	16
0xBB00D91C	STAT_PRVTE_DBG_CNTR71.REASON_71[15:0]	16
0xBB00D920	STAT_PRVTE_DBG_CNTR72.RESERVED[31:16]	16
0xBB00D920	STAT_PRVTE_DBG_CNTR72.REASON_72[15:0]	16
0xBB00D924	STAT_PRVTE_DBG_CNTR73.RESERVED[31:16]	16
0xBB00D924	STAT_PRVTE_DBG_CNTR73.REASON_73[15:0]	16
0xBB00D928	STAT_PRVTE_DBG_CNTR74.RESERVED[31:16]	16
0xBB00D928	STAT_PRVTE_DBG_CNTR74.REASON_74[15:0]	16
0xBB00D92C	STAT_PRVTE_DBG_CNTR75.RESERVED[31:16]	16
0xBB00D92C	STAT_PRVTE_DBG_CNTR75.REASON_75[15:0]	16
0xBB00D930	STAT_PRVTE_DBG_CNTR76.RESERVED[31:16]	16
0xBB00D930	STAT_PRVTE_DBG_CNTR76.REASON_76[15:0]	16
0xBB00D934	STAT_PRVTE_DBG_CNTR77.RESERVED[31:16]	16
0xBB00D934	STAT_PRVTE_DBG_CNTR77.REASON_77[15:0]	16
0xBB00D938	STAT_PRVTE_DBG_CNTR78.RESERVED[31:16]	16
0xBB00D938	STAT_PRVTE_DBG_CNTR78.REASON_78[15:0]	16
0xBB00D93C	STAT_PRVTE_DBG_CNTR79.RESERVED[31:16]	16
0xBB00D93C	STAT_PRVTE_DBG_CNTR79.REASON_79[15:0]	16
0xBB00D820	STAT_PRVTE_DBG_CNTR8.RESERVED[31:16]	16
0xBB00D820	STAT_PRVTE_DBG_CNTR8.REASON_8[15:0]	16
0xBB00D940	STAT_PRVTE_DBG_CNTR80.RESERVED[31:16]	16
0xBB00D940	STAT_PRVTE_DBG_CNTR80.REASON_80[15:0]	16
0xBB00D824	STAT_PRVTE_DBG_CNTR9.RESERVED[31:16]	16
0xBB00D824	STAT_PRVTE_DBG_CNTR9.REASON_9[15:0]	16
0xBB007EF4	STAT_RST.RESERVED[31:2]	30
0xBB007EF4	STAT_RST.RST_MIB_VAL[1:1]	1
0xBB007EF4	STAT_RST.RST_GLB_MIB[0:0]	1
0xBB00145C	STK_DEV_PORT_MAP_CTRL [0].DEV_PORT_MAP[15:0]	16
0xBB00145C	STK_DEV_PORT_MAP_CTRL [1].DEV_PORT_MAP[31:16]	16
0xBB001460	STK_DEV_PORT_MAP_CTRL [2].DEV_PORT_MAP[15:0]	16
0xBB001460	STK_DEV_PORT_MAP_CTRL [3].DEV_PORT_MAP[31:16]	16
0xBB001464	STK_DEV_PORT_MAP_CTRL [4].DEV_PORT_MAP[15:0]	16
0xBB001464	STK_DEV_PORT_MAP_CTRL [5].DEV_PORT_MAP[31:16]	16
0xBB001468	STK_DEV_PORT_MAP_CTRL [6].DEV_PORT_MAP[15:0]	16
0xBB001468	STK_DEV_PORT_MAP_CTRL [7].DEV_PORT_MAP[31:16]	16
0xBB00146C	STK_DEV_PORT_MAP_CTRL [8].DEV_PORT_MAP[15:0]	16
0xBB00146C	STK_DEV_PORT_MAP_CTRL [9].DEV_PORT_MAP[31:16]	16
0xBB001470	STK_DEV_PORT_MAP_CTRL [10].DEV_PORT_MAP[15:0]	16
0xBB001470	STK_DEV_PORT_MAP_CTRL [11].DEV_PORT_MAP[31:16]	16
0xBB001474	STK_DEV_PORT_MAP_CTRL [12].DEV_PORT_MAP[15:0]	16
0xBB001474	STK_DEV_PORT_MAP_CTRL [13].DEV_PORT_MAP[31:16]	16
0xBB001478	STK_DEV_PORT_MAP_CTRL [14].DEV_PORT_MAP[15:0]	16
0xBB001478	STK_DEV_PORT_MAP_CTRL [15].DEV_PORT_MAP[31:16]	16
0xBB001448	STK_GBL_CTRL.RESERVED[31:9]	23
0xBB001448	STK_GBL_CTRL.DROP_MY_DEV[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB001448	STK_GBL_CTRL.MY_DEV_ID[7:4]	4
0xBB001448	STK_GBL_CTRL.MASTER_DEV_ID[3:0]	4
0xBB00147C	STK_NON_UNICAST_BLOCK_CTRL [0].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB00147C	STK_NON_UNICAST_BLOCK_CTRL [1].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB001480	STK_NON_UNICAST_BLOCK_CTRL [2].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB001480	STK_NON_UNICAST_BLOCK_CTRL [3].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB001484	STK_NON_UNICAST_BLOCK_CTRL [4].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB001484	STK_NON_UNICAST_BLOCK_CTRL [5].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB001488	STK_NON_UNICAST_BLOCK_CTRL [6].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB001488	STK_NON_UNICAST_BLOCK_CTRL [7].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB00148C	STK_NON_UNICAST_BLOCK_CTRL [8].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB00148C	STK_NON_UNICAST_BLOCK_CTRL [9].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB001490	STK_NON_UNICAST_BLOCK_CTRL [10].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB001490	STK_NON_UNICAST_BLOCK_CTRL [11].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB001494	STK_NON_UNICAST_BLOCK_CTRL [12].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB001494	STK_NON_UNICAST_BLOCK_CTRL [13].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB001498	STK_NON_UNICAST_BLOCK_CTRL [14].NON_UNICAST_BLOCK_PM[15:0]	16
0xBB001498	STK_NON_UNICAST_BLOCK_CTRL [15].NON_UNICAST_BLOCK_PM[31:16]	16
0xBB00144C	STK_PORT_ID_CTRL [0].STK_PORT_ID[5:0]	6
0xBB00144C	STK_PORT_ID_CTRL [1].STK_PORT_ID[11:6]	6
0xBB00144C	STK_PORT_ID_CTRL [2].STK_PORT_ID[17:12]	6
0xBB00144C	STK_PORT_ID_CTRL [3].STK_PORT_ID[23:18]	6
0xBB00144C	STK_PORT_ID_CTRL [4].STK_PORT_ID[29:24]	6
0xBB001450	STK_PORT_ID_CTRL [5].STK_PORT_ID[5:0]	6
0xBB001450	STK_PORT_ID_CTRL [6].STK_PORT_ID[11:6]	6
0xBB001450	STK_PORT_ID_CTRL [7].STK_PORT_ID[17:12]	6
0xBB001450	STK_PORT_ID_CTRL [8].STK_PORT_ID[23:18]	6
0xBB001450	STK_PORT_ID_CTRL [9].STK_PORT_ID[29:24]	6
0xBB001454	STK_PORT_ID_CTRL [10].STK_PORT_ID[5:0]	6
0xBB001454	STK_PORT_ID_CTRL [11].STK_PORT_ID[11:6]	6
0xBB001454	STK_PORT_ID_CTRL [12].STK_PORT_ID[17:12]	6
0xBB001454	STK_PORT_ID_CTRL [13].STK_PORT_ID[23:18]	6
0xBB001454	STK_PORT_ID_CTRL [14].STK_PORT_ID[29:24]	6
0xBB001458	STK_PORT_ID_CTRL [15].STK_PORT_ID[5:0]	6
0xBB00B000	STORM_CTRL.RESERVED[31:11]	21
0xBB00B000	STORM_CTRL.INC_BYPASS_PKT[10:10]	1
0xBB00B000	STORM_CTRL.INC_IFG[9:9]	1
0xBB00B000	STORM_CTRL.ARP_VLAN_CONSTR[8:8]	1
0xBB00B000	STORM_CTRL.DHCP_VLAN_CONSTR[7:7]	1
0xBB00B000	STORM_CTRL.ADMIT_RIP[6:6]	1
0xBB00B000	STORM_CTRL.ADMIT_DHCP[5:5]	1
0xBB00B000	STORM_CTRL.ADMIT_ARPREQ[4:4]	1
0xBB00B000	STORM_CTRL.ADMIT_RMA[3:3]	1
0xBB00B000	STORM_CTRL.ADMIT_BPDU[2:2]	1
0xBB00B000	STORM_CTRL.ADMIT_RTKPKT[1:1]	1
0xBB00B000	STORM_CTRL.ADMIT_IGMP[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B004	STORM_LB_CTRL.TICK[31:16]	16
0xBB00B004	STORM_LB_CTRL.TKN[15:0]	16
0xBB00B008	STORM_LB_PPS_CTRL.RESERVED[31:16]	16
0xBB00B008	STORM_LB_PPS_CTRL.TICK[15:4]	12
0xBB00B008	STORM_LB_PPS_CTRL.TKN[3:0]	4
0xBB00A000	STORM_LB_PROTO_CTRL.TICK[31:0]	32
0xBB00B3C4	STORM_PORT_BC_CTRL [0].RESERVED[31:25]	7
0xBB00B3C4	STORM_PORT_BC_CTRL [0].EN[24:24]	1
0xBB00B3C4	STORM_PORT_BC_CTRL [0].RATE[23:0]	24
0xBB00B3C8	STORM_PORT_BC_CTRL [0].RESERVED[31:16]	16
0xBB00B3C8	STORM_PORT_BC_CTRL [0].BURST[15:0]	16
0xBB00B3CC	STORM_PORT_BC_CTRL [1].RESERVED[31:25]	7
0xBB00B3CC	STORM_PORT_BC_CTRL [1].EN[24:24]	1
0xBB00B3CC	STORM_PORT_BC_CTRL [1].RATE[23:0]	24
0xBB00B3D0	STORM_PORT_BC_CTRL [1].RESERVED[31:16]	16
0xBB00B3D0	STORM_PORT_BC_CTRL [1].BURST[15:0]	16
0xBB00B3D4	STORM_PORT_BC_CTRL [2].RESERVED[31:25]	7
0xBB00B3D4	STORM_PORT_BC_CTRL [2].EN[24:24]	1
0xBB00B3D4	STORM_PORT_BC_CTRL [2].RATE[23:0]	24
0xBB00B3D8	STORM_PORT_BC_CTRL [2].RESERVED[31:16]	16
0xBB00B3D8	STORM_PORT_BC_CTRL [2].BURST[15:0]	16
0xBB00B3DC	STORM_PORT_BC_CTRL [3].RESERVED[31:25]	7
0xBB00B3DC	STORM_PORT_BC_CTRL [3].EN[24:24]	1
0xBB00B3DC	STORM_PORT_BC_CTRL [3].RATE[23:0]	24
0xBB00B3E0	STORM_PORT_BC_CTRL [3].RESERVED[31:16]	16
0xBB00B3E0	STORM_PORT_BC_CTRL [3].BURST[15:0]	16
0xBB00B3E4	STORM_PORT_BC_CTRL [4].RESERVED[31:25]	7
0xBB00B3E4	STORM_PORT_BC_CTRL [4].EN[24:24]	1
0xBB00B3E4	STORM_PORT_BC_CTRL [4].RATE[23:0]	24
0xBB00B3E8	STORM_PORT_BC_CTRL [4].RESERVED[31:16]	16
0xBB00B3E8	STORM_PORT_BC_CTRL [4].BURST[15:0]	16
0xBB00B3EC	STORM_PORT_BC_CTRL [5].RESERVED[31:25]	7
0xBB00B3EC	STORM_PORT_BC_CTRL [5].EN[24:24]	1
0xBB00B3EC	STORM_PORT_BC_CTRL [5].RATE[23:0]	24
0xBB00B3F0	STORM_PORT_BC_CTRL [5].RESERVED[31:16]	16
0xBB00B3F0	STORM_PORT_BC_CTRL [5].BURST[15:0]	16
0xBB00B3F4	STORM_PORT_BC_CTRL [6].RESERVED[31:25]	7
0xBB00B3F4	STORM_PORT_BC_CTRL [6].EN[24:24]	1
0xBB00B3F4	STORM_PORT_BC_CTRL [6].RATE[23:0]	24
0xBB00B3F8	STORM_PORT_BC_CTRL [6].RESERVED[31:16]	16
0xBB00B3F8	STORM_PORT_BC_CTRL [6].BURST[15:0]	16
0xBB00B3FC	STORM_PORT_BC_CTRL [7].RESERVED[31:25]	7
0xBB00B3FC	STORM_PORT_BC_CTRL [7].EN[24:24]	1
0xBB00B3FC	STORM_PORT_BC_CTRL [7].RATE[23:0]	24
0xBB00B400	STORM_PORT_BC_CTRL [7].RESERVED[31:16]	16
0xBB00B400	STORM_PORT_BC_CTRL [7].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B404	STORM_PORT_BC_CTRL [8].RESERVED[31:25]	7
0xBB00B404	STORM_PORT_BC_CTRL [8].EN[24:24]	1
0xBB00B404	STORM_PORT_BC_CTRL [8].RATE[23:0]	24
0xBB00B408	STORM_PORT_BC_CTRL [8].RESERVED[31:16]	16
0xBB00B408	STORM_PORT_BC_CTRL [8].BURST[15:0]	16
0xBB00B40C	STORM_PORT_BC_CTRL [9].RESERVED[31:25]	7
0xBB00B40C	STORM_PORT_BC_CTRL [9].EN[24:24]	1
0xBB00B40C	STORM_PORT_BC_CTRL [9].RATE[23:0]	24
0xBB00B410	STORM_PORT_BC_CTRL [9].RESERVED[31:16]	16
0xBB00B410	STORM_PORT_BC_CTRL [9].BURST[15:0]	16
0xBB00B414	STORM_PORT_BC_CTRL [10].RESERVED[31:25]	7
0xBB00B414	STORM_PORT_BC_CTRL [10].EN[24:24]	1
0xBB00B414	STORM_PORT_BC_CTRL [10].RATE[23:0]	24
0xBB00B418	STORM_PORT_BC_CTRL [10].RESERVED[31:16]	16
0xBB00B418	STORM_PORT_BC_CTRL [10].BURST[15:0]	16
0xBB00B41C	STORM_PORT_BC_CTRL [11].RESERVED[31:25]	7
0xBB00B41C	STORM_PORT_BC_CTRL [11].EN[24:24]	1
0xBB00B41C	STORM_PORT_BC_CTRL [11].RATE[23:0]	24
0xBB00B420	STORM_PORT_BC_CTRL [11].RESERVED[31:16]	16
0xBB00B420	STORM_PORT_BC_CTRL [11].BURST[15:0]	16
0xBB00B424	STORM_PORT_BC_CTRL [12].RESERVED[31:25]	7
0xBB00B424	STORM_PORT_BC_CTRL [12].EN[24:24]	1
0xBB00B424	STORM_PORT_BC_CTRL [12].RATE[23:0]	24
0xBB00B428	STORM_PORT_BC_CTRL [12].RESERVED[31:16]	16
0xBB00B428	STORM_PORT_BC_CTRL [12].BURST[15:0]	16
0xBB00B42C	STORM_PORT_BC_CTRL [13].RESERVED[31:25]	7
0xBB00B42C	STORM_PORT_BC_CTRL [13].EN[24:24]	1
0xBB00B42C	STORM_PORT_BC_CTRL [13].RATE[23:0]	24
0xBB00B430	STORM_PORT_BC_CTRL [13].RESERVED[31:16]	16
0xBB00B430	STORM_PORT_BC_CTRL [13].BURST[15:0]	16
0xBB00B434	STORM_PORT_BC_CTRL [14].RESERVED[31:25]	7
0xBB00B434	STORM_PORT_BC_CTRL [14].EN[24:24]	1
0xBB00B434	STORM_PORT_BC_CTRL [14].RATE[23:0]	24
0xBB00B438	STORM_PORT_BC_CTRL [14].RESERVED[31:16]	16
0xBB00B438	STORM_PORT_BC_CTRL [14].BURST[15:0]	16
0xBB00B43C	STORM_PORT_BC_CTRL [15].RESERVED[31:25]	7
0xBB00B43C	STORM_PORT_BC_CTRL [15].EN[24:24]	1
0xBB00B43C	STORM_PORT_BC_CTRL [15].RATE[23:0]	24
0xBB00B440	STORM_PORT_BC_CTRL [15].RESERVED[31:16]	16
0xBB00B440	STORM_PORT_BC_CTRL [15].BURST[15:0]	16
0xBB00B444	STORM_PORT_BC_CTRL [16].RESERVED[31:25]	7
0xBB00B444	STORM_PORT_BC_CTRL [16].EN[24:24]	1
0xBB00B444	STORM_PORT_BC_CTRL [16].RATE[23:0]	24
0xBB00B448	STORM_PORT_BC_CTRL [16].RESERVED[31:16]	16
0xBB00B448	STORM_PORT_BC_CTRL [16].BURST[15:0]	16
0xBB00B44C	STORM_PORT_BC_CTRL [17].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B44C	STORM_PORT_BC_CTRL [17].EN[24:24]	1
0xBB00B44C	STORM_PORT_BC_CTRL [17].RATE[23:0]	24
0xBB00B450	STORM_PORT_BC_CTRL [17].RESERVED[31:16]	16
0xBB00B450	STORM_PORT_BC_CTRL [17].BURST[15:0]	16
0xBB00B454	STORM_PORT_BC_CTRL [18].RESERVED[31:25]	7
0xBB00B454	STORM_PORT_BC_CTRL [18].EN[24:24]	1
0xBB00B454	STORM_PORT_BC_CTRL [18].RATE[23:0]	24
0xBB00B458	STORM_PORT_BC_CTRL [18].RESERVED[31:16]	16
0xBB00B458	STORM_PORT_BC_CTRL [18].BURST[15:0]	16
0xBB00B45C	STORM_PORT_BC_CTRL [19].RESERVED[31:25]	7
0xBB00B45C	STORM_PORT_BC_CTRL [19].EN[24:24]	1
0xBB00B45C	STORM_PORT_BC_CTRL [19].RATE[23:0]	24
0xBB00B460	STORM_PORT_BC_CTRL [19].RESERVED[31:16]	16
0xBB00B460	STORM_PORT_BC_CTRL [19].BURST[15:0]	16
0xBB00B464	STORM_PORT_BC_CTRL [20].RESERVED[31:25]	7
0xBB00B464	STORM_PORT_BC_CTRL [20].EN[24:24]	1
0xBB00B464	STORM_PORT_BC_CTRL [20].RATE[23:0]	24
0xBB00B468	STORM_PORT_BC_CTRL [20].RESERVED[31:16]	16
0xBB00B468	STORM_PORT_BC_CTRL [20].BURST[15:0]	16
0xBB00B46C	STORM_PORT_BC_CTRL [21].RESERVED[31:25]	7
0xBB00B46C	STORM_PORT_BC_CTRL [21].EN[24:24]	1
0xBB00B46C	STORM_PORT_BC_CTRL [21].RATE[23:0]	24
0xBB00B470	STORM_PORT_BC_CTRL [21].RESERVED[31:16]	16
0xBB00B470	STORM_PORT_BC_CTRL [21].BURST[15:0]	16
0xBB00B474	STORM_PORT_BC_CTRL [22].RESERVED[31:25]	7
0xBB00B474	STORM_PORT_BC_CTRL [22].EN[24:24]	1
0xBB00B474	STORM_PORT_BC_CTRL [22].RATE[23:0]	24
0xBB00B478	STORM_PORT_BC_CTRL [22].RESERVED[31:16]	16
0xBB00B478	STORM_PORT_BC_CTRL [22].BURST[15:0]	16
0xBB00B47C	STORM_PORT_BC_CTRL [23].RESERVED[31:25]	7
0xBB00B47C	STORM_PORT_BC_CTRL [23].EN[24:24]	1
0xBB00B47C	STORM_PORT_BC_CTRL [23].RATE[23:0]	24
0xBB00B480	STORM_PORT_BC_CTRL [23].RESERVED[31:16]	16
0xBB00B480	STORM_PORT_BC_CTRL [23].BURST[15:0]	16
0xBB00B484	STORM_PORT_BC_CTRL [24].RESERVED[31:25]	7
0xBB00B484	STORM_PORT_BC_CTRL [24].EN[24:24]	1
0xBB00B484	STORM_PORT_BC_CTRL [24].RATE[23:0]	24
0xBB00B488	STORM_PORT_BC_CTRL [24].RESERVED[31:16]	16
0xBB00B488	STORM_PORT_BC_CTRL [24].BURST[15:0]	16
0xBB00B48C	STORM_PORT_BC_CTRL [25].RESERVED[31:25]	7
0xBB00B48C	STORM_PORT_BC_CTRL [25].EN[24:24]	1
0xBB00B48C	STORM_PORT_BC_CTRL [25].RATE[23:0]	24
0xBB00B490	STORM_PORT_BC_CTRL [25].RESERVED[31:16]	16
0xBB00B490	STORM_PORT_BC_CTRL [25].BURST[15:0]	16
0xBB00B494	STORM_PORT_BC_CTRL [26].RESERVED[31:25]	7
0xBB00B494	STORM_PORT_BC_CTRL [26].EN[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B494	STORM_PORT_BC_CTRL [26].RATE[23:0]	24
0xBB00B498	STORM_PORT_BC_CTRL [26].RESERVED[31:16]	16
0xBB00B498	STORM_PORT_BC_CTRL [26].BURST[15:0]	16
0xBB00B49C	STORM_PORT_BC_CTRL [27].RESERVED[31:25]	7
0xBB00B49C	STORM_PORT_BC_CTRL [27].EN[24:24]	1
0xBB00B49C	STORM_PORT_BC_CTRL [27].RATE[23:0]	24
0xBB00B4A0	STORM_PORT_BC_CTRL [27].RESERVED[31:16]	16
0xBB00B4A0	STORM_PORT_BC_CTRL [27].BURST[15:0]	16
0xBB00B4A4	STORM_PORT_BC_CTRL [28].RESERVED[31:25]	7
0xBB00B4A4	STORM_PORT_BC_CTRL [28].EN[24:24]	1
0xBB00B4A4	STORM_PORT_BC_CTRL [28].RATE[23:0]	24
0xBB00B4A8	STORM_PORT_BC_CTRL [28].RESERVED[31:16]	16
0xBB00B4A8	STORM_PORT_BC_CTRL [28].BURST[15:0]	16
0xBB00B4AC	STORM_PORT_BC_CTRL [29].RESERVED[31:25]	7
0xBB00B4AC	STORM_PORT_BC_CTRL [29].EN[24:24]	1
0xBB00B4AC	STORM_PORT_BC_CTRL [29].RATE[23:0]	24
0xBB00B4B0	STORM_PORT_BC_CTRL [29].RESERVED[31:16]	16
0xBB00B4B0	STORM_PORT_BC_CTRL [29].BURST[15:0]	16
0xBB00B4B4	STORM_PORT_BC_CTRL [30].RESERVED[31:25]	7
0xBB00B4B4	STORM_PORT_BC_CTRL [30].EN[24:24]	1
0xBB00B4B4	STORM_PORT_BC_CTRL [30].RATE[23:0]	24
0xBB00B4B8	STORM_PORT_BC_CTRL [30].RESERVED[31:16]	16
0xBB00B4B8	STORM_PORT_BC_CTRL [30].BURST[15:0]	16
0xBB00B4BC	STORM_PORT_BC_CTRL [31].RESERVED[31:25]	7
0xBB00B4BC	STORM_PORT_BC_CTRL [31].EN[24:24]	1
0xBB00B4BC	STORM_PORT_BC_CTRL [31].RATE[23:0]	24
0xBB00B4C0	STORM_PORT_BC_CTRL [31].RESERVED[31:16]	16
0xBB00B4C0	STORM_PORT_BC_CTRL [31].BURST[15:0]	16
0xBB00B4C4	STORM_PORT_BC_CTRL [32].RESERVED[31:25]	7
0xBB00B4C4	STORM_PORT_BC_CTRL [32].EN[24:24]	1
0xBB00B4C4	STORM_PORT_BC_CTRL [32].RATE[23:0]	24
0xBB00B4C8	STORM_PORT_BC_CTRL [32].RESERVED[31:16]	16
0xBB00B4C8	STORM_PORT_BC_CTRL [32].BURST[15:0]	16
0xBB00B4CC	STORM_PORT_BC_CTRL [33].RESERVED[31:25]	7
0xBB00B4CC	STORM_PORT_BC_CTRL [33].EN[24:24]	1
0xBB00B4CC	STORM_PORT_BC_CTRL [33].RATE[23:0]	24
0xBB00B4D0	STORM_PORT_BC_CTRL [33].RESERVED[31:16]	16
0xBB00B4D0	STORM_PORT_BC_CTRL [33].BURST[15:0]	16
0xBB00B4D4	STORM_PORT_BC_CTRL [34].RESERVED[31:25]	7
0xBB00B4D4	STORM_PORT_BC_CTRL [34].EN[24:24]	1
0xBB00B4D4	STORM_PORT_BC_CTRL [34].RATE[23:0]	24
0xBB00B4D8	STORM_PORT_BC_CTRL [34].RESERVED[31:16]	16
0xBB00B4D8	STORM_PORT_BC_CTRL [34].BURST[15:0]	16
0xBB00B4DC	STORM_PORT_BC_CTRL [35].RESERVED[31:25]	7
0xBB00B4DC	STORM_PORT_BC_CTRL [35].EN[24:24]	1
0xBB00B4DC	STORM_PORT_BC_CTRL [35].RATE[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B4E0	STORM_PORT_BC_CTRL [35].RESERVED[31:16]	16
0xBB00B4E0	STORM_PORT_BC_CTRL [35].BURST[15:0]	16
0xBB00B4E4	STORM_PORT_BC_CTRL [36].RESERVED[31:25]	7
0xBB00B4E4	STORM_PORT_BC_CTRL [36].EN[24:24]	1
0xBB00B4E4	STORM_PORT_BC_CTRL [36].RATE[23:0]	24
0xBB00B4E8	STORM_PORT_BC_CTRL [36].RESERVED[31:16]	16
0xBB00B4E8	STORM_PORT_BC_CTRL [36].BURST[15:0]	16
0xBB00B4EC	STORM_PORT_BC_CTRL [37].RESERVED[31:25]	7
0xBB00B4EC	STORM_PORT_BC_CTRL [37].EN[24:24]	1
0xBB00B4EC	STORM_PORT_BC_CTRL [37].RATE[23:0]	24
0xBB00B4F0	STORM_PORT_BC_CTRL [37].RESERVED[31:16]	16
0xBB00B4F0	STORM_PORT_BC_CTRL [37].BURST[15:0]	16
0xBB00B4F4	STORM_PORT_BC_CTRL [38].RESERVED[31:25]	7
0xBB00B4F4	STORM_PORT_BC_CTRL [38].EN[24:24]	1
0xBB00B4F4	STORM_PORT_BC_CTRL [38].RATE[23:0]	24
0xBB00B4F8	STORM_PORT_BC_CTRL [38].RESERVED[31:16]	16
0xBB00B4F8	STORM_PORT_BC_CTRL [38].BURST[15:0]	16
0xBB00B4FC	STORM_PORT_BC_CTRL [39].RESERVED[31:25]	7
0xBB00B4FC	STORM_PORT_BC_CTRL [39].EN[24:24]	1
0xBB00B4FC	STORM_PORT_BC_CTRL [39].RATE[23:0]	24
0xBB00B500	STORM_PORT_BC_CTRL [39].RESERVED[31:16]	16
0xBB00B500	STORM_PORT_BC_CTRL [39].BURST[15:0]	16
0xBB00B504	STORM_PORT_BC_CTRL [40].RESERVED[31:25]	7
0xBB00B504	STORM_PORT_BC_CTRL [40].EN[24:24]	1
0xBB00B504	STORM_PORT_BC_CTRL [40].RATE[23:0]	24
0xBB00B508	STORM_PORT_BC_CTRL [40].RESERVED[31:16]	16
0xBB00B508	STORM_PORT_BC_CTRL [40].BURST[15:0]	16
0xBB00B50C	STORM_PORT_BC_CTRL [41].RESERVED[31:25]	7
0xBB00B50C	STORM_PORT_BC_CTRL [41].EN[24:24]	1
0xBB00B50C	STORM_PORT_BC_CTRL [41].RATE[23:0]	24
0xBB00B510	STORM_PORT_BC_CTRL [41].RESERVED[31:16]	16
0xBB00B510	STORM_PORT_BC_CTRL [41].BURST[15:0]	16
0xBB00B514	STORM_PORT_BC_CTRL [42].RESERVED[31:25]	7
0xBB00B514	STORM_PORT_BC_CTRL [42].EN[24:24]	1
0xBB00B514	STORM_PORT_BC_CTRL [42].RATE[23:0]	24
0xBB00B518	STORM_PORT_BC_CTRL [42].RESERVED[31:16]	16
0xBB00B518	STORM_PORT_BC_CTRL [42].BURST[15:0]	16
0xBB00B51C	STORM_PORT_BC_CTRL [43].RESERVED[31:25]	7
0xBB00B51C	STORM_PORT_BC_CTRL [43].EN[24:24]	1
0xBB00B51C	STORM_PORT_BC_CTRL [43].RATE[23:0]	24
0xBB00B520	STORM_PORT_BC_CTRL [43].RESERVED[31:16]	16
0xBB00B520	STORM_PORT_BC_CTRL [43].BURST[15:0]	16
0xBB00B524	STORM_PORT_BC_CTRL [44].RESERVED[31:25]	7
0xBB00B524	STORM_PORT_BC_CTRL [44].EN[24:24]	1
0xBB00B524	STORM_PORT_BC_CTRL [44].RATE[23:0]	24
0xBB00B528	STORM_PORT_BC_CTRL [44].RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B528	STORM_PORT_BC_CTRL [44].BURST[15:0]	16
0xBB00B52C	STORM_PORT_BC_CTRL [45].RESERVED[31:25]	7
0xBB00B52C	STORM_PORT_BC_CTRL [45].EN[24:24]	1
0xBB00B52C	STORM_PORT_BC_CTRL [45].RATE[23:0]	24
0xBB00B530	STORM_PORT_BC_CTRL [45].RESERVED[31:16]	16
0xBB00B530	STORM_PORT_BC_CTRL [45].BURST[15:0]	16
0xBB00B534	STORM_PORT_BC_CTRL [46].RESERVED[31:25]	7
0xBB00B534	STORM_PORT_BC_CTRL [46].EN[24:24]	1
0xBB00B534	STORM_PORT_BC_CTRL [46].RATE[23:0]	24
0xBB00B538	STORM_PORT_BC_CTRL [46].RESERVED[31:16]	16
0xBB00B538	STORM_PORT_BC_CTRL [46].BURST[15:0]	16
0xBB00B53C	STORM_PORT_BC_CTRL [47].RESERVED[31:25]	7
0xBB00B53C	STORM_PORT_BC_CTRL [47].EN[24:24]	1
0xBB00B53C	STORM_PORT_BC_CTRL [47].RATE[23:0]	24
0xBB00B540	STORM_PORT_BC_CTRL [47].RESERVED[31:16]	16
0xBB00B540	STORM_PORT_BC_CTRL [47].BURST[15:0]	16
0xBB00B544	STORM_PORT_BC_CTRL [48].RESERVED[31:25]	7
0xBB00B544	STORM_PORT_BC_CTRL [48].EN[24:24]	1
0xBB00B544	STORM_PORT_BC_CTRL [48].RATE[23:0]	24
0xBB00B548	STORM_PORT_BC_CTRL [48].RESERVED[31:16]	16
0xBB00B548	STORM_PORT_BC_CTRL [48].BURST[15:0]	16
0xBB00B54C	STORM_PORT_BC_CTRL [49].RESERVED[31:25]	7
0xBB00B54C	STORM_PORT_BC_CTRL [49].EN[24:24]	1
0xBB00B54C	STORM_PORT_BC_CTRL [49].RATE[23:0]	24
0xBB00B550	STORM_PORT_BC_CTRL [49].RESERVED[31:16]	16
0xBB00B550	STORM_PORT_BC_CTRL [49].BURST[15:0]	16
0xBB00B554	STORM_PORT_BC_CTRL [50].RESERVED[31:25]	7
0xBB00B554	STORM_PORT_BC_CTRL [50].EN[24:24]	1
0xBB00B554	STORM_PORT_BC_CTRL [50].RATE[23:0]	24
0xBB00B558	STORM_PORT_BC_CTRL [50].RESERVED[31:16]	16
0xBB00B558	STORM_PORT_BC_CTRL [50].BURST[15:0]	16
0xBB00B55C	STORM_PORT_BC_CTRL [51].RESERVED[31:25]	7
0xBB00B55C	STORM_PORT_BC_CTRL [51].EN[24:24]	1
0xBB00B55C	STORM_PORT_BC_CTRL [51].RATE[23:0]	24
0xBB00B560	STORM_PORT_BC_CTRL [51].RESERVED[31:16]	16
0xBB00B560	STORM_PORT_BC_CTRL [51].BURST[15:0]	16
0xBB00B564	STORM_PORT_BC_CTRL [52].RESERVED[31:25]	7
0xBB00B564	STORM_PORT_BC_CTRL [52].EN[24:24]	1
0xBB00B564	STORM_PORT_BC_CTRL [52].RATE[23:0]	24
0xBB00B568	STORM_PORT_BC_CTRL [52].RESERVED[31:16]	16
0xBB00B568	STORM_PORT_BC_CTRL [52].BURST[15:0]	16
0xBB00B56C	STORM_PORT_BC_CTRL [53].RESERVED[31:25]	7
0xBB00B56C	STORM_PORT_BC_CTRL [53].EN[24:24]	1
0xBB00B56C	STORM_PORT_BC_CTRL [53].RATE[23:0]	24
0xBB00B570	STORM_PORT_BC_CTRL [53].RESERVED[31:16]	16
0xBB00B570	STORM_PORT_BC_CTRL [53].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B574	STORM_PORT_BC_CTRL [54].RESERVED[31:25]	7
0xBB00B574	STORM_PORT_BC_CTRL [54].EN[24:24]	1
0xBB00B574	STORM_PORT_BC_CTRL [54].RATE[23:0]	24
0xBB00B578	STORM_PORT_BC_CTRL [54].RESERVED[31:16]	16
0xBB00B578	STORM_PORT_BC_CTRL [54].BURST[15:0]	16
0xBB00B57C	STORM_PORT_BC_CTRL [55].RESERVED[31:25]	7
0xBB00B57C	STORM_PORT_BC_CTRL [55].EN[24:24]	1
0xBB00B57C	STORM_PORT_BC_CTRL [55].RATE[23:0]	24
0xBB00B580	STORM_PORT_BC_CTRL [55].RESERVED[31:16]	16
0xBB00B580	STORM_PORT_BC_CTRL [55].BURST[15:0]	16
0xBB00B584	STORM_PORT_BC_CTRL [56].RESERVED[31:25]	7
0xBB00B584	STORM_PORT_BC_CTRL [56].EN[24:24]	1
0xBB00B584	STORM_PORT_BC_CTRL [56].RATE[23:0]	24
0xBB00B588	STORM_PORT_BC_CTRL [56].RESERVED[31:16]	16
0xBB00B588	STORM_PORT_BC_CTRL [56].BURST[15:0]	16
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [30].FLAG[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B594	STORM_PORT_BC_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00B598	STORM_PORT_BC_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [0].RST[0:0]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [1].RST[1:1]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [2].RST[2:2]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [3].RST[3:3]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [4].RST[4:4]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [5].RST[5:5]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [6].RST[6:6]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [7].RST[7:7]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [8].RST[8:8]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [9].RST[9:9]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [10].RST[10:10]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [11].RST[11:11]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [12].RST[12:12]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [13].RST[13:13]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [14].RST[14:14]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [15].RST[15:15]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [16].RST[16:16]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [17].RST[17:17]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [18].RST[18:18]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [19].RST[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B58C	STORM_PORT_BC_LB_RST [20].RST[20:20]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [21].RST[21:21]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [22].RST[22:22]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [23].RST[23:23]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [24].RST[24:24]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [25].RST[25:25]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [26].RST[26:26]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [27].RST[27:27]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [28].RST[28:28]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [29].RST[29:29]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [30].RST[30:30]	1
0xBB00B58C	STORM_PORT_BC_LB_RST [31].RST[31:31]	1
0xBB00B590	STORM_PORT_BC_LB_RST [32].RST[0:0]	1
0xBB00B590	STORM_PORT_BC_LB_RST [33].RST[1:1]	1
0xBB00B590	STORM_PORT_BC_LB_RST [34].RST[2:2]	1
0xBB00B590	STORM_PORT_BC_LB_RST [35].RST[3:3]	1
0xBB00B590	STORM_PORT_BC_LB_RST [36].RST[4:4]	1
0xBB00B590	STORM_PORT_BC_LB_RST [37].RST[5:5]	1
0xBB00B590	STORM_PORT_BC_LB_RST [38].RST[6:6]	1
0xBB00B590	STORM_PORT_BC_LB_RST [39].RST[7:7]	1
0xBB00B590	STORM_PORT_BC_LB_RST [40].RST[8:8]	1
0xBB00B590	STORM_PORT_BC_LB_RST [41].RST[9:9]	1
0xBB00B590	STORM_PORT_BC_LB_RST [42].RST[10:10]	1
0xBB00B590	STORM_PORT_BC_LB_RST [43].RST[11:11]	1
0xBB00B590	STORM_PORT_BC_LB_RST [44].RST[12:12]	1
0xBB00B590	STORM_PORT_BC_LB_RST [45].RST[13:13]	1
0xBB00B590	STORM_PORT_BC_LB_RST [46].RST[14:14]	1
0xBB00B590	STORM_PORT_BC_LB_RST [47].RST[15:15]	1
0xBB00B590	STORM_PORT_BC_LB_RST [48].RST[16:16]	1
0xBB00B590	STORM_PORT_BC_LB_RST [49].RST[17:17]	1
0xBB00B590	STORM_PORT_BC_LB_RST [50].RST[18:18]	1
0xBB00B590	STORM_PORT_BC_LB_RST [51].RST[19:19]	1
0xBB00B590	STORM_PORT_BC_LB_RST [52].RST[20:20]	1
0xBB00B590	STORM_PORT_BC_LB_RST [53].RST[21:21]	1
0xBB00B590	STORM_PORT_BC_LB_RST [54].RST[22:22]	1
0xBB00B590	STORM_PORT_BC_LB_RST [55].RST[23:23]	1
0xBB00B590	STORM_PORT_BC_LB_RST [56].RST[24:24]	1
0xBB00B00C	STORM_PORT_CTRL [0].MODE[0:0]	1
0xBB00B00C	STORM_PORT_CTRL [1].MODE[1:1]	1
0xBB00B00C	STORM_PORT_CTRL [2].MODE[2:2]	1
0xBB00B00C	STORM_PORT_CTRL [3].MODE[3:3]	1
0xBB00B00C	STORM_PORT_CTRL [4].MODE[4:4]	1
0xBB00B00C	STORM_PORT_CTRL [5].MODE[5:5]	1
0xBB00B00C	STORM_PORT_CTRL [6].MODE[6:6]	1
0xBB00B00C	STORM_PORT_CTRL [7].MODE[7:7]	1
0xBB00B00C	STORM_PORT_CTRL [8].MODE[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B00C	STORM_PORT_CTRL [9].MODE[9:9]	1
0xBB00B00C	STORM_PORT_CTRL [10].MODE[10:10]	1
0xBB00B00C	STORM_PORT_CTRL [11].MODE[11:11]	1
0xBB00B00C	STORM_PORT_CTRL [12].MODE[12:12]	1
0xBB00B00C	STORM_PORT_CTRL [13].MODE[13:13]	1
0xBB00B00C	STORM_PORT_CTRL [14].MODE[14:14]	1
0xBB00B00C	STORM_PORT_CTRL [15].MODE[15:15]	1
0xBB00B00C	STORM_PORT_CTRL [16].MODE[16:16]	1
0xBB00B00C	STORM_PORT_CTRL [17].MODE[17:17]	1
0xBB00B00C	STORM_PORT_CTRL [18].MODE[18:18]	1
0xBB00B00C	STORM_PORT_CTRL [19].MODE[19:19]	1
0xBB00B00C	STORM_PORT_CTRL [20].MODE[20:20]	1
0xBB00B00C	STORM_PORT_CTRL [21].MODE[21:21]	1
0xBB00B00C	STORM_PORT_CTRL [22].MODE[22:22]	1
0xBB00B00C	STORM_PORT_CTRL [23].MODE[23:23]	1
0xBB00B00C	STORM_PORT_CTRL [24].MODE[24:24]	1
0xBB00B00C	STORM_PORT_CTRL [25].MODE[25:25]	1
0xBB00B00C	STORM_PORT_CTRL [26].MODE[26:26]	1
0xBB00B00C	STORM_PORT_CTRL [27].MODE[27:27]	1
0xBB00B00C	STORM_PORT_CTRL [28].MODE[28:28]	1
0xBB00B00C	STORM_PORT_CTRL [29].MODE[29:29]	1
0xBB00B00C	STORM_PORT_CTRL [30].MODE[30:30]	1
0xBB00B00C	STORM_PORT_CTRL [31].MODE[31:31]	1
0xBB00B010	STORM_PORT_CTRL [32].MODE[0:0]	1
0xBB00B010	STORM_PORT_CTRL [33].MODE[1:1]	1
0xBB00B010	STORM_PORT_CTRL [34].MODE[2:2]	1
0xBB00B010	STORM_PORT_CTRL [35].MODE[3:3]	1
0xBB00B010	STORM_PORT_CTRL [36].MODE[4:4]	1
0xBB00B010	STORM_PORT_CTRL [37].MODE[5:5]	1
0xBB00B010	STORM_PORT_CTRL [38].MODE[6:6]	1
0xBB00B010	STORM_PORT_CTRL [39].MODE[7:7]	1
0xBB00B010	STORM_PORT_CTRL [40].MODE[8:8]	1
0xBB00B010	STORM_PORT_CTRL [41].MODE[9:9]	1
0xBB00B010	STORM_PORT_CTRL [42].MODE[10:10]	1
0xBB00B010	STORM_PORT_CTRL [43].MODE[11:11]	1
0xBB00B010	STORM_PORT_CTRL [44].MODE[12:12]	1
0xBB00B010	STORM_PORT_CTRL [45].MODE[13:13]	1
0xBB00B010	STORM_PORT_CTRL [46].MODE[14:14]	1
0xBB00B010	STORM_PORT_CTRL [47].MODE[15:15]	1
0xBB00B010	STORM_PORT_CTRL [48].MODE[16:16]	1
0xBB00B010	STORM_PORT_CTRL [49].MODE[17:17]	1
0xBB00B010	STORM_PORT_CTRL [50].MODE[18:18]	1
0xBB00B010	STORM_PORT_CTRL [51].MODE[19:19]	1
0xBB00B010	STORM_PORT_CTRL [52].MODE[20:20]	1
0xBB00B010	STORM_PORT_CTRL [53].MODE[21:21]	1
0xBB00B010	STORM_PORT_CTRL [54].MODE[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B010	STORM_PORT_CTRL [55].MODE[23:23]	1
0xBB00B010	STORM_PORT_CTRL [56].MODE[24:24]	1
0xBB00B1EC	STORM_PORT_MC_CTRL [0].RESERVED[31:26]	6
0xBB00B1EC	STORM_PORT_MC_CTRL [0].TYPE[25:25]	1
0xBB00B1EC	STORM_PORT_MC_CTRL [0].EN[24:24]	1
0xBB00B1EC	STORM_PORT_MC_CTRL [0].RATE[23:0]	24
0xBB00B1F0	STORM_PORT_MC_CTRL [0].RESERVED[31:16]	16
0xBB00B1F0	STORM_PORT_MC_CTRL [0].BURST[15:0]	16
0xBB00B1F4	STORM_PORT_MC_CTRL [1].RESERVED[31:26]	6
0xBB00B1F4	STORM_PORT_MC_CTRL [1].TYPE[25:25]	1
0xBB00B1F4	STORM_PORT_MC_CTRL [1].EN[24:24]	1
0xBB00B1F4	STORM_PORT_MC_CTRL [1].RATE[23:0]	24
0xBB00B1F8	STORM_PORT_MC_CTRL [1].RESERVED[31:16]	16
0xBB00B1F8	STORM_PORT_MC_CTRL [1].BURST[15:0]	16
0xBB00B1FC	STORM_PORT_MC_CTRL [2].RESERVED[31:26]	6
0xBB00B1FC	STORM_PORT_MC_CTRL [2].TYPE[25:25]	1
0xBB00B1FC	STORM_PORT_MC_CTRL [2].EN[24:24]	1
0xBB00B1FC	STORM_PORT_MC_CTRL [2].RATE[23:0]	24
0xBB00B200	STORM_PORT_MC_CTRL [2].RESERVED[31:16]	16
0xBB00B200	STORM_PORT_MC_CTRL [2].BURST[15:0]	16
0xBB00B204	STORM_PORT_MC_CTRL [3].RESERVED[31:26]	6
0xBB00B204	STORM_PORT_MC_CTRL [3].TYPE[25:25]	1
0xBB00B204	STORM_PORT_MC_CTRL [3].EN[24:24]	1
0xBB00B204	STORM_PORT_MC_CTRL [3].RATE[23:0]	24
0xBB00B208	STORM_PORT_MC_CTRL [3].RESERVED[31:16]	16
0xBB00B208	STORM_PORT_MC_CTRL [3].BURST[15:0]	16
0xBB00B20C	STORM_PORT_MC_CTRL [4].RESERVED[31:26]	6
0xBB00B20C	STORM_PORT_MC_CTRL [4].TYPE[25:25]	1
0xBB00B20C	STORM_PORT_MC_CTRL [4].EN[24:24]	1
0xBB00B20C	STORM_PORT_MC_CTRL [4].RATE[23:0]	24
0xBB00B210	STORM_PORT_MC_CTRL [4].RESERVED[31:16]	16
0xBB00B210	STORM_PORT_MC_CTRL [4].BURST[15:0]	16
0xBB00B214	STORM_PORT_MC_CTRL [5].RESERVED[31:26]	6
0xBB00B214	STORM_PORT_MC_CTRL [5].TYPE[25:25]	1
0xBB00B214	STORM_PORT_MC_CTRL [5].EN[24:24]	1
0xBB00B214	STORM_PORT_MC_CTRL [5].RATE[23:0]	24
0xBB00B218	STORM_PORT_MC_CTRL [5].RESERVED[31:16]	16
0xBB00B218	STORM_PORT_MC_CTRL [5].BURST[15:0]	16
0xBB00B21C	STORM_PORT_MC_CTRL [6].RESERVED[31:26]	6
0xBB00B21C	STORM_PORT_MC_CTRL [6].TYPE[25:25]	1
0xBB00B21C	STORM_PORT_MC_CTRL [6].EN[24:24]	1
0xBB00B21C	STORM_PORT_MC_CTRL [6].RATE[23:0]	24
0xBB00B220	STORM_PORT_MC_CTRL [6].RESERVED[31:16]	16
0xBB00B220	STORM_PORT_MC_CTRL [6].BURST[15:0]	16
0xBB00B224	STORM_PORT_MC_CTRL [7].RESERVED[31:26]	6
0xBB00B224	STORM_PORT_MC_CTRL [7].TYPE[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B224	STORM_PORT_MC_CTRL [7].EN[24:24]	1
0xBB00B224	STORM_PORT_MC_CTRL [7].RATE[23:0]	24
0xBB00B228	STORM_PORT_MC_CTRL [7].RESERVED[31:16]	16
0xBB00B228	STORM_PORT_MC_CTRL [7].BURST[15:0]	16
0xBB00B22C	STORM_PORT_MC_CTRL [8].RESERVED[31:26]	6
0xBB00B22C	STORM_PORT_MC_CTRL [8].TYPE[25:25]	1
0xBB00B22C	STORM_PORT_MC_CTRL [8].EN[24:24]	1
0xBB00B22C	STORM_PORT_MC_CTRL [8].RATE[23:0]	24
0xBB00B230	STORM_PORT_MC_CTRL [8].RESERVED[31:16]	16
0xBB00B230	STORM_PORT_MC_CTRL [8].BURST[15:0]	16
0xBB00B234	STORM_PORT_MC_CTRL [9].RESERVED[31:26]	6
0xBB00B234	STORM_PORT_MC_CTRL [9].TYPE[25:25]	1
0xBB00B234	STORM_PORT_MC_CTRL [9].EN[24:24]	1
0xBB00B234	STORM_PORT_MC_CTRL [9].RATE[23:0]	24
0xBB00B238	STORM_PORT_MC_CTRL [9].RESERVED[31:16]	16
0xBB00B238	STORM_PORT_MC_CTRL [9].BURST[15:0]	16
0xBB00B23C	STORM_PORT_MC_CTRL [10].RESERVED[31:26]	6
0xBB00B23C	STORM_PORT_MC_CTRL [10].TYPE[25:25]	1
0xBB00B23C	STORM_PORT_MC_CTRL [10].EN[24:24]	1
0xBB00B23C	STORM_PORT_MC_CTRL [10].RATE[23:0]	24
0xBB00B240	STORM_PORT_MC_CTRL [10].RESERVED[31:16]	16
0xBB00B240	STORM_PORT_MC_CTRL [10].BURST[15:0]	16
0xBB00B244	STORM_PORT_MC_CTRL [11].RESERVED[31:26]	6
0xBB00B244	STORM_PORT_MC_CTRL [11].TYPE[25:25]	1
0xBB00B244	STORM_PORT_MC_CTRL [11].EN[24:24]	1
0xBB00B244	STORM_PORT_MC_CTRL [11].RATE[23:0]	24
0xBB00B248	STORM_PORT_MC_CTRL [11].RESERVED[31:16]	16
0xBB00B248	STORM_PORT_MC_CTRL [11].BURST[15:0]	16
0xBB00B24C	STORM_PORT_MC_CTRL [12].RESERVED[31:26]	6
0xBB00B24C	STORM_PORT_MC_CTRL [12].TYPE[25:25]	1
0xBB00B24C	STORM_PORT_MC_CTRL [12].EN[24:24]	1
0xBB00B24C	STORM_PORT_MC_CTRL [12].RATE[23:0]	24
0xBB00B250	STORM_PORT_MC_CTRL [12].RESERVED[31:16]	16
0xBB00B250	STORM_PORT_MC_CTRL [12].BURST[15:0]	16
0xBB00B254	STORM_PORT_MC_CTRL [13].RESERVED[31:26]	6
0xBB00B254	STORM_PORT_MC_CTRL [13].TYPE[25:25]	1
0xBB00B254	STORM_PORT_MC_CTRL [13].EN[24:24]	1
0xBB00B254	STORM_PORT_MC_CTRL [13].RATE[23:0]	24
0xBB00B258	STORM_PORT_MC_CTRL [13].RESERVED[31:16]	16
0xBB00B258	STORM_PORT_MC_CTRL [13].BURST[15:0]	16
0xBB00B25C	STORM_PORT_MC_CTRL [14].RESERVED[31:26]	6
0xBB00B25C	STORM_PORT_MC_CTRL [14].TYPE[25:25]	1
0xBB00B25C	STORM_PORT_MC_CTRL [14].EN[24:24]	1
0xBB00B25C	STORM_PORT_MC_CTRL [14].RATE[23:0]	24
0xBB00B260	STORM_PORT_MC_CTRL [14].RESERVED[31:16]	16
0xBB00B260	STORM_PORT_MC_CTRL [14].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B264	STORM_PORT_MC_CTRL [15].RESERVED[31:26]	6
0xBB00B264	STORM_PORT_MC_CTRL [15].TYPE[25:25]	1
0xBB00B264	STORM_PORT_MC_CTRL [15].EN[24:24]	1
0xBB00B264	STORM_PORT_MC_CTRL [15].RATE[23:0]	24
0xBB00B268	STORM_PORT_MC_CTRL [15].RESERVED[31:16]	16
0xBB00B268	STORM_PORT_MC_CTRL [15].BURST[15:0]	16
0xBB00B26C	STORM_PORT_MC_CTRL [16].RESERVED[31:26]	6
0xBB00B26C	STORM_PORT_MC_CTRL [16].TYPE[25:25]	1
0xBB00B26C	STORM_PORT_MC_CTRL [16].EN[24:24]	1
0xBB00B26C	STORM_PORT_MC_CTRL [16].RATE[23:0]	24
0xBB00B270	STORM_PORT_MC_CTRL [16].RESERVED[31:16]	16
0xBB00B270	STORM_PORT_MC_CTRL [16].BURST[15:0]	16
0xBB00B274	STORM_PORT_MC_CTRL [17].RESERVED[31:26]	6
0xBB00B274	STORM_PORT_MC_CTRL [17].TYPE[25:25]	1
0xBB00B274	STORM_PORT_MC_CTRL [17].EN[24:24]	1
0xBB00B274	STORM_PORT_MC_CTRL [17].RATE[23:0]	24
0xBB00B278	STORM_PORT_MC_CTRL [17].RESERVED[31:16]	16
0xBB00B278	STORM_PORT_MC_CTRL [17].BURST[15:0]	16
0xBB00B27C	STORM_PORT_MC_CTRL [18].RESERVED[31:26]	6
0xBB00B27C	STORM_PORT_MC_CTRL [18].TYPE[25:25]	1
0xBB00B27C	STORM_PORT_MC_CTRL [18].EN[24:24]	1
0xBB00B27C	STORM_PORT_MC_CTRL [18].RATE[23:0]	24
0xBB00B280	STORM_PORT_MC_CTRL [18].RESERVED[31:16]	16
0xBB00B280	STORM_PORT_MC_CTRL [18].BURST[15:0]	16
0xBB00B284	STORM_PORT_MC_CTRL [19].RESERVED[31:26]	6
0xBB00B284	STORM_PORT_MC_CTRL [19].TYPE[25:25]	1
0xBB00B284	STORM_PORT_MC_CTRL [19].EN[24:24]	1
0xBB00B284	STORM_PORT_MC_CTRL [19].RATE[23:0]	24
0xBB00B288	STORM_PORT_MC_CTRL [19].RESERVED[31:16]	16
0xBB00B288	STORM_PORT_MC_CTRL [19].BURST[15:0]	16
0xBB00B28C	STORM_PORT_MC_CTRL [20].RESERVED[31:26]	6
0xBB00B28C	STORM_PORT_MC_CTRL [20].TYPE[25:25]	1
0xBB00B28C	STORM_PORT_MC_CTRL [20].EN[24:24]	1
0xBB00B28C	STORM_PORT_MC_CTRL [20].RATE[23:0]	24
0xBB00B290	STORM_PORT_MC_CTRL [20].RESERVED[31:16]	16
0xBB00B290	STORM_PORT_MC_CTRL [20].BURST[15:0]	16
0xBB00B294	STORM_PORT_MC_CTRL [21].RESERVED[31:26]	6
0xBB00B294	STORM_PORT_MC_CTRL [21].TYPE[25:25]	1
0xBB00B294	STORM_PORT_MC_CTRL [21].EN[24:24]	1
0xBB00B294	STORM_PORT_MC_CTRL [21].RATE[23:0]	24
0xBB00B298	STORM_PORT_MC_CTRL [21].RESERVED[31:16]	16
0xBB00B298	STORM_PORT_MC_CTRL [21].BURST[15:0]	16
0xBB00B29C	STORM_PORT_MC_CTRL [22].RESERVED[31:26]	6
0xBB00B29C	STORM_PORT_MC_CTRL [22].TYPE[25:25]	1
0xBB00B29C	STORM_PORT_MC_CTRL [22].EN[24:24]	1
0xBB00B29C	STORM_PORT_MC_CTRL [22].RATE[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B2A0	STORM_PORT_MC_CTRL [22].RESERVED[31:16]	16
0xBB00B2A0	STORM_PORT_MC_CTRL [22].BURST[15:0]	16
0xBB00B2A4	STORM_PORT_MC_CTRL [23].RESERVED[31:26]	6
0xBB00B2A4	STORM_PORT_MC_CTRL [23].TYPE[25:25]	1
0xBB00B2A4	STORM_PORT_MC_CTRL [23].EN[24:24]	1
0xBB00B2A4	STORM_PORT_MC_CTRL [23].RATE[23:0]	24
0xBB00B2A8	STORM_PORT_MC_CTRL [23].RESERVED[31:16]	16
0xBB00B2A8	STORM_PORT_MC_CTRL [23].BURST[15:0]	16
0xBB00B2AC	STORM_PORT_MC_CTRL [24].RESERVED[31:26]	6
0xBB00B2AC	STORM_PORT_MC_CTRL [24].TYPE[25:25]	1
0xBB00B2AC	STORM_PORT_MC_CTRL [24].EN[24:24]	1
0xBB00B2AC	STORM_PORT_MC_CTRL [24].RATE[23:0]	24
0xBB00B2B0	STORM_PORT_MC_CTRL [24].RESERVED[31:16]	16
0xBB00B2B0	STORM_PORT_MC_CTRL [24].BURST[15:0]	16
0xBB00B2B4	STORM_PORT_MC_CTRL [25].RESERVED[31:26]	6
0xBB00B2B4	STORM_PORT_MC_CTRL [25].TYPE[25:25]	1
0xBB00B2B4	STORM_PORT_MC_CTRL [25].EN[24:24]	1
0xBB00B2B4	STORM_PORT_MC_CTRL [25].RATE[23:0]	24
0xBB00B2B8	STORM_PORT_MC_CTRL [25].RESERVED[31:16]	16
0xBB00B2B8	STORM_PORT_MC_CTRL [25].BURST[15:0]	16
0xBB00B2BC	STORM_PORT_MC_CTRL [26].RESERVED[31:26]	6
0xBB00B2BC	STORM_PORT_MC_CTRL [26].TYPE[25:25]	1
0xBB00B2BC	STORM_PORT_MC_CTRL [26].EN[24:24]	1
0xBB00B2BC	STORM_PORT_MC_CTRL [26].RATE[23:0]	24
0xBB00B2C0	STORM_PORT_MC_CTRL [26].RESERVED[31:16]	16
0xBB00B2C0	STORM_PORT_MC_CTRL [26].BURST[15:0]	16
0xBB00B2C4	STORM_PORT_MC_CTRL [27].RESERVED[31:26]	6
0xBB00B2C4	STORM_PORT_MC_CTRL [27].TYPE[25:25]	1
0xBB00B2C4	STORM_PORT_MC_CTRL [27].EN[24:24]	1
0xBB00B2C4	STORM_PORT_MC_CTRL [27].RATE[23:0]	24
0xBB00B2C8	STORM_PORT_MC_CTRL [27].RESERVED[31:16]	16
0xBB00B2C8	STORM_PORT_MC_CTRL [27].BURST[15:0]	16
0xBB00B2CC	STORM_PORT_MC_CTRL [28].RESERVED[31:26]	6
0xBB00B2CC	STORM_PORT_MC_CTRL [28].TYPE[25:25]	1
0xBB00B2CC	STORM_PORT_MC_CTRL [28].EN[24:24]	1
0xBB00B2CC	STORM_PORT_MC_CTRL [28].RATE[23:0]	24
0xBB00B2D0	STORM_PORT_MC_CTRL [28].RESERVED[31:16]	16
0xBB00B2D0	STORM_PORT_MC_CTRL [28].BURST[15:0]	16
0xBB00B2D4	STORM_PORT_MC_CTRL [29].RESERVED[31:26]	6
0xBB00B2D4	STORM_PORT_MC_CTRL [29].TYPE[25:25]	1
0xBB00B2D4	STORM_PORT_MC_CTRL [29].EN[24:24]	1
0xBB00B2D4	STORM_PORT_MC_CTRL [29].RATE[23:0]	24
0xBB00B2D8	STORM_PORT_MC_CTRL [29].RESERVED[31:16]	16
0xBB00B2D8	STORM_PORT_MC_CTRL [29].BURST[15:0]	16
0xBB00B2DC	STORM_PORT_MC_CTRL [30].RESERVED[31:26]	6
0xBB00B2DC	STORM_PORT_MC_CTRL [30].TYPE[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B2DC	STORM_PORT_MC_CTRL [30].EN[24:24]	1
0xBB00B2DC	STORM_PORT_MC_CTRL [30].RATE[23:0]	24
0xBB00B2E0	STORM_PORT_MC_CTRL [30].RESERVED[31:16]	16
0xBB00B2E0	STORM_PORT_MC_CTRL [30].BURST[15:0]	16
0xBB00B2E4	STORM_PORT_MC_CTRL [31].RESERVED[31:26]	6
0xBB00B2E4	STORM_PORT_MC_CTRL [31].TYPE[25:25]	1
0xBB00B2E4	STORM_PORT_MC_CTRL [31].EN[24:24]	1
0xBB00B2E4	STORM_PORT_MC_CTRL [31].RATE[23:0]	24
0xBB00B2E8	STORM_PORT_MC_CTRL [31].RESERVED[31:16]	16
0xBB00B2E8	STORM_PORT_MC_CTRL [31].BURST[15:0]	16
0xBB00B2EC	STORM_PORT_MC_CTRL [32].RESERVED[31:26]	6
0xBB00B2EC	STORM_PORT_MC_CTRL [32].TYPE[25:25]	1
0xBB00B2EC	STORM_PORT_MC_CTRL [32].EN[24:24]	1
0xBB00B2EC	STORM_PORT_MC_CTRL [32].RATE[23:0]	24
0xBB00B2F0	STORM_PORT_MC_CTRL [32].RESERVED[31:16]	16
0xBB00B2F0	STORM_PORT_MC_CTRL [32].BURST[15:0]	16
0xBB00B2F4	STORM_PORT_MC_CTRL [33].RESERVED[31:26]	6
0xBB00B2F4	STORM_PORT_MC_CTRL [33].TYPE[25:25]	1
0xBB00B2F4	STORM_PORT_MC_CTRL [33].EN[24:24]	1
0xBB00B2F4	STORM_PORT_MC_CTRL [33].RATE[23:0]	24
0xBB00B2F8	STORM_PORT_MC_CTRL [33].RESERVED[31:16]	16
0xBB00B2F8	STORM_PORT_MC_CTRL [33].BURST[15:0]	16
0xBB00B2FC	STORM_PORT_MC_CTRL [34].RESERVED[31:26]	6
0xBB00B2FC	STORM_PORT_MC_CTRL [34].TYPE[25:25]	1
0xBB00B2FC	STORM_PORT_MC_CTRL [34].EN[24:24]	1
0xBB00B2FC	STORM_PORT_MC_CTRL [34].RATE[23:0]	24
0xBB00B300	STORM_PORT_MC_CTRL [34].RESERVED[31:16]	16
0xBB00B300	STORM_PORT_MC_CTRL [34].BURST[15:0]	16
0xBB00B304	STORM_PORT_MC_CTRL [35].RESERVED[31:26]	6
0xBB00B304	STORM_PORT_MC_CTRL [35].TYPE[25:25]	1
0xBB00B304	STORM_PORT_MC_CTRL [35].EN[24:24]	1
0xBB00B304	STORM_PORT_MC_CTRL [35].RATE[23:0]	24
0xBB00B308	STORM_PORT_MC_CTRL [35].RESERVED[31:16]	16
0xBB00B308	STORM_PORT_MC_CTRL [35].BURST[15:0]	16
0xBB00B30C	STORM_PORT_MC_CTRL [36].RESERVED[31:26]	6
0xBB00B30C	STORM_PORT_MC_CTRL [36].TYPE[25:25]	1
0xBB00B30C	STORM_PORT_MC_CTRL [36].EN[24:24]	1
0xBB00B30C	STORM_PORT_MC_CTRL [36].RATE[23:0]	24
0xBB00B310	STORM_PORT_MC_CTRL [36].RESERVED[31:16]	16
0xBB00B310	STORM_PORT_MC_CTRL [36].BURST[15:0]	16
0xBB00B314	STORM_PORT_MC_CTRL [37].RESERVED[31:26]	6
0xBB00B314	STORM_PORT_MC_CTRL [37].TYPE[25:25]	1
0xBB00B314	STORM_PORT_MC_CTRL [37].EN[24:24]	1
0xBB00B314	STORM_PORT_MC_CTRL [37].RATE[23:0]	24
0xBB00B318	STORM_PORT_MC_CTRL [37].RESERVED[31:16]	16
0xBB00B318	STORM_PORT_MC_CTRL [37].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B31C	STORM_PORT_MC_CTRL [38].RESERVED[31:26]	6
0xBB00B31C	STORM_PORT_MC_CTRL [38].TYPE[25:25]	1
0xBB00B31C	STORM_PORT_MC_CTRL [38].EN[24:24]	1
0xBB00B31C	STORM_PORT_MC_CTRL [38].RATE[23:0]	24
0xBB00B320	STORM_PORT_MC_CTRL [38].RESERVED[31:16]	16
0xBB00B320	STORM_PORT_MC_CTRL [38].BURST[15:0]	16
0xBB00B324	STORM_PORT_MC_CTRL [39].RESERVED[31:26]	6
0xBB00B324	STORM_PORT_MC_CTRL [39].TYPE[25:25]	1
0xBB00B324	STORM_PORT_MC_CTRL [39].EN[24:24]	1
0xBB00B324	STORM_PORT_MC_CTRL [39].RATE[23:0]	24
0xBB00B328	STORM_PORT_MC_CTRL [39].RESERVED[31:16]	16
0xBB00B328	STORM_PORT_MC_CTRL [39].BURST[15:0]	16
0xBB00B32C	STORM_PORT_MC_CTRL [40].RESERVED[31:26]	6
0xBB00B32C	STORM_PORT_MC_CTRL [40].TYPE[25:25]	1
0xBB00B32C	STORM_PORT_MC_CTRL [40].EN[24:24]	1
0xBB00B32C	STORM_PORT_MC_CTRL [40].RATE[23:0]	24
0xBB00B330	STORM_PORT_MC_CTRL [40].RESERVED[31:16]	16
0xBB00B330	STORM_PORT_MC_CTRL [40].BURST[15:0]	16
0xBB00B334	STORM_PORT_MC_CTRL [41].RESERVED[31:26]	6
0xBB00B334	STORM_PORT_MC_CTRL [41].TYPE[25:25]	1
0xBB00B334	STORM_PORT_MC_CTRL [41].EN[24:24]	1
0xBB00B334	STORM_PORT_MC_CTRL [41].RATE[23:0]	24
0xBB00B338	STORM_PORT_MC_CTRL [41].RESERVED[31:16]	16
0xBB00B338	STORM_PORT_MC_CTRL [41].BURST[15:0]	16
0xBB00B33C	STORM_PORT_MC_CTRL [42].RESERVED[31:26]	6
0xBB00B33C	STORM_PORT_MC_CTRL [42].TYPE[25:25]	1
0xBB00B33C	STORM_PORT_MC_CTRL [42].EN[24:24]	1
0xBB00B33C	STORM_PORT_MC_CTRL [42].RATE[23:0]	24
0xBB00B340	STORM_PORT_MC_CTRL [42].RESERVED[31:16]	16
0xBB00B340	STORM_PORT_MC_CTRL [42].BURST[15:0]	16
0xBB00B344	STORM_PORT_MC_CTRL [43].RESERVED[31:26]	6
0xBB00B344	STORM_PORT_MC_CTRL [43].TYPE[25:25]	1
0xBB00B344	STORM_PORT_MC_CTRL [43].EN[24:24]	1
0xBB00B344	STORM_PORT_MC_CTRL [43].RATE[23:0]	24
0xBB00B348	STORM_PORT_MC_CTRL [43].RESERVED[31:16]	16
0xBB00B348	STORM_PORT_MC_CTRL [43].BURST[15:0]	16
0xBB00B34C	STORM_PORT_MC_CTRL [44].RESERVED[31:26]	6
0xBB00B34C	STORM_PORT_MC_CTRL [44].TYPE[25:25]	1
0xBB00B34C	STORM_PORT_MC_CTRL [44].EN[24:24]	1
0xBB00B34C	STORM_PORT_MC_CTRL [44].RATE[23:0]	24
0xBB00B350	STORM_PORT_MC_CTRL [44].RESERVED[31:16]	16
0xBB00B350	STORM_PORT_MC_CTRL [44].BURST[15:0]	16
0xBB00B354	STORM_PORT_MC_CTRL [45].RESERVED[31:26]	6
0xBB00B354	STORM_PORT_MC_CTRL [45].TYPE[25:25]	1
0xBB00B354	STORM_PORT_MC_CTRL [45].EN[24:24]	1
0xBB00B354	STORM_PORT_MC_CTRL [45].RATE[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B358	STORM_PORT_MC_CTRL [45].RESERVED[31:16]	16
0xBB00B358	STORM_PORT_MC_CTRL [45].BURST[15:0]	16
0xBB00B35C	STORM_PORT_MC_CTRL [46].RESERVED[31:26]	6
0xBB00B35C	STORM_PORT_MC_CTRL [46].TYPE[25:25]	1
0xBB00B35C	STORM_PORT_MC_CTRL [46].EN[24:24]	1
0xBB00B35C	STORM_PORT_MC_CTRL [46].RATE[23:0]	24
0xBB00B360	STORM_PORT_MC_CTRL [46].RESERVED[31:16]	16
0xBB00B360	STORM_PORT_MC_CTRL [46].BURST[15:0]	16
0xBB00B364	STORM_PORT_MC_CTRL [47].RESERVED[31:26]	6
0xBB00B364	STORM_PORT_MC_CTRL [47].TYPE[25:25]	1
0xBB00B364	STORM_PORT_MC_CTRL [47].EN[24:24]	1
0xBB00B364	STORM_PORT_MC_CTRL [47].RATE[23:0]	24
0xBB00B368	STORM_PORT_MC_CTRL [47].RESERVED[31:16]	16
0xBB00B368	STORM_PORT_MC_CTRL [47].BURST[15:0]	16
0xBB00B36C	STORM_PORT_MC_CTRL [48].RESERVED[31:26]	6
0xBB00B36C	STORM_PORT_MC_CTRL [48].TYPE[25:25]	1
0xBB00B36C	STORM_PORT_MC_CTRL [48].EN[24:24]	1
0xBB00B36C	STORM_PORT_MC_CTRL [48].RATE[23:0]	24
0xBB00B370	STORM_PORT_MC_CTRL [48].RESERVED[31:16]	16
0xBB00B370	STORM_PORT_MC_CTRL [48].BURST[15:0]	16
0xBB00B374	STORM_PORT_MC_CTRL [49].RESERVED[31:26]	6
0xBB00B374	STORM_PORT_MC_CTRL [49].TYPE[25:25]	1
0xBB00B374	STORM_PORT_MC_CTRL [49].EN[24:24]	1
0xBB00B374	STORM_PORT_MC_CTRL [49].RATE[23:0]	24
0xBB00B378	STORM_PORT_MC_CTRL [49].RESERVED[31:16]	16
0xBB00B378	STORM_PORT_MC_CTRL [49].BURST[15:0]	16
0xBB00B37C	STORM_PORT_MC_CTRL [50].RESERVED[31:26]	6
0xBB00B37C	STORM_PORT_MC_CTRL [50].TYPE[25:25]	1
0xBB00B37C	STORM_PORT_MC_CTRL [50].EN[24:24]	1
0xBB00B37C	STORM_PORT_MC_CTRL [50].RATE[23:0]	24
0xBB00B380	STORM_PORT_MC_CTRL [50].RESERVED[31:16]	16
0xBB00B380	STORM_PORT_MC_CTRL [50].BURST[15:0]	16
0xBB00B384	STORM_PORT_MC_CTRL [51].RESERVED[31:26]	6
0xBB00B384	STORM_PORT_MC_CTRL [51].TYPE[25:25]	1
0xBB00B384	STORM_PORT_MC_CTRL [51].EN[24:24]	1
0xBB00B384	STORM_PORT_MC_CTRL [51].RATE[23:0]	24
0xBB00B388	STORM_PORT_MC_CTRL [51].RESERVED[31:16]	16
0xBB00B388	STORM_PORT_MC_CTRL [51].BURST[15:0]	16
0xBB00B38C	STORM_PORT_MC_CTRL [52].RESERVED[31:26]	6
0xBB00B38C	STORM_PORT_MC_CTRL [52].TYPE[25:25]	1
0xBB00B38C	STORM_PORT_MC_CTRL [52].EN[24:24]	1
0xBB00B38C	STORM_PORT_MC_CTRL [52].RATE[23:0]	24
0xBB00B390	STORM_PORT_MC_CTRL [52].RESERVED[31:16]	16
0xBB00B390	STORM_PORT_MC_CTRL [52].BURST[15:0]	16
0xBB00B394	STORM_PORT_MC_CTRL [53].RESERVED[31:26]	6
0xBB00B394	STORM_PORT_MC_CTRL [53].TYPE[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B394	STORM_PORT_MC_CTRL [53].EN[24:24]	1
0xBB00B394	STORM_PORT_MC_CTRL [53].RATE[23:0]	24
0xBB00B398	STORM_PORT_MC_CTRL [53].RESERVED[31:16]	16
0xBB00B398	STORM_PORT_MC_CTRL [53].BURST[15:0]	16
0xBB00B39C	STORM_PORT_MC_CTRL [54].RESERVED[31:26]	6
0xBB00B39C	STORM_PORT_MC_CTRL [54].TYPE[25:25]	1
0xBB00B39C	STORM_PORT_MC_CTRL [54].EN[24:24]	1
0xBB00B39C	STORM_PORT_MC_CTRL [54].RATE[23:0]	24
0xBB00B3A0	STORM_PORT_MC_CTRL [54].RESERVED[31:16]	16
0xBB00B3A0	STORM_PORT_MC_CTRL [54].BURST[15:0]	16
0xBB00B3A4	STORM_PORT_MC_CTRL [55].RESERVED[31:26]	6
0xBB00B3A4	STORM_PORT_MC_CTRL [55].TYPE[25:25]	1
0xBB00B3A4	STORM_PORT_MC_CTRL [55].EN[24:24]	1
0xBB00B3A4	STORM_PORT_MC_CTRL [55].RATE[23:0]	24
0xBB00B3A8	STORM_PORT_MC_CTRL [55].RESERVED[31:16]	16
0xBB00B3A8	STORM_PORT_MC_CTRL [55].BURST[15:0]	16
0xBB00B3AC	STORM_PORT_MC_CTRL [56].RESERVED[31:26]	6
0xBB00B3AC	STORM_PORT_MC_CTRL [56].TYPE[25:25]	1
0xBB00B3AC	STORM_PORT_MC_CTRL [56].EN[24:24]	1
0xBB00B3AC	STORM_PORT_MC_CTRL [56].RATE[23:0]	24
0xBB00B3B0	STORM_PORT_MC_CTRL [56].RESERVED[31:16]	16
0xBB00B3B0	STORM_PORT_MC_CTRL [56].BURST[15:0]	16
0xBB00B3BC	STORM_PORT_MC_EXCFED_FLAG [0].FLAG[0:0]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [23].FLAG[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00B3BC	STORM_PORT_MC_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00B3C0	STORM_PORT_MC_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [0].RST[0:0]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [1].RST[1:1]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [2].RST[2:2]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [3].RST[3:3]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [4].RST[4:4]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [5].RST[5:5]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [6].RST[6:6]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [7].RST[7:7]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [8].RST[8:8]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [9].RST[9:9]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [10].RST[10:10]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [11].RST[11:11]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [12].RST[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B3B4	STORM_PORT_MC_LB_RST [13].RST[13:13]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [14].RST[14:14]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [15].RST[15:15]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [16].RST[16:16]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [17].RST[17:17]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [18].RST[18:18]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [19].RST[19:19]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [20].RST[20:20]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [21].RST[21:21]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [22].RST[22:22]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [23].RST[23:23]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [24].RST[24:24]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [25].RST[25:25]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [26].RST[26:26]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [27].RST[27:27]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [28].RST[28:28]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [29].RST[29:29]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [30].RST[30:30]	1
0xBB00B3B4	STORM_PORT_MC_LB_RST [31].RST[31:31]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [32].RST[0:0]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [33].RST[1:1]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [34].RST[2:2]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [35].RST[3:3]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [36].RST[4:4]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [37].RST[5:5]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [38].RST[6:6]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [39].RST[7:7]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [40].RST[8:8]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [41].RST[9:9]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [42].RST[10:10]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [43].RST[11:11]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [44].RST[12:12]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [45].RST[13:13]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [46].RST[14:14]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [47].RST[15:15]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [48].RST[16:16]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [49].RST[17:17]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [50].RST[18:18]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [51].RST[19:19]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [52].RST[20:20]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [53].RST[21:21]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [54].RST[22:22]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [55].RST[23:23]	1
0xBB00B3B8	STORM_PORT_MC_LB_RST [56].RST[24:24]	1
0xBB00A2E0	STORM_PORT_PROTO_ARP_CTRL [0].RESERVED[31:21]	11
0xBB00A2E0	STORM_PORT_PROTO_ARP_CTRL [0].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A2E0	STORM_PORT_PROTO_ARP_CTRL [0].RESERVED[19:17]	3
0xBB00A2E0	STORM_PORT_PROTO_ARP_CTRL [0].RATE[16:8]	9
0xBB00A2E0	STORM_PORT_PROTO_ARP_CTRL [0].BURST[7:0]	8
0xBB00A2E4	STORM_PORT_PROTO_ARP_CTRL [1].RESERVED[31:21]	11
0xBB00A2E4	STORM_PORT_PROTO_ARP_CTRL [1].EN[20:20]	1
0xBB00A2E4	STORM_PORT_PROTO_ARP_CTRL [1].RESERVED[19:17]	3
0xBB00A2E4	STORM_PORT_PROTO_ARP_CTRL [1].RATE[16:8]	9
0xBB00A2E4	STORM_PORT_PROTO_ARP_CTRL [1].BURST[7:0]	8
0xBB00A2E8	STORM_PORT_PROTO_ARP_CTRL [2].RESERVED[31:21]	11
0xBB00A2E8	STORM_PORT_PROTO_ARP_CTRL [2].EN[20:20]	1
0xBB00A2E8	STORM_PORT_PROTO_ARP_CTRL [2].RESERVED[19:17]	3
0xBB00A2E8	STORM_PORT_PROTO_ARP_CTRL [2].RATE[16:8]	9
0xBB00A2E8	STORM_PORT_PROTO_ARP_CTRL [2].BURST[7:0]	8
0xBB00A2EC	STORM_PORT_PROTO_ARP_CTRL [3].RESERVED[31:21]	11
0xBB00A2EC	STORM_PORT_PROTO_ARP_CTRL [3].EN[20:20]	1
0xBB00A2EC	STORM_PORT_PROTO_ARP_CTRL [3].RESERVED[19:17]	3
0xBB00A2EC	STORM_PORT_PROTO_ARP_CTRL [3].RATE[16:8]	9
0xBB00A2EC	STORM_PORT_PROTO_ARP_CTRL [3].BURST[7:0]	8
0xBB00A2F0	STORM_PORT_PROTO_ARP_CTRL [4].RESERVED[31:21]	11
0xBB00A2F0	STORM_PORT_PROTO_ARP_CTRL [4].EN[20:20]	1
0xBB00A2F0	STORM_PORT_PROTO_ARP_CTRL [4].RESERVED[19:17]	3
0xBB00A2F0	STORM_PORT_PROTO_ARP_CTRL [4].RATE[16:8]	9
0xBB00A2F0	STORM_PORT_PROTO_ARP_CTRL [4].BURST[7:0]	8
0xBB00A2F4	STORM_PORT_PROTO_ARP_CTRL [5].RESERVED[31:21]	11
0xBB00A2F4	STORM_PORT_PROTO_ARP_CTRL [5].EN[20:20]	1
0xBB00A2F4	STORM_PORT_PROTO_ARP_CTRL [5].RESERVED[19:17]	3
0xBB00A2F4	STORM_PORT_PROTO_ARP_CTRL [5].RATE[16:8]	9
0xBB00A2F4	STORM_PORT_PROTO_ARP_CTRL [5].BURST[7:0]	8
0xBB00A2F8	STORM_PORT_PROTO_ARP_CTRL [6].RESERVED[31:21]	11
0xBB00A2F8	STORM_PORT_PROTO_ARP_CTRL [6].EN[20:20]	1
0xBB00A2F8	STORM_PORT_PROTO_ARP_CTRL [6].RESERVED[19:17]	3
0xBB00A2F8	STORM_PORT_PROTO_ARP_CTRL [6].RATE[16:8]	9
0xBB00A2F8	STORM_PORT_PROTO_ARP_CTRL [6].BURST[7:0]	8
0xBB00A2FC	STORM_PORT_PROTO_ARP_CTRL [7].RESERVED[31:21]	11
0xBB00A2FC	STORM_PORT_PROTO_ARP_CTRL [7].EN[20:20]	1
0xBB00A2FC	STORM_PORT_PROTO_ARP_CTRL [7].RESERVED[19:17]	3
0xBB00A2FC	STORM_PORT_PROTO_ARP_CTRL [7].RATE[16:8]	9
0xBB00A2FC	STORM_PORT_PROTO_ARP_CTRL [7].BURST[7:0]	8
0xBB00A300	STORM_PORT_PROTO_ARP_CTRL [8].RESERVED[31:21]	11
0xBB00A300	STORM_PORT_PROTO_ARP_CTRL [8].EN[20:20]	1
0xBB00A300	STORM_PORT_PROTO_ARP_CTRL [8].RESERVED[19:17]	3
0xBB00A300	STORM_PORT_PROTO_ARP_CTRL [8].RATE[16:8]	9
0xBB00A300	STORM_PORT_PROTO_ARP_CTRL [8].BURST[7:0]	8
0xBB00A304	STORM_PORT_PROTO_ARP_CTRL [9].RESERVED[31:21]	11
0xBB00A304	STORM_PORT_PROTO_ARP_CTRL [9].EN[20:20]	1
0xBB00A304	STORM_PORT_PROTO_ARP_CTRL [9].RESERVED[19:17]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A304	STORM_PORT_PROTO_ARP_CTRL [9].RATE[16:8]	9
0xBB00A304	STORM_PORT_PROTO_ARP_CTRL [9].BURST[7:0]	8
0xBB00A308	STORM_PORT_PROTO_ARP_CTRL [10].RESERVED[31:21]	11
0xBB00A308	STORM_PORT_PROTO_ARP_CTRL [10].EN[20:20]	1
0xBB00A308	STORM_PORT_PROTO_ARP_CTRL [10].RESERVED[19:17]	3
0xBB00A308	STORM_PORT_PROTO_ARP_CTRL [10].RATE[16:8]	9
0xBB00A308	STORM_PORT_PROTO_ARP_CTRL [10].BURST[7:0]	8
0xBB00A30C	STORM_PORT_PROTO_ARP_CTRL [11].RESERVED[31:21]	11
0xBB00A30C	STORM_PORT_PROTO_ARP_CTRL [11].EN[20:20]	1
0xBB00A30C	STORM_PORT_PROTO_ARP_CTRL [11].RESERVED[19:17]	3
0xBB00A30C	STORM_PORT_PROTO_ARP_CTRL [11].RATE[16:8]	9
0xBB00A30C	STORM_PORT_PROTO_ARP_CTRL [11].BURST[7:0]	8
0xBB00A310	STORM_PORT_PROTO_ARP_CTRL [12].RESERVED[31:21]	11
0xBB00A310	STORM_PORT_PROTO_ARP_CTRL [12].EN[20:20]	1
0xBB00A310	STORM_PORT_PROTO_ARP_CTRL [12].RESERVED[19:17]	3
0xBB00A310	STORM_PORT_PROTO_ARP_CTRL [12].RATE[16:8]	9
0xBB00A310	STORM_PORT_PROTO_ARP_CTRL [12].BURST[7:0]	8
0xBB00A314	STORM_PORT_PROTO_ARP_CTRL [13].RESERVED[31:21]	11
0xBB00A314	STORM_PORT_PROTO_ARP_CTRL [13].EN[20:20]	1
0xBB00A314	STORM_PORT_PROTO_ARP_CTRL [13].RESERVED[19:17]	3
0xBB00A314	STORM_PORT_PROTO_ARP_CTRL [13].RATE[16:8]	9
0xBB00A314	STORM_PORT_PROTO_ARP_CTRL [13].BURST[7:0]	8
0xBB00A318	STORM_PORT_PROTO_ARP_CTRL [14].RESERVED[31:21]	11
0xBB00A318	STORM_PORT_PROTO_ARP_CTRL [14].EN[20:20]	1
0xBB00A318	STORM_PORT_PROTO_ARP_CTRL [14].RESERVED[19:17]	3
0xBB00A318	STORM_PORT_PROTO_ARP_CTRL [14].RATE[16:8]	9
0xBB00A318	STORM_PORT_PROTO_ARP_CTRL [14].BURST[7:0]	8
0xBB00A31C	STORM_PORT_PROTO_ARP_CTRL [15].RESERVED[31:21]	11
0xBB00A31C	STORM_PORT_PROTO_ARP_CTRL [15].EN[20:20]	1
0xBB00A31C	STORM_PORT_PROTO_ARP_CTRL [15].RESERVED[19:17]	3
0xBB00A31C	STORM_PORT_PROTO_ARP_CTRL [15].RATE[16:8]	9
0xBB00A31C	STORM_PORT_PROTO_ARP_CTRL [15].BURST[7:0]	8
0xBB00A320	STORM_PORT_PROTO_ARP_CTRL [16].RESERVED[31:21]	11
0xBB00A320	STORM_PORT_PROTO_ARP_CTRL [16].EN[20:20]	1
0xBB00A320	STORM_PORT_PROTO_ARP_CTRL [16].RESERVED[19:17]	3
0xBB00A320	STORM_PORT_PROTO_ARP_CTRL [16].RATE[16:8]	9
0xBB00A320	STORM_PORT_PROTO_ARP_CTRL [16].BURST[7:0]	8
0xBB00A324	STORM_PORT_PROTO_ARP_CTRL [17].RESERVED[31:21]	11
0xBB00A324	STORM_PORT_PROTO_ARP_CTRL [17].EN[20:20]	1
0xBB00A324	STORM_PORT_PROTO_ARP_CTRL [17].RESERVED[19:17]	3
0xBB00A324	STORM_PORT_PROTO_ARP_CTRL [17].RATE[16:8]	9
0xBB00A324	STORM_PORT_PROTO_ARP_CTRL [17].BURST[7:0]	8
0xBB00A328	STORM_PORT_PROTO_ARP_CTRL [18].RESERVED[31:21]	11
0xBB00A328	STORM_PORT_PROTO_ARP_CTRL [18].EN[20:20]	1
0xBB00A328	STORM_PORT_PROTO_ARP_CTRL [18].RESERVED[19:17]	3
0xBB00A328	STORM_PORT_PROTO_ARP_CTRL [18].RATE[16:8]	9

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A328	STORM_PORT_PROTO_ARP_CTRL [18].BURST[7:0]	8
0xBB00A32C	STORM_PORT_PROTO_ARP_CTRL [19].RESERVED[31:21]	11
0xBB00A32C	STORM_PORT_PROTO_ARP_CTRL [19].EN[20:20]	1
0xBB00A32C	STORM_PORT_PROTO_ARP_CTRL [19].RESERVED[19:17]	3
0xBB00A32C	STORM_PORT_PROTO_ARP_CTRL [19].RATE[16:8]	9
0xBB00A32C	STORM_PORT_PROTO_ARP_CTRL [19].BURST[7:0]	8
0xBB00A330	STORM_PORT_PROTO_ARP_CTRL [20].RESERVED[31:21]	11
0xBB00A330	STORM_PORT_PROTO_ARP_CTRL [20].EN[20:20]	1
0xBB00A330	STORM_PORT_PROTO_ARP_CTRL [20].RESERVED[19:17]	3
0xBB00A330	STORM_PORT_PROTO_ARP_CTRL [20].RATE[16:8]	9
0xBB00A330	STORM_PORT_PROTO_ARP_CTRL [20].BURST[7:0]	8
0xBB00A334	STORM_PORT_PROTO_ARP_CTRL [21].RESERVED[31:21]	11
0xBB00A334	STORM_PORT_PROTO_ARP_CTRL [21].EN[20:20]	1
0xBB00A334	STORM_PORT_PROTO_ARP_CTRL [21].RESERVED[19:17]	3
0xBB00A334	STORM_PORT_PROTO_ARP_CTRL [21].RATE[16:8]	9
0xBB00A334	STORM_PORT_PROTO_ARP_CTRL [21].BURST[7:0]	8
0xBB00A338	STORM_PORT_PROTO_ARP_CTRL [22].RESERVED[31:21]	11
0xBB00A338	STORM_PORT_PROTO_ARP_CTRL [22].EN[20:20]	1
0xBB00A338	STORM_PORT_PROTO_ARP_CTRL [22].RESERVED[19:17]	3
0xBB00A338	STORM_PORT_PROTO_ARP_CTRL [22].RATE[16:8]	9
0xBB00A338	STORM_PORT_PROTO_ARP_CTRL [22].BURST[7:0]	8
0xBB00A33C	STORM_PORT_PROTO_ARP_CTRL [23].RESERVED[31:21]	11
0xBB00A33C	STORM_PORT_PROTO_ARP_CTRL [23].EN[20:20]	1
0xBB00A33C	STORM_PORT_PROTO_ARP_CTRL [23].RESERVED[19:17]	3
0xBB00A33C	STORM_PORT_PROTO_ARP_CTRL [23].RATE[16:8]	9
0xBB00A33C	STORM_PORT_PROTO_ARP_CTRL [23].BURST[7:0]	8
0xBB00A340	STORM_PORT_PROTO_ARP_CTRL [24].RESERVED[31:21]	11
0xBB00A340	STORM_PORT_PROTO_ARP_CTRL [24].EN[20:20]	1
0xBB00A340	STORM_PORT_PROTO_ARP_CTRL [24].RESERVED[19:17]	3
0xBB00A340	STORM_PORT_PROTO_ARP_CTRL [24].RATE[16:8]	9
0xBB00A340	STORM_PORT_PROTO_ARP_CTRL [24].BURST[7:0]	8
0xBB00A344	STORM_PORT_PROTO_ARP_CTRL [25].RESERVED[31:21]	11
0xBB00A344	STORM_PORT_PROTO_ARP_CTRL [25].EN[20:20]	1
0xBB00A344	STORM_PORT_PROTO_ARP_CTRL [25].RESERVED[19:17]	3
0xBB00A344	STORM_PORT_PROTO_ARP_CTRL [25].RATE[16:8]	9
0xBB00A344	STORM_PORT_PROTO_ARP_CTRL [25].BURST[7:0]	8
0xBB00A348	STORM_PORT_PROTO_ARP_CTRL [26].RESERVED[31:21]	11
0xBB00A348	STORM_PORT_PROTO_ARP_CTRL [26].EN[20:20]	1
0xBB00A348	STORM_PORT_PROTO_ARP_CTRL [26].RESERVED[19:17]	3
0xBB00A348	STORM_PORT_PROTO_ARP_CTRL [26].RATE[16:8]	9
0xBB00A348	STORM_PORT_PROTO_ARP_CTRL [26].BURST[7:0]	8
0xBB00A34C	STORM_PORT_PROTO_ARP_CTRL [27].RESERVED[31:21]	11
0xBB00A34C	STORM_PORT_PROTO_ARP_CTRL [27].EN[20:20]	1
0xBB00A34C	STORM_PORT_PROTO_ARP_CTRL [27].RESERVED[19:17]	3
0xBB00A34C	STORM_PORT_PROTO_ARP_CTRL [27].RATE[16:8]	9
0xBB00A34C	STORM_PORT_PROTO_ARP_CTRL [27].BURST[7:0]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A350	STORM_PORT_PROTO_ARP_CTRL [28].RESERVED[31:21]	11
0xBB00A350	STORM_PORT_PROTO_ARP_CTRL [28].EN[20:20]	1
0xBB00A350	STORM_PORT_PROTO_ARP_CTRL [28].RESERVED[19:17]	3
0xBB00A350	STORM_PORT_PROTO_ARP_CTRL [28].RATE[16:8]	9
0xBB00A350	STORM_PORT_PROTO_ARP_CTRL [28].BURST[7:0]	8
0xBB00A354	STORM_PORT_PROTO_ARP_CTRL [29].RESERVED[31:21]	11
0xBB00A354	STORM_PORT_PROTO_ARP_CTRL [29].EN[20:20]	1
0xBB00A354	STORM_PORT_PROTO_ARP_CTRL [29].RESERVED[19:17]	3
0xBB00A354	STORM_PORT_PROTO_ARP_CTRL [29].RATE[16:8]	9
0xBB00A354	STORM_PORT_PROTO_ARP_CTRL [29].BURST[7:0]	8
0xBB00A358	STORM_PORT_PROTO_ARP_CTRL [30].RESERVED[31:21]	11
0xBB00A358	STORM_PORT_PROTO_ARP_CTRL [30].EN[20:20]	1
0xBB00A358	STORM_PORT_PROTO_ARP_CTRL [30].RESERVED[19:17]	3
0xBB00A358	STORM_PORT_PROTO_ARP_CTRL [30].RATE[16:8]	9
0xBB00A358	STORM_PORT_PROTO_ARP_CTRL [30].BURST[7:0]	8
0xBB00A35C	STORM_PORT_PROTO_ARP_CTRL [31].RESERVED[31:21]	11
0xBB00A35C	STORM_PORT_PROTO_ARP_CTRL [31].EN[20:20]	1
0xBB00A35C	STORM_PORT_PROTO_ARP_CTRL [31].RESERVED[19:17]	3
0xBB00A35C	STORM_PORT_PROTO_ARP_CTRL [31].RATE[16:8]	9
0xBB00A35C	STORM_PORT_PROTO_ARP_CTRL [31].BURST[7:0]	8
0xBB00A360	STORM_PORT_PROTO_ARP_CTRL [32].RESERVED[31:21]	11
0xBB00A360	STORM_PORT_PROTO_ARP_CTRL [32].EN[20:20]	1
0xBB00A360	STORM_PORT_PROTO_ARP_CTRL [32].RESERVED[19:17]	3
0xBB00A360	STORM_PORT_PROTO_ARP_CTRL [32].RATE[16:8]	9
0xBB00A360	STORM_PORT_PROTO_ARP_CTRL [32].BURST[7:0]	8
0xBB00A364	STORM_PORT_PROTO_ARP_CTRL [33].RESERVED[31:21]	11
0xBB00A364	STORM_PORT_PROTO_ARP_CTRL [33].EN[20:20]	1
0xBB00A364	STORM_PORT_PROTO_ARP_CTRL [33].RESERVED[19:17]	3
0xBB00A364	STORM_PORT_PROTO_ARP_CTRL [33].RATE[16:8]	9
0xBB00A364	STORM_PORT_PROTO_ARP_CTRL [33].BURST[7:0]	8
0xBB00A368	STORM_PORT_PROTO_ARP_CTRL [34].RESERVED[31:21]	11
0xBB00A368	STORM_PORT_PROTO_ARP_CTRL [34].EN[20:20]	1
0xBB00A368	STORM_PORT_PROTO_ARP_CTRL [34].RESERVED[19:17]	3
0xBB00A368	STORM_PORT_PROTO_ARP_CTRL [34].RATE[16:8]	9
0xBB00A368	STORM_PORT_PROTO_ARP_CTRL [34].BURST[7:0]	8
0xBB00A36C	STORM_PORT_PROTO_ARP_CTRL [35].RESERVED[31:21]	11
0xBB00A36C	STORM_PORT_PROTO_ARP_CTRL [35].EN[20:20]	1
0xBB00A36C	STORM_PORT_PROTO_ARP_CTRL [35].RESERVED[19:17]	3
0xBB00A36C	STORM_PORT_PROTO_ARP_CTRL [35].RATE[16:8]	9
0xBB00A36C	STORM_PORT_PROTO_ARP_CTRL [35].BURST[7:0]	8
0xBB00A370	STORM_PORT_PROTO_ARP_CTRL [36].RESERVED[31:21]	11
0xBB00A370	STORM_PORT_PROTO_ARP_CTRL [36].EN[20:20]	1
0xBB00A370	STORM_PORT_PROTO_ARP_CTRL [36].RESERVED[19:17]	3
0xBB00A370	STORM_PORT_PROTO_ARP_CTRL [36].RATE[16:8]	9
0xBB00A370	STORM_PORT_PROTO_ARP_CTRL [36].BURST[7:0]	8
0xBB00A374	STORM_PORT_PROTO_ARP_CTRL [37].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A374	STORM_PORT_PROTO_ARP_CTRL [37].EN[20:20]	1
0xBB00A374	STORM_PORT_PROTO_ARP_CTRL [37].RESERVED[19:17]	3
0xBB00A374	STORM_PORT_PROTO_ARP_CTRL [37].RATE[16:8]	9
0xBB00A374	STORM_PORT_PROTO_ARP_CTRL [37].BURST[7:0]	8
0xBB00A378	STORM_PORT_PROTO_ARP_CTRL [38].RESERVED[31:21]	11
0xBB00A378	STORM_PORT_PROTO_ARP_CTRL [38].EN[20:20]	1
0xBB00A378	STORM_PORT_PROTO_ARP_CTRL [38].RESERVED[19:17]	3
0xBB00A378	STORM_PORT_PROTO_ARP_CTRL [38].RATE[16:8]	9
0xBB00A378	STORM_PORT_PROTO_ARP_CTRL [38].BURST[7:0]	8
0xBB00A37C	STORM_PORT_PROTO_ARP_CTRL [39].RESERVED[31:21]	11
0xBB00A37C	STORM_PORT_PROTO_ARP_CTRL [39].EN[20:20]	1
0xBB00A37C	STORM_PORT_PROTO_ARP_CTRL [39].RESERVED[19:17]	3
0xBB00A37C	STORM_PORT_PROTO_ARP_CTRL [39].RATE[16:8]	9
0xBB00A37C	STORM_PORT_PROTO_ARP_CTRL [39].BURST[7:0]	8
0xBB00A380	STORM_PORT_PROTO_ARP_CTRL [40].RESERVED[31:21]	11
0xBB00A380	STORM_PORT_PROTO_ARP_CTRL [40].EN[20:20]	1
0xBB00A380	STORM_PORT_PROTO_ARP_CTRL [40].RESERVED[19:17]	3
0xBB00A380	STORM_PORT_PROTO_ARP_CTRL [40].RATE[16:8]	9
0xBB00A380	STORM_PORT_PROTO_ARP_CTRL [40].BURST[7:0]	8
0xBB00A384	STORM_PORT_PROTO_ARP_CTRL [41].RESERVED[31:21]	11
0xBB00A384	STORM_PORT_PROTO_ARP_CTRL [41].EN[20:20]	1
0xBB00A384	STORM_PORT_PROTO_ARP_CTRL [41].RESERVED[19:17]	3
0xBB00A384	STORM_PORT_PROTO_ARP_CTRL [41].RATE[16:8]	9
0xBB00A384	STORM_PORT_PROTO_ARP_CTRL [41].BURST[7:0]	8
0xBB00A388	STORM_PORT_PROTO_ARP_CTRL [42].RESERVED[31:21]	11
0xBB00A388	STORM_PORT_PROTO_ARP_CTRL [42].EN[20:20]	1
0xBB00A388	STORM_PORT_PROTO_ARP_CTRL [42].RESERVED[19:17]	3
0xBB00A388	STORM_PORT_PROTO_ARP_CTRL [42].RATE[16:8]	9
0xBB00A388	STORM_PORT_PROTO_ARP_CTRL [42].BURST[7:0]	8
0xBB00A38C	STORM_PORT_PROTO_ARP_CTRL [43].RESERVED[31:21]	11
0xBB00A38C	STORM_PORT_PROTO_ARP_CTRL [43].EN[20:20]	1
0xBB00A38C	STORM_PORT_PROTO_ARP_CTRL [43].RESERVED[19:17]	3
0xBB00A38C	STORM_PORT_PROTO_ARP_CTRL [43].RATE[16:8]	9
0xBB00A38C	STORM_PORT_PROTO_ARP_CTRL [43].BURST[7:0]	8
0xBB00A390	STORM_PORT_PROTO_ARP_CTRL [44].RESERVED[31:21]	11
0xBB00A390	STORM_PORT_PROTO_ARP_CTRL [44].EN[20:20]	1
0xBB00A390	STORM_PORT_PROTO_ARP_CTRL [44].RESERVED[19:17]	3
0xBB00A390	STORM_PORT_PROTO_ARP_CTRL [44].RATE[16:8]	9
0xBB00A390	STORM_PORT_PROTO_ARP_CTRL [44].BURST[7:0]	8
0xBB00A394	STORM_PORT_PROTO_ARP_CTRL [45].RESERVED[31:21]	11
0xBB00A394	STORM_PORT_PROTO_ARP_CTRL [45].EN[20:20]	1
0xBB00A394	STORM_PORT_PROTO_ARP_CTRL [45].RESERVED[19:17]	3
0xBB00A394	STORM_PORT_PROTO_ARP_CTRL [45].RATE[16:8]	9
0xBB00A394	STORM_PORT_PROTO_ARP_CTRL [45].BURST[7:0]	8
0xBB00A398	STORM_PORT_PROTO_ARP_CTRL [46].RESERVED[31:21]	11
0xBB00A398	STORM_PORT_PROTO_ARP_CTRL [46].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A398	STORM_PORT_PROTO_ARP_CTRL [46].RESERVED[19:17]	3
0xBB00A398	STORM_PORT_PROTO_ARP_CTRL [46].RATE[16:8]	9
0xBB00A398	STORM_PORT_PROTO_ARP_CTRL [46].BURST[7:0]	8
0xBB00A39C	STORM_PORT_PROTO_ARP_CTRL [47].RESERVED[31:21]	11
0xBB00A39C	STORM_PORT_PROTO_ARP_CTRL [47].EN[20:20]	1
0xBB00A39C	STORM_PORT_PROTO_ARP_CTRL [47].RESERVED[19:17]	3
0xBB00A39C	STORM_PORT_PROTO_ARP_CTRL [47].RATE[16:8]	9
0xBB00A39C	STORM_PORT_PROTO_ARP_CTRL [47].BURST[7:0]	8
0xBB00A3A0	STORM_PORT_PROTO_ARP_CTRL [48].RESERVED[31:21]	11
0xBB00A3A0	STORM_PORT_PROTO_ARP_CTRL [48].EN[20:20]	1
0xBB00A3A0	STORM_PORT_PROTO_ARP_CTRL [48].RESERVED[19:17]	3
0xBB00A3A0	STORM_PORT_PROTO_ARP_CTRL [48].RATE[16:8]	9
0xBB00A3A0	STORM_PORT_PROTO_ARP_CTRL [48].BURST[7:0]	8
0xBB00A3A4	STORM_PORT_PROTO_ARP_CTRL [49].RESERVED[31:21]	11
0xBB00A3A4	STORM_PORT_PROTO_ARP_CTRL [49].EN[20:20]	1
0xBB00A3A4	STORM_PORT_PROTO_ARP_CTRL [49].RESERVED[19:17]	3
0xBB00A3A4	STORM_PORT_PROTO_ARP_CTRL [49].RATE[16:8]	9
0xBB00A3A4	STORM_PORT_PROTO_ARP_CTRL [49].BURST[7:0]	8
0xBB00A3A8	STORM_PORT_PROTO_ARP_CTRL [50].RESERVED[31:21]	11
0xBB00A3A8	STORM_PORT_PROTO_ARP_CTRL [50].EN[20:20]	1
0xBB00A3A8	STORM_PORT_PROTO_ARP_CTRL [50].RESERVED[19:17]	3
0xBB00A3A8	STORM_PORT_PROTO_ARP_CTRL [50].RATE[16:8]	9
0xBB00A3A8	STORM_PORT_PROTO_ARP_CTRL [50].BURST[7:0]	8
0xBB00A3AC	STORM_PORT_PROTO_ARP_CTRL [51].RESERVED[31:21]	11
0xBB00A3AC	STORM_PORT_PROTO_ARP_CTRL [51].EN[20:20]	1
0xBB00A3AC	STORM_PORT_PROTO_ARP_CTRL [51].RESERVED[19:17]	3
0xBB00A3AC	STORM_PORT_PROTO_ARP_CTRL [51].RATE[16:8]	9
0xBB00A3AC	STORM_PORT_PROTO_ARP_CTRL [51].BURST[7:0]	8
0xBB00A3B0	STORM_PORT_PROTO_ARP_CTRL [52].RESERVED[31:21]	11
0xBB00A3B0	STORM_PORT_PROTO_ARP_CTRL [52].EN[20:20]	1
0xBB00A3B0	STORM_PORT_PROTO_ARP_CTRL [52].RESERVED[19:17]	3
0xBB00A3B0	STORM_PORT_PROTO_ARP_CTRL [52].RATE[16:8]	9
0xBB00A3B0	STORM_PORT_PROTO_ARP_CTRL [52].BURST[7:0]	8
0xBB00A3B4	STORM_PORT_PROTO_ARP_CTRL [53].RESERVED[31:21]	11
0xBB00A3B4	STORM_PORT_PROTO_ARP_CTRL [53].EN[20:20]	1
0xBB00A3B4	STORM_PORT_PROTO_ARP_CTRL [53].RESERVED[19:17]	3
0xBB00A3B4	STORM_PORT_PROTO_ARP_CTRL [53].RATE[16:8]	9
0xBB00A3B4	STORM_PORT_PROTO_ARP_CTRL [53].BURST[7:0]	8
0xBB00A3B8	STORM_PORT_PROTO_ARP_CTRL [54].RESERVED[31:21]	11
0xBB00A3B8	STORM_PORT_PROTO_ARP_CTRL [54].EN[20:20]	1
0xBB00A3B8	STORM_PORT_PROTO_ARP_CTRL [54].RESERVED[19:17]	3
0xBB00A3B8	STORM_PORT_PROTO_ARP_CTRL [54].RATE[16:8]	9
0xBB00A3B8	STORM_PORT_PROTO_ARP_CTRL [54].BURST[7:0]	8
0xBB00A3BC	STORM_PORT_PROTO_ARP_CTRL [55].RESERVED[31:21]	11
0xBB00A3BC	STORM_PORT_PROTO_ARP_CTRL [55].EN[20:20]	1
0xBB00A3BC	STORM_PORT_PROTO_ARP_CTRL [55].RESERVED[19:17]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A3BC	STORM_PORT_PROTO_ARP_CTRL [55].RATE[16:8]	9
0xBB00A3BC	STORM_PORT_PROTO_ARP_CTRL [55].BURST[7:0]	8
0xBB00A3C0	STORM_PORT_PROTO_ARP_CTRL [56].RESERVED[31:21]	11
0xBB00A3C0	STORM_PORT_PROTO_ARP_CTRL [56].EN[20:20]	1
0xBB00A3C0	STORM_PORT_PROTO_ARP_CTRL [56].RESERVED[19:17]	3
0xBB00A3C0	STORM_PORT_PROTO_ARP_CTRL [56].RATE[16:8]	9
0xBB00A3C0	STORM_PORT_PROTO_ARP_CTRL [56].BURST[7:0]	8
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00A3CC	STORM_PORT_PROTO_ARP_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [38].FLAG[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCFED_FLAG [50].FLAG[18:18]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00A3D0	STORM_PORT_PROTO_ARP_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [0].RST[0:0]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [1].RST[1:1]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [2].RST[2:2]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [3].RST[3:3]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [4].RST[4:4]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [5].RST[5:5]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [6].RST[6:6]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [7].RST[7:7]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [8].RST[8:8]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [9].RST[9:9]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [10].RST[10:10]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [11].RST[11:11]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [12].RST[12:12]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [13].RST[13:13]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [14].RST[14:14]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [15].RST[15:15]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [16].RST[16:16]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [17].RST[17:17]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [18].RST[18:18]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [19].RST[19:19]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [20].RST[20:20]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [21].RST[21:21]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [22].RST[22:22]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [23].RST[23:23]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [24].RST[24:24]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [25].RST[25:25]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [26].RST[26:26]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [27].RST[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [28].RST[28:28]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [29].RST[29:29]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [30].RST[30:30]	1
0xBB00A3C4	STORM_PORT_PROTO_ARP_LB_RST [31].RST[31:31]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [32].RST[0:0]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [33].RST[1:1]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [34].RST[2:2]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [35].RST[3:3]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [36].RST[4:4]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [37].RST[5:5]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [38].RST[6:6]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [39].RST[7:7]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [40].RST[8:8]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [41].RST[9:9]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [42].RST[10:10]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [43].RST[11:11]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [44].RST[12:12]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [45].RST[13:13]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [46].RST[14:14]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [47].RST[15:15]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [48].RST[16:16]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [49].RST[17:17]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [50].RST[18:18]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [51].RST[19:19]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [52].RST[20:20]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [53].RST[21:21]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [54].RST[22:22]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [55].RST[23:23]	1
0xBB00A3C8	STORM_PORT_PROTO_ARP_LB_RST [56].RST[24:24]	1
0xBB00A0F8	STORM_PORT_PROTO_BPDU_CTRL [0].RESERVED[31:21]	11
0xBB00A0F8	STORM_PORT_PROTO_BPDU_CTRL [0].EN[20:20]	1
0xBB00A0F8	STORM_PORT_PROTO_BPDU_CTRL [0].RESERVED[19:17]	3
0xBB00A0F8	STORM_PORT_PROTO_BPDU_CTRL [0].RATE[16:8]	9
0xBB00A0F8	STORM_PORT_PROTO_BPDU_CTRL [0].BURST[7:0]	8
0xBB00A0FC	STORM_PORT_PROTO_BPDU_CTRL [1].RESERVED[31:21]	11
0xBB00A0FC	STORM_PORT_PROTO_BPDU_CTRL [1].EN[20:20]	1
0xBB00A0FC	STORM_PORT_PROTO_BPDU_CTRL [1].RESERVED[19:17]	3
0xBB00A0FC	STORM_PORT_PROTO_BPDU_CTRL [1].RATE[16:8]	9
0xBB00A0FC	STORM_PORT_PROTO_BPDU_CTRL [1].BURST[7:0]	8
0xBB00A100	STORM_PORT_PROTO_BPDU_CTRL [2].RESERVED[31:21]	11
0xBB00A100	STORM_PORT_PROTO_BPDU_CTRL [2].EN[20:20]	1
0xBB00A100	STORM_PORT_PROTO_BPDU_CTRL [2].RESERVED[19:17]	3
0xBB00A100	STORM_PORT_PROTO_BPDU_CTRL [2].RATE[16:8]	9
0xBB00A100	STORM_PORT_PROTO_BPDU_CTRL [2].BURST[7:0]	8
0xBB00A104	STORM_PORT_PROTO_BPDU_CTRL [3].RESERVED[31:21]	11
0xBB00A104	STORM_PORT_PROTO_BPDU_CTRL [3].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A104	STORM_PORT_PROTO_BPDU_CTRL [3].RESERVED[19:17]	3
0xBB00A104	STORM_PORT_PROTO_BPDU_CTRL [3].RATE[16:8]	9
0xBB00A104	STORM_PORT_PROTO_BPDU_CTRL [3].BURST[7:0]	8
0xBB00A108	STORM_PORT_PROTO_BPDU_CTRL [4].RESERVED[31:21]	11
0xBB00A108	STORM_PORT_PROTO_BPDU_CTRL [4].EN[20:20]	1
0xBB00A108	STORM_PORT_PROTO_BPDU_CTRL [4].RESERVED[19:17]	3
0xBB00A108	STORM_PORT_PROTO_BPDU_CTRL [4].RATE[16:8]	9
0xBB00A108	STORM_PORT_PROTO_BPDU_CTRL [4].BURST[7:0]	8
0xBB00A10C	STORM_PORT_PROTO_BPDU_CTRL [5].RESERVED[31:21]	11
0xBB00A10C	STORM_PORT_PROTO_BPDU_CTRL [5].EN[20:20]	1
0xBB00A10C	STORM_PORT_PROTO_BPDU_CTRL [5].RESERVED[19:17]	3
0xBB00A10C	STORM_PORT_PROTO_BPDU_CTRL [5].RATE[16:8]	9
0xBB00A10C	STORM_PORT_PROTO_BPDU_CTRL [5].BURST[7:0]	8
0xBB00A110	STORM_PORT_PROTO_BPDU_CTRL [6].RESERVED[31:21]	11
0xBB00A110	STORM_PORT_PROTO_BPDU_CTRL [6].EN[20:20]	1
0xBB00A110	STORM_PORT_PROTO_BPDU_CTRL [6].RESERVED[19:17]	3
0xBB00A110	STORM_PORT_PROTO_BPDU_CTRL [6].RATE[16:8]	9
0xBB00A110	STORM_PORT_PROTO_BPDU_CTRL [6].BURST[7:0]	8
0xBB00A114	STORM_PORT_PROTO_BPDU_CTRL [7].RESERVED[31:21]	11
0xBB00A114	STORM_PORT_PROTO_BPDU_CTRL [7].EN[20:20]	1
0xBB00A114	STORM_PORT_PROTO_BPDU_CTRL [7].RESERVED[19:17]	3
0xBB00A114	STORM_PORT_PROTO_BPDU_CTRL [7].RATE[16:8]	9
0xBB00A114	STORM_PORT_PROTO_BPDU_CTRL [7].BURST[7:0]	8
0xBB00A118	STORM_PORT_PROTO_BPDU_CTRL [8].RESERVED[31:21]	11
0xBB00A118	STORM_PORT_PROTO_BPDU_CTRL [8].EN[20:20]	1
0xBB00A118	STORM_PORT_PROTO_BPDU_CTRL [8].RESERVED[19:17]	3
0xBB00A118	STORM_PORT_PROTO_BPDU_CTRL [8].RATE[16:8]	9
0xBB00A118	STORM_PORT_PROTO_BPDU_CTRL [8].BURST[7:0]	8
0xBB00A11C	STORM_PORT_PROTO_BPDU_CTRL [9].RESERVED[31:21]	11
0xBB00A11C	STORM_PORT_PROTO_BPDU_CTRL [9].EN[20:20]	1
0xBB00A11C	STORM_PORT_PROTO_BPDU_CTRL [9].RESERVED[19:17]	3
0xBB00A11C	STORM_PORT_PROTO_BPDU_CTRL [9].RATE[16:8]	9
0xBB00A11C	STORM_PORT_PROTO_BPDU_CTRL [9].BURST[7:0]	8
0xBB00A120	STORM_PORT_PROTO_BPDU_CTRL [10].RESERVED[31:21]	11
0xBB00A120	STORM_PORT_PROTO_BPDU_CTRL [10].EN[20:20]	1
0xBB00A120	STORM_PORT_PROTO_BPDU_CTRL [10].RESERVED[19:17]	3
0xBB00A120	STORM_PORT_PROTO_BPDU_CTRL [10].RATE[16:8]	9
0xBB00A120	STORM_PORT_PROTO_BPDU_CTRL [10].BURST[7:0]	8
0xBB00A124	STORM_PORT_PROTO_BPDU_CTRL [11].RESERVED[31:21]	11
0xBB00A124	STORM_PORT_PROTO_BPDU_CTRL [11].EN[20:20]	1
0xBB00A124	STORM_PORT_PROTO_BPDU_CTRL [11].RESERVED[19:17]	3
0xBB00A124	STORM_PORT_PROTO_BPDU_CTRL [11].RATE[16:8]	9
0xBB00A124	STORM_PORT_PROTO_BPDU_CTRL [11].BURST[7:0]	8
0xBB00A128	STORM_PORT_PROTO_BPDU_CTRL [12].RESERVED[31:21]	11
0xBB00A128	STORM_PORT_PROTO_BPDU_CTRL [12].EN[20:20]	1
0xBB00A128	STORM_PORT_PROTO_BPDU_CTRL [12].RESERVED[19:17]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A128	STORM_PORT_PROTO_BPDU_CTRL [12].RATE[16:8]	9
0xBB00A128	STORM_PORT_PROTO_BPDU_CTRL [12].BURST[7:0]	8
0xBB00A12C	STORM_PORT_PROTO_BPDU_CTRL [13].RESERVED[31:21]	11
0xBB00A12C	STORM_PORT_PROTO_BPDU_CTRL [13].EN[20:20]	1
0xBB00A12C	STORM_PORT_PROTO_BPDU_CTRL [13].RESERVED[19:17]	3
0xBB00A12C	STORM_PORT_PROTO_BPDU_CTRL [13].RATE[16:8]	9
0xBB00A12C	STORM_PORT_PROTO_BPDU_CTRL [13].BURST[7:0]	8
0xBB00A130	STORM_PORT_PROTO_BPDU_CTRL [14].RESERVED[31:21]	11
0xBB00A130	STORM_PORT_PROTO_BPDU_CTRL [14].EN[20:20]	1
0xBB00A130	STORM_PORT_PROTO_BPDU_CTRL [14].RESERVED[19:17]	3
0xBB00A130	STORM_PORT_PROTO_BPDU_CTRL [14].RATE[16:8]	9
0xBB00A130	STORM_PORT_PROTO_BPDU_CTRL [14].BURST[7:0]	8
0xBB00A134	STORM_PORT_PROTO_BPDU_CTRL [15].RESERVED[31:21]	11
0xBB00A134	STORM_PORT_PROTO_BPDU_CTRL [15].EN[20:20]	1
0xBB00A134	STORM_PORT_PROTO_BPDU_CTRL [15].RESERVED[19:17]	3
0xBB00A134	STORM_PORT_PROTO_BPDU_CTRL [15].RATE[16:8]	9
0xBB00A134	STORM_PORT_PROTO_BPDU_CTRL [15].BURST[7:0]	8
0xBB00A138	STORM_PORT_PROTO_BPDU_CTRL [16].RESERVED[31:21]	11
0xBB00A138	STORM_PORT_PROTO_BPDU_CTRL [16].EN[20:20]	1
0xBB00A138	STORM_PORT_PROTO_BPDU_CTRL [16].RESERVED[19:17]	3
0xBB00A138	STORM_PORT_PROTO_BPDU_CTRL [16].RATE[16:8]	9
0xBB00A138	STORM_PORT_PROTO_BPDU_CTRL [16].BURST[7:0]	8
0xBB00A13C	STORM_PORT_PROTO_BPDU_CTRL [17].RESERVED[31:21]	11
0xBB00A13C	STORM_PORT_PROTO_BPDU_CTRL [17].EN[20:20]	1
0xBB00A13C	STORM_PORT_PROTO_BPDU_CTRL [17].RESERVED[19:17]	3
0xBB00A13C	STORM_PORT_PROTO_BPDU_CTRL [17].RATE[16:8]	9
0xBB00A13C	STORM_PORT_PROTO_BPDU_CTRL [17].BURST[7:0]	8
0xBB00A140	STORM_PORT_PROTO_BPDU_CTRL [18].RESERVED[31:21]	11
0xBB00A140	STORM_PORT_PROTO_BPDU_CTRL [18].EN[20:20]	1
0xBB00A140	STORM_PORT_PROTO_BPDU_CTRL [18].RESERVED[19:17]	3
0xBB00A140	STORM_PORT_PROTO_BPDU_CTRL [18].RATE[16:8]	9
0xBB00A140	STORM_PORT_PROTO_BPDU_CTRL [18].BURST[7:0]	8
0xBB00A144	STORM_PORT_PROTO_BPDU_CTRL [19].RESERVED[31:21]	11
0xBB00A144	STORM_PORT_PROTO_BPDU_CTRL [19].EN[20:20]	1
0xBB00A144	STORM_PORT_PROTO_BPDU_CTRL [19].RESERVED[19:17]	3
0xBB00A144	STORM_PORT_PROTO_BPDU_CTRL [19].RATE[16:8]	9
0xBB00A144	STORM_PORT_PROTO_BPDU_CTRL [19].BURST[7:0]	8
0xBB00A148	STORM_PORT_PROTO_BPDU_CTRL [20].RESERVED[31:21]	11
0xBB00A148	STORM_PORT_PROTO_BPDU_CTRL [20].EN[20:20]	1
0xBB00A148	STORM_PORT_PROTO_BPDU_CTRL [20].RESERVED[19:17]	3
0xBB00A148	STORM_PORT_PROTO_BPDU_CTRL [20].RATE[16:8]	9
0xBB00A148	STORM_PORT_PROTO_BPDU_CTRL [20].BURST[7:0]	8
0xBB00A14C	STORM_PORT_PROTO_BPDU_CTRL [21].RESERVED[31:21]	11
0xBB00A14C	STORM_PORT_PROTO_BPDU_CTRL [21].EN[20:20]	1
0xBB00A14C	STORM_PORT_PROTO_BPDU_CTRL [21].RESERVED[19:17]	3
0xBB00A14C	STORM_PORT_PROTO_BPDU_CTRL [21].RATE[16:8]	9

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A14C	STORM_PORT_PROTO_BPDU_CTRL [21].BURST[7:0]	8
0xBB00A150	STORM_PORT_PROTO_BPDU_CTRL [22].RESERVED[31:21]	11
0xBB00A150	STORM_PORT_PROTO_BPDU_CTRL [22].EN[20:20]	1
0xBB00A150	STORM_PORT_PROTO_BPDU_CTRL [22].RESERVED[19:17]	3
0xBB00A150	STORM_PORT_PROTO_BPDU_CTRL [22].RATE[16:8]	9
0xBB00A150	STORM_PORT_PROTO_BPDU_CTRL [22].BURST[7:0]	8
0xBB00A154	STORM_PORT_PROTO_BPDU_CTRL [23].RESERVED[31:21]	11
0xBB00A154	STORM_PORT_PROTO_BPDU_CTRL [23].EN[20:20]	1
0xBB00A154	STORM_PORT_PROTO_BPDU_CTRL [23].RESERVED[19:17]	3
0xBB00A154	STORM_PORT_PROTO_BPDU_CTRL [23].RATE[16:8]	9
0xBB00A154	STORM_PORT_PROTO_BPDU_CTRL [23].BURST[7:0]	8
0xBB00A158	STORM_PORT_PROTO_BPDU_CTRL [24].RESERVED[31:21]	11
0xBB00A158	STORM_PORT_PROTO_BPDU_CTRL [24].EN[20:20]	1
0xBB00A158	STORM_PORT_PROTO_BPDU_CTRL [24].RESERVED[19:17]	3
0xBB00A158	STORM_PORT_PROTO_BPDU_CTRL [24].RATE[16:8]	9
0xBB00A158	STORM_PORT_PROTO_BPDU_CTRL [24].BURST[7:0]	8
0xBB00A15C	STORM_PORT_PROTO_BPDU_CTRL [25].RESERVED[31:21]	11
0xBB00A15C	STORM_PORT_PROTO_BPDU_CTRL [25].EN[20:20]	1
0xBB00A15C	STORM_PORT_PROTO_BPDU_CTRL [25].RESERVED[19:17]	3
0xBB00A15C	STORM_PORT_PROTO_BPDU_CTRL [25].RATE[16:8]	9
0xBB00A15C	STORM_PORT_PROTO_BPDU_CTRL [25].BURST[7:0]	8
0xBB00A160	STORM_PORT_PROTO_BPDU_CTRL [26].RESERVED[31:21]	11
0xBB00A160	STORM_PORT_PROTO_BPDU_CTRL [26].EN[20:20]	1
0xBB00A160	STORM_PORT_PROTO_BPDU_CTRL [26].RESERVED[19:17]	3
0xBB00A160	STORM_PORT_PROTO_BPDU_CTRL [26].RATE[16:8]	9
0xBB00A160	STORM_PORT_PROTO_BPDU_CTRL [26].BURST[7:0]	8
0xBB00A164	STORM_PORT_PROTO_BPDU_CTRL [27].RESERVED[31:21]	11
0xBB00A164	STORM_PORT_PROTO_BPDU_CTRL [27].EN[20:20]	1
0xBB00A164	STORM_PORT_PROTO_BPDU_CTRL [27].RESERVED[19:17]	3
0xBB00A164	STORM_PORT_PROTO_BPDU_CTRL [27].RATE[16:8]	9
0xBB00A164	STORM_PORT_PROTO_BPDU_CTRL [27].BURST[7:0]	8
0xBB00A168	STORM_PORT_PROTO_BPDU_CTRL [28].RESERVED[31:21]	11
0xBB00A168	STORM_PORT_PROTO_BPDU_CTRL [28].EN[20:20]	1
0xBB00A168	STORM_PORT_PROTO_BPDU_CTRL [28].RESERVED[19:17]	3
0xBB00A168	STORM_PORT_PROTO_BPDU_CTRL [28].RATE[16:8]	9
0xBB00A168	STORM_PORT_PROTO_BPDU_CTRL [28].BURST[7:0]	8
0xBB00A16C	STORM_PORT_PROTO_BPDU_CTRL [29].RESERVED[31:21]	11
0xBB00A16C	STORM_PORT_PROTO_BPDU_CTRL [29].EN[20:20]	1
0xBB00A16C	STORM_PORT_PROTO_BPDU_CTRL [29].RESERVED[19:17]	3
0xBB00A16C	STORM_PORT_PROTO_BPDU_CTRL [29].RATE[16:8]	9
0xBB00A16C	STORM_PORT_PROTO_BPDU_CTRL [29].BURST[7:0]	8
0xBB00A170	STORM_PORT_PROTO_BPDU_CTRL [30].RESERVED[31:21]	11
0xBB00A170	STORM_PORT_PROTO_BPDU_CTRL [30].EN[20:20]	1
0xBB00A170	STORM_PORT_PROTO_BPDU_CTRL [30].RESERVED[19:17]	3
0xBB00A170	STORM_PORT_PROTO_BPDU_CTRL [30].RATE[16:8]	9
0xBB00A170	STORM_PORT_PROTO_BPDU_CTRL [30].BURST[7:0]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A174	STORM_PORT_PROTO_BPDU_CTRL [31].RESERVED[31:21]	11
0xBB00A174	STORM_PORT_PROTO_BPDU_CTRL [31].EN[20:20]	1
0xBB00A174	STORM_PORT_PROTO_BPDU_CTRL [31].RESERVED[19:17]	3
0xBB00A174	STORM_PORT_PROTO_BPDU_CTRL [31].RATE[16:8]	9
0xBB00A174	STORM_PORT_PROTO_BPDU_CTRL [31].BURST[7:0]	8
0xBB00A178	STORM_PORT_PROTO_BPDU_CTRL [32].RESERVED[31:21]	11
0xBB00A178	STORM_PORT_PROTO_BPDU_CTRL [32].EN[20:20]	1
0xBB00A178	STORM_PORT_PROTO_BPDU_CTRL [32].RESERVED[19:17]	3
0xBB00A178	STORM_PORT_PROTO_BPDU_CTRL [32].RATE[16:8]	9
0xBB00A178	STORM_PORT_PROTO_BPDU_CTRL [32].BURST[7:0]	8
0xBB00A17C	STORM_PORT_PROTO_BPDU_CTRL [33].RESERVED[31:21]	11
0xBB00A17C	STORM_PORT_PROTO_BPDU_CTRL [33].EN[20:20]	1
0xBB00A17C	STORM_PORT_PROTO_BPDU_CTRL [33].RESERVED[19:17]	3
0xBB00A17C	STORM_PORT_PROTO_BPDU_CTRL [33].RATE[16:8]	9
0xBB00A17C	STORM_PORT_PROTO_BPDU_CTRL [33].BURST[7:0]	8
0xBB00A180	STORM_PORT_PROTO_BPDU_CTRL [34].RESERVED[31:21]	11
0xBB00A180	STORM_PORT_PROTO_BPDU_CTRL [34].EN[20:20]	1
0xBB00A180	STORM_PORT_PROTO_BPDU_CTRL [34].RESERVED[19:17]	3
0xBB00A180	STORM_PORT_PROTO_BPDU_CTRL [34].RATE[16:8]	9
0xBB00A180	STORM_PORT_PROTO_BPDU_CTRL [34].BURST[7:0]	8
0xBB00A184	STORM_PORT_PROTO_BPDU_CTRL [35].RESERVED[31:21]	11
0xBB00A184	STORM_PORT_PROTO_BPDU_CTRL [35].EN[20:20]	1
0xBB00A184	STORM_PORT_PROTO_BPDU_CTRL [35].RESERVED[19:17]	3
0xBB00A184	STORM_PORT_PROTO_BPDU_CTRL [35].RATE[16:8]	9
0xBB00A184	STORM_PORT_PROTO_BPDU_CTRL [35].BURST[7:0]	8
0xBB00A188	STORM_PORT_PROTO_BPDU_CTRL [36].RESERVED[31:21]	11
0xBB00A188	STORM_PORT_PROTO_BPDU_CTRL [36].EN[20:20]	1
0xBB00A188	STORM_PORT_PROTO_BPDU_CTRL [36].RESERVED[19:17]	3
0xBB00A188	STORM_PORT_PROTO_BPDU_CTRL [36].RATE[16:8]	9
0xBB00A188	STORM_PORT_PROTO_BPDU_CTRL [36].BURST[7:0]	8
0xBB00A18C	STORM_PORT_PROTO_BPDU_CTRL [37].RESERVED[31:21]	11
0xBB00A18C	STORM_PORT_PROTO_BPDU_CTRL [37].EN[20:20]	1
0xBB00A18C	STORM_PORT_PROTO_BPDU_CTRL [37].RESERVED[19:17]	3
0xBB00A18C	STORM_PORT_PROTO_BPDU_CTRL [37].RATE[16:8]	9
0xBB00A18C	STORM_PORT_PROTO_BPDU_CTRL [37].BURST[7:0]	8
0xBB00A190	STORM_PORT_PROTO_BPDU_CTRL [38].RESERVED[31:21]	11
0xBB00A190	STORM_PORT_PROTO_BPDU_CTRL [38].EN[20:20]	1
0xBB00A190	STORM_PORT_PROTO_BPDU_CTRL [38].RESERVED[19:17]	3
0xBB00A190	STORM_PORT_PROTO_BPDU_CTRL [38].RATE[16:8]	9
0xBB00A190	STORM_PORT_PROTO_BPDU_CTRL [38].BURST[7:0]	8
0xBB00A194	STORM_PORT_PROTO_BPDU_CTRL [39].RESERVED[31:21]	11
0xBB00A194	STORM_PORT_PROTO_BPDU_CTRL [39].EN[20:20]	1
0xBB00A194	STORM_PORT_PROTO_BPDU_CTRL [39].RESERVED[19:17]	3
0xBB00A194	STORM_PORT_PROTO_BPDU_CTRL [39].RATE[16:8]	9
0xBB00A194	STORM_PORT_PROTO_BPDU_CTRL [39].BURST[7:0]	8
0xBB00A198	STORM_PORT_PROTO_BPDU_CTRL [40].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A198	STORM_PORT_PROTO_BPDU_CTRL [40].EN[20:20]	1
0xBB00A198	STORM_PORT_PROTO_BPDU_CTRL [40].RESERVED[19:17]	3
0xBB00A198	STORM_PORT_PROTO_BPDU_CTRL [40].RATE[16:8]	9
0xBB00A198	STORM_PORT_PROTO_BPDU_CTRL [40].BURST[7:0]	8
0xBB00A19C	STORM_PORT_PROTO_BPDU_CTRL [41].RESERVED[31:21]	11
0xBB00A19C	STORM_PORT_PROTO_BPDU_CTRL [41].EN[20:20]	1
0xBB00A19C	STORM_PORT_PROTO_BPDU_CTRL [41].RESERVED[19:17]	3
0xBB00A19C	STORM_PORT_PROTO_BPDU_CTRL [41].RATE[16:8]	9
0xBB00A19C	STORM_PORT_PROTO_BPDU_CTRL [41].BURST[7:0]	8
0xBB00A1A0	STORM_PORT_PROTO_BPDU_CTRL [42].RESERVED[31:21]	11
0xBB00A1A0	STORM_PORT_PROTO_BPDU_CTRL [42].EN[20:20]	1
0xBB00A1A0	STORM_PORT_PROTO_BPDU_CTRL [42].RESERVED[19:17]	3
0xBB00A1A0	STORM_PORT_PROTO_BPDU_CTRL [42].RATE[16:8]	9
0xBB00A1A0	STORM_PORT_PROTO_BPDU_CTRL [42].BURST[7:0]	8
0xBB00A1A4	STORM_PORT_PROTO_BPDU_CTRL [43].RESERVED[31:21]	11
0xBB00A1A4	STORM_PORT_PROTO_BPDU_CTRL [43].EN[20:20]	1
0xBB00A1A4	STORM_PORT_PROTO_BPDU_CTRL [43].RESERVED[19:17]	3
0xBB00A1A4	STORM_PORT_PROTO_BPDU_CTRL [43].RATE[16:8]	9
0xBB00A1A4	STORM_PORT_PROTO_BPDU_CTRL [43].BURST[7:0]	8
0xBB00A1A8	STORM_PORT_PROTO_BPDU_CTRL [44].RESERVED[31:21]	11
0xBB00A1A8	STORM_PORT_PROTO_BPDU_CTRL [44].EN[20:20]	1
0xBB00A1A8	STORM_PORT_PROTO_BPDU_CTRL [44].RESERVED[19:17]	3
0xBB00A1A8	STORM_PORT_PROTO_BPDU_CTRL [44].RATE[16:8]	9
0xBB00A1A8	STORM_PORT_PROTO_BPDU_CTRL [44].BURST[7:0]	8
0xBB00A1AC	STORM_PORT_PROTO_BPDU_CTRL [45].RESERVED[31:21]	11
0xBB00A1AC	STORM_PORT_PROTO_BPDU_CTRL [45].EN[20:20]	1
0xBB00A1AC	STORM_PORT_PROTO_BPDU_CTRL [45].RESERVED[19:17]	3
0xBB00A1AC	STORM_PORT_PROTO_BPDU_CTRL [45].RATE[16:8]	9
0xBB00A1AC	STORM_PORT_PROTO_BPDU_CTRL [45].BURST[7:0]	8
0xBB00A1B0	STORM_PORT_PROTO_BPDU_CTRL [46].RESERVED[31:21]	11
0xBB00A1B0	STORM_PORT_PROTO_BPDU_CTRL [46].EN[20:20]	1
0xBB00A1B0	STORM_PORT_PROTO_BPDU_CTRL [46].RESERVED[19:17]	3
0xBB00A1B0	STORM_PORT_PROTO_BPDU_CTRL [46].RATE[16:8]	9
0xBB00A1B0	STORM_PORT_PROTO_BPDU_CTRL [46].BURST[7:0]	8
0xBB00A1B4	STORM_PORT_PROTO_BPDU_CTRL [47].RESERVED[31:21]	11
0xBB00A1B4	STORM_PORT_PROTO_BPDU_CTRL [47].EN[20:20]	1
0xBB00A1B4	STORM_PORT_PROTO_BPDU_CTRL [47].RESERVED[19:17]	3
0xBB00A1B4	STORM_PORT_PROTO_BPDU_CTRL [47].RATE[16:8]	9
0xBB00A1B4	STORM_PORT_PROTO_BPDU_CTRL [47].BURST[7:0]	8
0xBB00A1B8	STORM_PORT_PROTO_BPDU_CTRL [48].RESERVED[31:21]	11
0xBB00A1B8	STORM_PORT_PROTO_BPDU_CTRL [48].EN[20:20]	1
0xBB00A1B8	STORM_PORT_PROTO_BPDU_CTRL [48].RESERVED[19:17]	3
0xBB00A1B8	STORM_PORT_PROTO_BPDU_CTRL [48].RATE[16:8]	9
0xBB00A1B8	STORM_PORT_PROTO_BPDU_CTRL [48].BURST[7:0]	8
0xBB00A1BC	STORM_PORT_PROTO_BPDU_CTRL [49].RESERVED[31:21]	11
0xBB00A1BC	STORM_PORT_PROTO_BPDU_CTRL [49].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A1BC	STORM_PORT_PROTO_BPDU_CTRL [49].RESERVED[19:17]	3
0xBB00A1BC	STORM_PORT_PROTO_BPDU_CTRL [49].RATE[16:8]	9
0xBB00A1BC	STORM_PORT_PROTO_BPDU_CTRL [49].BURST[7:0]	8
0xBB00A1C0	STORM_PORT_PROTO_BPDU_CTRL [50].RESERVED[31:21]	11
0xBB00A1C0	STORM_PORT_PROTO_BPDU_CTRL [50].EN[20:20]	1
0xBB00A1C0	STORM_PORT_PROTO_BPDU_CTRL [50].RESERVED[19:17]	3
0xBB00A1C0	STORM_PORT_PROTO_BPDU_CTRL [50].RATE[16:8]	9
0xBB00A1C0	STORM_PORT_PROTO_BPDU_CTRL [50].BURST[7:0]	8
0xBB00A1C4	STORM_PORT_PROTO_BPDU_CTRL [51].RESERVED[31:21]	11
0xBB00A1C4	STORM_PORT_PROTO_BPDU_CTRL [51].EN[20:20]	1
0xBB00A1C4	STORM_PORT_PROTO_BPDU_CTRL [51].RESERVED[19:17]	3
0xBB00A1C4	STORM_PORT_PROTO_BPDU_CTRL [51].RATE[16:8]	9
0xBB00A1C4	STORM_PORT_PROTO_BPDU_CTRL [51].BURST[7:0]	8
0xBB00A1C8	STORM_PORT_PROTO_BPDU_CTRL [52].RESERVED[31:21]	11
0xBB00A1C8	STORM_PORT_PROTO_BPDU_CTRL [52].EN[20:20]	1
0xBB00A1C8	STORM_PORT_PROTO_BPDU_CTRL [52].RESERVED[19:17]	3
0xBB00A1C8	STORM_PORT_PROTO_BPDU_CTRL [52].RATE[16:8]	9
0xBB00A1C8	STORM_PORT_PROTO_BPDU_CTRL [52].BURST[7:0]	8
0xBB00A1CC	STORM_PORT_PROTO_BPDU_CTRL [53].RESERVED[31:21]	11
0xBB00A1CC	STORM_PORT_PROTO_BPDU_CTRL [53].EN[20:20]	1
0xBB00A1CC	STORM_PORT_PROTO_BPDU_CTRL [53].RESERVED[19:17]	3
0xBB00A1CC	STORM_PORT_PROTO_BPDU_CTRL [53].RATE[16:8]	9
0xBB00A1CC	STORM_PORT_PROTO_BPDU_CTRL [53].BURST[7:0]	8
0xBB00A1D0	STORM_PORT_PROTO_BPDU_CTRL [54].RESERVED[31:21]	11
0xBB00A1D0	STORM_PORT_PROTO_BPDU_CTRL [54].EN[20:20]	1
0xBB00A1D0	STORM_PORT_PROTO_BPDU_CTRL [54].RESERVED[19:17]	3
0xBB00A1D0	STORM_PORT_PROTO_BPDU_CTRL [54].RATE[16:8]	9
0xBB00A1D0	STORM_PORT_PROTO_BPDU_CTRL [54].BURST[7:0]	8
0xBB00A1D4	STORM_PORT_PROTO_BPDU_CTRL [55].RESERVED[31:21]	11
0xBB00A1D4	STORM_PORT_PROTO_BPDU_CTRL [55].EN[20:20]	1
0xBB00A1D4	STORM_PORT_PROTO_BPDU_CTRL [55].RESERVED[19:17]	3
0xBB00A1D4	STORM_PORT_PROTO_BPDU_CTRL [55].RATE[16:8]	9
0xBB00A1D4	STORM_PORT_PROTO_BPDU_CTRL [55].BURST[7:0]	8
0xBB00A1D8	STORM_PORT_PROTO_BPDU_CTRL [56].RESERVED[31:21]	11
0xBB00A1D8	STORM_PORT_PROTO_BPDU_CTRL [56].EN[20:20]	1
0xBB00A1D8	STORM_PORT_PROTO_BPDU_CTRL [56].RESERVED[19:17]	3
0xBB00A1D8	STORM_PORT_PROTO_BPDU_CTRL [56].RATE[16:8]	9
0xBB00A1D8	STORM_PORT_PROTO_BPDU_CTRL [56].BURST[7:0]	8
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [7].FLAG[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00A1E4	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [53].FLAG[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00A1E8	STORM_PORT_PROTO_BPDU_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [0].RST[0:0]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [1].RST[1:1]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [2].RST[2:2]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [3].RST[3:3]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [4].RST[4:4]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [5].RST[5:5]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [6].RST[6:6]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [7].RST[7:7]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [8].RST[8:8]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [9].RST[9:9]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [10].RST[10:10]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [11].RST[11:11]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [12].RST[12:12]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [13].RST[13:13]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [14].RST[14:14]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [15].RST[15:15]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [16].RST[16:16]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [17].RST[17:17]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [18].RST[18:18]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [19].RST[19:19]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [20].RST[20:20]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [21].RST[21:21]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [22].RST[22:22]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [23].RST[23:23]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [24].RST[24:24]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [25].RST[25:25]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [26].RST[26:26]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [27].RST[27:27]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [28].RST[28:28]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [29].RST[29:29]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [30].RST[30:30]	1
0xBB00A1DC	STORM_PORT_PROTO_BPDU_LB_RST [31].RST[31:31]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [32].RST[0:0]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [33].RST[1:1]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [34].RST[2:2]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [35].RST[3:3]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [36].RST[4:4]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [37].RST[5:5]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [38].RST[6:6]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [39].RST[7:7]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [40].RST[8:8]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [41].RST[9:9]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [42].RST[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [43].RST[11:11]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [44].RST[12:12]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [45].RST[13:13]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [46].RST[14:14]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [47].RST[15:15]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [48].RST[16:16]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [49].RST[17:17]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [50].RST[18:18]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [51].RST[19:19]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [52].RST[20:20]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [53].RST[21:21]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [54].RST[22:22]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [55].RST[23:23]	1
0xBB00A1E0	STORM_PORT_PROTO_BPDU_LB_RST [56].RST[24:24]	1
0xBB00A004	STORM_PORT_PROTO_DHCP_CTRL [0].RESERVED[31:21]	11
0xBB00A004	STORM_PORT_PROTO_DHCP_CTRL [0].EN[20:20]	1
0xBB00A004	STORM_PORT_PROTO_DHCP_CTRL [0].RESERVED[19:17]	3
0xBB00A004	STORM_PORT_PROTO_DHCP_CTRL [0].RATE[16:8]	9
0xBB00A004	STORM_PORT_PROTO_DHCP_CTRL [0].BURST[7:0]	8
0xBB00A008	STORM_PORT_PROTO_DHCP_CTRL [1].RESERVED[31:21]	11
0xBB00A008	STORM_PORT_PROTO_DHCP_CTRL [1].EN[20:20]	1
0xBB00A008	STORM_PORT_PROTO_DHCP_CTRL [1].RESERVED[19:17]	3
0xBB00A008	STORM_PORT_PROTO_DHCP_CTRL [1].RATE[16:8]	9
0xBB00A008	STORM_PORT_PROTO_DHCP_CTRL [1].BURST[7:0]	8
0xBB00A00C	STORM_PORT_PROTO_DHCP_CTRL [2].RESERVED[31:21]	11
0xBB00A00C	STORM_PORT_PROTO_DHCP_CTRL [2].EN[20:20]	1
0xBB00A00C	STORM_PORT_PROTO_DHCP_CTRL [2].RESERVED[19:17]	3
0xBB00A00C	STORM_PORT_PROTO_DHCP_CTRL [2].RATE[16:8]	9
0xBB00A00C	STORM_PORT_PROTO_DHCP_CTRL [2].BURST[7:0]	8
0xBB00A010	STORM_PORT_PROTO_DHCP_CTRL [3].RESERVED[31:21]	11
0xBB00A010	STORM_PORT_PROTO_DHCP_CTRL [3].EN[20:20]	1
0xBB00A010	STORM_PORT_PROTO_DHCP_CTRL [3].RESERVED[19:17]	3
0xBB00A010	STORM_PORT_PROTO_DHCP_CTRL [3].RATE[16:8]	9
0xBB00A010	STORM_PORT_PROTO_DHCP_CTRL [3].BURST[7:0]	8
0xBB00A014	STORM_PORT_PROTO_DHCP_CTRL [4].RESERVED[31:21]	11
0xBB00A014	STORM_PORT_PROTO_DHCP_CTRL [4].EN[20:20]	1
0xBB00A014	STORM_PORT_PROTO_DHCP_CTRL [4].RESERVED[19:17]	3
0xBB00A014	STORM_PORT_PROTO_DHCP_CTRL [4].RATE[16:8]	9
0xBB00A014	STORM_PORT_PROTO_DHCP_CTRL [4].BURST[7:0]	8
0xBB00A018	STORM_PORT_PROTO_DHCP_CTRL [5].RESERVED[31:21]	11
0xBB00A018	STORM_PORT_PROTO_DHCP_CTRL [5].EN[20:20]	1
0xBB00A018	STORM_PORT_PROTO_DHCP_CTRL [5].RESERVED[19:17]	3
0xBB00A018	STORM_PORT_PROTO_DHCP_CTRL [5].RATE[16:8]	9
0xBB00A018	STORM_PORT_PROTO_DHCP_CTRL [5].BURST[7:0]	8
0xBB00A01C	STORM_PORT_PROTO_DHCP_CTRL [6].RESERVED[31:21]	11
0xBB00A01C	STORM_PORT_PROTO_DHCP_CTRL [6].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A01C	STORM_PORT_PROTO_DHCP_CTRL [6].RESERVED[19:17]	3
0xBB00A01C	STORM_PORT_PROTO_DHCP_CTRL [6].RATE[16:8]	9
0xBB00A01C	STORM_PORT_PROTO_DHCP_CTRL [6].BURST[7:0]	8
0xBB00A020	STORM_PORT_PROTO_DHCP_CTRL [7].RESERVED[31:21]	11
0xBB00A020	STORM_PORT_PROTO_DHCP_CTRL [7].EN[20:20]	1
0xBB00A020	STORM_PORT_PROTO_DHCP_CTRL [7].RESERVED[19:17]	3
0xBB00A020	STORM_PORT_PROTO_DHCP_CTRL [7].RATE[16:8]	9
0xBB00A020	STORM_PORT_PROTO_DHCP_CTRL [7].BURST[7:0]	8
0xBB00A024	STORM_PORT_PROTO_DHCP_CTRL [8].RESERVED[31:21]	11
0xBB00A024	STORM_PORT_PROTO_DHCP_CTRL [8].EN[20:20]	1
0xBB00A024	STORM_PORT_PROTO_DHCP_CTRL [8].RESERVED[19:17]	3
0xBB00A024	STORM_PORT_PROTO_DHCP_CTRL [8].RATE[16:8]	9
0xBB00A024	STORM_PORT_PROTO_DHCP_CTRL [8].BURST[7:0]	8
0xBB00A028	STORM_PORT_PROTO_DHCP_CTRL [9].RESERVED[31:21]	11
0xBB00A028	STORM_PORT_PROTO_DHCP_CTRL [9].EN[20:20]	1
0xBB00A028	STORM_PORT_PROTO_DHCP_CTRL [9].RESERVED[19:17]	3
0xBB00A028	STORM_PORT_PROTO_DHCP_CTRL [9].RATE[16:8]	9
0xBB00A028	STORM_PORT_PROTO_DHCP_CTRL [9].BURST[7:0]	8
0xBB00A02C	STORM_PORT_PROTO_DHCP_CTRL [10].RESERVED[31:21]	11
0xBB00A02C	STORM_PORT_PROTO_DHCP_CTRL [10].EN[20:20]	1
0xBB00A02C	STORM_PORT_PROTO_DHCP_CTRL [10].RESERVED[19:17]	3
0xBB00A02C	STORM_PORT_PROTO_DHCP_CTRL [10].RATE[16:8]	9
0xBB00A02C	STORM_PORT_PROTO_DHCP_CTRL [10].BURST[7:0]	8
0xBB00A030	STORM_PORT_PROTO_DHCP_CTRL [11].RESERVED[31:21]	11
0xBB00A030	STORM_PORT_PROTO_DHCP_CTRL [11].EN[20:20]	1
0xBB00A030	STORM_PORT_PROTO_DHCP_CTRL [11].RESERVED[19:17]	3
0xBB00A030	STORM_PORT_PROTO_DHCP_CTRL [11].RATE[16:8]	9
0xBB00A030	STORM_PORT_PROTO_DHCP_CTRL [11].BURST[7:0]	8
0xBB00A034	STORM_PORT_PROTO_DHCP_CTRL [12].RESERVED[31:21]	11
0xBB00A034	STORM_PORT_PROTO_DHCP_CTRL [12].EN[20:20]	1
0xBB00A034	STORM_PORT_PROTO_DHCP_CTRL [12].RESERVED[19:17]	3
0xBB00A034	STORM_PORT_PROTO_DHCP_CTRL [12].RATE[16:8]	9
0xBB00A034	STORM_PORT_PROTO_DHCP_CTRL [12].BURST[7:0]	8
0xBB00A038	STORM_PORT_PROTO_DHCP_CTRL [13].RESERVED[31:21]	11
0xBB00A038	STORM_PORT_PROTO_DHCP_CTRL [13].EN[20:20]	1
0xBB00A038	STORM_PORT_PROTO_DHCP_CTRL [13].RESERVED[19:17]	3
0xBB00A038	STORM_PORT_PROTO_DHCP_CTRL [13].RATE[16:8]	9
0xBB00A038	STORM_PORT_PROTO_DHCP_CTRL [13].BURST[7:0]	8
0xBB00A03C	STORM_PORT_PROTO_DHCP_CTRL [14].RESERVED[31:21]	11
0xBB00A03C	STORM_PORT_PROTO_DHCP_CTRL [14].EN[20:20]	1
0xBB00A03C	STORM_PORT_PROTO_DHCP_CTRL [14].RESERVED[19:17]	3
0xBB00A03C	STORM_PORT_PROTO_DHCP_CTRL [14].RATE[16:8]	9
0xBB00A03C	STORM_PORT_PROTO_DHCP_CTRL [14].BURST[7:0]	8
0xBB00A040	STORM_PORT_PROTO_DHCP_CTRL [15].RESERVED[31:21]	11
0xBB00A040	STORM_PORT_PROTO_DHCP_CTRL [15].EN[20:20]	1
0xBB00A040	STORM_PORT_PROTO_DHCP_CTRL [15].RESERVED[19:17]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A040	STORM_PORT_PROTO_DHCP_CTRL [15].RATE[16:8]	9
0xBB00A040	STORM_PORT_PROTO_DHCP_CTRL [15].BURST[7:0]	8
0xBB00A044	STORM_PORT_PROTO_DHCP_CTRL [16].RESERVED[31:21]	11
0xBB00A044	STORM_PORT_PROTO_DHCP_CTRL [16].EN[20:20]	1
0xBB00A044	STORM_PORT_PROTO_DHCP_CTRL [16].RESERVED[19:17]	3
0xBB00A044	STORM_PORT_PROTO_DHCP_CTRL [16].RATE[16:8]	9
0xBB00A044	STORM_PORT_PROTO_DHCP_CTRL [16].BURST[7:0]	8
0xBB00A048	STORM_PORT_PROTO_DHCP_CTRL [17].RESERVED[31:21]	11
0xBB00A048	STORM_PORT_PROTO_DHCP_CTRL [17].EN[20:20]	1
0xBB00A048	STORM_PORT_PROTO_DHCP_CTRL [17].RESERVED[19:17]	3
0xBB00A048	STORM_PORT_PROTO_DHCP_CTRL [17].RATE[16:8]	9
0xBB00A048	STORM_PORT_PROTO_DHCP_CTRL [17].BURST[7:0]	8
0xBB00A04C	STORM_PORT_PROTO_DHCP_CTRL [18].RESERVED[31:21]	11
0xBB00A04C	STORM_PORT_PROTO_DHCP_CTRL [18].EN[20:20]	1
0xBB00A04C	STORM_PORT_PROTO_DHCP_CTRL [18].RESERVED[19:17]	3
0xBB00A04C	STORM_PORT_PROTO_DHCP_CTRL [18].RATE[16:8]	9
0xBB00A04C	STORM_PORT_PROTO_DHCP_CTRL [18].BURST[7:0]	8
0xBB00A050	STORM_PORT_PROTO_DHCP_CTRL [19].RESERVED[31:21]	11
0xBB00A050	STORM_PORT_PROTO_DHCP_CTRL [19].EN[20:20]	1
0xBB00A050	STORM_PORT_PROTO_DHCP_CTRL [19].RESERVED[19:17]	3
0xBB00A050	STORM_PORT_PROTO_DHCP_CTRL [19].RATE[16:8]	9
0xBB00A050	STORM_PORT_PROTO_DHCP_CTRL [19].BURST[7:0]	8
0xBB00A054	STORM_PORT_PROTO_DHCP_CTRL [20].RESERVED[31:21]	11
0xBB00A054	STORM_PORT_PROTO_DHCP_CTRL [20].EN[20:20]	1
0xBB00A054	STORM_PORT_PROTO_DHCP_CTRL [20].RESERVED[19:17]	3
0xBB00A054	STORM_PORT_PROTO_DHCP_CTRL [20].RATE[16:8]	9
0xBB00A054	STORM_PORT_PROTO_DHCP_CTRL [20].BURST[7:0]	8
0xBB00A058	STORM_PORT_PROTO_DHCP_CTRL [21].RESERVED[31:21]	11
0xBB00A058	STORM_PORT_PROTO_DHCP_CTRL [21].EN[20:20]	1
0xBB00A058	STORM_PORT_PROTO_DHCP_CTRL [21].RESERVED[19:17]	3
0xBB00A058	STORM_PORT_PROTO_DHCP_CTRL [21].RATE[16:8]	9
0xBB00A058	STORM_PORT_PROTO_DHCP_CTRL [21].BURST[7:0]	8
0xBB00A05C	STORM_PORT_PROTO_DHCP_CTRL [22].RESERVED[31:21]	11
0xBB00A05C	STORM_PORT_PROTO_DHCP_CTRL [22].EN[20:20]	1
0xBB00A05C	STORM_PORT_PROTO_DHCP_CTRL [22].RESERVED[19:17]	3
0xBB00A05C	STORM_PORT_PROTO_DHCP_CTRL [22].RATE[16:8]	9
0xBB00A05C	STORM_PORT_PROTO_DHCP_CTRL [22].BURST[7:0]	8
0xBB00A060	STORM_PORT_PROTO_DHCP_CTRL [23].RESERVED[31:21]	11
0xBB00A060	STORM_PORT_PROTO_DHCP_CTRL [23].EN[20:20]	1
0xBB00A060	STORM_PORT_PROTO_DHCP_CTRL [23].RESERVED[19:17]	3
0xBB00A060	STORM_PORT_PROTO_DHCP_CTRL [23].RATE[16:8]	9
0xBB00A060	STORM_PORT_PROTO_DHCP_CTRL [23].BURST[7:0]	8
0xBB00A064	STORM_PORT_PROTO_DHCP_CTRL [24].RESERVED[31:21]	11
0xBB00A064	STORM_PORT_PROTO_DHCP_CTRL [24].EN[20:20]	1
0xBB00A064	STORM_PORT_PROTO_DHCP_CTRL [24].RESERVED[19:17]	3
0xBB00A064	STORM_PORT_PROTO_DHCP_CTRL [24].RATE[16:8]	9

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A064	STORM_PORT_PROTO_DHCP_CTRL [24].BURST[7:0]	8
0xBB00A068	STORM_PORT_PROTO_DHCP_CTRL [25].RESERVED[31:21]	11
0xBB00A068	STORM_PORT_PROTO_DHCP_CTRL [25].EN[20:20]	1
0xBB00A068	STORM_PORT_PROTO_DHCP_CTRL [25].RESERVED[19:17]	3
0xBB00A068	STORM_PORT_PROTO_DHCP_CTRL [25].RATE[16:8]	9
0xBB00A068	STORM_PORT_PROTO_DHCP_CTRL [25].BURST[7:0]	8
0xBB00A06C	STORM_PORT_PROTO_DHCP_CTRL [26].RESERVED[31:21]	11
0xBB00A06C	STORM_PORT_PROTO_DHCP_CTRL [26].EN[20:20]	1
0xBB00A06C	STORM_PORT_PROTO_DHCP_CTRL [26].RESERVED[19:17]	3
0xBB00A06C	STORM_PORT_PROTO_DHCP_CTRL [26].RATE[16:8]	9
0xBB00A06C	STORM_PORT_PROTO_DHCP_CTRL [26].BURST[7:0]	8
0xBB00A070	STORM_PORT_PROTO_DHCP_CTRL [27].RESERVED[31:21]	11
0xBB00A070	STORM_PORT_PROTO_DHCP_CTRL [27].EN[20:20]	1
0xBB00A070	STORM_PORT_PROTO_DHCP_CTRL [27].RESERVED[19:17]	3
0xBB00A070	STORM_PORT_PROTO_DHCP_CTRL [27].RATE[16:8]	9
0xBB00A070	STORM_PORT_PROTO_DHCP_CTRL [27].BURST[7:0]	8
0xBB00A074	STORM_PORT_PROTO_DHCP_CTRL [28].RESERVED[31:21]	11
0xBB00A074	STORM_PORT_PROTO_DHCP_CTRL [28].EN[20:20]	1
0xBB00A074	STORM_PORT_PROTO_DHCP_CTRL [28].RESERVED[19:17]	3
0xBB00A074	STORM_PORT_PROTO_DHCP_CTRL [28].RATE[16:8]	9
0xBB00A074	STORM_PORT_PROTO_DHCP_CTRL [28].BURST[7:0]	8
0xBB00A078	STORM_PORT_PROTO_DHCP_CTRL [29].RESERVED[31:21]	11
0xBB00A078	STORM_PORT_PROTO_DHCP_CTRL [29].EN[20:20]	1
0xBB00A078	STORM_PORT_PROTO_DHCP_CTRL [29].RESERVED[19:17]	3
0xBB00A078	STORM_PORT_PROTO_DHCP_CTRL [29].RATE[16:8]	9
0xBB00A078	STORM_PORT_PROTO_DHCP_CTRL [29].BURST[7:0]	8
0xBB00A07C	STORM_PORT_PROTO_DHCP_CTRL [30].RESERVED[31:21]	11
0xBB00A07C	STORM_PORT_PROTO_DHCP_CTRL [30].EN[20:20]	1
0xBB00A07C	STORM_PORT_PROTO_DHCP_CTRL [30].RESERVED[19:17]	3
0xBB00A07C	STORM_PORT_PROTO_DHCP_CTRL [30].RATE[16:8]	9
0xBB00A07C	STORM_PORT_PROTO_DHCP_CTRL [30].BURST[7:0]	8
0xBB00A080	STORM_PORT_PROTO_DHCP_CTRL [31].RESERVED[31:21]	11
0xBB00A080	STORM_PORT_PROTO_DHCP_CTRL [31].EN[20:20]	1
0xBB00A080	STORM_PORT_PROTO_DHCP_CTRL [31].RESERVED[19:17]	3
0xBB00A080	STORM_PORT_PROTO_DHCP_CTRL [31].RATE[16:8]	9
0xBB00A080	STORM_PORT_PROTO_DHCP_CTRL [31].BURST[7:0]	8
0xBB00A084	STORM_PORT_PROTO_DHCP_CTRL [32].RESERVED[31:21]	11
0xBB00A084	STORM_PORT_PROTO_DHCP_CTRL [32].EN[20:20]	1
0xBB00A084	STORM_PORT_PROTO_DHCP_CTRL [32].RESERVED[19:17]	3
0xBB00A084	STORM_PORT_PROTO_DHCP_CTRL [32].RATE[16:8]	9
0xBB00A084	STORM_PORT_PROTO_DHCP_CTRL [32].BURST[7:0]	8
0xBB00A088	STORM_PORT_PROTO_DHCP_CTRL [33].RESERVED[31:21]	11
0xBB00A088	STORM_PORT_PROTO_DHCP_CTRL [33].EN[20:20]	1
0xBB00A088	STORM_PORT_PROTO_DHCP_CTRL [33].RESERVED[19:17]	3
0xBB00A088	STORM_PORT_PROTO_DHCP_CTRL [33].RATE[16:8]	9
0xBB00A088	STORM_PORT_PROTO_DHCP_CTRL [33].BURST[7:0]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A08C	STORM_PORT_PROTO_DHCP_CTRL [34].RESERVED[31:21]	11
0xBB00A08C	STORM_PORT_PROTO_DHCP_CTRL [34].EN[20:20]	1
0xBB00A08C	STORM_PORT_PROTO_DHCP_CTRL [34].RESERVED[19:17]	3
0xBB00A08C	STORM_PORT_PROTO_DHCP_CTRL [34].RATE[16:8]	9
0xBB00A08C	STORM_PORT_PROTO_DHCP_CTRL [34].BURST[7:0]	8
0xBB00A090	STORM_PORT_PROTO_DHCP_CTRL [35].RESERVED[31:21]	11
0xBB00A090	STORM_PORT_PROTO_DHCP_CTRL [35].EN[20:20]	1
0xBB00A090	STORM_PORT_PROTO_DHCP_CTRL [35].RESERVED[19:17]	3
0xBB00A090	STORM_PORT_PROTO_DHCP_CTRL [35].RATE[16:8]	9
0xBB00A090	STORM_PORT_PROTO_DHCP_CTRL [35].BURST[7:0]	8
0xBB00A094	STORM_PORT_PROTO_DHCP_CTRL [36].RESERVED[31:21]	11
0xBB00A094	STORM_PORT_PROTO_DHCP_CTRL [36].EN[20:20]	1
0xBB00A094	STORM_PORT_PROTO_DHCP_CTRL [36].RESERVED[19:17]	3
0xBB00A094	STORM_PORT_PROTO_DHCP_CTRL [36].RATE[16:8]	9
0xBB00A094	STORM_PORT_PROTO_DHCP_CTRL [36].BURST[7:0]	8
0xBB00A098	STORM_PORT_PROTO_DHCP_CTRL [37].RESERVED[31:21]	11
0xBB00A098	STORM_PORT_PROTO_DHCP_CTRL [37].EN[20:20]	1
0xBB00A098	STORM_PORT_PROTO_DHCP_CTRL [37].RESERVED[19:17]	3
0xBB00A098	STORM_PORT_PROTO_DHCP_CTRL [37].RATE[16:8]	9
0xBB00A098	STORM_PORT_PROTO_DHCP_CTRL [37].BURST[7:0]	8
0xBB00A09C	STORM_PORT_PROTO_DHCP_CTRL [38].RESERVED[31:21]	11
0xBB00A09C	STORM_PORT_PROTO_DHCP_CTRL [38].EN[20:20]	1
0xBB00A09C	STORM_PORT_PROTO_DHCP_CTRL [38].RESERVED[19:17]	3
0xBB00A09C	STORM_PORT_PROTO_DHCP_CTRL [38].RATE[16:8]	9
0xBB00A09C	STORM_PORT_PROTO_DHCP_CTRL [38].BURST[7:0]	8
0xBB00A0A0	STORM_PORT_PROTO_DHCP_CTRL [39].RESERVED[31:21]	11
0xBB00A0A0	STORM_PORT_PROTO_DHCP_CTRL [39].EN[20:20]	1
0xBB00A0A0	STORM_PORT_PROTO_DHCP_CTRL [39].RESERVED[19:17]	3
0xBB00A0A0	STORM_PORT_PROTO_DHCP_CTRL [39].RATE[16:8]	9
0xBB00A0A0	STORM_PORT_PROTO_DHCP_CTRL [39].BURST[7:0]	8
0xBB00A0A4	STORM_PORT_PROTO_DHCP_CTRL [40].RESERVED[31:21]	11
0xBB00A0A4	STORM_PORT_PROTO_DHCP_CTRL [40].EN[20:20]	1
0xBB00A0A4	STORM_PORT_PROTO_DHCP_CTRL [40].RESERVED[19:17]	3
0xBB00A0A4	STORM_PORT_PROTO_DHCP_CTRL [40].RATE[16:8]	9
0xBB00A0A4	STORM_PORT_PROTO_DHCP_CTRL [40].BURST[7:0]	8
0xBB00A0A8	STORM_PORT_PROTO_DHCP_CTRL [41].RESERVED[31:21]	11
0xBB00A0A8	STORM_PORT_PROTO_DHCP_CTRL [41].EN[20:20]	1
0xBB00A0A8	STORM_PORT_PROTO_DHCP_CTRL [41].RESERVED[19:17]	3
0xBB00A0A8	STORM_PORT_PROTO_DHCP_CTRL [41].RATE[16:8]	9
0xBB00A0A8	STORM_PORT_PROTO_DHCP_CTRL [41].BURST[7:0]	8
0xBB00A0AC	STORM_PORT_PROTO_DHCP_CTRL [42].RESERVED[31:21]	11
0xBB00A0AC	STORM_PORT_PROTO_DHCP_CTRL [42].EN[20:20]	1
0xBB00A0AC	STORM_PORT_PROTO_DHCP_CTRL [42].RESERVED[19:17]	3
0xBB00A0AC	STORM_PORT_PROTO_DHCP_CTRL [42].RATE[16:8]	9
0xBB00A0AC	STORM_PORT_PROTO_DHCP_CTRL [42].BURST[7:0]	8
0xBB00A0B0	STORM_PORT_PROTO_DHCP_CTRL [43].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A0B0	STORM_PORT_PROTO_DHCP_CTRL [43].EN[20:20]	1
0xBB00A0B0	STORM_PORT_PROTO_DHCP_CTRL [43].RESERVED[19:17]	3
0xBB00A0B0	STORM_PORT_PROTO_DHCP_CTRL [43].RATE[16:8]	9
0xBB00A0B0	STORM_PORT_PROTO_DHCP_CTRL [43].BURST[7:0]	8
0xBB00A0B4	STORM_PORT_PROTO_DHCP_CTRL [44].RESERVED[31:21]	11
0xBB00A0B4	STORM_PORT_PROTO_DHCP_CTRL [44].EN[20:20]	1
0xBB00A0B4	STORM_PORT_PROTO_DHCP_CTRL [44].RESERVED[19:17]	3
0xBB00A0B4	STORM_PORT_PROTO_DHCP_CTRL [44].RATE[16:8]	9
0xBB00A0B4	STORM_PORT_PROTO_DHCP_CTRL [44].BURST[7:0]	8
0xBB00A0B8	STORM_PORT_PROTO_DHCP_CTRL [45].RESERVED[31:21]	11
0xBB00A0B8	STORM_PORT_PROTO_DHCP_CTRL [45].EN[20:20]	1
0xBB00A0B8	STORM_PORT_PROTO_DHCP_CTRL [45].RESERVED[19:17]	3
0xBB00A0B8	STORM_PORT_PROTO_DHCP_CTRL [45].RATE[16:8]	9
0xBB00A0B8	STORM_PORT_PROTO_DHCP_CTRL [45].BURST[7:0]	8
0xBB00A0BC	STORM_PORT_PROTO_DHCP_CTRL [46].RESERVED[31:21]	11
0xBB00A0BC	STORM_PORT_PROTO_DHCP_CTRL [46].EN[20:20]	1
0xBB00A0BC	STORM_PORT_PROTO_DHCP_CTRL [46].RESERVED[19:17]	3
0xBB00A0BC	STORM_PORT_PROTO_DHCP_CTRL [46].RATE[16:8]	9
0xBB00A0BC	STORM_PORT_PROTO_DHCP_CTRL [46].BURST[7:0]	8
0xBB00A0C0	STORM_PORT_PROTO_DHCP_CTRL [47].RESERVED[31:21]	11
0xBB00A0C0	STORM_PORT_PROTO_DHCP_CTRL [47].EN[20:20]	1
0xBB00A0C0	STORM_PORT_PROTO_DHCP_CTRL [47].RESERVED[19:17]	3
0xBB00A0C0	STORM_PORT_PROTO_DHCP_CTRL [47].RATE[16:8]	9
0xBB00A0C0	STORM_PORT_PROTO_DHCP_CTRL [47].BURST[7:0]	8
0xBB00A0C4	STORM_PORT_PROTO_DHCP_CTRL [48].RESERVED[31:21]	11
0xBB00A0C4	STORM_PORT_PROTO_DHCP_CTRL [48].EN[20:20]	1
0xBB00A0C4	STORM_PORT_PROTO_DHCP_CTRL [48].RESERVED[19:17]	3
0xBB00A0C4	STORM_PORT_PROTO_DHCP_CTRL [48].RATE[16:8]	9
0xBB00A0C4	STORM_PORT_PROTO_DHCP_CTRL [48].BURST[7:0]	8
0xBB00A0C8	STORM_PORT_PROTO_DHCP_CTRL [49].RESERVED[31:21]	11
0xBB00A0C8	STORM_PORT_PROTO_DHCP_CTRL [49].EN[20:20]	1
0xBB00A0C8	STORM_PORT_PROTO_DHCP_CTRL [49].RESERVED[19:17]	3
0xBB00A0C8	STORM_PORT_PROTO_DHCP_CTRL [49].RATE[16:8]	9
0xBB00A0C8	STORM_PORT_PROTO_DHCP_CTRL [49].BURST[7:0]	8
0xBB00A0CC	STORM_PORT_PROTO_DHCP_CTRL [50].RESERVED[31:21]	11
0xBB00A0CC	STORM_PORT_PROTO_DHCP_CTRL [50].EN[20:20]	1
0xBB00A0CC	STORM_PORT_PROTO_DHCP_CTRL [50].RESERVED[19:17]	3
0xBB00A0CC	STORM_PORT_PROTO_DHCP_CTRL [50].RATE[16:8]	9
0xBB00A0CC	STORM_PORT_PROTO_DHCP_CTRL [50].BURST[7:0]	8
0xBB00A0D0	STORM_PORT_PROTO_DHCP_CTRL [51].RESERVED[31:21]	11
0xBB00A0D0	STORM_PORT_PROTO_DHCP_CTRL [51].EN[20:20]	1
0xBB00A0D0	STORM_PORT_PROTO_DHCP_CTRL [51].RESERVED[19:17]	3
0xBB00A0D0	STORM_PORT_PROTO_DHCP_CTRL [51].RATE[16:8]	9
0xBB00A0D0	STORM_PORT_PROTO_DHCP_CTRL [51].BURST[7:0]	8
0xBB00A0D4	STORM_PORT_PROTO_DHCP_CTRL [52].RESERVED[31:21]	11
0xBB00A0D4	STORM_PORT_PROTO_DHCP_CTRL [52].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A0D4	STORM_PORT_PROTO_DHCP_CTRL [52].RESERVED[19:17]	3
0xBB00A0D4	STORM_PORT_PROTO_DHCP_CTRL [52].RATE[16:8]	9
0xBB00A0D4	STORM_PORT_PROTO_DHCP_CTRL [52].BURST[7:0]	8
0xBB00A0D8	STORM_PORT_PROTO_DHCP_CTRL [53].RESERVED[31:21]	11
0xBB00A0D8	STORM_PORT_PROTO_DHCP_CTRL [53].EN[20:20]	1
0xBB00A0D8	STORM_PORT_PROTO_DHCP_CTRL [53].RESERVED[19:17]	3
0xBB00A0D8	STORM_PORT_PROTO_DHCP_CTRL [53].RATE[16:8]	9
0xBB00A0D8	STORM_PORT_PROTO_DHCP_CTRL [53].BURST[7:0]	8
0xBB00A0DC	STORM_PORT_PROTO_DHCP_CTRL [54].RESERVED[31:21]	11
0xBB00A0DC	STORM_PORT_PROTO_DHCP_CTRL [54].EN[20:20]	1
0xBB00A0DC	STORM_PORT_PROTO_DHCP_CTRL [54].RESERVED[19:17]	3
0xBB00A0DC	STORM_PORT_PROTO_DHCP_CTRL [54].RATE[16:8]	9
0xBB00A0DC	STORM_PORT_PROTO_DHCP_CTRL [54].BURST[7:0]	8
0xBB00A0E0	STORM_PORT_PROTO_DHCP_CTRL [55].RESERVED[31:21]	11
0xBB00A0E0	STORM_PORT_PROTO_DHCP_CTRL [55].EN[20:20]	1
0xBB00A0E0	STORM_PORT_PROTO_DHCP_CTRL [55].RESERVED[19:17]	3
0xBB00A0E0	STORM_PORT_PROTO_DHCP_CTRL [55].RATE[16:8]	9
0xBB00A0E0	STORM_PORT_PROTO_DHCP_CTRL [55].BURST[7:0]	8
0xBB00A0E4	STORM_PORT_PROTO_DHCP_CTRL [56].RESERVED[31:21]	11
0xBB00A0E4	STORM_PORT_PROTO_DHCP_CTRL [56].EN[20:20]	1
0xBB00A0E4	STORM_PORT_PROTO_DHCP_CTRL [56].RESERVED[19:17]	3
0xBB00A0E4	STORM_PORT_PROTO_DHCP_CTRL [56].RATE[16:8]	9
0xBB00A0E4	STORM_PORT_PROTO_DHCP_CTRL [56].BURST[7:0]	8
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [22].FLAG[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00A0F0	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00A0F4	STORM_PORT_PROTO_DHCP_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [0].RST[0:0]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [1].RST[1:1]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [2].RST[2:2]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [3].RST[3:3]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [4].RST[4:4]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [5].RST[5:5]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [6].RST[6:6]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [7].RST[7:7]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [8].RST[8:8]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [9].RST[9:9]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [10].RST[10:10]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [11].RST[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [12].RST[12:12]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [13].RST[13:13]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [14].RST[14:14]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [15].RST[15:15]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [16].RST[16:16]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [17].RST[17:17]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [18].RST[18:18]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [19].RST[19:19]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [20].RST[20:20]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [21].RST[21:21]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [22].RST[22:22]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [23].RST[23:23]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [24].RST[24:24]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [25].RST[25:25]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [26].RST[26:26]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [27].RST[27:27]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [28].RST[28:28]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [29].RST[29:29]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [30].RST[30:30]	1
0xBB00A0E8	STORM_PORT_PROTO_DHCP_LB_RST [31].RST[31:31]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [32].RST[0:0]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [33].RST[1:1]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [34].RST[2:2]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [35].RST[3:3]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [36].RST[4:4]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [37].RST[5:5]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [38].RST[6:6]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [39].RST[7:7]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [40].RST[8:8]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [41].RST[9:9]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [42].RST[10:10]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [43].RST[11:11]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [44].RST[12:12]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [45].RST[13:13]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [46].RST[14:14]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [47].RST[15:15]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [48].RST[16:16]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [49].RST[17:17]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [50].RST[18:18]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [51].RST[19:19]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [52].RST[20:20]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [53].RST[21:21]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [54].RST[22:22]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [55].RST[23:23]	1
0xBB00A0EC	STORM_PORT_PROTO_DHCP_LB_RST [56].RST[24:24]	1
0xBB00A1EC	STORM_PORT_PROTO_IGMP_CTRL [0].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A1EC	STORM_PORT_PROTO_IGMP_CTRL [0].EN[20:20]	1
0xBB00A1EC	STORM_PORT_PROTO_IGMP_CTRL [0].RESERVED[19:17]	3
0xBB00A1EC	STORM_PORT_PROTO_IGMP_CTRL [0].RATE[16:8]	9
0xBB00A1EC	STORM_PORT_PROTO_IGMP_CTRL [0].BURST[7:0]	8
0xBB00A1F0	STORM_PORT_PROTO_IGMP_CTRL [1].RESERVED[31:21]	11
0xBB00A1F0	STORM_PORT_PROTO_IGMP_CTRL [1].EN[20:20]	1
0xBB00A1F0	STORM_PORT_PROTO_IGMP_CTRL [1].RESERVED[19:17]	3
0xBB00A1F0	STORM_PORT_PROTO_IGMP_CTRL [1].RATE[16:8]	9
0xBB00A1F0	STORM_PORT_PROTO_IGMP_CTRL [1].BURST[7:0]	8
0xBB00A1F4	STORM_PORT_PROTO_IGMP_CTRL [2].RESERVED[31:21]	11
0xBB00A1F4	STORM_PORT_PROTO_IGMP_CTRL [2].EN[20:20]	1
0xBB00A1F4	STORM_PORT_PROTO_IGMP_CTRL [2].RESERVED[19:17]	3
0xBB00A1F4	STORM_PORT_PROTO_IGMP_CTRL [2].RATE[16:8]	9
0xBB00A1F4	STORM_PORT_PROTO_IGMP_CTRL [2].BURST[7:0]	8
0xBB00A1F8	STORM_PORT_PROTO_IGMP_CTRL [3].RESERVED[31:21]	11
0xBB00A1F8	STORM_PORT_PROTO_IGMP_CTRL [3].EN[20:20]	1
0xBB00A1F8	STORM_PORT_PROTO_IGMP_CTRL [3].RESERVED[19:17]	3
0xBB00A1F8	STORM_PORT_PROTO_IGMP_CTRL [3].RATE[16:8]	9
0xBB00A1F8	STORM_PORT_PROTO_IGMP_CTRL [3].BURST[7:0]	8
0xBB00A1FC	STORM_PORT_PROTO_IGMP_CTRL [4].RESERVED[31:21]	11
0xBB00A1FC	STORM_PORT_PROTO_IGMP_CTRL [4].EN[20:20]	1
0xBB00A1FC	STORM_PORT_PROTO_IGMP_CTRL [4].RESERVED[19:17]	3
0xBB00A1FC	STORM_PORT_PROTO_IGMP_CTRL [4].RATE[16:8]	9
0xBB00A1FC	STORM_PORT_PROTO_IGMP_CTRL [4].BURST[7:0]	8
0xBB00A200	STORM_PORT_PROTO_IGMP_CTRL [5].RESERVED[31:21]	11
0xBB00A200	STORM_PORT_PROTO_IGMP_CTRL [5].EN[20:20]	1
0xBB00A200	STORM_PORT_PROTO_IGMP_CTRL [5].RESERVED[19:17]	3
0xBB00A200	STORM_PORT_PROTO_IGMP_CTRL [5].RATE[16:8]	9
0xBB00A200	STORM_PORT_PROTO_IGMP_CTRL [5].BURST[7:0]	8
0xBB00A204	STORM_PORT_PROTO_IGMP_CTRL [6].RESERVED[31:21]	11
0xBB00A204	STORM_PORT_PROTO_IGMP_CTRL [6].EN[20:20]	1
0xBB00A204	STORM_PORT_PROTO_IGMP_CTRL [6].RESERVED[19:17]	3
0xBB00A204	STORM_PORT_PROTO_IGMP_CTRL [6].RATE[16:8]	9
0xBB00A204	STORM_PORT_PROTO_IGMP_CTRL [6].BURST[7:0]	8
0xBB00A208	STORM_PORT_PROTO_IGMP_CTRL [7].RESERVED[31:21]	11
0xBB00A208	STORM_PORT_PROTO_IGMP_CTRL [7].EN[20:20]	1
0xBB00A208	STORM_PORT_PROTO_IGMP_CTRL [7].RESERVED[19:17]	3
0xBB00A208	STORM_PORT_PROTO_IGMP_CTRL [7].RATE[16:8]	9
0xBB00A208	STORM_PORT_PROTO_IGMP_CTRL [7].BURST[7:0]	8
0xBB00A20C	STORM_PORT_PROTO_IGMP_CTRL [8].RESERVED[31:21]	11
0xBB00A20C	STORM_PORT_PROTO_IGMP_CTRL [8].EN[20:20]	1
0xBB00A20C	STORM_PORT_PROTO_IGMP_CTRL [8].RESERVED[19:17]	3
0xBB00A20C	STORM_PORT_PROTO_IGMP_CTRL [8].RATE[16:8]	9
0xBB00A20C	STORM_PORT_PROTO_IGMP_CTRL [8].BURST[7:0]	8
0xBB00A210	STORM_PORT_PROTO_IGMP_CTRL [9].RESERVED[31:21]	11
0xBB00A210	STORM_PORT_PROTO_IGMP_CTRL [9].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A210	STORM_PORT_PROTO_IGMP_CTRL [9].RESERVED[19:17]	3
0xBB00A210	STORM_PORT_PROTO_IGMP_CTRL [9].RATE[16:8]	9
0xBB00A210	STORM_PORT_PROTO_IGMP_CTRL [9].BURST[7:0]	8
0xBB00A214	STORM_PORT_PROTO_IGMP_CTRL [10].RESERVED[31:21]	11
0xBB00A214	STORM_PORT_PROTO_IGMP_CTRL [10].EN[20:20]	1
0xBB00A214	STORM_PORT_PROTO_IGMP_CTRL [10].RESERVED[19:17]	3
0xBB00A214	STORM_PORT_PROTO_IGMP_CTRL [10].RATE[16:8]	9
0xBB00A214	STORM_PORT_PROTO_IGMP_CTRL [10].BURST[7:0]	8
0xBB00A218	STORM_PORT_PROTO_IGMP_CTRL [11].RESERVED[31:21]	11
0xBB00A218	STORM_PORT_PROTO_IGMP_CTRL [11].EN[20:20]	1
0xBB00A218	STORM_PORT_PROTO_IGMP_CTRL [11].RESERVED[19:17]	3
0xBB00A218	STORM_PORT_PROTO_IGMP_CTRL [11].RATE[16:8]	9
0xBB00A218	STORM_PORT_PROTO_IGMP_CTRL [11].BURST[7:0]	8
0xBB00A21C	STORM_PORT_PROTO_IGMP_CTRL [12].RESERVED[31:21]	11
0xBB00A21C	STORM_PORT_PROTO_ICMP_CTRL [12].EN[20:20]	1
0xBB00A21C	STORM_PORT_PROTO_IGMP_CTRL [12].RESERVED[19:17]	3
0xBB00A21C	STORM_PORT_PROTO_IGMP_CTRL [12].RATE[16:8]	9
0xBB00A21C	STORM_PORT_PROTO_IGMP_CTRL [12].BURST[7:0]	8
0xBB00A220	STORM_PORT_PROTO_IGMP_CTRL [13].RESERVED[31:21]	11
0xBB00A220	STORM_PORT_PROTO_IGMP_CTRL [13].EN[20:20]	1
0xBB00A220	STORM_PORT_PROTO_IGMP_CTRL [13].RESERVED[19:17]	3
0xBB00A220	STORM_PORT_PROTO_IGMP_CTRL [13].RATE[16:8]	9
0xBB00A220	STORM_PORT_PROTO_IGMP_CTRL [13].BURST[7:0]	8
0xBB00A224	STORM_PORT_PROTO_IGMP_CTRL [14].RESERVED[31:21]	11
0xBB00A224	STORM_PORT_PROTO_IGMP_CTRL [14].EN[20:20]	1
0xBB00A224	STORM_PORT_PROTO_IGMP_CTRL [14].RESERVED[19:17]	3
0xBB00A224	STORM_PORT_PROTO_IGMP_CTRL [14].RATE[16:8]	9
0xBB00A224	STORM_PORT_PROTO_IGMP_CTRL [14].BURST[7:0]	8
0xBB00A228	STORM_PORT_PROTO_IGMP_CTRL [15].RESERVED[31:21]	11
0xBB00A228	STORM_PORT_PROTO_IGMP_CTRL [15].EN[20:20]	1
0xBB00A228	STORM_PORT_PROTO_IGMP_CTRL [15].RESERVED[19:17]	3
0xBB00A228	STORM_PORT_PROTO_IGMP_CTRL [15].RATE[16:8]	9
0xBB00A228	STORM_PORT_PROTO_IGMP_CTRL [15].BURST[7:0]	8
0xBB00A22C	STORM_PORT_PROTO_IGMP_CTRL [16].RESERVED[31:21]	11
0xBB00A22C	STORM_PORT_PROTO_IGMP_CTRL [16].EN[20:20]	1
0xBB00A22C	STORM_PORT_PROTO_IGMP_CTRL [16].RESERVED[19:17]	3
0xBB00A22C	STORM_PORT_PROTO_IGMP_CTRL [16].RATE[16:8]	9
0xBB00A22C	STORM_PORT_PROTO_IGMP_CTRL [16].BURST[7:0]	8
0xBB00A230	STORM_PORT_PROTO_IGMP_CTRL [17].RESERVED[31:21]	11
0xBB00A230	STORM_PORT_PROTO_IGMP_CTRL [17].EN[20:20]	1
0xBB00A230	STORM_PORT_PROTO_IGMP_CTRL [17].RESERVED[19:17]	3
0xBB00A230	STORM_PORT_PROTO_IGMP_CTRL [17].RATE[16:8]	9
0xBB00A230	STORM_PORT_PROTO_IGMP_CTRL [17].BURST[7:0]	8
0xBB00A234	STORM_PORT_PROTO_IGMP_CTRL [18].RESERVED[31:21]	11
0xBB00A234	STORM_PORT_PROTO_IGMP_CTRL [18].EN[20:20]	1
0xBB00A234	STORM_PORT_PROTO_IGMP_CTRL [18].RESERVED[19:17]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A234	STORM_PORT_PROTO_IGMP_CTRL [18].RATE[16:8]	9
0xBB00A234	STORM_PORT_PROTO_IGMP_CTRL [18].BURST[7:0]	8
0xBB00A238	STORM_PORT_PROTO_IGMP_CTRL [19].RESERVED[31:21]	11
0xBB00A238	STORM_PORT_PROTO_IGMP_CTRL [19].EN[20:20]	1
0xBB00A238	STORM_PORT_PROTO_IGMP_CTRL [19].RESERVED[19:17]	3
0xBB00A238	STORM_PORT_PROTO_IGMP_CTRL [19].RATE[16:8]	9
0xBB00A238	STORM_PORT_PROTO_IGMP_CTRL [19].BURST[7:0]	8
0xBB00A23C	STORM_PORT_PROTO_IGMP_CTRL [20].RESERVED[31:21]	11
0xBB00A23C	STORM_PORT_PROTO_IGMP_CTRL [20].EN[20:20]	1
0xBB00A23C	STORM_PORT_PROTO_IGMP_CTRL [20].RESERVED[19:17]	3
0xBB00A23C	STORM_PORT_PROTO_IGMP_CTRL [20].RATE[16:8]	9
0xBB00A23C	STORM_PORT_PROTO_IGMP_CTRL [20].BURST[7:0]	8
0xBB00A240	STORM_PORT_PROTO_IGMP_CTRL [21].RESERVED[31:21]	11
0xBB00A240	STORM_PORT_PROTO_IGMP_CTRL [21].EN[20:20]	1
0xBB00A240	STORM_PORT_PROTO_IGMP_CTRL [21].RESERVED[19:17]	3
0xBB00A240	STORM_PORT_PROTO_IGMP_CTRL [21].RATE[16:8]	9
0xBB00A240	STORM_PORT_PROTO_IGMP_CTRL [21].BURST[7:0]	8
0xBB00A244	STORM_PORT_PROTO_IGMP_CTRL [22].RESERVED[31:21]	11
0xBB00A244	STORM_PORT_PROTO_IGMP_CTRL [22].EN[20:20]	1
0xBB00A244	STORM_PORT_PROTO_IGMP_CTRL [22].RESERVED[19:17]	3
0xBB00A244	STORM_PORT_PROTO_IGMP_CTRL [22].RATE[16:8]	9
0xBB00A244	STORM_PORT_PROTO_IGMP_CTRL [22].BURST[7:0]	8
0xBB00A248	STORM_PORT_PROTO_IGMP_CTRL [23].RESERVED[31:21]	11
0xBB00A248	STORM_PORT_PROTO_IGMP_CTRL [23].EN[20:20]	1
0xBB00A248	STORM_PORT_PROTO_IGMP_CTRL [23].RESERVED[19:17]	3
0xBB00A248	STORM_PORT_PROTO_IGMP_CTRL [23].RATE[16:8]	9
0xBB00A248	STORM_PORT_PROTO_IGMP_CTRL [23].BURST[7:0]	8
0xBB00A24C	STORM_PORT_PROTO_IGMP_CTRL [24].RESERVED[31:21]	11
0xBB00A24C	STORM_PORT_PROTO_IGMP_CTRL [24].EN[20:20]	1
0xBB00A24C	STORM_PORT_PROTO_IGMP_CTRL [24].RESERVED[19:17]	3
0xBB00A24C	STORM_PORT_PROTO_IGMP_CTRL [24].RATE[16:8]	9
0xBB00A24C	STORM_PORT_PROTO_IGMP_CTRL [24].BURST[7:0]	8
0xBB00A250	STORM_PORT_PROTO_IGMP_CTRL [25].RESERVED[31:21]	11
0xBB00A250	STORM_PORT_PROTO_IGMP_CTRL [25].EN[20:20]	1
0xBB00A250	STORM_PORT_PROTO_IGMP_CTRL [25].RESERVED[19:17]	3
0xBB00A250	STORM_PORT_PROTO_IGMP_CTRL [25].RATE[16:8]	9
0xBB00A250	STORM_PORT_PROTO_IGMP_CTRL [25].BURST[7:0]	8
0xBB00A254	STORM_PORT_PROTO_IGMP_CTRL [26].RESERVED[31:21]	11
0xBB00A254	STORM_PORT_PROTO_IGMP_CTRL [26].EN[20:20]	1
0xBB00A254	STORM_PORT_PROTO_IGMP_CTRL [26].RESERVED[19:17]	3
0xBB00A254	STORM_PORT_PROTO_IGMP_CTRL [26].RATE[16:8]	9
0xBB00A254	STORM_PORT_PROTO_IGMP_CTRL [26].BURST[7:0]	8
0xBB00A258	STORM_PORT_PROTO_IGMP_CTRL [27].RESERVED[31:21]	11
0xBB00A258	STORM_PORT_PROTO_IGMP_CTRL [27].EN[20:20]	1
0xBB00A258	STORM_PORT_PROTO_IGMP_CTRL [27].RESERVED[19:17]	3
0xBB00A258	STORM_PORT_PROTO_IGMP_CTRL [27].RATE[16:8]	9

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A258	STORM_PORT_PROTO_IGMP_CTRL [27].BURST[7:0]	8
0xBB00A25C	STORM_PORT_PROTO_IGMP_CTRL [28].RESERVED[31:21]	11
0xBB00A25C	STORM_PORT_PROTO_IGMP_CTRL [28].EN[20:20]	1
0xBB00A25C	STORM_PORT_PROTO_IGMP_CTRL [28].RESERVED[19:17]	3
0xBB00A25C	STORM_PORT_PROTO_IGMP_CTRL [28].RATE[16:8]	9
0xBB00A25C	STORM_PORT_PROTO_IGMP_CTRL [28].BURST[7:0]	8
0xBB00A260	STORM_PORT_PROTO_IGMP_CTRL [29].RESERVED[31:21]	11
0xBB00A260	STORM_PORT_PROTO_IGMP_CTRL [29].EN[20:20]	1
0xBB00A260	STORM_PORT_PROTO_IGMP_CTRL [29].RESERVED[19:17]	3
0xBB00A260	STORM_PORT_PROTO_IGMP_CTRL [29].RATE[16:8]	9
0xBB00A260	STORM_PORT_PROTO_IGMP_CTRL [29].BURST[7:0]	8
0xBB00A264	STORM_PORT_PROTO_IGMP_CTRL [30].RESERVED[31:21]	11
0xBB00A264	STORM_PORT_PROTO_IGMP_CTRL [30].EN[20:20]	1
0xBB00A264	STORM_PORT_PROTO_IGMP_CTRL [30].RESERVED[19:17]	3
0xBB00A264	STORM_PORT_PROTO_IGMP_CTRL [30].RATE[16:8]	9
0xBB00A264	STORM_PORT_PROTO_IGMP_CTRL [30].BURST[7:0]	8
0xBB00A268	STORM_PORT_PROTO_IGMP_CTRL [31].RESERVED[31:21]	11
0xBB00A268	STORM_PORT_PROTO_IGMP_CTRL [31].EN[20:20]	1
0xBB00A268	STORM_PORT_PROTO_IGMP_CTRL [31].RESERVED[19:17]	3
0xBB00A268	STORM_PORT_PROTO_IGMP_CTRL [31].RATE[16:8]	9
0xBB00A268	STORM_PORT_PROTO_IGMP_CTRL [31].BURST[7:0]	8
0xBB00A26C	STORM_PORT_PROTO_IGMP_CTRL [32].RESERVED[31:21]	11
0xBB00A26C	STORM_PORT_PROTO_IGMP_CTRL [32].EN[20:20]	1
0xBB00A26C	STORM_PORT_PROTO_IGMP_CTRL [32].RESERVED[19:17]	3
0xBB00A26C	STORM_PORT_PROTO_IGMP_CTRL [32].RATE[16:8]	9
0xBB00A26C	STORM_PORT_PROTO_IGMP_CTRL [32].BURST[7:0]	8
0xBB00A270	STORM_PORT_PROTO_IGMP_CTRL [33].RESERVED[31:21]	11
0xBB00A270	STORM_PORT_PROTO_IGMP_CTRL [33].EN[20:20]	1
0xBB00A270	STORM_PORT_PROTO_IGMP_CTRL [33].RESERVED[19:17]	3
0xBB00A270	STORM_PORT_PROTO_IGMP_CTRL [33].RATE[16:8]	9
0xBB00A270	STORM_PORT_PROTO_IGMP_CTRL [33].BURST[7:0]	8
0xBB00A274	STORM_PORT_PROTO_IGMP_CTRL [34].RESERVED[31:21]	11
0xBB00A274	STORM_PORT_PROTO_IGMP_CTRL [34].EN[20:20]	1
0xBB00A274	STORM_PORT_PROTO_IGMP_CTRL [34].RESERVED[19:17]	3
0xBB00A274	STORM_PORT_PROTO_IGMP_CTRL [34].RATE[16:8]	9
0xBB00A274	STORM_PORT_PROTO_IGMP_CTRL [34].BURST[7:0]	8
0xBB00A278	STORM_PORT_PROTO_IGMP_CTRL [35].RESERVED[31:21]	11
0xBB00A278	STORM_PORT_PROTO_IGMP_CTRL [35].EN[20:20]	1
0xBB00A278	STORM_PORT_PROTO_IGMP_CTRL [35].RESERVED[19:17]	3
0xBB00A278	STORM_PORT_PROTO_IGMP_CTRL [35].RATE[16:8]	9
0xBB00A278	STORM_PORT_PROTO_IGMP_CTRL [35].BURST[7:0]	8
0xBB00A27C	STORM_PORT_PROTO_IGMP_CTRL [36].RESERVED[31:21]	11
0xBB00A27C	STORM_PORT_PROTO_IGMP_CTRL [36].EN[20:20]	1
0xBB00A27C	STORM_PORT_PROTO_IGMP_CTRL [36].RESERVED[19:17]	3
0xBB00A27C	STORM_PORT_PROTO_IGMP_CTRL [36].RATE[16:8]	9
0xBB00A27C	STORM_PORT_PROTO_IGMP_CTRL [36].BURST[7:0]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A280	STORM_PORT_PROTO_IGMP_CTRL [37].RESERVED[31:21]	11
0xBB00A280	STORM_PORT_PROTO_IGMP_CTRL [37].EN[20:20]	1
0xBB00A280	STORM_PORT_PROTO_IGMP_CTRL [37].RESERVED[19:17]	3
0xBB00A280	STORM_PORT_PROTO_IGMP_CTRL [37].RATE[16:8]	9
0xBB00A280	STORM_PORT_PROTO_IGMP_CTRL [37].BURST[7:0]	8
0xBB00A284	STORM_PORT_PROTO_IGMP_CTRL [38].RESERVED[31:21]	11
0xBB00A284	STORM_PORT_PROTO_IGMP_CTRL [38].EN[20:20]	1
0xBB00A284	STORM_PORT_PROTO_IGMP_CTRL [38].RESERVED[19:17]	3
0xBB00A284	STORM_PORT_PROTO_IGMP_CTRL [38].RATE[16:8]	9
0xBB00A284	STORM_PORT_PROTO_IGMP_CTRL [38].BURST[7:0]	8
0xBB00A288	STORM_PORT_PROTO_IGMP_CTRL [39].RESERVED[31:21]	11
0xBB00A288	STORM_PORT_PROTO_IGMP_CTRL [39].EN[20:20]	1
0xBB00A288	STORM_PORT_PROTO_IGMP_CTRL [39].RESERVED[19:17]	3
0xBB00A288	STORM_PORT_PROTO_IGMP_CTRL [39].RATE[16:8]	9
0xBB00A288	STORM_PORT_PROTO_IGMP_CTRL [39].BURST[7:0]	8
0xBB00A28C	STORM_PORT_PROTO_IGMP_CTRL [40].RESERVED[31:21]	11
0xBB00A28C	STORM_PORT_PROTO_IGMP_CTRL [40].EN[20:20]	1
0xBB00A28C	STORM_PORT_PROTO_IGMP_CTRL [40].RESERVED[19:17]	3
0xBB00A28C	STORM_PORT_PROTO_IGMP_CTRL [40].RATE[16:8]	9
0xBB00A28C	STORM_PORT_PROTO_IGMP_CTRL [40].BURST[7:0]	8
0xBB00A290	STORM_PORT_PROTO_IGMP_CTRL [41].RESERVED[31:21]	11
0xBB00A290	STORM_PORT_PROTO_IGMP_CTRL [41].EN[20:20]	1
0xBB00A290	STORM_PORT_PROTO_IGMP_CTRL [41].RESERVED[19:17]	3
0xBB00A290	STORM_PORT_PROTO_IGMP_CTRL [41].RATE[16:8]	9
0xBB00A290	STORM_PORT_PROTO_IGMP_CTRL [41].BURST[7:0]	8
0xBB00A294	STORM_PORT_PROTO_IGMP_CTRL [42].RESERVED[31:21]	11
0xBB00A294	STORM_PORT_PROTO_IGMP_CTRL [42].EN[20:20]	1
0xBB00A294	STORM_PORT_PROTO_IGMP_CTRL [42].RESERVED[19:17]	3
0xBB00A294	STORM_PORT_PROTO_IGMP_CTRL [42].RATE[16:8]	9
0xBB00A294	STORM_PORT_PROTO_IGMP_CTRL [42].BURST[7:0]	8
0xBB00A298	STORM_PORT_PROTO_IGMP_CTRL [43].RESERVED[31:21]	11
0xBB00A298	STORM_PORT_PROTO_IGMP_CTRL [43].EN[20:20]	1
0xBB00A298	STORM_PORT_PROTO_IGMP_CTRL [43].RESERVED[19:17]	3
0xBB00A298	STORM_PORT_PROTO_IGMP_CTRL [43].RATE[16:8]	9
0xBB00A298	STORM_PORT_PROTO_IGMP_CTRL [43].BURST[7:0]	8
0xBB00A29C	STORM_PORT_PROTO_IGMP_CTRL [44].RESERVED[31:21]	11
0xBB00A29C	STORM_PORT_PROTO_IGMP_CTRL [44].EN[20:20]	1
0xBB00A29C	STORM_PORT_PROTO_IGMP_CTRL [44].RESERVED[19:17]	3
0xBB00A29C	STORM_PORT_PROTO_IGMP_CTRL [44].RATE[16:8]	9
0xBB00A29C	STORM_PORT_PROTO_IGMP_CTRL [44].BURST[7:0]	8
0xBB00A2A0	STORM_PORT_PROTO_IGMP_CTRL [45].RESERVED[31:21]	11
0xBB00A2A0	STORM_PORT_PROTO_IGMP_CTRL [45].EN[20:20]	1
0xBB00A2A0	STORM_PORT_PROTO_IGMP_CTRL [45].RESERVED[19:17]	3
0xBB00A2A0	STORM_PORT_PROTO_IGMP_CTRL [45].RATE[16:8]	9
0xBB00A2A0	STORM_PORT_PROTO_IGMP_CTRL [45].BURST[7:0]	8
0xBB00A2A4	STORM_PORT_PROTO_IGMP_CTRL [46].RESERVED[31:21]	11

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A2A4	STORM_PORT_PROTO_IGMP_CTRL [46].EN[20:20]	1
0xBB00A2A4	STORM_PORT_PROTO_IGMP_CTRL [46].RESERVED[19:17]	3
0xBB00A2A4	STORM_PORT_PROTO_IGMP_CTRL [46].RATE[16:8]	9
0xBB00A2A4	STORM_PORT_PROTO_IGMP_CTRL [46].BURST[7:0]	8
0xBB00A2A8	STORM_PORT_PROTO_IGMP_CTRL [47].RESERVED[31:21]	11
0xBB00A2A8	STORM_PORT_PROTO_IGMP_CTRL [47].EN[20:20]	1
0xBB00A2A8	STORM_PORT_PROTO_IGMP_CTRL [47].RESERVED[19:17]	3
0xBB00A2A8	STORM_PORT_PROTO_IGMP_CTRL [47].RATE[16:8]	9
0xBB00A2A8	STORM_PORT_PROTO_IGMP_CTRL [47].BURST[7:0]	8
0xBB00A2AC	STORM_PORT_PROTO_IGMP_CTRL [48].RESERVED[31:21]	11
0xBB00A2AC	STORM_PORT_PROTO_IGMP_CTRL [48].EN[20:20]	1
0xBB00A2AC	STORM_PORT_PROTO_IGMP_CTRL [48].RESERVED[19:17]	3
0xBB00A2AC	STORM_PORT_PROTO_IGMP_CTRL [48].RATE[16:8]	9
0xBB00A2AC	STORM_PORT_PROTO_IGMP_CTRL [48].BURST[7:0]	8
0xBB00A2B0	STORM_PORT_PROTO_ICMP_CTRL [49].RESERVED[31:21]	11
0xBB00A2B0	STORM_PORT_PROTO_IGMP_CTRL [49].EN[20:20]	1
0xBB00A2B0	STORM_PORT_PROTO_IGMP_CTRL [49].RESERVED[19:17]	3
0xBB00A2B0	STORM_PORT_PROTO_IGMP_CTRL [49].RATE[16:8]	9
0xBB00A2B0	STORM_PORT_PROTO_IGMP_CTRL [49].BURST[7:0]	8
0xBB00A2B4	STORM_PORT_PROTO_IGMP_CTRL [50].RESERVED[31:21]	11
0xBB00A2B4	STORM_PORT_PROTO_IGMP_CTRL [50].EN[20:20]	1
0xBB00A2B4	STORM_PORT_PROTO_IGMP_CTRL [50].RESERVED[19:17]	3
0xBB00A2B4	STORM_PORT_PROTO_IGMP_CTRL [50].RATE[16:8]	9
0xBB00A2B4	STORM_PORT_PROTO_IGMP_CTRL [50].BURST[7:0]	8
0xBB00A2B8	STORM_PORT_PROTO_IGMP_CTRL [51].RESERVED[31:21]	11
0xBB00A2B8	STORM_PORT_PROTO_IGMP_CTRL [51].EN[20:20]	1
0xBB00A2B8	STORM_PORT_PROTO_IGMP_CTRL [51].RESERVED[19:17]	3
0xBB00A2B8	STORM_PORT_PROTO_IGMP_CTRL [51].RATE[16:8]	9
0xBB00A2B8	STORM_PORT_PROTO_IGMP_CTRL [51].BURST[7:0]	8
0xBB00A2BC	STORM_PORT_PROTO_IGMP_CTRL [52].RESERVED[31:21]	11
0xBB00A2BC	STORM_PORT_PROTO_IGMP_CTRL [52].EN[20:20]	1
0xBB00A2BC	STORM_PORT_PROTO_IGMP_CTRL [52].RESERVED[19:17]	3
0xBB00A2BC	STORM_PORT_PROTO_IGMP_CTRL [52].RATE[16:8]	9
0xBB00A2BC	STORM_PORT_PROTO_IGMP_CTRL [52].BURST[7:0]	8
0xBB00A2C0	STORM_PORT_PROTO_IGMP_CTRL [53].RESERVED[31:21]	11
0xBB00A2C0	STORM_PORT_PROTO_IGMP_CTRL [53].EN[20:20]	1
0xBB00A2C0	STORM_PORT_PROTO_IGMP_CTRL [53].RESERVED[19:17]	3
0xBB00A2C0	STORM_PORT_PROTO_IGMP_CTRL [53].RATE[16:8]	9
0xBB00A2C0	STORM_PORT_PROTO_IGMP_CTRL [53].BURST[7:0]	8
0xBB00A2C4	STORM_PORT_PROTO_IGMP_CTRL [54].RESERVED[31:21]	11
0xBB00A2C4	STORM_PORT_PROTO_IGMP_CTRL [54].EN[20:20]	1
0xBB00A2C4	STORM_PORT_PROTO_IGMP_CTRL [54].RESERVED[19:17]	3
0xBB00A2C4	STORM_PORT_PROTO_IGMP_CTRL [54].RATE[16:8]	9
0xBB00A2C4	STORM_PORT_PROTO_IGMP_CTRL [54].BURST[7:0]	8
0xBB00A2C8	STORM_PORT_PROTO_IGMP_CTRL [55].RESERVED[31:21]	11
0xBB00A2C8	STORM_PORT_PROTO_IGMP_CTRL [55].EN[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A2C8	STORM_PORT_PROTO_IGMP_CTRL [55].RESERVED[19:17]	3
0xBB00A2C8	STORM_PORT_PROTO_IGMP_CTRL [55].RATE[16:8]	9
0xBB00A2C8	STORM_PORT_PROTO_IGMP_CTRL [55].BURST[7:0]	8
0xBB00A2CC	STORM_PORT_PROTO_IGMP_CTRL [56].RESERVED[31:21]	11
0xBB00A2CC	STORM_PORT_PROTO_IGMP_CTRL [56].EN[20:20]	1
0xBB00A2CC	STORM_PORT_PROTO_IGMP_CTRL [56].RESERVED[19:17]	3
0xBB00A2CC	STORM_PORT_PROTO_IGMP_CTRL [56].RATE[16:8]	9
0xBB00A2CC	STORM_PORT_PROTO_IGMP_CTRL [56].BURST[7:0]	8
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00A2D8	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [37].FLAG[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [41].FLAG[9:9]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00A2DC	STORM_PORT_PROTO_IGMP_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [0].RST[0:0]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [1].RST[1:1]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [2].RST[2:2]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [3].RST[3:3]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [4].RST[4:4]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [5].RST[5:5]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [6].RST[6:6]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [7].RST[7:7]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [8].RST[8:8]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [9].RST[9:9]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [10].RST[10:10]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [11].RST[11:11]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [12].RST[12:12]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [13].RST[13:13]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [14].RST[14:14]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [15].RST[15:15]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [16].RST[16:16]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [17].RST[17:17]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [18].RST[18:18]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [19].RST[19:19]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [20].RST[20:20]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [21].RST[21:21]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [22].RST[22:22]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [23].RST[23:23]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [24].RST[24:24]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [25].RST[25:25]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [26].RST[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [27].RST[27:27]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [28].RST[28:28]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [29].RST[29:29]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [30].RST[30:30]	1
0xBB00A2D0	STORM_PORT_PROTO_IGMP_LB_RST [31].RST[31:31]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [32].RST[0:0]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [33].RST[1:1]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [34].RST[2:2]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [35].RST[3:3]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [36].RST[4:4]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [37].RST[5:5]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [38].RST[6:6]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [39].RST[7:7]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [40].RST[8:8]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [41].RST[9:9]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [42].RST[10:10]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [43].RST[11:11]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [44].RST[12:12]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [45].RST[13:13]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [46].RST[14:14]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [47].RST[15:15]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [48].RST[16:16]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [49].RST[17:17]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [50].RST[18:18]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [51].RST[19:19]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [52].RST[20:20]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [53].RST[21:21]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [54].RST[22:22]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [55].RST[23:23]	1
0xBB00A2D4	STORM_PORT_PROTO_IGMP_LB_RST [56].RST[24:24]	1
0xBB00B014	STORM_PORT_UC_CTRL [0].RESERVED[31:26]	6
0xBB00B014	STORM_PORT_UC_CTRL [0].TYPE[25:25]	1
0xBB00B014	STORM_PORT_UC_CTRL [0].EN[24:24]	1
0xBB00B014	STORM_PORT_UC_CTRL [0].RATE[23:0]	24
0xBB00B018	STORM_PORT_UC_CTRL [0].RESERVED[31:16]	16
0xBB00B018	STORM_PORT_UC_CTRL [0].BURST[15:0]	16
0xBB00B01C	STORM_PORT_UC_CTRL [1].RESERVED[31:26]	6
0xBB00B01C	STORM_PORT_UC_CTRL [1].TYPE[25:25]	1
0xBB00B01C	STORM_PORT_UC_CTRL [1].EN[24:24]	1
0xBB00B01C	STORM_PORT_UC_CTRL [1].RATE[23:0]	24
0xBB00B020	STORM_PORT_UC_CTRL [1].RESERVED[31:16]	16
0xBB00B020	STORM_PORT_UC_CTRL [1].BURST[15:0]	16
0xBB00B024	STORM_PORT_UC_CTRL [2].RESERVED[31:26]	6
0xBB00B024	STORM_PORT_UC_CTRL [2].TYPE[25:25]	1
0xBB00B024	STORM_PORT_UC_CTRL [2].EN[24:24]	1
0xBB00B024	STORM_PORT_UC_CTRL [2].RATE[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B028	STORM_PORT_UC_CTRL [2].RESERVED[31:16]	16
0xBB00B028	STORM_PORT_UC_CTRL [2].BURST[15:0]	16
0xBB00B02C	STORM_PORT_UC_CTRL [3].RESERVED[31:26]	6
0xBB00B02C	STORM_PORT_UC_CTRL [3].TYPE[25:25]	1
0xBB00B02C	STORM_PORT_UC_CTRL [3].EN[24:24]	1
0xBB00B02C	STORM_PORT_UC_CTRL [3].RATE[23:0]	24
0xBB00B030	STORM_PORT_UC_CTRL [3].RESERVED[31:16]	16
0xBB00B030	STORM_PORT_UC_CTRL [3].BURST[15:0]	16
0xBB00B034	STORM_PORT_UC_CTRL [4].RESERVED[31:26]	6
0xBB00B034	STORM_PORT_UC_CTRL [4].TYPE[25:25]	1
0xBB00B034	STORM_PORT_UC_CTRL [4].EN[24:24]	1
0xBB00B034	STORM_PORT_UC_CTRL [4].RATE[23:0]	24
0xBB00B038	STORM_PORT_UC_CTRL [4].RESERVED[31:16]	16
0xBB00B038	STORM_PORT_UC_CTRL [4].BURST[15:0]	16
0xBB00B03C	STORM_PORT_UC_CTRL [5].RESERVED[31:26]	6
0xBB00B03C	STORM_PORT_UC_CTRL [5].TYPE[25:25]	1
0xBB00B03C	STORM_PORT_UC_CTRL [5].EN[24:24]	1
0xBB00B03C	STORM_PORT_UC_CTRL [5].RATE[23:0]	24
0xBB00B040	STORM_PORT_UC_CTRL [5].RESERVED[31:16]	16
0xBB00B040	STORM_PORT_UC_CTRL [5].BURST[15:0]	16
0xBB00B044	STORM_PORT_UC_CTRL [6].RESERVED[31:26]	6
0xBB00B044	STORM_PORT_UC_CTRL [6].TYPE[25:25]	1
0xBB00B044	STORM_PORT_UC_CTRL [6].EN[24:24]	1
0xBB00B044	STORM_PORT_UC_CTRL [6].RATE[23:0]	24
0xBB00B048	STORM_PORT_UC_CTRL [6].RESERVED[31:16]	16
0xBB00B048	STORM_PORT_UC_CTRL [6].BURST[15:0]	16
0xBB00B04C	STORM_PORT_UC_CTRL [7].RESERVED[31:26]	6
0xBB00B04C	STORM_PORT_UC_CTRL [7].TYPE[25:25]	1
0xBB00B04C	STORM_PORT_UC_CTRL [7].EN[24:24]	1
0xBB00B04C	STORM_PORT_UC_CTRL [7].RATE[23:0]	24
0xBB00B050	STORM_PORT_UC_CTRL [7].RESERVED[31:16]	16
0xBB00B050	STORM_PORT_UC_CTRL [7].BURST[15:0]	16
0xBB00B054	STORM_PORT_UC_CTRL [8].RESERVED[31:26]	6
0xBB00B054	STORM_PORT_UC_CTRL [8].TYPE[25:25]	1
0xBB00B054	STORM_PORT_UC_CTRL [8].EN[24:24]	1
0xBB00B054	STORM_PORT_UC_CTRL [8].RATE[23:0]	24
0xBB00B058	STORM_PORT_UC_CTRL [8].RESERVED[31:16]	16
0xBB00B058	STORM_PORT_UC_CTRL [8].BURST[15:0]	16
0xBB00B05C	STORM_PORT_UC_CTRL [9].RESERVED[31:26]	6
0xBB00B05C	STORM_PORT_UC_CTRL [9].TYPE[25:25]	1
0xBB00B05C	STORM_PORT_UC_CTRL [9].EN[24:24]	1
0xBB00B05C	STORM_PORT_UC_CTRL [9].RATE[23:0]	24
0xBB00B060	STORM_PORT_UC_CTRL [9].RESERVED[31:16]	16
0xBB00B060	STORM_PORT_UC_CTRL [9].BURST[15:0]	16
0xBB00B064	STORM_PORT_UC_CTRL [10].RESERVED[31:26]	6
0xBB00B064	STORM_PORT_UC_CTRL [10].TYPE[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B064	STORM_PORT_UC_CTRL [10].EN[24:24]	1
0xBB00B064	STORM_PORT_UC_CTRL [10].RATE[23:0]	24
0xBB00B068	STORM_PORT_UC_CTRL [10].RESERVED[31:16]	16
0xBB00B068	STORM_PORT_UC_CTRL [10].BURST[15:0]	16
0xBB00B06C	STORM_PORT_UC_CTRL [11].RESERVED[31:26]	6
0xBB00B06C	STORM_PORT_UC_CTRL [11].TYPE[25:25]	1
0xBB00B06C	STORM_PORT_UC_CTRL [11].EN[24:24]	1
0xBB00B06C	STORM_PORT_UC_CTRL [11].RATE[23:0]	24
0xBB00B070	STORM_PORT_UC_CTRL [11].RESERVED[31:16]	16
0xBB00B070	STORM_PORT_UC_CTRL [11].BURST[15:0]	16
0xBB00B074	STORM_PORT_UC_CTRL [12].RESERVED[31:26]	6
0xBB00B074	STORM_PORT_UC_CTRL [12].TYPE[25:25]	1
0xBB00B074	STORM_PORT_UC_CTRL [12].EN[24:24]	1
0xBB00B074	STORM_PORT_UC_CTRL [12].RATE[23:0]	24
0xBB00B078	STORM_PORT_UC_CTRL [12].RESERVED[31:16]	16
0xBB00B078	STORM_PORT_UC_CTRL [12].BURST[15:0]	16
0xBB00B07C	STORM_PORT_UC_CTRL [13].RESERVED[31:26]	6
0xBB00B07C	STORM_PORT_UC_CTRL [13].TYPE[25:25]	1
0xBB00B07C	STORM_PORT_UC_CTRL [13].EN[24:24]	1
0xBB00B07C	STORM_PORT_UC_CTRL [13].RATE[23:0]	24
0xBB00B080	STORM_PORT_UC_CTRL [13].RESERVED[31:16]	16
0xBB00B080	STORM_PORT_UC_CTRL [13].BURST[15:0]	16
0xBB00B084	STORM_PORT_UC_CTRL [14].RESERVED[31:26]	6
0xBB00B084	STORM_PORT_UC_CTRL [14].TYPE[25:25]	1
0xBB00B084	STORM_PORT_UC_CTRL [14].EN[24:24]	1
0xBB00B084	STORM_PORT_UC_CTRL [14].RATE[23:0]	24
0xBB00B088	STORM_PORT_UC_CTRL [14].RESERVED[31:16]	16
0xBB00B088	STORM_PORT_UC_CTRL [14].BURST[15:0]	16
0xBB00B08C	STORM_PORT_UC_CTRL [15].RESERVED[31:26]	6
0xBB00B08C	STORM_PORT_UC_CTRL [15].TYPE[25:25]	1
0xBB00B08C	STORM_PORT_UC_CTRL [15].EN[24:24]	1
0xBB00B08C	STORM_PORT_UC_CTRL [15].RATE[23:0]	24
0xBB00B090	STORM_PORT_UC_CTRL [15].RESERVED[31:16]	16
0xBB00B090	STORM_PORT_UC_CTRL [15].BURST[15:0]	16
0xBB00B094	STORM_PORT_UC_CTRL [16].RESERVED[31:26]	6
0xBB00B094	STORM_PORT_UC_CTRL [16].TYPE[25:25]	1
0xBB00B094	STORM_PORT_UC_CTRL [16].EN[24:24]	1
0xBB00B094	STORM_PORT_UC_CTRL [16].RATE[23:0]	24
0xBB00B098	STORM_PORT_UC_CTRL [16].RESERVED[31:16]	16
0xBB00B098	STORM_PORT_UC_CTRL [16].BURST[15:0]	16
0xBB00B09C	STORM_PORT_UC_CTRL [17].RESERVED[31:26]	6
0xBB00B09C	STORM_PORT_UC_CTRL [17].TYPE[25:25]	1
0xBB00B09C	STORM_PORT_UC_CTRL [17].EN[24:24]	1
0xBB00B09C	STORM_PORT_UC_CTRL [17].RATE[23:0]	24
0xBB00B0A0	STORM_PORT_UC_CTRL [17].RESERVED[31:16]	16
0xBB00B0A0	STORM_PORT_UC_CTRL [17].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B0A4	STORM_PORT_UC_CTRL [18].RESERVED[31:26]	6
0xBB00B0A4	STORM_PORT_UC_CTRL [18].TYPE[25:25]	1
0xBB00B0A4	STORM_PORT_UC_CTRL [18].EN[24:24]	1
0xBB00B0A4	STORM_PORT_UC_CTRL [18].RATE[23:0]	24
0xBB00B0A8	STORM_PORT_UC_CTRL [18].RESERVED[31:16]	16
0xBB00B0A8	STORM_PORT_UC_CTRL [18].BURST[15:0]	16
0xBB00B0AC	STORM_PORT_UC_CTRL [19].RESERVED[31:26]	6
0xBB00B0AC	STORM_PORT_UC_CTRL [19].TYPE[25:25]	1
0xBB00B0AC	STORM_PORT_UC_CTRL [19].EN[24:24]	1
0xBB00B0AC	STORM_PORT_UC_CTRL [19].RATE[23:0]	24
0xBB00B0B0	STORM_PORT_UC_CTRL [19].RESERVED[31:16]	16
0xBB00B0B0	STORM_PORT_UC_CTRL [19].BURST[15:0]	16
0xBB00B0B4	STORM_PORT_UC_CTRL [20].RESERVED[31:26]	6
0xBB00B0B4	STORM_PORT_UC_CTRL [20].TYPE[25:25]	1
0xBB00B0B4	STORM_PORT_UC_CTRL [20].EN[24:24]	1
0xBB00B0B4	STORM_PORT_UC_CTRL [20].RATE[23:0]	24
0xBB00B0B8	STORM_PORT_UC_CTRL [20].RESERVED[31:16]	16
0xBB00B0B8	STORM_PORT_UC_CTRL [20].BURST[15:0]	16
0xBB00B0BC	STORM_PORT_UC_CTRL [21].RESERVED[31:26]	6
0xBB00B0BC	STORM_PORT_UC_CTRL [21].TYPE[25:25]	1
0xBB00B0BC	STORM_PORT_UC_CTRL [21].EN[24:24]	1
0xBB00B0BC	STORM_PORT_UC_CTRL [21].RATE[23:0]	24
0xBB00B0C0	STORM_PORT_UC_CTRL [21].RESERVED[31:16]	16
0xBB00B0C0	STORM_PORT_UC_CTRL [21].BURST[15:0]	16
0xBB00B0C4	STORM_PORT_UC_CTRL [22].RESERVED[31:26]	6
0xBB00B0C4	STORM_PORT_UC_CTRL [22].TYPE[25:25]	1
0xBB00B0C4	STORM_PORT_UC_CTRL [22].EN[24:24]	1
0xBB00B0C4	STORM_PORT_UC_CTRL [22].RATE[23:0]	24
0xBB00B0C8	STORM_PORT_UC_CTRL [22].RESERVED[31:16]	16
0xBB00B0C8	STORM_PORT_UC_CTRL [22].BURST[15:0]	16
0xBB00B0CC	STORM_PORT_UC_CTRL [23].RESERVED[31:26]	6
0xBB00B0CC	STORM_PORT_UC_CTRL [23].TYPE[25:25]	1
0xBB00B0CC	STORM_PORT_UC_CTRL [23].EN[24:24]	1
0xBB00B0CC	STORM_PORT_UC_CTRL [23].RATE[23:0]	24
0xBB00B0D0	STORM_PORT_UC_CTRL [23].RESERVED[31:16]	16
0xBB00B0D0	STORM_PORT_UC_CTRL [23].BURST[15:0]	16
0xBB00B0D4	STORM_PORT_UC_CTRL [24].RESERVED[31:26]	6
0xBB00B0D4	STORM_PORT_UC_CTRL [24].TYPE[25:25]	1
0xBB00B0D4	STORM_PORT_UC_CTRL [24].EN[24:24]	1
0xBB00B0D4	STORM_PORT_UC_CTRL [24].RATE[23:0]	24
0xBB00B0D8	STORM_PORT_UC_CTRL [24].RESERVED[31:16]	16
0xBB00B0D8	STORM_PORT_UC_CTRL [24].BURST[15:0]	16
0xBB00B0DC	STORM_PORT_UC_CTRL [25].RESERVED[31:26]	6
0xBB00B0DC	STORM_PORT_UC_CTRL [25].TYPE[25:25]	1
0xBB00B0DC	STORM_PORT_UC_CTRL [25].EN[24:24]	1
0xBB00B0DC	STORM_PORT_UC_CTRL [25].RATE[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B0E0	STORM_PORT_UC_CTRL [25].RESERVED[31:16]	16
0xBB00B0E0	STORM_PORT_UC_CTRL [25].BURST[15:0]	16
0xBB00B0E4	STORM_PORT_UC_CTRL [26].RESERVED[31:26]	6
0xBB00B0E4	STORM_PORT_UC_CTRL [26].TYPE[25:25]	1
0xBB00B0E4	STORM_PORT_UC_CTRL [26].EN[24:24]	1
0xBB00B0E4	STORM_PORT_UC_CTRL [26].RATE[23:0]	24
0xBB00B0E8	STORM_PORT_UC_CTRL [26].RESERVED[31:16]	16
0xBB00B0E8	STORM_PORT_UC_CTRL [26].BURST[15:0]	16
0xBB00B0EC	STORM_PORT_UC_CTRL [27].RESERVED[31:26]	6
0xBB00B0EC	STORM_PORT_UC_CTRL [27].TYPE[25:25]	1
0xBB00B0EC	STORM_PORT_UC_CTRL [27].EN[24:24]	1
0xBB00B0EC	STORM_PORT_UC_CTRL [27].RATE[23:0]	24
0xBB00B0F0	STORM_PORT_UC_CTRL [27].RESERVED[31:16]	16
0xBB00B0F0	STORM_PORT_UC_CTRL [27].BURST[15:0]	16
0xBB00B0F4	STORM_PORT_UC_CTRL [28].RESERVED[31:26]	6
0xBB00B0F4	STORM_PORT_UC_CTRL [28].TYPE[25:25]	1
0xBB00B0F4	STORM_PORT_UC_CTRL [28].EN[24:24]	1
0xBB00B0F4	STORM_PORT_UC_CTRL [28].RATE[23:0]	24
0xBB00B0F8	STORM_PORT_UC_CTRL [28].RESERVED[31:16]	16
0xBB00B0F8	STORM_PORT_UC_CTRL [28].BURST[15:0]	16
0xBB00B0FC	STORM_PORT_UC_CTRL [29].RESERVED[31:26]	6
0xBB00B0FC	STORM_PORT_UC_CTRL [29].TYPE[25:25]	1
0xBB00B0FC	STORM_PORT_UC_CTRL [29].EN[24:24]	1
0xBB00B0FC	STORM_PORT_UC_CTRL [29].RATE[23:0]	24
0xBB00B100	STORM_PORT_UC_CTRL [29].RESERVED[31:16]	16
0xBB00B100	STORM_PORT_UC_CTRL [29].BURST[15:0]	16
0xBB00B104	STORM_PORT_UC_CTRL [30].RESERVED[31:26]	6
0xBB00B104	STORM_PORT_UC_CTRL [30].TYPE[25:25]	1
0xBB00B104	STORM_PORT_UC_CTRL [30].EN[24:24]	1
0xBB00B104	STORM_PORT_UC_CTRL [30].RATE[23:0]	24
0xBB00B108	STORM_PORT_UC_CTRL [30].RESERVED[31:16]	16
0xBB00B108	STORM_PORT_UC_CTRL [30].BURST[15:0]	16
0xBB00B10C	STORM_PORT_UC_CTRL [31].RESERVED[31:26]	6
0xBB00B10C	STORM_PORT_UC_CTRL [31].TYPE[25:25]	1
0xBB00B10C	STORM_PORT_UC_CTRL [31].EN[24:24]	1
0xBB00B10C	STORM_PORT_UC_CTRL [31].RATE[23:0]	24
0xBB00B110	STORM_PORT_UC_CTRL [31].RESERVED[31:16]	16
0xBB00B110	STORM_PORT_UC_CTRL [31].BURST[15:0]	16
0xBB00B114	STORM_PORT_UC_CTRL [32].RESERVED[31:26]	6
0xBB00B114	STORM_PORT_UC_CTRL [32].TYPE[25:25]	1
0xBB00B114	STORM_PORT_UC_CTRL [32].EN[24:24]	1
0xBB00B114	STORM_PORT_UC_CTRL [32].RATE[23:0]	24
0xBB00B118	STORM_PORT_UC_CTRL [32].RESERVED[31:16]	16
0xBB00B118	STORM_PORT_UC_CTRL [32].BURST[15:0]	16
0xBB00B11C	STORM_PORT_UC_CTRL [33].RESERVED[31:26]	6
0xBB00B11C	STORM_PORT_UC_CTRL [33].TYPE[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B11C	STORM_PORT_UC_CTRL [33].EN[24:24]	1
0xBB00B11C	STORM_PORT_UC_CTRL [33].RATE[23:0]	24
0xBB00B120	STORM_PORT_UC_CTRL [33].RESERVED[31:16]	16
0xBB00B120	STORM_PORT_UC_CTRL [33].BURST[15:0]	16
0xBB00B124	STORM_PORT_UC_CTRL [34].RESERVED[31:26]	6
0xBB00B124	STORM_PORT_UC_CTRL [34].TYPE[25:25]	1
0xBB00B124	STORM_PORT_UC_CTRL [34].EN[24:24]	1
0xBB00B124	STORM_PORT_UC_CTRL [34].RATE[23:0]	24
0xBB00B128	STORM_PORT_UC_CTRL [34].RESERVED[31:16]	16
0xBB00B128	STORM_PORT_UC_CTRL [34].BURST[15:0]	16
0xBB00B12C	STORM_PORT_UC_CTRL [35].RESERVED[31:26]	6
0xBB00B12C	STORM_PORT_UC_CTRL [35].TYPE[25:25]	1
0xBB00B12C	STORM_PORT_UC_CTRL [35].EN[24:24]	1
0xBB00B12C	STORM_PORT_UC_CTRL [35].RATE[23:0]	24
0xBB00B130	STORM_PORT_UC_CTRL [35].RESERVED[31:16]	16
0xBB00B130	STORM_PORT_UC_CTRL [35].BURST[15:0]	16
0xBB00B134	STORM_PORT_UC_CTRL [36].RESERVED[31:26]	6
0xBB00B134	STORM_PORT_UC_CTRL [36].TYPE[25:25]	1
0xBB00B134	STORM_PORT_UC_CTRL [36].EN[24:24]	1
0xBB00B134	STORM_PORT_UC_CTRL [36].RATE[23:0]	24
0xBB00B138	STORM_PORT_UC_CTRL [36].RESERVED[31:16]	16
0xBB00B138	STORM_PORT_UC_CTRL [36].BURST[15:0]	16
0xBB00B13C	STORM_PORT_UC_CTRL [37].RESERVED[31:26]	6
0xBB00B13C	STORM_PORT_UC_CTRL [37].TYPE[25:25]	1
0xBB00B13C	STORM_PORT_UC_CTRL [37].EN[24:24]	1
0xBB00B13C	STORM_PORT_UC_CTRL [37].RATE[23:0]	24
0xBB00B140	STORM_PORT_UC_CTRL [37].RESERVED[31:16]	16
0xBB00B140	STORM_PORT_UC_CTRL [37].BURST[15:0]	16
0xBB00B144	STORM_PORT_UC_CTRL [38].RESERVED[31:26]	6
0xBB00B144	STORM_PORT_UC_CTRL [38].TYPE[25:25]	1
0xBB00B144	STORM_PORT_UC_CTRL [38].EN[24:24]	1
0xBB00B144	STORM_PORT_UC_CTRL [38].RATE[23:0]	24
0xBB00B148	STORM_PORT_UC_CTRL [38].RESERVED[31:16]	16
0xBB00B148	STORM_PORT_UC_CTRL [38].BURST[15:0]	16
0xBB00B14C	STORM_PORT_UC_CTRL [39].RESERVED[31:26]	6
0xBB00B14C	STORM_PORT_UC_CTRL [39].TYPE[25:25]	1
0xBB00B14C	STORM_PORT_UC_CTRL [39].EN[24:24]	1
0xBB00B14C	STORM_PORT_UC_CTRL [39].RATE[23:0]	24
0xBB00B150	STORM_PORT_UC_CTRL [39].RESERVED[31:16]	16
0xBB00B150	STORM_PORT_UC_CTRL [39].BURST[15:0]	16
0xBB00B154	STORM_PORT_UC_CTRL [40].RESERVED[31:26]	6
0xBB00B154	STORM_PORT_UC_CTRL [40].TYPE[25:25]	1
0xBB00B154	STORM_PORT_UC_CTRL [40].EN[24:24]	1
0xBB00B154	STORM_PORT_UC_CTRL [40].RATE[23:0]	24
0xBB00B158	STORM_PORT_UC_CTRL [40].RESERVED[31:16]	16
0xBB00B158	STORM_PORT_UC_CTRL [40].BURST[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B15C	STORM_PORT_UC_CTRL [41].RESERVED[31:26]	6
0xBB00B15C	STORM_PORT_UC_CTRL [41].TYPE[25:25]	1
0xBB00B15C	STORM_PORT_UC_CTRL [41].EN[24:24]	1
0xBB00B15C	STORM_PORT_UC_CTRL [41].RATE[23:0]	24
0xBB00B160	STORM_PORT_UC_CTRL [41].RESERVED[31:16]	16
0xBB00B160	STORM_PORT_UC_CTRL [41].BURST[15:0]	16
0xBB00B164	STORM_PORT_UC_CTRL [42].RESERVED[31:26]	6
0xBB00B164	STORM_PORT_UC_CTRL [42].TYPE[25:25]	1
0xBB00B164	STORM_PORT_UC_CTRL [42].EN[24:24]	1
0xBB00B164	STORM_PORT_UC_CTRL [42].RATE[23:0]	24
0xBB00B168	STORM_PORT_UC_CTRL [42].RESERVED[31:16]	16
0xBB00B168	STORM_PORT_UC_CTRL [42].BURST[15:0]	16
0xBB00B16C	STORM_PORT_UC_CTRL [43].RESERVED[31:26]	6
0xBB00B16C	STORM_PORT_UC_CTRL [43].TYPE[25:25]	1
0xBB00B16C	STORM_PORT_UC_CTRL [43].EN[24:24]	1
0xBB00B16C	STORM_PORT_UC_CTRL [43].RATE[23:0]	24
0xBB00B170	STORM_PORT_UC_CTRL [43].RESERVED[31:16]	16
0xBB00B170	STORM_PORT_UC_CTRL [43].BURST[15:0]	16
0xBB00B174	STORM_PORT_UC_CTRL [44].RESERVED[31:26]	6
0xBB00B174	STORM_PORT_UC_CTRL [44].TYPE[25:25]	1
0xBB00B174	STORM_PORT_UC_CTRL [44].EN[24:24]	1
0xBB00B174	STORM_PORT_UC_CTRL [44].RATE[23:0]	24
0xBB00B178	STORM_PORT_UC_CTRL [44].RESERVED[31:16]	16
0xBB00B178	STORM_PORT_UC_CTRL [44].BURST[15:0]	16
0xBB00B17C	STORM_PORT_UC_CTRL [45].RESERVED[31:26]	6
0xBB00B17C	STORM_PORT_UC_CTRL [45].TYPE[25:25]	1
0xBB00B17C	STORM_PORT_UC_CTRL [45].EN[24:24]	1
0xBB00B17C	STORM_PORT_UC_CTRL [45].RATE[23:0]	24
0xBB00B180	STORM_PORT_UC_CTRL [45].RESERVED[31:16]	16
0xBB00B180	STORM_PORT_UC_CTRL [45].BURST[15:0]	16
0xBB00B184	STORM_PORT_UC_CTRL [46].RESERVED[31:26]	6
0xBB00B184	STORM_PORT_UC_CTRL [46].TYPE[25:25]	1
0xBB00B184	STORM_PORT_UC_CTRL [46].EN[24:24]	1
0xBB00B184	STORM_PORT_UC_CTRL [46].RATE[23:0]	24
0xBB00B188	STORM_PORT_UC_CTRL [46].RESERVED[31:16]	16
0xBB00B188	STORM_PORT_UC_CTRL [46].BURST[15:0]	16
0xBB00B18C	STORM_PORT_UC_CTRL [47].RESERVED[31:26]	6
0xBB00B18C	STORM_PORT_UC_CTRL [47].TYPE[25:25]	1
0xBB00B18C	STORM_PORT_UC_CTRL [47].EN[24:24]	1
0xBB00B18C	STORM_PORT_UC_CTRL [47].RATE[23:0]	24
0xBB00B190	STORM_PORT_UC_CTRL [47].RESERVED[31:16]	16
0xBB00B190	STORM_PORT_UC_CTRL [47].BURST[15:0]	16
0xBB00B194	STORM_PORT_UC_CTRL [48].RESERVED[31:26]	6
0xBB00B194	STORM_PORT_UC_CTRL [48].TYPE[25:25]	1
0xBB00B194	STORM_PORT_UC_CTRL [48].EN[24:24]	1
0xBB00B194	STORM_PORT_UC_CTRL [48].RATE[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B198	STORM_PORT_UC_CTRL [48].RESERVED[31:16]	16
0xBB00B198	STORM_PORT_UC_CTRL [48].BURST[15:0]	16
0xBB00B19C	STORM_PORT_UC_CTRL [49].RESERVED[31:26]	6
0xBB00B19C	STORM_PORT_UC_CTRL [49].TYPE[25:25]	1
0xBB00B19C	STORM_PORT_UC_CTRL [49].EN[24:24]	1
0xBB00B19C	STORM_PORT_UC_CTRL [49].RATE[23:0]	24
0xBB00B1A0	STORM_PORT_UC_CTRL [49].RESERVED[31:16]	16
0xBB00B1A0	STORM_PORT_UC_CTRL [49].BURST[15:0]	16
0xBB00B1A4	STORM_PORT_UC_CTRL [50].RESERVED[31:26]	6
0xBB00B1A4	STORM_PORT_UC_CTRL [50].TYPE[25:25]	1
0xBB00B1A4	STORM_PORT_UC_CTRL [50].EN[24:24]	1
0xBB00B1A4	STORM_PORT_UC_CTRL [50].RATE[23:0]	24
0xBB00B1A8	STORM_PORT_UC_CTRL [50].RESERVED[31:16]	16
0xBB00B1A8	STORM_PORT_UC_CTRL [50].BURST[15:0]	16
0xBB00B1AC	STORM_PORT_UC_CTRL [51].RESERVED[31:26]	6
0xBB00B1AC	STORM_PORT_UC_CTRL [51].TYPE[25:25]	1
0xBB00B1AC	STORM_PORT_UC_CTRL [51].EN[24:24]	1
0xBB00B1AC	STORM_PORT_UC_CTRL [51].RATE[23:0]	24
0xBB00B1B0	STORM_PORT_UC_CTRL [51].RESERVED[31:16]	16
0xBB00B1B0	STORM_PORT_UC_CTRL [51].BURST[15:0]	16
0xBB00B1B4	STORM_PORT_UC_CTRL [52].RESERVED[31:26]	6
0xBB00B1B4	STORM_PORT_UC_CTRL [52].TYPE[25:25]	1
0xBB00B1B4	STORM_PORT_UC_CTRL [52].EN[24:24]	1
0xBB00B1B4	STORM_PORT_UC_CTRL [52].RATE[23:0]	24
0xBB00B1B8	STORM_PORT_UC_CTRL [52].RESERVED[31:16]	16
0xBB00B1B8	STORM_PORT_UC_CTRL [52].BURST[15:0]	16
0xBB00B1BC	STORM_PORT_UC_CTRL [53].RESERVED[31:26]	6
0xBB00B1BC	STORM_PORT_UC_CTRL [53].TYPE[25:25]	1
0xBB00B1BC	STORM_PORT_UC_CTRL [53].EN[24:24]	1
0xBB00B1BC	STORM_PORT_UC_CTRL [53].RATE[23:0]	24
0xBB00B1C0	STORM_PORT_UC_CTRL [53].RESERVED[31:16]	16
0xBB00B1C0	STORM_PORT_UC_CTRL [53].BURST[15:0]	16
0xBB00B1C4	STORM_PORT_UC_CTRL [54].RESERVED[31:26]	6
0xBB00B1C4	STORM_PORT_UC_CTRL [54].TYPE[25:25]	1
0xBB00B1C4	STORM_PORT_UC_CTRL [54].EN[24:24]	1
0xBB00B1C4	STORM_PORT_UC_CTRL [54].RATE[23:0]	24
0xBB00B1C8	STORM_PORT_UC_CTRL [54].RESERVED[31:16]	16
0xBB00B1C8	STORM_PORT_UC_CTRL [54].BURST[15:0]	16
0xBB00B1CC	STORM_PORT_UC_CTRL [55].RESERVED[31:26]	6
0xBB00B1CC	STORM_PORT_UC_CTRL [55].TYPE[25:25]	1
0xBB00B1CC	STORM_PORT_UC_CTRL [55].EN[24:24]	1
0xBB00B1CC	STORM_PORT_UC_CTRL [55].RATE[23:0]	24
0xBB00B1D0	STORM_PORT_UC_CTRL [55].RESERVED[31:16]	16
0xBB00B1D0	STORM_PORT_UC_CTRL [55].BURST[15:0]	16
0xBB00B1D4	STORM_PORT_UC_CTRL [56].RESERVED[31:26]	6
0xBB00B1D4	STORM_PORT_UC_CTRL [56].TYPE[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B1D4	STORM_PORT_UC_CTRL [56].EN[24:24]	1
0xBB00B1D4	STORM_PORT_UC_CTRL [56].RATE[23:0]	24
0xBB00B1D8	STORM_PORT_UC_CTRL [56].RESERVED[31:16]	16
0xBB00B1D8	STORM_PORT_UC_CTRL [56].BURST[15:0]	16
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [0].FLAG[0:0]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [1].FLAG[1:1]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [2].FLAG[2:2]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [3].FLAG[3:3]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [4].FLAG[4:4]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [5].FLAG[5:5]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [6].FLAG[6:6]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [7].FLAG[7:7]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [8].FLAG[8:8]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [9].FLAG[9:9]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [10].FLAG[10:10]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [11].FLAG[11:11]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [12].FLAG[12:12]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [13].FLAG[13:13]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [14].FLAG[14:14]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [15].FLAG[15:15]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [16].FLAG[16:16]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [17].FLAG[17:17]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [18].FLAG[18:18]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [19].FLAG[19:19]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [20].FLAG[20:20]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [21].FLAG[21:21]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [22].FLAG[22:22]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [23].FLAG[23:23]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [24].FLAG[24:24]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [25].FLAG[25:25]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [26].FLAG[26:26]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [27].FLAG[27:27]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [28].FLAG[28:28]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [29].FLAG[29:29]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [30].FLAG[30:30]	1
0xBB00B1E4	STORM_PORT_UC_EXCEED_FLAG [31].FLAG[31:31]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [32].FLAG[0:0]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [33].FLAG[1:1]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [34].FLAG[2:2]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [35].FLAG[3:3]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [36].FLAG[4:4]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [37].FLAG[5:5]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [38].FLAG[6:6]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [39].FLAG[7:7]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [40].FLAG[8:8]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [41].FLAG[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [42].FLAG[10:10]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [43].FLAG[11:11]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [44].FLAG[12:12]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [45].FLAG[13:13]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [46].FLAG[14:14]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [47].FLAG[15:15]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [48].FLAG[16:16]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [49].FLAG[17:17]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [50].FLAG[18:18]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [51].FLAG[19:19]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [52].FLAG[20:20]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [53].FLAG[21:21]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [54].FLAG[22:22]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [55].FLAG[23:23]	1
0xBB00B1E8	STORM_PORT_UC_EXCEED_FLAG [56].FLAG[24:24]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [0].RST[0:0]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [1].RST[1:1]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [2].RST[2:2]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [3].RST[3:3]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [4].RST[4:4]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [5].RST[5:5]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [6].RST[6:6]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [7].RST[7:7]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [8].RST[8:8]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [9].RST[9:9]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [10].RST[10:10]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [11].RST[11:11]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [12].RST[12:12]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [13].RST[13:13]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [14].RST[14:14]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [15].RST[15:15]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [16].RST[16:16]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [17].RST[17:17]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [18].RST[18:18]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [19].RST[19:19]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [20].RST[20:20]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [21].RST[21:21]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [22].RST[22:22]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [23].RST[23:23]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [24].RST[24:24]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [25].RST[25:25]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [26].RST[26:26]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [27].RST[27:27]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [28].RST[28:28]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [29].RST[29:29]	1
0xBB00B1DC	STORM_PORT_UC_LB_RST [30].RST[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B1DC	STORM_PORT_UC_LB_RST [31].RST[31:31]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [32].RST[0:0]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [33].RST[1:1]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [34].RST[2:2]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [35].RST[3:3]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [36].RST[4:4]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [37].RST[5:5]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [38].RST[6:6]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [39].RST[7:7]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [40].RST[8:8]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [41].RST[9:9]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [42].RST[10:10]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [43].RST[11:11]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [44].RST[12:12]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [45].RST[13:13]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [46].RST[14:14]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [47].RST[15:15]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [48].RST[16:16]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [49].RST[17:17]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [50].RST[18:18]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [51].RST[19:19]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [52].RST[20:20]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [53].RST[21:21]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [54].RST[22:22]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [55].RST[23:23]	1
0xBB00B1E0	STORM_PORT_UC_LB_RST [56].RST[24:24]	1
0xBB008000	ST_CTRL_RESERVED[31:1]	31
0xBB008000	ST_CTRL_MSTI_MODE[0:0]	1
0xBB008608	SWRED_DROP_CNTR [0].DROP_CNTR_GRP[15:0]	16
0xBB008608	SWRED_DROP_CNTR [1].DROP_CNTR_GRP[31:16]	16
0xBB00860C	SWRED_DROP_CNTR [2].DROP_CNTR_GRP[15:0]	16
0xBB00860C	SWRED_DROP_CNTR [3].DROP_CNTR_GRP[31:16]	16
0xBB008604	SWRED_DROP_CNTR_CIDX [0].COLOR_IDX_GRP[1:0]	2
0xBB008604	SWRED_DROP_CNTR_CIDX [1].COLOR_IDX_GRP[3:2]	2
0xBB008604	SWRED_DROP_CNTR_CIDX [2].COLOR_IDX_GRP[5:4]	2
0xBB008604	SWRED_DROP_CNTR_CIDX [3].COLOR_IDX_GRP[7:6]	2
0xBB008600	SWRED_DROP_CNTR_PIDX [0].PORT_IDX_GRP[5:0]	6
0xBB008600	SWRED_DROP_CNTR_PIDX [1].PORT_IDX_GRP[11:6]	6
0xBB008600	SWRED_DROP_CNTR_PIDX [2].PORT_IDX_GRP[17:12]	6
0xBB008600	SWRED_DROP_CNTR_PIDX [3].PORT_IDX_GRP[23:18]	6
0xBB008610	SWRED_DROP_CNTR_RST.RESERVED[31:1]	31
0xBB008610	SWRED_DROP_CNTR_RST.RST[0:0]	1
0xBB0027C4	SWRED_Q_DROP_RATE [0][0].RATE[7:0]	8
0xBB0027C4	SWRED_Q_DROP_RATE [0][1].RATE[15:8]	8
0xBB0027C4	SWRED_Q_DROP_RATE [0][2].RATE[23:16]	8
0xBB0027C8	SWRED_Q_DROP_RATE [1][0].RATE[7:0]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0027C8	SWRED_Q_DROP_RATE [1][1].RATE[15:8]	8
0xBB0027C8	SWRED_Q_DROP_RATE [1][2].RATE[23:16]	8
0xBB0027CC	SWRED_Q_DROP_RATE [2][0].RATE[7:0]	8
0xBB0027CC	SWRED_Q_DROP_RATE [2][1].RATE[15:8]	8
0xBB0027CC	SWRED_Q_DROP_RATE [2][2].RATE[23:16]	8
0xBB0027D0	SWRED_Q_DROP_RATE [3][0].RATE[7:0]	8
0xBB0027D0	SWRED_Q_DROP_RATE [3][1].RATE[15:8]	8
0xBB0027D0	SWRED_Q_DROP_RATE [3][2].RATE[23:16]	8
0xBB0027D4	SWRED_Q_DROP_RATE [4][0].RATE[7:0]	8
0xBB0027D4	SWRED_Q_DROP_RATE [4][1].RATE[15:8]	8
0xBB0027D4	SWRED_Q_DROP_RATE [4][2].RATE[23:16]	8
0xBB0027D8	SWRED_Q_DROP_RATE [5][0].RATE[7:0]	8
0xBB0027D8	SWRED_Q_DROP_RATE [5][1].RATE[15:8]	8
0xBB0027D8	SWRED_Q_DROP_RATE [5][2].RATE[23:16]	8
0xBB0027DC	SWRED_Q_DROP_RATE [6][0].RATE[7:0]	8
0xBB0027DC	SWRED_Q_DROP_RATE [6][1].RATE[15:8]	8
0xBB0027DC	SWRED_Q_DROP_RATE [6][2].RATE[23:16]	8
0xBB0027E0	SWRED_Q_DROP_RATE [7][0].RATE[7:0]	8
0xBB0027E0	SWRED_Q_DROP_RATE [7][1].RATE[15:8]	8
0xBB0027E0	SWRED_Q_DROP_RATE [7][2].RATE[23:16]	8
0xBB0027E4	SWRED_Q_DROP_RATE [8][0].RATE[7:0]	8
0xBB0027E4	SWRED_Q_DROP_RATE [8][1].RATE[15:8]	8
0xBB0027E4	SWRED_Q_DROP_RATE [8][2].RATE[23:16]	8
0xBB0027E8	SWRED_Q_DROP_RATE [9][0].RATE[7:0]	8
0xBB0027E8	SWRED_Q_DROP_RATE [9][1].RATE[15:8]	8
0xBB0027E8	SWRED_Q_DROP_RATE [9][2].RATE[23:16]	8
0xBB0027EC	SWRED_Q_DROP_RATE [10][0].RATE[7:0]	8
0xBB0027EC	SWRED_Q_DROP_RATE [10][1].RATE[15:8]	8
0xBB0027EC	SWRED_Q_DROP_RATE [10][2].RATE[23:16]	8
0xBB0027F0	SWRED_Q_DROP_RATE [11][0].RATE[7:0]	8
0xBB0027F0	SWRED_Q_DROP_RATE [11][1].RATE[15:8]	8
0xBB0027F0	SWRED_Q_DROP_RATE [11][2].RATE[23:16]	8
0xBB0027F4	SWRED_Q_THR [0][0].RESERVED[31:29]	3
0xBB0027F4	SWRED_Q_THR [0][0].MAX[28:16]	13
0xBB0027F4	SWRED_Q_THR [0][0].RESERVED[15:13]	3
0xBB0027F4	SWRED_Q_THR [0][0].MIN[12:0]	13
0xBB0027F8	SWRED_Q_THR [0][1].RESERVED[31:29]	3
0xBB0027F8	SWRED_Q_THR [0][1].MAX[28:16]	13
0xBB0027F8	SWRED_Q_THR [0][1].RESERVED[15:13]	3
0xBB0027F8	SWRED_Q_THR [0][1].MIN[12:0]	13
0xBB0027FC	SWRED_Q_THR [0][2].RESERVED[31:29]	3
0xBB0027FC	SWRED_Q_THR [0][2].MAX[28:16]	13
0xBB0027FC	SWRED_Q_THR [0][2].RESERVED[15:13]	3
0xBB0027FC	SWRED_Q_THR [0][2].MIN[12:0]	13
0xBB002800	SWRED_Q_THR [1][0].RESERVED[31:29]	3
0xBB002800	SWRED_Q_THR [1][0].MAX[28:16]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002800	SWRED_Q_THR [1][0].RESERVED[15:13]	3
0xBB002800	SWRED_Q_THR [1][0].MIN[12:0]	13
0xBB002804	SWRED_Q_THR [1][1].RESERVED[31:29]	3
0xBB002804	SWRED_Q_THR [1][1].MAX[28:16]	13
0xBB002804	SWRED_Q_THR [1][1].RESERVED[15:13]	3
0xBB002804	SWRED_Q_THR [1][1].MIN[12:0]	13
0xBB002808	SWRED_Q_THR [1][2].RESERVED[31:29]	3
0xBB002808	SWRED_Q_THR [1][2].MAX[28:16]	13
0xBB002808	SWRED_Q_THR [1][2].RESERVED[15:13]	3
0xBB002808	SWRED_Q_THR [1][2].MIN[12:0]	13
0xBB00280C	SWRED_Q_THR [2][0].RESERVED[31:29]	3
0xBB00280C	SWRED_Q_THR [2][0].MAX[28:16]	13
0xBB00280C	SWRED_Q_THR [2][0].RESERVED[15:13]	3
0xBB00280C	SWRED_Q_THR [2][0].MIN[12:0]	13
0xBB002810	SWRED_Q_THR [2][1].RESERVED[31:29]	3
0xBB002810	SWRED_Q_THR [2][1].MAX[28:16]	13
0xBB002810	SWRED_Q_THR [2][1].RESERVED[15:13]	3
0xBB002810	SWRED_Q_THR [2][1].MIN[12:0]	13
0xBB002814	SWRED_Q_THR [2][2].RESERVED[31:29]	3
0xBB002814	SWRED_Q_THR [2][2].MAX[28:16]	13
0xBB002814	SWRED_Q_THR [2][2].RESERVED[15:13]	3
0xBB002814	SWRED_Q_THR [2][2].MIN[12:0]	13
0xBB002818	SWRED_Q_THR [3][0].RESERVED[31:29]	3
0xBB002818	SWRED_Q_THR [3][0].MAX[28:16]	13
0xBB002818	SWRED_Q_THR [3][0].RESERVED[15:13]	3
0xBB002818	SWRED_Q_THR [3][0].MIN[12:0]	13
0xBB00281C	SWRED_Q_THR [3][1].RESERVED[31:29]	3
0xBB00281C	SWRED_Q_THR [3][1].MAX[28:16]	13
0xBB00281C	SWRED_Q_THR [3][1].RESERVED[15:13]	3
0xBB00281C	SWRED_Q_THR [3][1].MIN[12:0]	13
0xBB002820	SWRED_Q_THR [3][2].RESERVED[31:29]	3
0xBB002820	SWRED_Q_THR [3][2].MAX[28:16]	13
0xBB002820	SWRED_Q_THR [3][2].RESERVED[15:13]	3
0xBB002820	SWRED_Q_THR [3][2].MIN[12:0]	13
0xBB002824	SWRED_Q_THR [4][0].RESERVED[31:29]	3
0xBB002824	SWRED_Q_THR [4][0].MAX[28:16]	13
0xBB002824	SWRED_Q_THR [4][0].RESERVED[15:13]	3
0xBB002824	SWRED_Q_THR [4][0].MIN[12:0]	13
0xBB002828	SWRED_Q_THR [4][1].RESERVED[31:29]	3
0xBB002828	SWRED_Q_THR [4][1].MAX[28:16]	13
0xBB002828	SWRED_Q_THR [4][1].RESERVED[15:13]	3
0xBB002828	SWRED_Q_THR [4][1].MIN[12:0]	13
0xBB00282C	SWRED_Q_THR [4][2].RESERVED[31:29]	3
0xBB00282C	SWRED_Q_THR [4][2].MAX[28:16]	13
0xBB00282C	SWRED_Q_THR [4][2].RESERVED[15:13]	3
0xBB00282C	SWRED_Q_THR [4][2].MIN[12:0]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB002830	SWRED_Q_THR [5][0].RESERVED[31:29]	3
0xBB002830	SWRED_Q_THR [5][0].MAX[28:16]	13
0xBB002830	SWRED_Q_THR [5][0].RESERVED[15:13]	3
0xBB002830	SWRED_Q_THR [5][0].MIN[12:0]	13
0xBB002834	SWRED_Q_THR [5][1].RESERVED[31:29]	3
0xBB002834	SWRED_Q_THR [5][1].MAX[28:16]	13
0xBB002834	SWRED_Q_THR [5][1].RESERVED[15:13]	3
0xBB002834	SWRED_Q_THR [5][1].MIN[12:0]	13
0xBB002838	SWRED_Q_THR [5][2].RESERVED[31:29]	3
0xBB002838	SWRED_Q_THR [5][2].MAX[28:16]	13
0xBB002838	SWRED_Q_THR [5][2].RESERVED[15:13]	3
0xBB002838	SWRED_Q_THR [5][2].MIN[12:0]	13
0xBB00283C	SWRED_Q_THR [6][0].RESERVED[31:29]	3
0xBB00283C	SWRED_Q_THR [6][0].MAX[28:16]	13
0xBB00283C	SWRED_Q_THR [6][0].RESERVED[15:13]	3
0xBB00283C	SWRED_Q_THR [6][0].MIN[12:0]	13
0xBB002840	SWRED_Q_THR [6][1].RESERVED[31:29]	3
0xBB002840	SWRED_Q_THR [6][1].MAX[28:16]	13
0xBB002840	SWRED_Q_THR [6][1].RESERVED[15:13]	3
0xBB002840	SWRED_Q_THR [6][1].MIN[12:0]	13
0xBB002844	SWRED_Q_THR [6][2].RESERVED[31:29]	3
0xBB002844	SWRED_Q_THR [6][2].MAX[28:16]	13
0xBB002844	SWRED_Q_THR [6][2].RESERVED[15:13]	3
0xBB002844	SWRED_Q_THR [6][2].MIN[12:0]	13
0xBB002848	SWRED_Q_THR [7][0] RESERVED[31:29]	3
0xBB002848	SWRED_Q_THR [7][0].MAX[28:16]	13
0xBB002848	SWRED_Q_THR [7][0].RESERVED[15:13]	3
0xBB002848	SWRED_Q_THR [7][0].MIN[12:0]	13
0xBB00284C	SWRED_Q_THR [7][1].RESERVED[31:29]	3
0xBB00284C	SWRED_Q_THR [7][1].MAX[28:16]	13
0xBB00284C	SWRED_Q_THR [7][1].RESERVED[15:13]	3
0xBB00284C	SWRED_Q_THR [7][1].MIN[12:0]	13
0xBB002850	SWRED_Q_THR [7][2] RESERVED[31:29]	3
0xBB002850	SWRED_Q_THR [7][2].MAX[28:16]	13
0xBB002850	SWRED_Q_THR [7][2].RESERVED[15:13]	3
0xBB002850	SWRED_Q_THR [7][2].MIN[12:0]	13
0xBB002854	SWRED_Q_THR [8][0].RESERVED[31:29]	3
0xBB002854	SWRED_Q_THR [8][0].MAX[28:16]	13
0xBB002854	SWRED_Q_THR [8][0].RESERVED[15:13]	3
0xBB002854	SWRED_Q_THR [8][0].MIN[12:0]	13
0xBB002858	SWRED_Q_THR [8][1].RESERVED[31:29]	3
0xBB002858	SWRED_Q_THR [8][1].MAX[28:16]	13
0xBB002858	SWRED_Q_THR [8][1].RESERVED[15:13]	3
0xBB002858	SWRED_Q_THR [8][1].MIN[12:0]	13
0xBB00285C	SWRED_Q_THR [8][2].RESERVED[31:29]	3
0xBB00285C	SWRED_Q_THR [8][2].MAX[28:16]	13

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00285C	SWRED_QTHR[8][2].RESERVED[15:13]	3
0xBB00285C	SWRED_QTHR[8][2].MIN[12:0]	13
0xBB002860	SWRED_QTHR[9][0].RESERVED[31:29]	3
0xBB002860	SWRED_QTHR[9][0].MAX[28:16]	13
0xBB002860	SWRED_QTHR[9][0].RESERVED[15:13]	3
0xBB002860	SWRED_QTHR[9][0].MIN[12:0]	13
0xBB002864	SWRED_QTHR[9][1].RESERVED[31:29]	3
0xBB002864	SWRED_QTHR[9][1].MAX[28:16]	13
0xBB002864	SWRED_QTHR[9][1].RESERVED[15:13]	3
0xBB002864	SWRED_QTHR[9][1].MIN[12:0]	13
0xBB002868	SWRED_QTHR[9][2].RESERVED[31:29]	3
0xBB002868	SWRED_QTHR[9][2].MAX[28:16]	13
0xBB002868	SWRED_QTHR[9][2].RESERVED[15:13]	3
0xBB002868	SWRED_QTHR[9][2].MIN[12:0]	13
0xBB00286C	SWRED_QTHR[10][0].RESERVED[31:29]	3
0xBB00286C	SWRED_QTHR[10][0].MAX[28:16]	13
0xBB00286C	SWRED_QTHR[10][0].RESERVED[15:13]	3
0xBB00286C	SWRED_QTHR[10][0].MIN[12:0]	13
0xBB002870	SWRED_QTHR[10][1].RESERVED[31:29]	3
0xBB002870	SWRED_QTHR[10][1].MAX[28:16]	13
0xBB002870	SWRED_QTHR[10][1].RESERVED[15:13]	3
0xBB002870	SWRED_QTHR[10][1].MIN[12:0]	13
0xBB002874	SWRED_QTHR[10][2].RESERVED[31:29]	3
0xBB002874	SWRED_QTHR[10][2].MAX[28:16]	13
0xBB002874	SWRED_QTHR[10][2].RESERVED[15:13]	3
0xBB002874	SWRED_QTHR[10][2].MIN[12:0]	13
0xBB002878	SWRED_QTHR[11][0].RESERVED[31:29]	3
0xBB002878	SWRED_QTHR[11][0].MAX[28:16]	13
0xBB002878	SWRED_QTHR[11][0].RESERVED[15:13]	3
0xBB002878	SWRED_QTHR[11][0].MIN[12:0]	13
0xBB00287C	SWRED_QTHR[11][1].RESERVED[31:29]	3
0xBB00287C	SWRED_QTHR[11][1].MAX[28:16]	13
0xBB00287C	SWRED_QTHR[11][1].RESERVED[15:13]	3
0xBB00287C	SWRED_QTHR[11][1].MIN[12:0]	13
0xBB002880	SWRED_QTHR[11][2].RESERVED[31:29]	3
0xBB002880	SWRED_QTHR[11][2].MAX[28:16]	13
0xBB002880	SWRED_QTHR[11][2].RESERVED[15:13]	3
0xBB002880	SWRED_QTHR[11][2].MIN[12:0]	13
0xBB00066C	SW_LED_LOAD.RESERVED[31:1]	31
0xBB00066C	SW_LED_LOAD.SW_LED_LOAD[0:0]	1
0xBB008500	TBL_ACCESS_CTRL_0.RESERVED[31:21]	11
0xBB008500	TBL_ACCESS_CTRL_0.EXEC[20:20]	1
0xBB008500	TBL_ACCESS_CTRL_0.CMD[19:19]	1
0xBB008500	TBL_ACCESS_CTRL_0.TBL[18:15]	4
0xBB008500	TBL_ACCESS_CTRL_0.ADDR[14:0]	15
0xBB0040C0	TBL_ACCESS_CTRL_1.RESERVED[31:18]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0040C0	TBL_ACCESS_CTRL_1.EXEC[17:17]	1
0xBB0040C0	TBL_ACCESS_CTRL_1.CMD[16:16]	1
0xBB0040C0	TBL_ACCESS_CTRL_1.TBL[15:14]	2
0xBB0040C0	TBL_ACCESS_CTRL_1.ADDR[13:0]	14
0xBB008528	TBL_ACCESS_CTRL_2.RESERVED[31:20]	12
0xBB008528	TBL_ACCESS_CTRL_2.EXEC[19:19]	1
0xBB008528	TBL_ACCESS_CTRL_2.CMD[18:18]	1
0xBB008528	TBL_ACCESS_CTRL_2.TBL[17:14]	4
0xBB008528	TBL_ACCESS_CTRL_2.ADDR[13:0]	14
0xBB000200	TBL_ACCESS_CTRL_3.RESERVED[31:17]	15
0xBB000200	TBL_ACCESS_CTRL_3.EXEC[16:16]	1
0xBB000200	TBL_ACCESS_CTRL_3.CMD[15:15]	1
0xBB000200	TBL_ACCESS_CTRL_3.TBL[14:12]	3
0xBB000200	TBL_ACCESS_CTRL_3.ADDR[11:0]	12
0xBB0020DC	TBL_ACCESS_CTRL_4.RESERVED[31:9]	23
0xBB0020DC	TBL_ACCESS_CTRL_4.EXEC[8:8]	1
0xBB0020DC	TBL_ACCESS_CTRL_4.CMD[7:7]	1
0xBB0020DC	TBL_ACCESS_CTRL_4.TBL[6:6]	1
0xBB0020DC	TBL_ACCESS_CTRL_4.ADDR[5:0]	6
0xBB007E1C	TBL_ACCESS_CTRL_5.RESERVED[31:10]	22
0xBB007E1C	TBL_ACCESS_CTRL_5.EXEC[9:9]	1
0xBB007E1C	TBL_ACCESS_CTRL_5.CMD[8:8]	1
0xBB007E1C	TBL_ACCESS_CTRL_5.TBL[7:6]	2
0xBB007E1C	TBL_ACCESS_CTRL_5.ADDR[5:0]	6
0xBB008508	TBL_ACCESS_DATA_0 [0].DATA[31:0]	32
0xBB00850C	TBL_ACCESS_DATA_0 [1].DATA[31:0]	32
0xBB008510	TBL_ACCESS_DATA_0 [2].DATA[31:0]	32
0xBB008514	TBL_ACCESS_DATA_0 [3].DATA[31:0]	32
0xBB008518	TBL_ACCESS_DATA_0 [4].DATA[31:0]	32
0xBB00851C	TBL_ACCESS_DATA_0 [5].DATA[31:0]	32
0xBB008520	TBL_ACCESS_DATA_0 [6].DATA[31:0]	32
0xBB008524	TBL_ACCESS_DATA_0 [7].DATA[31:0]	32
0xBB0040C4	TBL_ACCESS_DATA_1 [0].DATA[31:0]	32
0xBB0040C8	TBL_ACCESS_DATA_1 [1].DATA[31:0]	32
0xBB0040CC	TBL_ACCESS_DATA_1 [2].DATA[31:0]	32
0xBB0040D0	TBL_ACCESS_DATA_1 [3].DATA[31:0]	32
0xBB0040D4	TBL_ACCESS_DATA_1 [4].DATA[31:0]	32
0xBB0040D8	TBL_ACCESS_DATA_1 [5].DATA[31:0]	32
0xBB0040DC	TBL_ACCESS_DATA_1 [6].DATA[31:0]	32
0xBB0040E0	TBL_ACCESS_DATA_1 [7].DATA[31:0]	32
0xBB0040E4	TBL_ACCESS_DATA_1 [8].DATA[31:0]	32
0xBB0040E8	TBL_ACCESS_DATA_1 [9].DATA[31:0]	32
0xBB0040EC	TBL_ACCESS_DATA_1 [10].DATA[31:0]	32
0xBB0040F0	TBL_ACCESS_DATA_1 [11].DATA[31:0]	32
0xBB0040F4	TBL_ACCESS_DATA_1 [12].DATA[31:0]	32
0xBB0040F8	TBL_ACCESS_DATA_1 [13].DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0040FC	TBL_ACCESS_DATA_1 [14].DATA[31:0]	32
0xBB004100	TBL_ACCESS_DATA_1 [15].DATA[31:0]	32
0xBB004104	TBL_ACCESS_DATA_1 [16].DATA[31:0]	32
0xBB004108	TBL_ACCESS_DATA_1 [17].DATA[31:0]	32
0xBB00410C	TBL_ACCESS_DATA_1 [18].DATA[31:0]	32
0xBB004110	TBL_ACCESS_DATA_1 [19].DATA[31:0]	32
0xBB004114	TBL_ACCESS_DATA_1 [20].DATA[31:0]	32
0xBB004118	TBL_ACCESS_DATA_1 [21].DATA[31:0]	32
0xBB00852C	TBL_ACCESS_DATA_2 [0].DATA[31:0]	32
0xBB008530	TBL_ACCESS_DATA_2 [1].DATA[31:0]	32
0xBB008534	TBL_ACCESS_DATA_2 [2].DATA[31:0]	32
0xBB008538	TBL_ACCESS_DATA_2 [3].DATA[31:0]	32
0xBB00853C	TBL_ACCESS_DATA_2 [4].DATA[31:0]	32
0xBB008540	TBL_ACCESS_DATA_2 [5].DATA[31:0]	32
0xBB000204	TBL_ACCESS_DATA_3 [0].DATA[31:0]	32
0xBB000208	TBL_ACCESS_DATA_3 [1].DATA[31:0]	32
0xBB00020C	TBL_ACCESS_DATA_3 [2].DATA[31:0]	32
0xBB000210	TBL_ACCESS_DATA_3 [3].DATA[31:0]	32
0xBB000214	TBL_ACCESS_DATA_3 [4].DATA[31:0]	32
0xBB000218	TBL_ACCESS_DATA_3 [5].DATA[31:0]	32
0xBB00021C	TBL_ACCESS_DATA_3 [6].DATA[31:0]	32
0xBB000220	TBL_ACCESS_DATA_3 [7].DATA[31:0]	32
0xBB000224	TBL_ACCESS_DATA_3 [8].DATA[31:0]	32
0xBB0020E0	TBL_ACCESS_DATA_4 [0].DATA[31:0]	32
0xBB0020E4	TBL_ACCESS_DATA_4 [1].DATA[31:0]	32
0xBB0020E8	TBL_ACCESS_DATA_4 [2].DATA[31:0]	32
0xBB0020EC	TBL_ACCESS_DATA_4 [3].DATA[31:0]	32
0xBB0020F0	TBL_ACCESS_DATA_4 [4].DATA[31:0]	32
0xBB0020F4	TBL_ACCESS_DATA_4 [5].DATA[31:0]	32
0xBB0020F8	TBL_ACCESS_DATA_4 [6].DATA[31:0]	32
0xBB0020FC	TBL_ACCESS_DATA_4 [7].DATA[31:0]	32
0xBB002100	TBL_ACCESS_DATA_4 [8].DATA[31:0]	32
0xBB002104	TBL_ACCESS_DATA_4 [9].DATA[31:0]	32
0xBB002108	TBL_ACCESS_DATA_4 [10].DATA[31:0]	32
0xBB00210C	TBL_ACCESS_DATA_4 [11].DATA[31:0]	32
0xBB002110	TBL_ACCESS_DATA_4 [12].DATA[31:0]	32
0xBB002114	TBL_ACCESS_DATA_4 [13].DATA[31:0]	32
0xBB002118	TBL_ACCESS_DATA_4 [14].DATA[31:0]	32
0xBB00211C	TBL_ACCESS_DATA_4 [15].DATA[31:0]	32
0xBB002120	TBL_ACCESS_DATA_4 [16].DATA[31:0]	32
0xBB002124	TBL_ACCESS_DATA_4 [17].DATA[31:0]	32
0xBB002128	TBL_ACCESS_DATA_4 [18].DATA[31:0]	32
0xBB00212C	TBL_ACCESS_DATA_4 [19].DATA[31:0]	32
0xBB002130	TBL_ACCESS_DATA_4 [20].DATA[31:0]	32
0xBB002134	TBL_ACCESS_DATA_4 [21].DATA[31:0]	32
0xBB002138	TBL_ACCESS_DATA_4 [22].DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00213C	TBL_ACCESS_DATA_4 [23].DATA[31:0]	32
0xBB002140	TBL_ACCESS_DATA_4 [24].DATA[31:0]	32
0xBB002144	TBL_ACCESS_DATA_4 [25].DATA[31:0]	32
0xBB002148	TBL_ACCESS_DATA_4 [26].DATA[31:0]	32
0xBB00214C	TBL_ACCESS_DATA_4 [27].DATA[31:0]	32
0xBB002150	TBL_ACCESS_DATA_4 [28].DATA[31:0]	32
0xBB007E20	TBL_ACCESS_DATA_5 [0].DATA[31:0]	32
0xBB007E24	TBL_ACCESS_DATA_5 [1].DATA[31:0]	32
0xBB007E28	TBL_ACCESS_DATA_5 [2].DATA[31:0]	32
0xBB007E2C	TBL_ACCESS_DATA_5 [3].DATA[31:0]	32
0xBB007E30	TBL_ACCESS_DATA_5 [4].DATA[31:0]	32
0xBB007E34	TBL_ACCESS_DATA_5 [5].DATA[31:0]	32
0xBB007E38	TBL_ACCESS_DATA_5 [6].DATA[31:0]	32
0xBB007E3C	TBL_ACCESS_DATA_5 [7].DATA[31:0]	32
0xBB007E40	TBL_ACCESS_DATA_5 [8].DATA[31:0]	32
0xBB007E44	TBL_ACCESS_DATA_5 [9].DATA[31:0]	32
0xBB007E48	TBL_ACCESS_DATA_5 [10].DATA[31:0]	32
0xBB007E4C	TBL_ACCESS_DATA_5 [11].DATA[31:0]	32
0xBB007E50	TBL_ACCESS_DATA_5 [12].DATA[31:0]	32
0xBB007E54	TBL_ACCESS_DATA_5 [13].DATA[31:0]	32
0xBB007E58	TBL_ACCESS_DATA_5 [14].DATA[31:0]	32
0xBB007E5C	TBL_ACCESS_DATA_5 [15].DATA[31:0]	32
0xBB007E60	TBL_ACCESS_DATA_5 [16].DATA[31:0]	32
0xBB007E64	TBL_ACCESS_DATA_5 [17].DATA[31:0]	32
0xBB007E68	TBL_ACCESS_DATA_5 [18].DATA[31:0]	32
0xBB007E6C	TBL_ACCESS_DATA_5 [19].DATA[31:0]	32
0xBB007E70	TBL_ACCESS_DATA_5 [20].DATA[31:0]	32
0xBB007E74	TBL_ACCESS_DATA_5 [21].DATA[31:0]	32
0xBB007E78	TBL_ACCESS_DATA_5 [22].DATA[31:0]	32
0xBB007E7C	TBL_ACCESS_DATA_5 [23].DATA[31:0]	32
0xBB007E80	TBL_ACCESS_DATA_5 [24].DATA[31:0]	32
0xBB007E84	TBL_ACCESS_DATA_5 [25].DATA[31:0]	32
0xBB007E88	TBL_ACCESS_DATA_5 [26].DATA[31:0]	32
0xBB007E8C	TBL_ACCESS_DATA_5 [27].DATA[31:0]	32
0xBB007E90	TBL_ACCESS_DATA_5 [28].DATA[31:0]	32
0xBB007E94	TBL_ACCESS_DATA_5 [29].DATA[31:0]	32
0xBB007E98	TBL_ACCESS_DATA_5 [30].DATA[31:0]	32
0xBB007E9C	TBL_ACCESS_DATA_5 [31].DATA[31:0]	32
0xBB007EA0	TBL_ACCESS_DATA_5 [32].DATA[31:0]	32
0xBB007EA4	TBL_ACCESS_DATA_5 [33].DATA[31:0]	32
0xBB007EA8	TBL_ACCESS_DATA_5 [34].DATA[31:0]	32
0xBB007EAC	TBL_ACCESS_DATA_5 [35].DATA[31:0]	32
0xBB007EB0	TBL_ACCESS_DATA_5 [36].DATA[31:0]	32
0xBB007EB4	TBL_ACCESS_DATA_5 [37].DATA[31:0]	32
0xBB007EB8	TBL_ACCESS_DATA_5 [38].DATA[31:0]	32
0xBB007EBC	TBL_ACCESS_DATA_5 [39].DATA[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007EC0	TBL_ACCESS_DATA_5 [40].DATA[31:0]	32
0xBB007EC4	TBL_ACCESS_DATA_5 [41].DATA[31:0]	32
0xBB007EC8	TBL_ACCESS_DATA_5 [42].DATA[31:0]	32
0xBB007ECC	TBL_ACCESS_DATA_5 [43].DATA[31:0]	32
0xBB007ED0	TBL_ACCESS_DATA_5 [44].DATA[31:0]	32
0xBB007ED4	TBL_ACCESS_DATA_5 [45].DATA[31:0]	32
0xBB007ED8	TBL_ACCESS_DATA_5 [46].DATA[31:0]	32
0xBB007EDC	TBL_ACCESS_DATA_5 [47].DATA[31:0]	32
0xBB007EE0	TBL_ACCESS_DATA_5 [48].DATA[31:0]	32
0xBB007EE4	TBL_ACCESS_DATA_5 [49].DATA[31:0]	32
0xBB007EE8	TBL_ACCESS_DATA_5 [50].DATA[31:0]	32
0xBB007EEC	TBL_ACCESS_DATA_5 [51].DATA[31:0]	32
0xBB007EF0	TBL_ACCESS_DATA_5 [52].DATA[31:0]	32
0xBB008504	TBL_ACCESS_L2_METHOD_CTRL.RESERVED[31:19]	13
0xBB008504	TBL_ACCESS_L2_METHOD_CTRL.METHOD[18:16]	3
0xBB008504	TBL_ACCESS_L2_METHOD_CTRL.HIT[15:15]	1
0xBB008504	TBL_ACCESS_L2_METHOD_CTRL.HIT_ADDR[14:0]	15
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [0].GARP_ACT[1:0]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [1].GARP_ACT[3:2]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [2].GARP_ACT[5:4]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [3].GARP_ACT[7:6]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [4].GARP_ACT[9:8]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [5].GARP_ACT[11:10]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [6].GARP_ACT[13:12]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [7].GARP_ACT[15:14]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [8].GARP_ACT[17:16]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [9].GARP_ACT[19:18]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [10].GARP_ACT[21:20]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [11].GARP_ACT[23:22]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [12].GARP_ACT[25:24]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [13].GARP_ACT[27:26]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [14].GARP_ACT[29:28]	2
0xBB008C04	TRAP_ARP_GRAT_PORT_ACT [15].GARP_ACT[31:30]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [16].GARP_ACT[1:0]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [17].GARP_ACT[3:2]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [18].GARP_ACT[5:4]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [19].GARP_ACT[7:6]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [20].GARP_ACT[9:8]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [21].GARP_ACT[11:10]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [22].GARP_ACT[13:12]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [23].GARP_ACT[15:14]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [24].GARP_ACT[17:16]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [25].GARP_ACT[19:18]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [26].GARP_ACT[21:20]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [27].GARP_ACT[23:22]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [28].GARP_ACT[25:24]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [29].GARP_ACT[27:26]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [30].GARP_ACT[29:28]	2
0xBB008C08	TRAP_ARP_GRAT_PORT_ACT [31].GARP_ACT[31:30]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [32].GARP_ACT[1:0]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [33].GARP_ACT[3:2]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [34].GARP_ACT[5:4]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [35].GARP_ACT[7:6]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [36].GARP_ACT[9:8]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [37].GARP_ACT[11:10]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [38].GARP_ACT[13:12]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [39].GARP_ACT[15:14]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [40].GARP_ACT[17:16]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [41].GARP_ACT[19:18]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [42].GARP_ACT[21:20]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [43].GARP_ACT[23:22]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [44].GARP_ACT[25:24]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [45].GARP_ACT[27:26]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [46].GARP_ACT[29:28]	2
0xBB008C0C	TRAP_ARP_GRAT_PORT_ACT [47].GARP_ACT[31:30]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [48].GARP_ACT[1:0]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [49].GARP_ACT[3:2]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [50].GARP_ACT[5:4]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [51].GARP_ACT[7:6]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [52].GARP_ACT[9:8]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [53].GARP_ACT[11:10]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [54].GARP_ACT[13:12]	2
0xBB008C10	TRAP_ARP_GRAT_PORT_ACT [55].GARP_ACT[15:14]	2
0xBB008C00	TRAP_CTRL_RESERVED[31:3]	29
0xBB008C00	TRAP_CTRL.CPU_SEL[2:2]	1
0xBB008C00	TRAP_CTRL.L2_CRC_ERR_ACT[1:0]	2
0xBB00BA78	TRK_CTRL_RESERVED[31:7]	25
0xBB00BA78	TRK_CTRLLINK_DOWN_AVOID[6:6]	1
0xBB00BA78	TRK_CTRL.CONGST_AVOID[5:5]	1
0xBB00BA78	TRK_CTRL.LOCAL_FIRST[4:4]	1
0xBB00BA78	TRK_CTRL.STK_HASH_CAL[3:3]	1
0xBB00BA78	TRK_CTRL.TRK_STAND_ALONE_MODE[2:2]	1
0xBB00BA78	TRK_CTRL.SEP_PORT_SEL[1:1]	1
0xBB00BA78	TRK_CTRL.NON_TMN_TUNNEL_HASH_SEL[0:0]	1
0xBB00BA70	TRK_HASH_CTRL [0].RESERVED[31:14]	18
0xBB00BA70	TRK_HASH_CTRL [0].L3_HASH_MSK[13:4]	10
0xBB00BA70	TRK_HASH_CTRL [0].L2_HASH_MSK[3:0]	4
0xBB00BA74	TRK_HASH_CTRL [1].RESERVED[31:14]	18
0xBB00BA74	TRK_HASH_CTRL [1].L3_HASH_MSK[13:4]	10
0xBB00BA74	TRK_HASH_CTRL [1].L2_HASH_MSK[3:0]	4
0xBB00B800	TRK_ID_CTRL [0].RESERVED[31:8]	24
0xBB00B800	TRK_ID_CTRL [0].TRK_VALID[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B800	TRK_ID_CTRL [0].TRK_ID[6:0]	7
0xBB00B804	TRK_ID_CTRL [1].RESERVED[31:8]	24
0xBB00B804	TRK_ID_CTRL [1].TRK_VALID[7:7]	1
0xBB00B804	TRK_ID_CTRL [1].TRK_ID[6:0]	7
0xBB00B808	TRK_ID_CTRL [2].RESERVED[31:8]	24
0xBB00B808	TRK_ID_CTRL [2].TRK_VALID[7:7]	1
0xBB00B808	TRK_ID_CTRL [2].TRK_ID[6:0]	7
0xBB00B80C	TRK_ID_CTRL [3].RESERVED[31:8]	24
0xBB00B80C	TRK_ID_CTRL [3].TRK_VALID[7:7]	1
0xBB00B80C	TRK_ID_CTRL [3].TRK_ID[6:0]	7
0xBB00B810	TRK_ID_CTRL [4].RESERVED[31:8]	24
0xBB00B810	TRK_ID_CTRL [4].TRK_VALID[7:7]	1
0xBB00B810	TRK_ID_CTRL [4].TRK_ID[6:0]	7
0xBB00B814	TRK_ID_CTRL [5].RESERVED[31:8]	24
0xBB00B814	TRK_ID_CTRL [5].TRK_VALID[7:7]	1
0xBB00B814	TRK_ID_CTRL [5].TRK_ID[6:0]	7
0xBB00B818	TRK_ID_CTRL [6].RESERVED[31:8]	24
0xBB00B818	TRK_ID_CTRL [6].TRK_VALID[7:7]	1
0xBB00B818	TRK_ID_CTRL [6].TRK_ID[6:0]	7
0xBB00B81C	TRK_ID_CTRL [7].RESERVED[31:8]	24
0xBB00B81C	TRK_ID_CTRL [7].TRK_VALID[7:7]	1
0xBB00B81C	TRK_ID_CTRL [7].TRK_ID[6:0]	7
0xBB00B820	TRK_ID_CTRL [8].RESERVED[31:8]	24
0xBB00B820	TRK_ID_CTRL [8].TRK_VALID[7:7]	1
0xBB00B820	TRK_ID_CTRL [8].TRK_ID[6:0]	7
0xBB00B824	TRK_ID_CTRL [9].RESERVED[31:8]	24
0xBB00B824	TRK_ID_CTRL [9].TRK_VALID[7:7]	1
0xBB00B824	TRK_ID_CTRL [9].TRK_ID[6:0]	7
0xBB00B828	TRK_ID_CTRL [10].RESERVED[31:8]	24
0xBB00B828	TRK_ID_CTRL [10].TRK_VALID[7:7]	1
0xBB00B828	TRK_ID_CTRL [10].TRK_ID[6:0]	7
0xBB00B82C	TRK_ID_CTRL [11].RESERVED[31:8]	24
0xBB00B82C	TRK_ID_CTRL [11].TRK_VALID[7:7]	1
0xBB00B82C	TRK_ID_CTRL [11].TRK_ID[6:0]	7
0xBB00B830	TRK_ID_CTRL [12].RESERVED[31:8]	24
0xBB00B830	TRK_ID_CTRL [12].TRK_VALID[7:7]	1
0xBB00B830	TRK_ID_CTRL [12].TRK_ID[6:0]	7
0xBB00B834	TRK_ID_CTRL [13].RESERVED[31:8]	24
0xBB00B834	TRK_ID_CTRL [13].TRK_VALID[7:7]	1
0xBB00B834	TRK_ID_CTRL [13].TRK_ID[6:0]	7
0xBB00B838	TRK_ID_CTRL [14].RESERVED[31:8]	24
0xBB00B838	TRK_ID_CTRL [14].TRK_VALID[7:7]	1
0xBB00B838	TRK_ID_CTRL [14].TRK_ID[6:0]	7
0xBB00B83C	TRK_ID_CTRL [15].RESERVED[31:8]	24
0xBB00B83C	TRK_ID_CTRL [15].TRK_VALID[7:7]	1
0xBB00B83C	TRK_ID_CTRL [15].TRK_ID[6:0]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B840	TRK_ID_CTRL [16].RESERVED[31:8]	24
0xBB00B840	TRK_ID_CTRL [16].TRK_VALID[7:7]	1
0xBB00B840	TRK_ID_CTRL [16].TRK_ID[6:0]	7
0xBB00B844	TRK_ID_CTRL [17].RESERVED[31:8]	24
0xBB00B844	TRK_ID_CTRL [17].TRK_VALID[7:7]	1
0xBB00B844	TRK_ID_CTRL [17].TRK_ID[6:0]	7
0xBB00B848	TRK_ID_CTRL [18].RESERVED[31:8]	24
0xBB00B848	TRK_ID_CTRL [18].TRK_VALID[7:7]	1
0xBB00B848	TRK_ID_CTRL [18].TRK_ID[6:0]	7
0xBB00B84C	TRK_ID_CTRL [19].RESERVED[31:8]	24
0xBB00B84C	TRK_ID_CTRL [19].TRK_VALID[7:7]	1
0xBB00B84C	TRK_ID_CTRL [19].TRK_ID[6:0]	7
0xBB00B850	TRK_ID_CTRL [20].RESERVED[31:8]	24
0xBB00B850	TRK_ID_CTRL [20].TRK_VALID[7:7]	1
0xBB00B850	TRK_ID_CTRL [20].TRK_ID[6:0]	7
0xBB00B854	TRK_ID_CTRL [21].RESERVED[31:8]	24
0xBB00B854	TRK_ID_CTRL [21].TRK_VALID[7:7]	1
0xBB00B854	TRK_ID_CTRL [21].TRK_ID[6:0]	7
0xBB00B858	TRK_ID_CTRL [22].RESERVED[31:8]	24
0xBB00B858	TRK_ID_CTRL [22].TRK_VALID[7:7]	1
0xBB00B858	TRK_ID_CTRL [22].TRK_ID[6:0]	7
0xBB00B85C	TRK_ID_CTRL [23].RESERVED[31:8]	24
0xBB00B85C	TRK_ID_CTRL [23].TRK_VALID[7:7]	1
0xBB00B85C	TRK_ID_CTRL [23].TRK_ID[6:0]	7
0xBB00B860	TRK_ID_CTRL [24].RESERVED[31:8]	24
0xBB00B860	TRK_ID_CTRL [24].TRK_VALID[7:7]	1
0xBB00B860	TRK_ID_CTRL [24].TRK_ID[6:0]	7
0xBB00B864	TRK_ID_CTRL [25].RESERVED[31:8]	24
0xBB00B864	TRK_ID_CTRL [25].TRK_VALID[7:7]	1
0xBB00B864	TRK_ID_CTRL [25].TRK_ID[6:0]	7
0xBB00B868	TRK_ID_CTRL [26].RESERVED[31:8]	24
0xBB00B868	TRK_ID_CTRL [26].TRK_VALID[7:7]	1
0xBB00B868	TRK_ID_CTRL [26].TRK_ID[6:0]	7
0xBB00B86C	TRK_ID_CTRL [27].RESERVED[31:8]	24
0xBB00B86C	TRK_ID_CTRL [27].TRK_VALID[7:7]	1
0xBB00B86C	TRK_ID_CTRL [27].TRK_ID[6:0]	7
0xBB00B870	TRK_ID_CTRL [28].RESERVED[31:8]	24
0xBB00B870	TRK_ID_CTRL [28].TRK_VALID[7:7]	1
0xBB00B870	TRK_ID_CTRL [28].TRK_ID[6:0]	7
0xBB00B874	TRK_ID_CTRL [29].RESERVED[31:8]	24
0xBB00B874	TRK_ID_CTRL [29].TRK_VALID[7:7]	1
0xBB00B874	TRK_ID_CTRL [29].TRK_ID[6:0]	7
0xBB00B878	TRK_ID_CTRL [30].RESERVED[31:8]	24
0xBB00B878	TRK_ID_CTRL [30].TRK_VALID[7:7]	1
0xBB00B878	TRK_ID_CTRL [30].TRK_ID[6:0]	7
0xBB00B87C	TRK_ID_CTRL [31].RESERVED[31:8]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B87C	TRK_ID_CTRL [31].TRK_VALID[7:7]	1
0xBB00B87C	TRK_ID_CTRL [31].TRK_ID[6:0]	7
0xBB00B880	TRK_ID_CTRL [32].RESERVED[31:8]	24
0xBB00B880	TRK_ID_CTRL [32].TRK_VALID[7:7]	1
0xBB00B880	TRK_ID_CTRL [32].TRK_ID[6:0]	7
0xBB00B884	TRK_ID_CTRL [33].RESERVED[31:8]	24
0xBB00B884	TRK_ID_CTRL [33].TRK_VALID[7:7]	1
0xBB00B884	TRK_ID_CTRL [33].TRK_ID[6:0]	7
0xBB00B888	TRK_ID_CTRL [34].RESERVED[31:8]	24
0xBB00B888	TRK_ID_CTRL [34].TRK_VALID[7:7]	1
0xBB00B888	TRK_ID_CTRL [34].TRK_ID[6:0]	7
0xBB00B88C	TRK_ID_CTRL [35].RESERVED[31:8]	24
0xBB00B88C	TRK_ID_CTRL [35].TRK_VALID[7:7]	1
0xBB00B88C	TRK_ID_CTRL [35].TRK_ID[6:0]	7
0xBB00B890	TRK_ID_CTRL [36].RESERVED[31:8]	24
0xBB00B890	TRK_ID_CTRL [36].TRK_VALID[7:7]	1
0xBB00B890	TRK_ID_CTRL [36].TRK_ID[6:0]	7
0xBB00B894	TRK_ID_CTRL [37].RESERVED[31:8]	24
0xBB00B894	TRK_ID_CTRL [37].TRK_VALID[7:7]	1
0xBB00B894	TRK_ID_CTRL [37].TRK_ID[6:0]	7
0xBB00B898	TRK_ID_CTRL [38].RESERVED[31:8]	24
0xBB00B898	TRK_ID_CTRL [38].TRK_VALID[7:7]	1
0xBB00B898	TRK_ID_CTRL [38].TRK_ID[6:0]	7
0xBB00B89C	TRK_ID_CTRL [39].RESERVED[31:8]	24
0xBB00B89C	TRK_ID_CTRL [39].TRK_VALID[7:7]	1
0xBB00B89C	TRK_ID_CTRL [39].TRK_ID[6:0]	7
0xBB00B8A0	TRK_ID_CTRL [40].RESERVED[31:8]	24
0xBB00B8A0	TRK_ID_CTRL [40].TRK_VALID[7:7]	1
0xBB00B8A0	TRK_ID_CTRL [40].TRK_ID[6:0]	7
0xBB00B8A4	TRK_ID_CTRL [41].RESERVED[31:8]	24
0xBB00B8A4	TRK_ID_CTRL [41].TRK_VALID[7:7]	1
0xBB00B8A4	TRK_ID_CTRL [41].TRK_ID[6:0]	7
0xBB00B8A8	TRK_ID_CTRL [42].RESERVED[31:8]	24
0xBB00B8A8	TRK_ID_CTRL [42].TRK_VALID[7:7]	1
0xBB00B8A8	TRK_ID_CTRL [42].TRK_ID[6:0]	7
0xBB00B8AC	TRK_ID_CTRL [43].RESERVED[31:8]	24
0xBB00B8AC	TRK_ID_CTRL [43].TRK_VALID[7:7]	1
0xBB00B8AC	TRK_ID_CTRL [43].TRK_ID[6:0]	7
0xBB00B8B0	TRK_ID_CTRL [44].RESERVED[31:8]	24
0xBB00B8B0	TRK_ID_CTRL [44].TRK_VALID[7:7]	1
0xBB00B8B0	TRK_ID_CTRL [44].TRK_ID[6:0]	7
0xBB00B8B4	TRK_ID_CTRL [45].RESERVED[31:8]	24
0xBB00B8B4	TRK_ID_CTRL [45].TRK_VALID[7:7]	1
0xBB00B8B4	TRK_ID_CTRL [45].TRK_ID[6:0]	7
0xBB00B8B8	TRK_ID_CTRL [46].RESERVED[31:8]	24
0xBB00B8B8	TRK_ID_CTRL [46].TRK_VALID[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B8B8	TRK_ID_CTRL [46].TRK_ID[6:0]	7
0xBB00B8BC	TRK_ID_CTRL [47].RESERVED[31:8]	24
0xBB00B8BC	TRK_ID_CTRL [47].TRK_VALID[7:7]	1
0xBB00B8BC	TRK_ID_CTRL [47].TRK_ID[6:0]	7
0xBB00B8C0	TRK_ID_CTRL [48].RESERVED[31:8]	24
0xBB00B8C0	TRK_ID_CTRL [48].TRK_VALID[7:7]	1
0xBB00B8C0	TRK_ID_CTRL [48].TRK_ID[6:0]	7
0xBB00B8C4	TRK_ID_CTRL [49].RESERVED[31:8]	24
0xBB00B8C4	TRK_ID_CTRL [49].TRK_VALID[7:7]	1
0xBB00B8C4	TRK_ID_CTRL [49].TRK_ID[6:0]	7
0xBB00B8C8	TRK_ID_CTRL [50].RESERVED[31:8]	24
0xBB00B8C8	TRK_ID_CTRL [50].TRK_VALID[7:7]	1
0xBB00B8C8	TRK_ID_CTRL [50].TRK_ID[6:0]	7
0xBB00B8CC	TRK_ID_CTRL [51].RESERVED[31:8]	24
0xBB00B8CC	TRK_ID_CTRL [51].TRK_VALID[7:7]	1
0xBB00B8CC	TRK_ID_CTRL [51].TRK_ID[6:0]	7
0xBB00BA84	TRK_LOCAL_TBL [0].RESERVED[31:13]	19
0xBB00BA84	TRK_LOCAL_TBL [0].NUM_TX_CANDI[12:9]	4
0xBB00BA84	TRK_LOCAL_TBL [0].L2_HASH_MSK_IDX[8:8]	1
0xBB00BA84	TRK_LOCAL_TBL [0].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BA84	TRK_LOCAL_TBL [0].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BA84	TRK_LOCAL_TBL [0].TRK_PORT7[5:0]	6
0xBB00BA88	TRK_LOCAL_TBL [0].RESERVED[31:30]	2
0xBB00BA88	TRK_LOCAL_TBL [0].TRK_PORT6[29:24]	6
0xBB00BA88	TRK_LOCAL_TBL [0].TRK_PORT5[23:18]	6
0xBB00BA88	TRK_LOCAL_TBL [0].TRK_PORT4[17:12]	6
0xBB00BA88	TRK_LOCAL_TBL [0].TRK_PORT3[11:6]	6
0xBB00BA88	TRK_LOCAL_TBL [0].TRK_PORT2[5:0]	6
0xBB00BA8C	TRK_LOCAL_TBL [0].RESERVED[31:28]	4
0xBB00BA8C	TRK_LOCAL_TBL [0].TRK_PORT1[27:22]	6
0xBB00BA8C	TRK_LOCAL_TBL [0].TRK_PORT0[21:16]	6
0xBB00BA8C	TRK_LOCAL_TBL [0].SEP_FLOOD_PORT[15:10]	6
0xBB00BA8C	TRK_LOCAL_TBL [0].SEP_KWN_MC_PORT[9:4]	6
0xBB00BA8C	TRK_LOCAL_TBL [0].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BA90	TRK_LOCAL_TBL [0].RESERVED[31:30]	2
0xBB00BA90	TRK_LOCAL_TBL [0].AVL_TRK_PORT7[29:24]	6
0xBB00BA90	TRK_LOCAL_TBL [0].AVL_TRK_PORT6[23:18]	6
0xBB00BA90	TRK_LOCAL_TBL [0].AVL_TRK_PORT5[17:12]	6
0xBB00BA90	TRK_LOCAL_TBL [0].AVL_TRK_PORT4[11:6]	6
0xBB00BA90	TRK_LOCAL_TBL [0].AVL_TRK_PORT3[5:0]	6
0xBB00BA94	TRK_LOCAL_TBL [0].RESERVED[31:30]	2
0xBB00BA94	TRK_LOCAL_TBL [0].AVL_TRK_PORT2[29:24]	6
0xBB00BA94	TRK_LOCAL_TBL [0].AVL_TRK_PORT1[23:18]	6
0xBB00BA94	TRK_LOCAL_TBL [0].AVL_TRK_PORT0[17:12]	6
0xBB00BA94	TRK_LOCAL_TBL [0].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BA94	TRK_LOCAL_TBL [0].AVL_SEP_KWN_MC_PORT[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BA98	TRK_LOCAL_TBL [1].RESERVED[31:13]	19
0xBB00BA98	TRK_LOCAL_TBL [1].NUM_TX_CANDI[12:9]	4
0xBB00BA98	TRK_LOCAL_TBL [1].L2_HASH_MSK_IDX[8:8]	1
0xBB00BA98	TRK_LOCAL_TBL [1].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BA98	TRK_LOCAL_TBL [1].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BA98	TRK_LOCAL_TBL [1].TRK_PORT7[5:0]	6
0xBB00BA9C	TRK_LOCAL_TBL [1].RESERVED[31:30]	2
0xBB00BA9C	TRK_LOCAL_TBL [1].TRK_PORT6[29:24]	6
0xBB00BA9C	TRK_LOCAL_TBL [1].TRK_PORT5[23:18]	6
0xBB00BA9C	TRK_LOCAL_TBL [1].TRK_PORT4[17:12]	6
0xBB00BA9C	TRK_LOCAL_TBL [1].TRK_PORT3[11:6]	6
0xBB00BA9C	TRK_LOCAL_TBL [1].TRK_PORT2[5:0]	6
0xBB00BAA0	TRK_LOCAL_TBL [1].RESERVED[31:28]	4
0xBB00BAA0	TRK_LOCAL_TBL [1].TRK_PORT1[27:22]	6
0xBB00BAA0	TRK_LOCAL_TBL [1].TRK_PORT0[21:16]	6
0xBB00BAA0	TRK_LOCAL_TBL [1].SEP_FLOOD_PORT[15:10]	6
0xBB00BAA0	TRK_LOCAL_TBL [1].SEP_KWN_MC_PORT[9:4]	6
0xBB00BAA0	TRK_LOCAL_TBL [1].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BAA4	TRK_LOCAL_TBL [1].RESERVED[31:30]	2
0xBB00BAA4	TRK_LOCAL_TBL [1].AVL_TRK_PORT7[29:24]	6
0xBB00BAA4	TRK_LOCAL_TBL [1].AVL_TRK_PORT6[23:18]	6
0xBB00BAA4	TRK_LOCAL_TBL [1].AVL_TRK_PORT5[17:12]	6
0xBB00BAA4	TRK_LOCAL_TBL [1].AVL_TRK_PORT4[11:6]	6
0xBB00BAA4	TRK_LOCAL_TBL [1].AVL_TRK_PORT3[5:0]	6
0xBB00BAA8	TRK_LOCAL_TBL [1].RESERVED[31:30]	2
0xBB00BAA8	TRK_LOCAL_TBL [1].AVL_TRK_PORT2[29:24]	6
0xBB00BAA8	TRK_LOCAL_TBL [1].AVL_TRK_PORT1[23:18]	6
0xBB00BAA8	TRK_LOCAL_TBL [1].AVL_TRK_PORT0[17:12]	6
0xBB00BAA8	TRK_LOCAL_TBL [1].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BAA8	TRK_LOCAL_TBL [1].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BAAC	TRK_LOCAL_TBL [2].RESERVED[31:13]	19
0xBB00BAAC	TRK_LOCAL_TBL [2].NUM_TX_CANDI[12:9]	4
0xBB00BAAC	TRK_LOCAL_TBL [2].L2_HASH_MSK_IDX[8:8]	1
0xBB00BAAC	TRK_LOCAL_TBL [2].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BAAC	TRK_LOCAL_TBL [2].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BAAC	TRK_LOCAL_TBL [2].TRK_PORT7[5:0]	6
0xBB00BAB0	TRK_LOCAL_TBL [2].RESERVED[31:30]	2
0xBB00BAB0	TRK_LOCAL_TBL [2].TRK_PORT6[29:24]	6
0xBB00BAB0	TRK_LOCAL_TBL [2].TRK_PORT5[23:18]	6
0xBB00BAB0	TRK_LOCAL_TBL [2].TRK_PORT4[17:12]	6
0xBB00BAB0	TRK_LOCAL_TBL [2].TRK_PORT3[11:6]	6
0xBB00BAB0	TRK_LOCAL_TBL [2].TRK_PORT2[5:0]	6
0xBB00BAB4	TRK_LOCAL_TBL [2].RESERVED[31:28]	4
0xBB00BAB4	TRK_LOCAL_TBL [2].TRK_PORT1[27:22]	6
0xBB00BAB4	TRK_LOCAL_TBL [2].TRK_PORT0[21:16]	6
0xBB00BAB4	TRK_LOCAL_TBL [2].SEP_FLOOD_PORT[15:10]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BAB4	TRK_LOCAL_TBL [2].SEP_KWN_MC_PORT[9:4]	6
0xBB00BAB4	TRK_LOCAL_TBL [2].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BAB8	TRK_LOCAL_TBL [2].RESERVED[31:30]	2
0xBB00BAB8	TRK_LOCAL_TBL [2].AVL_TRK_PORT7[29:24]	6
0xBB00BAB8	TRK_LOCAL_TBL [2].AVL_TRK_PORT6[23:18]	6
0xBB00BAB8	TRK_LOCAL_TBL [2].AVL_TRK_PORT5[17:12]	6
0xBB00BAB8	TRK_LOCAL_TBL [2].AVL_TRK_PORT4[11:6]	6
0xBB00BAB8	TRK_LOCAL_TBL [2].AVL_TRK_PORT3[5:0]	6
0xBB00BABC	TRK_LOCAL_TBL [2].RESERVED[31:30]	2
0xBB00BABC	TRK_LOCAL_TBL [2].AVL_TRK_PORT2[29:24]	6
0xBB00BABC	TRK_LOCAL_TBL [2].AVL_TRK_PORT1[23:18]	6
0xBB00BABC	TRK_LOCAL_TBL [2].AVL_TRK_PORT0[17:12]	6
0xBB00BABC	TRK_LOCAL_TBL [2].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BABC	TRK_LOCAL_TBL [2].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BAC0	TRK_LOCAL_TBL [3].RESERVED[31:13]	19
0xBB00BAC0	TRK_LOCAL_TBL [3].NUM_TX_CANDI[12:9]	4
0xBB00BAC0	TRK_LOCAL_TBL [3].L2_HASH_MSK_IDX[8:8]	1
0xBB00BAC0	TRK_LOCAL_TBL [3].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BAC0	TRK_LOCAL_TBL [3].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BAC0	TRK_LOCAL_TBL [3].TRK_PORT7[5:0]	6
0xBB00BAC4	TRK_LOCAL_TBL [3].RESERVED[31:30]	2
0xBB00BAC4	TRK_LOCAL_TBL [3].TRK_PORT6[29:24]	6
0xBB00BAC4	TRK_LOCAL_TBL [3].TRK_PORT5[23:18]	6
0xBB00BAC4	TRK_LOCAL_TBL [3].TRK_PORT4[17:12]	6
0xBB00BAC4	TRK_LOCAL_TBL [3].TRK_PORT3[11:6]	6
0xBB00BAC4	TRK_LOCAL_TBL [3].TRK_PORT2[5:0]	6
0xBB00BAC8	TRK_LOCAL_TBL [3].RESERVED[31:28]	4
0xBB00BAC8	TRK_LOCAL_TBL [3].TRK_PORT1[27:22]	6
0xBB00BAC8	TRK_LOCAL_TBL [3].TRK_PORT0[21:16]	6
0xBB00BAC8	TRK_LOCAL_TBL [3].SEP_FLOOD_PORT[15:10]	6
0xBB00BAC8	TRK_LOCAL_TBL [3].SEP_KWN_MC_PORT[9:4]	6
0xBB00BAC8	TRK_LOCAL_TBL [3].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BACC	TRK_LOCAL_TBL [3].RESERVED[31:30]	2
0xBB00BACC	TRK_LOCAL_TBL [3].AVL_TRK_PORT7[29:24]	6
0xBB00BACC	TRK_LOCAL_TBL [3].AVL_TRK_PORT6[23:18]	6
0xBB00BACC	TRK_LOCAL_TBL [3].AVL_TRK_PORT5[17:12]	6
0xBB00BACC	TRK_LOCAL_TBL [3].AVL_TRK_PORT4[11:6]	6
0xBB00BACC	TRK_LOCAL_TBL [3].AVL_TRK_PORT3[5:0]	6
0xBB00BAD0	TRK_LOCAL_TBL [3].RESERVED[31:30]	2
0xBB00BAD0	TRK_LOCAL_TBL [3].AVL_TRK_PORT2[29:24]	6
0xBB00BAD0	TRK_LOCAL_TBL [3].AVL_TRK_PORT1[23:18]	6
0xBB00BAD0	TRK_LOCAL_TBL [3].AVL_TRK_PORT0[17:12]	6
0xBB00BAD0	TRK_LOCAL_TBL [3].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BAD0	TRK_LOCAL_TBL [3].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BAD4	TRK_LOCAL_TBL [4].RESERVED[31:13]	19
0xBB00BAD4	TRK_LOCAL_TBL [4].NUM_TX_CANDI[12:9]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BAD4	TRK_LOCAL_TBL [4].L2_HASH_MSK_IDX[8:8]	1
0xBB00BAD4	TRK_LOCAL_TBL [4].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BAD4	TRK_LOCAL_TBL [4].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BAD4	TRK_LOCAL_TBL [4].TRK_PORT7[5:0]	6
0xBB00BAD8	TRK_LOCAL_TBL [4].RESERVED[31:30]	2
0xBB00BAD8	TRK_LOCAL_TBL [4].TRK_PORT6[29:24]	6
0xBB00BAD8	TRK_LOCAL_TBL [4].TRK_PORT5[23:18]	6
0xBB00BAD8	TRK_LOCAL_TBL [4].TRK_PORT4[17:12]	6
0xBB00BAD8	TRK_LOCAL_TBL [4].TRK_PORT3[11:6]	6
0xBB00BAD8	TRK_LOCAL_TBL [4].TRK_PORT2[5:0]	6
0xBB00BADC	TRK_LOCAL_TBL [4].RESERVED[31:28]	4
0xBB00BADC	TRK_LOCAL_TBL [4].TRK_PORT1[27:22]	6
0xBB00BADC	TRK_LOCAL_TBL [4].TRK_PORT0[21:16]	6
0xBB00BADC	TRK_LOCAL_TBL [4].SEP_FLOOD_PORT[15:10]	6
0xBB00BADC	TRK_LOCAL_TBL [4].SEP_KWN_MC_PORT[9:4]	6
0xBB00BADC	TRK_LOCAL_TBL [4].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BAE0	TRK_LOCAL_TBL [4].RESERVED[31:30]	2
0xBB00BAE0	TRK_LOCAL_TBL [4].AVL_TRK_PORT7[29:24]	6
0xBB00BAE0	TRK_LOCAL_TBL [4].AVL_TRK_PORT6[23:18]	6
0xBB00BAE0	TRK_LOCAL_TBL [4].AVL_TRK_PORT5[17:12]	6
0xBB00BAE0	TRK_LOCAL_TBL [4].AVL_TRK_PORT4[11:6]	6
0xBB00BAE0	TRK_LOCAL_TBL [4].AVL_TRK_PORT3[5:0]	6
0xBB00BAE4	TRK_LOCAL_TBL [4].RESERVED[31:30]	2
0xBB00BAE4	TRK_LOCAL_TBL [4].AVL_TRK_PORT2[29:24]	6
0xBB00BAE4	TRK_LOCAL_TBL [4].AVL_TRK_PORT1[23:18]	6
0xBB00BAE4	TRK_LOCAL_TBL [4].AVL_TRK_PORT0[17:12]	6
0xBB00BAE4	TRK_LOCAL_TBL [4].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BAE4	TRK_LOCAL_TBL [4].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BAE8	TRK_LOCAL_TBL [5].RESERVED[31:13]	19
0xBB00BAE8	TRK_LOCAL_TBL [5].NUM_TX_CANDI[12:9]	4
0xBB00BAE8	TRK_LOCAL_TBL [5].L2_HASH_MSK_IDX[8:8]	1
0xBB00BAE8	TRK_LOCAL_TBL [5].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BAE8	TRK_LOCAL_TBL [5].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BAE8	TRK_LOCAL_TBL [5].TRK_PORT7[5:0]	6
0xBB00BAEC	TRK_LOCAL_TBL [5].RESERVED[31:30]	2
0xBB00BAEC	TRK_LOCAL_TBL [5].TRK_PORT6[29:24]	6
0xBB00BAEC	TRK_LOCAL_TBL [5].TRK_PORT5[23:18]	6
0xBB00BAEC	TRK_LOCAL_TBL [5].TRK_PORT4[17:12]	6
0xBB00BAEC	TRK_LOCAL_TBL [5].TRK_PORT3[11:6]	6
0xBB00BAEC	TRK_LOCAL_TBL [5].TRK_PORT2[5:0]	6
0xBB00BAF0	TRK_LOCAL_TBL [5].RESERVED[31:28]	4
0xBB00BAF0	TRK_LOCAL_TBL [5].TRK_PORT1[27:22]	6
0xBB00BAF0	TRK_LOCAL_TBL [5].TRK_PORT0[21:16]	6
0xBB00BAF0	TRK_LOCAL_TBL [5].SEP_FLOOD_PORT[15:10]	6
0xBB00BAF0	TRK_LOCAL_TBL [5].SEP_KWN_MC_PORT[9:4]	6
0xBB00BAF0	TRK_LOCAL_TBL [5].NUM_AVL_TX_CANDI[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BAF4	TRK_LOCAL_TBL [5].RESERVED[31:30]	2
0xBB00BAF4	TRK_LOCAL_TBL [5].AVL_TRK_PORT7[29:24]	6
0xBB00BAF4	TRK_LOCAL_TBL [5].AVL_TRK_PORT6[23:18]	6
0xBB00BAF4	TRK_LOCAL_TBL [5].AVL_TRK_PORT5[17:12]	6
0xBB00BAF4	TRK_LOCAL_TBL [5].AVL_TRK_PORT4[11:6]	6
0xBB00BAF4	TRK_LOCAL_TBL [5].AVL_TRK_PORT3[5:0]	6
0xBB00BAF8	TRK_LOCAL_TBL [5].RESERVED[31:30]	2
0xBB00BAF8	TRK_LOCAL_TBL [5].AVL_TRK_PORT2[29:24]	6
0xBB00BAF8	TRK_LOCAL_TBL [5].AVL_TRK_PORT1[23:18]	6
0xBB00BAF8	TRK_LOCAL_TBL [5].AVL_TRK_PORT0[17:12]	6
0xBB00BAF8	TRK_LOCAL_TBL [5].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BAF8	TRK_LOCAL_TBL [5].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BAFC	TRK_LOCAL_TBL [6].RESERVED[31:13]	19
0xBB00BAFC	TRK_LOCAL_TBL [6].NUM_TX_CANDI[12:9]	4
0xBB00BAFC	TRK_LOCAL_TBL [6].L2_HASH_MSK_IDX[8:8]	1
0xBB00BAFC	TRK_LOCAL_TBL [6].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BAFC	TRK_LOCAL_TBL [6].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BAFC	TRK_LOCAL_TBL [6].TRK_PORT7[5:0]	6
0xBB00BB00	TRK_LOCAL_TBL [6].RESERVED[31:30]	2
0xBB00BB00	TRK_LOCAL_TBL [6].TRK_PORT6[29:24]	6
0xBB00BB00	TRK_LOCAL_TBL [6].TRK_PORT5[23:18]	6
0xBB00BB00	TRK_LOCAL_TBL [6].TRK_PORT4[17:12]	6
0xBB00BB00	TRK_LOCAL_TBL [6].TRK_PORT3[11:6]	6
0xBB00BB00	TRK_LOCAL_TBL [6].TRK_PORT2[5:0]	6
0xBB00BB04	TRK_LOCAL_TBL [6].RESERVED[31:28]	4
0xBB00BB04	TRK_LOCAL_TBL [6].TRK_PORT1[27:22]	6
0xBB00BB04	TRK_LOCAL_TBL [6].TRK_PORT0[21:16]	6
0xBB00BB04	TRK_LOCAL_TBL [6].SEP_FLOOD_PORT[15:10]	6
0xBB00BB04	TRK_LOCAL_TBL [6].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB04	TRK_LOCAL_TBL [6].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB08	TRK_LOCAL_TBL [6].RESERVED[31:30]	2
0xBB00BB08	TRK_LOCAL_TBL [6].AVL_TRK_PORT7[29:24]	6
0xBB00BB08	TRK_LOCAL_TBL [6].AVL_TRK_PORT6[23:18]	6
0xBB00BB08	TRK_LOCAL_TBL [6].AVL_TRK_PORT5[17:12]	6
0xBB00BB08	TRK_LOCAL_TBL [6].AVL_TRK_PORT4[11:6]	6
0xBB00BB08	TRK_LOCAL_TBL [6].AVL_TRK_PORT3[5:0]	6
0xBB00BB0C	TRK_LOCAL_TBL [6].RESERVED[31:30]	2
0xBB00BB0C	TRK_LOCAL_TBL [6].AVL_TRK_PORT2[29:24]	6
0xBB00BB0C	TRK_LOCAL_TBL [6].AVL_TRK_PORT1[23:18]	6
0xBB00BB0C	TRK_LOCAL_TBL [6].AVL_TRK_PORT0[17:12]	6
0xBB00BB0C	TRK_LOCAL_TBL [6].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB0C	TRK_LOCAL_TBL [6].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB10	TRK_LOCAL_TBL [7].RESERVED[31:13]	19
0xBB00BB10	TRK_LOCAL_TBL [7].NUM_TX_CANDI[12:9]	4
0xBB00BB10	TRK_LOCAL_TBL [7].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB10	TRK_LOCAL_TBL [7].IP4_HASH_MSK_IDX[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BB10	TRK_LOCAL_TBL [7].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB10	TRK_LOCAL_TBL [7].TRK_PORT7[5:0]	6
0xBB00BB14	TRK_LOCAL_TBL [7].RESERVED[31:30]	2
0xBB00BB14	TRK_LOCAL_TBL [7].TRK_PORT6[29:24]	6
0xBB00BB14	TRK_LOCAL_TBL [7].TRK_PORT5[23:18]	6
0xBB00BB14	TRK_LOCAL_TBL [7].TRK_PORT4[17:12]	6
0xBB00BB14	TRK_LOCAL_TBL [7].TRK_PORT3[11:6]	6
0xBB00BB14	TRK_LOCAL_TBL [7].TRK_PORT2[5:0]	6
0xBB00BB18	TRK_LOCAL_TBL [7].RESERVED[31:28]	4
0xBB00BB18	TRK_LOCAL_TBL [7].TRK_PORT1[27:22]	6
0xBB00BB18	TRK_LOCAL_TBL [7].TRK_PORT0[21:16]	6
0xBB00BB18	TRK_LOCAL_TBL [7].SEP_FLOOD_PORT[15:10]	6
0xBB00BB18	TRK_LOCAL_TBL [7].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB18	TRK_LOCAL_TBL [7].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB1C	TRK_LOCAL_TBL [7].RESERVED[31:30]	2
0xBB00BB1C	TRK_LOCAL_TBL [7].AVL_TRK_PORT7[29:24]	6
0xBB00BB1C	TRK_LOCAL_TBL [7].AVL_TRK_PORT6[23:18]	6
0xBB00BB1C	TRK_LOCAL_TBL [7].AVL_TRK_PORT5[17:12]	6
0xBB00BB1C	TRK_LOCAL_TBL [7].AVL_TRK_PORT4[11:6]	6
0xBB00BB1C	TRK_LOCAL_TBL [7].AVL_TRK_PORT3[5:0]	6
0xBB00BB20	TRK_LOCAL_TBL [7].RESERVED[31:30]	2
0xBB00BB20	TRK_LOCAL_TBL [7].AVL_TRK_PORT2[29:24]	6
0xBB00BB20	TRK_LOCAL_TBL [7].AVL_TRK_PORT1[23:18]	6
0xBB00BB20	TRK_LOCAL_TBL [7].AVL_TRK_PORT0[17:12]	6
0xBB00BB20	TRK_LOCAL_TBL [7].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB20	TRK_LOCAL_TBL [7].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB24	TRK_LOCAL_TBL [8].RESERVED[31:13]	19
0xBB00BB24	TRK_LOCAL_TBL [8].NUM_TX_CANDI[12:9]	4
0xBB00BB24	TRK_LOCAL_TBL [8].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB24	TRK_LOCAL_TBL [8].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB24	TRK_LOCAL_TBL [8].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB24	TRK_LOCAL_TBL [8].TRK_PORT7[5:0]	6
0xBB00BB28	TRK_LOCAL_TBL [8].RESERVED[31:30]	2
0xBB00BB28	TRK_LOCAL_TBL [8].TRK_PORT6[29:24]	6
0xBB00BB28	TRK_LOCAL_TBL [8].TRK_PORT5[23:18]	6
0xBB00BB28	TRK_LOCAL_TBL [8].TRK_PORT4[17:12]	6
0xBB00BB28	TRK_LOCAL_TBL [8].TRK_PORT3[11:6]	6
0xBB00BB28	TRK_LOCAL_TBL [8].TRK_PORT2[5:0]	6
0xBB00BB2C	TRK_LOCAL_TBL [8].RESERVED[31:28]	4
0xBB00BB2C	TRK_LOCAL_TBL [8].TRK_PORT1[27:22]	6
0xBB00BB2C	TRK_LOCAL_TBL [8].TRK_PORT0[21:16]	6
0xBB00BB2C	TRK_LOCAL_TBL [8].SEP_FLOOD_PORT[15:10]	6
0xBB00BB2C	TRK_LOCAL_TBL [8].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB2C	TRK_LOCAL_TBL [8].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB30	TRK_LOCAL_TBL [8].RESERVED[31:30]	2
0xBB00BB30	TRK_LOCAL_TBL [8].AVL_TRK_PORT7[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BB30	TRK_LOCAL_TBL [8].AVL_TRK_PORT6[23:18]	6
0xBB00BB30	TRK_LOCAL_TBL [8].AVL_TRK_PORT5[17:12]	6
0xBB00BB30	TRK_LOCAL_TBL [8].AVL_TRK_PORT4[11:6]	6
0xBB00BB30	TRK_LOCAL_TBL [8].AVL_TRK_PORT3[5:0]	6
0xBB00BB34	TRK_LOCAL_TBL [8].RESERVED[31:30]	2
0xBB00BB34	TRK_LOCAL_TBL [8].AVL_TRK_PORT2[29:24]	6
0xBB00BB34	TRK_LOCAL_TBL [8].AVL_TRK_PORT1[23:18]	6
0xBB00BB34	TRK_LOCAL_TBL [8].AVL_TRK_PORT0[17:12]	6
0xBB00BB34	TRK_LOCAL_TBL [8].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB34	TRK_LOCAL_TBL [8].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB38	TRK_LOCAL_TBL [9].RESERVED[31:13]	19
0xBB00BB38	TRK_LOCAL_TBL [9].NUM_TX_CANDI[12:9]	4
0xBB00BB38	TRK_LOCAL_TBL [9].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB38	TRK_LOCAL_TBL [9].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB38	TRK_LOCAL_TBL [9].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB38	TRK_LOCAL_TBL [9].TRK_PORT7[5:0]	6
0xBB00BB3C	TRK_LOCAL_TBL [9].RESERVED[31:30]	2
0xBB00BB3C	TRK_LOCAL_TBL [9].TRK_PORT6[29:24]	6
0xBB00BB3C	TRK_LOCAL_TBL [9].TRK_PORT5[23:18]	6
0xBB00BB3C	TRK_LOCAL_TBL [9].TRK_PORT4[17:12]	6
0xBB00BB3C	TRK_LOCAL_TBL [9].TRK_PORT3[11:6]	6
0xBB00BB3C	TRK_LOCAL_TBL [9].TRK_PORT2[5:0]	6
0xBB00BB40	TRK_LOCAL_TBL [9].RESERVED[31:28]	4
0xBB00BB40	TRK_LOCAL_TBL [9].TRK_PORT1[27:22]	6
0xBB00BB40	TRK_LOCAL_TBL [9].TRK_PORT0[21:16]	6
0xBB00BB40	TRK_LOCAL_TBL [9].SEP_FLOOD_PORT[15:10]	6
0xBB00BB40	TRK_LOCAL_TBL [9].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB40	TRK_LOCAL_TBL [9].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB44	TRK_LOCAL_TBL [9].RESERVED[31:30]	2
0xBB00BB44	TRK_LOCAL_TBL [9].AVL_TRK_PORT7[29:24]	6
0xBB00BB44	TRK_LOCAL_TBL [9].AVL_TRK_PORT6[23:18]	6
0xBB00BB44	TRK_LOCAL_TBL [9].AVL_TRK_PORT5[17:12]	6
0xBB00BB44	TRK_LOCAL_TBL [9].AVL_TRK_PORT4[11:6]	6
0xBB00BB44	TRK_LOCAL_TBL [9].AVL_TRK_PORT3[5:0]	6
0xBB00BB48	TRK_LOCAL_TBL [9].RESERVED[31:30]	2
0xBB00BB48	TRK_LOCAL_TBL [9].AVL_TRK_PORT2[29:24]	6
0xBB00BB48	TRK_LOCAL_TBL [9].AVL_TRK_PORT1[23:18]	6
0xBB00BB48	TRK_LOCAL_TBL [9].AVL_TRK_PORT0[17:12]	6
0xBB00BB48	TRK_LOCAL_TBL [9].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB48	TRK_LOCAL_TBL [9].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB4C	TRK_LOCAL_TBL [10].RESERVED[31:13]	19
0xBB00BB4C	TRK_LOCAL_TBL [10].NUM_TX_CANDI[12:9]	4
0xBB00BB4C	TRK_LOCAL_TBL [10].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB4C	TRK_LOCAL_TBL [10].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB4C	TRK_LOCAL_TBL [10].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB4C	TRK_LOCAL_TBL [10].TRK_PORT7[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BB50	TRK_LOCAL_TBL [10].RESERVED[31:30]	2
0xBB00BB50	TRK_LOCAL_TBL [10].TRK_PORT6[29:24]	6
0xBB00BB50	TRK_LOCAL_TBL [10].TRK_PORT5[23:18]	6
0xBB00BB50	TRK_LOCAL_TBL [10].TRK_PORT4[17:12]	6
0xBB00BB50	TRK_LOCAL_TBL [10].TRK_PORT3[11:6]	6
0xBB00BB50	TRK_LOCAL_TBL [10].TRK_PORT2[5:0]	6
0xBB00BB54	TRK_LOCAL_TBL [10].RESERVED[31:28]	4
0xBB00BB54	TRK_LOCAL_TBL [10].TRK_PORT1[27:22]	6
0xBB00BB54	TRK_LOCAL_TBL [10].TRK_PORT0[21:16]	6
0xBB00BB54	TRK_LOCAL_TBL [10].SEP_FLOOD_PORT[15:10]	6
0xBB00BB54	TRK_LOCAL_TBL [10].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB54	TRK_LOCAL_TBL [10].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB58	TRK_LOCAL_TBL [10].RESERVED[31:30]	2
0xBB00BB58	TRK_LOCAL_TBL [10].AVL_TRK_PORT7[29:24]	6
0xBB00BB58	TRK_LOCAL_TBL [10].AVL_TRK_PORT6[23:18]	6
0xBB00BB58	TRK_LOCAL_TBL [10].AVL_TRK_PORT5[17:12]	6
0xBB00BB58	TRK_LOCAL_TBL [10].AVL_TRK_PORT4[11:6]	6
0xBB00BB58	TRK_LOCAL_TBL [10].AVL_TRK_PORT3[5:0]	6
0xBB00BB5C	TRK_LOCAL_TBL [10].RESERVED[31:30]	2
0xBB00BB5C	TRK_LOCAL_TBL [10].AVL_TRK_PORT2[29:24]	6
0xBB00BB5C	TRK_LOCAL_TBL [10].AVL_TRK_PORT1[23:18]	6
0xBB00BB5C	TRK_LOCAL_TBL [10].AVL_TRK_PORT0[17:12]	6
0xBB00BB5C	TRK_LOCAL_TBL [10].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB5C	TRK_LOCAL_TBL [10].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB60	TRK_LOCAL_TBL [11].RESERVED[31:13]	19
0xBB00BB60	TRK_LOCAL_TBL [11].NUM_TX_CANDI[12:9]	4
0xBB00BB60	TRK_LOCAL_TBL [11].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB60	TRK_LOCAL_TBL [11].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB60	TRK_LOCAL_TBL [11].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB60	TRK_LOCAL_TBL [11].TRK_PORT7[5:0]	6
0xBB00BB64	TRK_LOCAL_TBL [11].RESERVED[31:30]	2
0xBB00BB64	TRK_LOCAL_TBL [11].TRK_PORT6[29:24]	6
0xBB00BB64	TRK_LOCAL_TBL [11].TRK_PORT5[23:18]	6
0xBB00BB64	TRK_LOCAL_TBL [11].TRK_PORT4[17:12]	6
0xBB00BB64	TRK_LOCAL_TBL [11].TRK_PORT3[11:6]	6
0xBB00BB64	TRK_LOCAL_TBL [11].TRK_PORT2[5:0]	6
0xBB00BB68	TRK_LOCAL_TBL [11].RESERVED[31:28]	4
0xBB00BB68	TRK_LOCAL_TBL [11].TRK_PORT1[27:22]	6
0xBB00BB68	TRK_LOCAL_TBL [11].TRK_PORT0[21:16]	6
0xBB00BB68	TRK_LOCAL_TBL [11].SEP_FLOOD_PORT[15:10]	6
0xBB00BB68	TRK_LOCAL_TBL [11].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB68	TRK_LOCAL_TBL [11].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB6C	TRK_LOCAL_TBL [11].RESERVED[31:30]	2
0xBB00BB6C	TRK_LOCAL_TBL [11].AVL_TRK_PORT7[29:24]	6
0xBB00BB6C	TRK_LOCAL_TBL [11].AVL_TRK_PORT6[23:18]	6
0xBB00BB6C	TRK_LOCAL_TBL [11].AVL_TRK_PORT5[17:12]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BB6C	TRK_LOCAL_TBL [11].AVL_TRK_PORT4[11:6]	6
0xBB00BB6C	TRK_LOCAL_TBL [11].AVL_TRK_PORT3[5:0]	6
0xBB00BB70	TRK_LOCAL_TBL [11].RESERVED[31:30]	2
0xBB00BB70	TRK_LOCAL_TBL [11].AVL_TRK_PORT2[29:24]	6
0xBB00BB70	TRK_LOCAL_TBL [11].AVL_TRK_PORT1[23:18]	6
0xBB00BB70	TRK_LOCAL_TBL [11].AVL_TRK_PORT0[17:12]	6
0xBB00BB70	TRK_LOCAL_TBL [11].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB70	TRK_LOCAL_TBL [11].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB74	TRK_LOCAL_TBL [12].RESERVED[31:13]	19
0xBB00BB74	TRK_LOCAL_TBL [12].NUM_TX_CANDI[12:9]	4
0xBB00BB74	TRK_LOCAL_TBL [12].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB74	TRK_LOCAL_TBL [12].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB74	TRK_LOCAL_TBL [12].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB74	TRK_LOCAL_TBL [12].TRK_PORT7[5:0]	6
0xBB00BB78	TRK_LOCAL_TBL [12].RESERVED[31:30]	2
0xBB00BB78	TRK_LOCAL_TBL [12].TRK_PORT6[29:24]	6
0xBB00BB78	TRK_LOCAL_TBL [12].TRK_PORT5[23:18]	6
0xBB00BB78	TRK_LOCAL_TBL [12].TRK_PORT4[17:12]	6
0xBB00BB78	TRK_LOCAL_TBL [12].TRK_PORT3[11:6]	6
0xBB00BB78	TRK_LOCAL_TBL [12].TRK_PORT2[5:0]	6
0xBB00BB7C	TRK_LOCAL_TBL [12].RESERVED[31:28]	4
0xBB00BB7C	TRK_LOCAL_TBL [12].TRK_PORT1[27:22]	6
0xBB00BB7C	TRK_LOCAL_TBL [12].TRK_PORT0[21:16]	6
0xBB00BB7C	TRK_LOCAL_TBL [12].SEP_FLOOD_PORT[15:10]	6
0xBB00BB7C	TRK_LOCAL_TBL [12].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB7C	TRK_LOCAL_TBL [12].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB80	TRK_LOCAL_TBL [12].RESERVED[31:30]	2
0xBB00BB80	TRK_LOCAL_TBL [12].AVL_TRK_PORT7[29:24]	6
0xBB00BB80	TRK_LOCAL_TBL [12].AVL_TRK_PORT6[23:18]	6
0xBB00BB80	TRK_LOCAL_TBL [12].AVL_TRK_PORT5[17:12]	6
0xBB00BB80	TRK_LOCAL_TBL [12].AVL_TRK_PORT4[11:6]	6
0xBB00BB80	TRK_LOCAL_TBL [12].AVL_TRK_PORT3[5:0]	6
0xBB00BB84	TRK_LOCAL_TBL [12].RESERVED[31:30]	2
0xBB00BB84	TRK_LOCAL_TBL [12].AVL_TRK_PORT2[29:24]	6
0xBB00BB84	TRK_LOCAL_TBL [12].AVL_TRK_PORT1[23:18]	6
0xBB00BB84	TRK_LOCAL_TBL [12].AVL_TRK_PORT0[17:12]	6
0xBB00BB84	TRK_LOCAL_TBL [12].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB84	TRK_LOCAL_TBL [12].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB88	TRK_LOCAL_TBL [13].RESERVED[31:13]	19
0xBB00BB88	TRK_LOCAL_TBL [13].NUM_TX_CANDI[12:9]	4
0xBB00BB88	TRK_LOCAL_TBL [13].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB88	TRK_LOCAL_TBL [13].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB88	TRK_LOCAL_TBL [13].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB88	TRK_LOCAL_TBL [13].TRK_PORT7[5:0]	6
0xBB00BB8C	TRK_LOCAL_TBL [13].RESERVED[31:30]	2
0xBB00BB8C	TRK_LOCAL_TBL [13].TRK_PORT6[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BB8C	TRK_LOCAL_TBL [13].TRK_PORT5[23:18]	6
0xBB00BB8C	TRK_LOCAL_TBL [13].TRK_PORT4[17:12]	6
0xBB00BB8C	TRK_LOCAL_TBL [13].TRK_PORT3[11:6]	6
0xBB00BB8C	TRK_LOCAL_TBL [13].TRK_PORT2[5:0]	6
0xBB00BB90	TRK_LOCAL_TBL [13].RESERVED[31:28]	4
0xBB00BB90	TRK_LOCAL_TBL [13].TRK_PORT1[27:22]	6
0xBB00BB90	TRK_LOCAL_TBL [13].TRK_PORT0[21:16]	6
0xBB00BB90	TRK_LOCAL_TBL [13].SEP_FLOOD_PORT[15:10]	6
0xBB00BB90	TRK_LOCAL_TBL [13].SEP_KWN_MC_PORT[9:4]	6
0xBB00BB90	TRK_LOCAL_TBL [13].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BB94	TRK_LOCAL_TBL [13].RESERVED[31:30]	2
0xBB00BB94	TRK_LOCAL_TBL [13].AVL_TRK_PORT7[29:24]	6
0xBB00BB94	TRK_LOCAL_TBL [13].AVL_TRK_PORT6[23:18]	6
0xBB00BB94	TRK_LOCAL_TBL [13].AVL_TRK_PORT5[17:12]	6
0xBB00BB94	TRK_LOCAL_TBL [13].AVL_TRK_PORT4[11:6]	6
0xBB00BB94	TRK_LOCAL_TBL [13].AVL_TRK_PORT3[5:0]	6
0xBB00BB98	TRK_LOCAL_TBL [13].RESERVED[31:30]	2
0xBB00BB98	TRK_LOCAL_TBL [13].AVL_TRK_PORT2[29:24]	6
0xBB00BB98	TRK_LOCAL_TBL [13].AVL_TRK_PORT1[23:18]	6
0xBB00BB98	TRK_LOCAL_TBL [13].AVL_TRK_PORT0[17:12]	6
0xBB00BB98	TRK_LOCAL_TBL [13].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BB98	TRK_LOCAL_TBL [13].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BB9C	TRK_LOCAL_TBL [14].RESERVED[31:13]	19
0xBB00BB9C	TRK_LOCAL_TBL [14].NUM_TX_CANDI[12:9]	4
0xBB00BB9C	TRK_LOCAL_TBL [14].L2_HASH_MSK_IDX[8:8]	1
0xBB00BB9C	TRK_LOCAL_TBL [14].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BB9C	TRK_LOCAL_TBL [14].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BB9C	TRK_LOCAL_TBL [14].TRK_PORT7[5:0]	6
0xBB00BBA0	TRK_LOCAL_TBL [14].RESERVED[31:30]	2
0xBB00BBA0	TRK_LOCAL_TBL [14].TRK_PORT6[29:24]	6
0xBB00BBA0	TRK_LOCAL_TBL [14].TRK_PORT5[23:18]	6
0xBB00BBA0	TRK_LOCAL_TBL [14].TRK_PORT4[17:12]	6
0xBB00BBA0	TRK_LOCAL_TBL [14].TRK_PORT3[11:6]	6
0xBB00BBA0	TRK_LOCAL_TBL [14].TRK_PORT2[5:0]	6
0xBB00BBA4	TRK_LOCAL_TBL [14].RESERVED[31:28]	4
0xBB00BBA4	TRK_LOCAL_TBL [14].TRK_PORT1[27:22]	6
0xBB00BBA4	TRK_LOCAL_TBL [14].TRK_PORT0[21:16]	6
0xBB00BBA4	TRK_LOCAL_TBL [14].SEP_FLOOD_PORT[15:10]	6
0xBB00BBA4	TRK_LOCAL_TBL [14].SEP_KWN_MC_PORT[9:4]	6
0xBB00BBA4	TRK_LOCAL_TBL [14].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BBA8	TRK_LOCAL_TBL [14].RESERVED[31:30]	2
0xBB00BBA8	TRK_LOCAL_TBL [14].AVL_TRK_PORT7[29:24]	6
0xBB00BBA8	TRK_LOCAL_TBL [14].AVL_TRK_PORT6[23:18]	6
0xBB00BBA8	TRK_LOCAL_TBL [14].AVL_TRK_PORT5[17:12]	6
0xBB00BBA8	TRK_LOCAL_TBL [14].AVL_TRK_PORT4[11:6]	6
0xBB00BBA8	TRK_LOCAL_TBL [14].AVL_TRK_PORT3[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BBAC	TRK_LOCAL_TBL [14].RESERVED[31:30]	2
0xBB00BBAC	TRK_LOCAL_TBL [14].AVL_TRK_PORT2[29:24]	6
0xBB00BBAC	TRK_LOCAL_TBL [14].AVL_TRK_PORT1[23:18]	6
0xBB00BBAC	TRK_LOCAL_TBL [14].AVL_TRK_PORT0[17:12]	6
0xBB00BBAC	TRK_LOCAL_TBL [14].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BBAC	TRK_LOCAL_TBL [14].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BBB0	TRK_LOCAL_TBL [15].RESERVED[31:13]	19
0xBB00BBB0	TRK_LOCAL_TBL [15].NUM_TX_CANDI[12:9]	4
0xBB00BBB0	TRK_LOCAL_TBL [15].L2_HASH_MSK_IDX[8:8]	1
0xBB00BBB0	TRK_LOCAL_TBL [15].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BBB0	TRK_LOCAL_TBL [15].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BBB0	TRK_LOCAL_TBL [15].TRK_PORT7[5:0]	6
0xBB00BBB4	TRK_LOCAL_TBL [15].RESERVED[31:30]	2
0xBB00BBB4	TRK_LOCAL_TBL [15].TRK_PORT6[29:24]	6
0xBB00BBB4	TRK_LOCAL_TBL [15].TRK_PORT5[23:18]	6
0xBB00BBB4	TRK_LOCAL_TBL [15].TRK_PORT4[17:12]	6
0xBB00BBB4	TRK_LOCAL_TBL [15].TRK_PORT3[11:6]	6
0xBB00BBB4	TRK_LOCAL_TBL [15].TRK_PORT2[5:0]	6
0xBB00BBB8	TRK_LOCAL_TBL [15].RESERVED[31:28]	4
0xBB00BBB8	TRK_LOCAL_TBL [15].TRK_PORT1[27:22]	6
0xBB00BBB8	TRK_LOCAL_TBL [15].TRK_PORT0[21:16]	6
0xBB00BBB8	TRK_LOCAL_TBL [15].SEP_FLOOD_PORT[15:10]	6
0xBB00BBB8	TRK_LOCAL_TBL [15].SEP_KWN_MC_PORT[9:4]	6
0xBB00BBB8	TRK_LOCAL_TBL [15].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BBC0	TRK_LOCAL_TBL [15].RESERVED[31:30]	2
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT7[29:24]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT6[23:18]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT5[17:12]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT4[11:6]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT3[5:0]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].RESERVED[31:30]	2
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT2[29:24]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT1[23:18]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_TRK_PORT0[17:12]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BBC0	TRK_LOCAL_TBL [15].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BBC4	TRK_LOCAL_TBL [16].RESERVED[31:13]	19
0xBB00BBC4	TRK_LOCAL_TBL [16].NUM_TX_CANDI[12:9]	4
0xBB00BBC4	TRK_LOCAL_TBL [16].L2_HASH_MSK_IDX[8:8]	1
0xBB00BBC4	TRK_LOCAL_TBL [16].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BBC4	TRK_LOCAL_TBL [16].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BBC4	TRK_LOCAL_TBL [16].TRK_PORT7[5:0]	6
0xBB00BBC8	TRK_LOCAL_TBL [16].RESERVED[31:30]	2
0xBB00BBC8	TRK_LOCAL_TBL [16].TRK_PORT6[29:24]	6
0xBB00BBC8	TRK_LOCAL_TBL [16].TRK_PORT5[23:18]	6
0xBB00BBC8	TRK_LOCAL_TBL [16].TRK_PORT4[17:12]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BBC8	TRK_LOCAL_TBL [16].TRK_PORT3[11:6]	6
0xBB00BBC8	TRK_LOCAL_TBL [16].TRK_PORT2[5:0]	6
0xBB00BBCC	TRK_LOCAL_TBL [16].RESERVED[31:28]	4
0xBB00BBCC	TRK_LOCAL_TBL [16].TRK_PORT1[27:22]	6
0xBB00BBCC	TRK_LOCAL_TBL [16].TRK_PORT0[21:16]	6
0xBB00BBCC	TRK_LOCAL_TBL [16].SEP_FLOOD_PORT[15:10]	6
0xBB00BBCC	TRK_LOCAL_TBL [16].SEP_KWN_MC_PORT[9:4]	6
0xBB00BBCC	TRK_LOCAL_TBL [16].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BBD0	TRK_LOCAL_TBL [16].RESERVED[31:30]	2
0xBB00BBD0	TRK_LOCAL_TBL [16].AVL_TRK_PORT7[29:24]	6
0xBB00BBD0	TRK_LOCAL_TBL [16].AVL_TRK_PORT6[23:18]	6
0xBB00BBD0	TRK_LOCAL_TBL [16].AVL_TRK_PORT5[17:12]	6
0xBB00BBD0	TRK_LOCAL_TBL [16].AVL_TRK_PORT4[11:6]	6
0xBB00BBD0	TRK_LOCAL_TBL [16].AVL_TRK_PORT3[5:0]	6
0xBB00BBD4	TRK_LOCAL_TBL [16].RESERVED[31:30]	2
0xBB00BBD4	TRK_LOCAL_TBL [16].AVL_TRK_PORT2[29:24]	6
0xBB00BBD4	TRK_LOCAL_TBL [16].AVL_TRK_PORT1[23:18]	6
0xBB00BBD4	TRK_LOCAL_TBL [16].AVL_TRK_PORT0[17:12]	6
0xBB00BBD4	TRK_LOCAL_TBL [16].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BBD4	TRK_LOCAL_TBL [16].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BBD8	TRK_LOCAL_TBL [17].RESERVED[31:13]	19
0xBB00BBD8	TRK_LOCAL_TBL [17].NUM_TX_CANDI[12:9]	4
0xBB00BBD8	TRK_LOCAL_TBL [17].L2_HASH_MSK_IDX[8:8]	1
0xBB00BBD8	TRK_LOCAL_TBL [17].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BBD8	TRK_LOCAL_TBL [17].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BBD8	TRK_LOCAL_TBL [17].TRK_PORT7[5:0]	6
0xBB00BBDC	TRK_LOCAL_TBL [17].RESERVED[31:30]	2
0xBB00BBDC	TRK_LOCAL_TBL [17].TRK_PORT6[29:24]	6
0xBB00BBDC	TRK_LOCAL_TBL [17].TRK_PORT5[23:18]	6
0xBB00BBDC	TRK_LOCAL_TBL [17].TRK_PORT4[17:12]	6
0xBB00BBDC	TRK_LOCAL_TBL [17].TRK_PORT3[11:6]	6
0xBB00BBDC	TRK_LOCAL_TBL [17].TRK_PORT2[5:0]	6
0xBB00BBE0	TRK_LOCAL_TBL [17].RESERVED[31:28]	4
0xBB00BBE0	TRK_LOCAL_TBL [17].TRK_PORT1[27:22]	6
0xBB00BBE0	TRK_LOCAL_TBL [17].TRK_PORT0[21:16]	6
0xBB00BBE0	TRK_LOCAL_TBL [17].SEP_FLOOD_PORT[15:10]	6
0xBB00BBE0	TRK_LOCAL_TBL [17].SEP_KWN_MC_PORT[9:4]	6
0xBB00BBE0	TRK_LOCAL_TBL [17].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BBE4	TRK_LOCAL_TBL [17].RESERVED[31:30]	2
0xBB00BBE4	TRK_LOCAL_TBL [17].AVL_TRK_PORT7[29:24]	6
0xBB00BBE4	TRK_LOCAL_TBL [17].AVL_TRK_PORT6[23:18]	6
0xBB00BBE4	TRK_LOCAL_TBL [17].AVL_TRK_PORT5[17:12]	6
0xBB00BBE4	TRK_LOCAL_TBL [17].AVL_TRK_PORT4[11:6]	6
0xBB00BBE4	TRK_LOCAL_TBL [17].AVL_TRK_PORT3[5:0]	6
0xBB00BBE8	TRK_LOCAL_TBL [17].RESERVED[31:30]	2
0xBB00BBE8	TRK_LOCAL_TBL [17].AVL_TRK_PORT2[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BBE8	TRK_LOCAL_TBL [17].AVL_TRK_PORT1[23:18]	6
0xBB00BBE8	TRK_LOCAL_TBL [17].AVL_TRK_PORT0[17:12]	6
0xBB00BBE8	TRK_LOCAL_TBL [17].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BBE8	TRK_LOCAL_TBL [17].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BBEC	TRK_LOCAL_TBL [18].RESERVED[31:13]	19
0xBB00BBEC	TRK_LOCAL_TBL [18].NUM_TX_CANDI[12:9]	4
0xBB00BBEC	TRK_LOCAL_TBL [18].L2_HASH_MSK_IDX[8:8]	1
0xBB00BBEC	TRK_LOCAL_TBL [18].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BBEC	TRK_LOCAL_TBL [18].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BBEC	TRK_LOCAL_TBL [18].TRK_PORT7[5:0]	6
0xBB00BBF0	TRK_LOCAL_TBL [18].RESERVED[31:30]	2
0xBB00BBF0	TRK_LOCAL_TBL [18].TRK_PORT6[29:24]	6
0xBB00BBF0	TRK_LOCAL_TBL [18].TRK_PORT5[23:18]	6
0xBB00BBF0	TRK_LOCAL_TBL [18].TRK_PORT4[17:12]	6
0xBB00BBF0	TRK_LOCAL_TBL [18].TRK_PORT3[11:6]	6
0xBB00BBF0	TRK_LOCAL_TBL [18].TRK_PORT2[5:0]	6
0xBB00BBF4	TRK_LOCAL_TBL [18].RESERVED[31:28]	4
0xBB00BBF4	TRK_LOCAL_TBL [18].TRK_PORT1[27:22]	6
0xBB00BBF4	TRK_LOCAL_TBL [18].TRK_PORT0[21:16]	6
0xBB00BBF4	TRK_LOCAL_TBL [18].SEP_FLOOD_PORT[15:10]	6
0xBB00BBF4	TRK_LOCAL_TBL [18].SEP_KWN_MC_PORT[9:4]	6
0xBB00BBF4	TRK_LOCAL_TBL [18].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BBF8	TRK_LOCAL_TBL [18].RESERVED[31:30]	2
0xBB00BBF8	TRK_LOCAL_TBL [18].AVL_TRK_PORT7[29:24]	6
0xBB00BBF8	TRK_LOCAL_TBL [18].AVL_TRK_PORT6[23:18]	6
0xBB00BBF8	TRK_LOCAL_TBL [18].AVL_TRK_PORT5[17:12]	6
0xBB00BBF8	TRK_LOCAL_TBL [18].AVL_TRK_PORT4[11:6]	6
0xBB00BBF8	TRK_LOCAL_TBL [18].AVL_TRK_PORT3[5:0]	6
0xBB00BBFC	TRK_LOCAL_TBL [18].RESERVED[31:30]	2
0xBB00BBFC	TRK_LOCAL_TBL [18].AVL_TRK_PORT2[29:24]	6
0xBB00BBFC	TRK_LOCAL_TBL [18].AVL_TRK_PORT1[23:18]	6
0xBB00BBFC	TRK_LOCAL_TBL [18].AVL_TRK_PORT0[17:12]	6
0xBB00BBFC	TRK_LOCAL_TBL [18].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BBFC	TRK_LOCAL_TBL [18].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC00	TRK_LOCAL_TBL [19].RESERVED[31:13]	19
0xBB00BC00	TRK_LOCAL_TBL [19].NUM_TX_CANDI[12:9]	4
0xBB00BC00	TRK_LOCAL_TBL [19].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC00	TRK_LOCAL_TBL [19].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC00	TRK_LOCAL_TBL [19].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC00	TRK_LOCAL_TBL [19].TRK_PORT7[5:0]	6
0xBB00BC04	TRK_LOCAL_TBL [19].RESERVED[31:30]	2
0xBB00BC04	TRK_LOCAL_TBL [19].TRK_PORT6[29:24]	6
0xBB00BC04	TRK_LOCAL_TBL [19].TRK_PORT5[23:18]	6
0xBB00BC04	TRK_LOCAL_TBL [19].TRK_PORT4[17:12]	6
0xBB00BC04	TRK_LOCAL_TBL [19].TRK_PORT3[11:6]	6
0xBB00BC04	TRK_LOCAL_TBL [19].TRK_PORT2[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BC08	TRK_LOCAL_TBL [19].RESERVED[31:28]	4
0xBB00BC08	TRK_LOCAL_TBL [19].TRK_PORT1[27:22]	6
0xBB00BC08	TRK_LOCAL_TBL [19].TRK_PORT0[21:16]	6
0xBB00BC08	TRK_LOCAL_TBL [19].SEP_FLOOD_PORT[15:10]	6
0xBB00BC08	TRK_LOCAL_TBL [19].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC08	TRK_LOCAL_TBL [19].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC0C	TRK_LOCAL_TBL [19].RESERVED[31:30]	2
0xBB00BC0C	TRK_LOCAL_TBL [19].AVL_TRK_PORT7[29:24]	6
0xBB00BC0C	TRK_LOCAL_TBL [19].AVL_TRK_PORT6[23:18]	6
0xBB00BC0C	TRK_LOCAL_TBL [19].AVL_TRK_PORT5[17:12]	6
0xBB00BC0C	TRK_LOCAL_TBL [19].AVL_TRK_PORT4[11:6]	6
0xBB00BC0C	TRK_LOCAL_TBL [19].AVL_TRK_PORT3[5:0]	6
0xBB00BC10	TRK_LOCAL_TBL [19].RESERVED[31:30]	2
0xBB00BC10	TRK_LOCAL_TBL [19].AVL_TRK_PORT2[29:24]	6
0xBB00BC10	TRK_LOCAL_TBL [19].AVL_TRK_PORT1[23:18]	6
0xBB00BC10	TRK_LOCAL_TBL [19].AVL_TRK_PORT0[17:12]	6
0xBB00BC10	TRK_LOCAL_TBL [19].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC10	TRK_LOCAL_TBL [19].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC14	TRK_LOCAL_TBL [20].RESERVED[31:13]	19
0xBB00BC14	TRK_LOCAL_TBL [20].NUM_TX_CANDI[12:9]	4
0xBB00BC14	TRK_LOCAL_TBL [20].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC14	TRK_LOCAL_TBL [20].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC14	TRK_LOCAL_TBL [20].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC14	TRK_LOCAL_TBL [20].TRK_PORT7[5:0]	6
0xBB00BC18	TRK_LOCAL_TBL [20].RESERVED[31:30]	2
0xBB00BC18	TRK_LOCAL_TBL [20].TRK_PORT6[29:24]	6
0xBB00BC18	TRK_LOCAL_TBL [20].TRK_PORT5[23:18]	6
0xBB00BC18	TRK_LOCAL_TBL [20].TRK_PORT4[17:12]	6
0xBB00BC18	TRK_LOCAL_TBL [20].TRK_PORT3[11:6]	6
0xBB00BC18	TRK_LOCAL_TBL [20].TRK_PORT2[5:0]	6
0xBB00BC1C	TRK_LOCAL_TBL [20].RESERVED[31:28]	4
0xBB00BC1C	TRK_LOCAL_TBL [20].TRK_PORT1[27:22]	6
0xBB00BC1C	TRK_LOCAL_TBL [20].TRK_PORT0[21:16]	6
0xBB00BC1C	TRK_LOCAL_TBL [20].SEP_FLOOD_PORT[15:10]	6
0xBB00BC1C	TRK_LOCAL_TBL [20].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC1C	TRK_LOCAL_TBL [20].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC20	TRK_LOCAL_TBL [20].RESERVED[31:30]	2
0xBB00BC20	TRK_LOCAL_TBL [20].AVL_TRK_PORT7[29:24]	6
0xBB00BC20	TRK_LOCAL_TBL [20].AVL_TRK_PORT6[23:18]	6
0xBB00BC20	TRK_LOCAL_TBL [20].AVL_TRK_PORT5[17:12]	6
0xBB00BC20	TRK_LOCAL_TBL [20].AVL_TRK_PORT4[11:6]	6
0xBB00BC20	TRK_LOCAL_TBL [20].AVL_TRK_PORT3[5:0]	6
0xBB00BC24	TRK_LOCAL_TBL [20].RESERVED[31:30]	2
0xBB00BC24	TRK_LOCAL_TBL [20].AVL_TRK_PORT2[29:24]	6
0xBB00BC24	TRK_LOCAL_TBL [20].AVL_TRK_PORT1[23:18]	6
0xBB00BC24	TRK_LOCAL_TBL [20].AVL_TRK_PORT0[17:12]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BC24	TRK_LOCAL_TBL [20].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC24	TRK_LOCAL_TBL [20].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC28	TRK_LOCAL_TBL [21].RESERVED[31:13]	19
0xBB00BC28	TRK_LOCAL_TBL [21].NUM_TX_CANDI[12:9]	4
0xBB00BC28	TRK_LOCAL_TBL [21].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC28	TRK_LOCAL_TBL [21].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC28	TRK_LOCAL_TBL [21].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC28	TRK_LOCAL_TBL [21].TRK_PORT7[5:0]	6
0xBB00BC2C	TRK_LOCAL_TBL [21].RESERVED[31:30]	2
0xBB00BC2C	TRK_LOCAL_TBL [21].TRK_PORT6[29:24]	6
0xBB00BC2C	TRK_LOCAL_TBL [21].TRK_PORT5[23:18]	6
0xBB00BC2C	TRK_LOCAL_TBL [21].TRK_PORT4[17:12]	6
0xBB00BC2C	TRK_LOCAL_TBL [21].TRK_PORT3[11:6]	6
0xBB00BC2C	TRK_LOCAL_TBL [21].TRK_PORT2[5:0]	6
0xBB00BC30	TRK_LOCAL_TBL [21].RESERVED[31:28]	4
0xBB00BC30	TRK_LOCAL_TBL [21].TRK_PORT1[27:22]	6
0xBB00BC30	TRK_LOCAL_TBL [21].TRK_PORT0[21:16]	6
0xBB00BC30	TRK_LOCAL_TBL [21].SEP_FLOOD_PORT[15:10]	6
0xBB00BC30	TRK_LOCAL_TBL [21].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC30	TRK_LOCAL_TBL [21].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC34	TRK_LOCAL_TBL [21].RESERVED[31:30]	2
0xBB00BC34	TRK_LOCAL_TBL [21].AVL_TRK_PORT7[29:24]	6
0xBB00BC34	TRK_LOCAL_TBL [21].AVL_TRK_PORT6[23:18]	6
0xBB00BC34	TRK_LOCAL_TBL [21].AVL_TRK_PORT5[17:12]	6
0xBB00BC34	TRK_LOCAL_TBL [21].AVL_TRK_PORT4[11:6]	6
0xBB00BC34	TRK_LOCAL_TBL [21].AVL_TRK_PORT3[5:0]	6
0xBB00BC38	TRK_LOCAL_TBL [21].RESERVED[31:30]	2
0xBB00BC38	TRK_LOCAL_TBL [21].AVL_TRK_PORT2[29:24]	6
0xBB00BC38	TRK_LOCAL_TBL [21].AVL_TRK_PORT1[23:18]	6
0xBB00BC38	TRK_LOCAL_TBL [21].AVL_TRK_PORT0[17:12]	6
0xBB00BC38	TRK_LOCAL_TBL [21].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC38	TRK_LOCAL_TBL [21].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC3C	TRK_LOCAL_TBL [22].RESERVED[31:13]	19
0xBB00BC3C	TRK_LOCAL_TBL [22].NUM_TX_CANDI[12:9]	4
0xBB00BC3C	TRK_LOCAL_TBL [22].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC3C	TRK_LOCAL_TBL [22].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC3C	TRK_LOCAL_TBL [22].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC3C	TRK_LOCAL_TBL [22].TRK_PORT7[5:0]	6
0xBB00BC40	TRK_LOCAL_TBL [22].RESERVED[31:30]	2
0xBB00BC40	TRK_LOCAL_TBL [22].TRK_PORT6[29:24]	6
0xBB00BC40	TRK_LOCAL_TBL [22].TRK_PORT5[23:18]	6
0xBB00BC40	TRK_LOCAL_TBL [22].TRK_PORT4[17:12]	6
0xBB00BC40	TRK_LOCAL_TBL [22].TRK_PORT3[11:6]	6
0xBB00BC40	TRK_LOCAL_TBL [22].TRK_PORT2[5:0]	6
0xBB00BC44	TRK_LOCAL_TBL [22].RESERVED[31:28]	4
0xBB00BC44	TRK_LOCAL_TBL [22].TRK_PORT1[27:22]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BC44	TRK_LOCAL_TBL [22].TRK_PORT0[21:16]	6
0xBB00BC44	TRK_LOCAL_TBL [22].SEP_FLOOD_PORT[15:10]	6
0xBB00BC44	TRK_LOCAL_TBL [22].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC44	TRK_LOCAL_TBL [22].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC48	TRK_LOCAL_TBL [22].RESERVED[31:30]	2
0xBB00BC48	TRK_LOCAL_TBL [22].AVL_TRK_PORT7[29:24]	6
0xBB00BC48	TRK_LOCAL_TBL [22].AVL_TRK_PORT6[23:18]	6
0xBB00BC48	TRK_LOCAL_TBL [22].AVL_TRK_PORTS[17:12]	6
0xBB00BC48	TRK_LOCAL_TBL [22].AVL_TRK_PORT4[11:6]	6
0xBB00BC48	TRK_LOCAL_TBL [22].AVL_TRK_PORT3[5:0]	6
0xBB00BC4C	TRK_LOCAL_TBL [22].RESERVED[31:30]	2
0xBB00BC4C	TRK_LOCAL_TBL [22].AVL_TRK_PORT2[29:24]	6
0xBB00BC4C	TRK_LOCAL_TBL [22].AVL_TRK_PORT1[23:18]	6
0xBB00BC4C	TRK_LOCAL_TBL [22].AVL_TRK_PORT0[17:12]	6
0xBB00BC4C	TRK_LOCAL_TBL [22].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC4C	TRK_LOCAL_TBL [22].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC50	TRK_LOCAL_TBL [23].RESERVED[31:13]	19
0xBB00BC50	TRK_LOCAL_TBL [23].NUM_TX_CANDI[12:9]	4
0xBB00BC50	TRK_LOCAL_TBL [23].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC50	TRK_LOCAL_TBL [23].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC50	TRK_LOCAL_TBL [23].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC50	TRK_LOCAL_TBL [23].TRK_PORT7[5:0]	6
0xBB00BC54	TRK_LOCAL_TBL [23].RESERVED[31:30]	2
0xBB00BC54	TRK_LOCAL_TBL [23].TRK_PORT6[29:24]	6
0xBB00BC54	TRK_LOCAL_TBL [23].TRK_PORT5[23:18]	6
0xBB00BC54	TRK_LOCAL_TBL [23].TRK_PORT4[17:12]	6
0xBB00BC54	TRK_LOCAL_TBL [23].TRK_PORT3[11:6]	6
0xBB00BC54	TRK_LOCAL_TBL [23].TRK_PORT2[5:0]	6
0xBB00BC58	TRK_LOCAL_TBL [23].RESERVED[31:28]	4
0xBB00BC58	TRK_LOCAL_TBL [23].TRK_PORT1[27:22]	6
0xBB00BC58	TRK_LOCAL_TBL [23].TRK_PORT0[21:16]	6
0xBB00BC58	TRK_LOCAL_TBL [23].SEP_FLOOD_PORT[15:10]	6
0xBB00BC58	TRK_LOCAL_TBL [23].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC58	TRK_LOCAL_TBL [23].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC5C	TRK_LOCAL_TBL [23].RESERVED[31:30]	2
0xBB00BC5C	TRK_LOCAL_TBL [23].AVL_TRK_PORT7[29:24]	6
0xBB00BC5C	TRK_LOCAL_TBL [23].AVL_TRK_PORT6[23:18]	6
0xBB00BC5C	TRK_LOCAL_TBL [23].AVL_TRK_PORTS[17:12]	6
0xBB00BC5C	TRK_LOCAL_TBL [23].AVL_TRK_PORT4[11:6]	6
0xBB00BC5C	TRK_LOCAL_TBL [23].AVL_TRK_PORT3[5:0]	6
0xBB00BC60	TRK_LOCAL_TBL [23].RESERVED[31:30]	2
0xBB00BC60	TRK_LOCAL_TBL [23].AVL_TRK_PORT2[29:24]	6
0xBB00BC60	TRK_LOCAL_TBL [23].AVL_TRK_PORT1[23:18]	6
0xBB00BC60	TRK_LOCAL_TBL [23].AVL_TRK_PORT0[17:12]	6
0xBB00BC60	TRK_LOCAL_TBL [23].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC60	TRK_LOCAL_TBL [23].AVL_SEP_KWN_MC_PORT[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BC64	TRK_LOCAL_TBL [24].RESERVED[31:13]	19
0xBB00BC64	TRK_LOCAL_TBL [24].NUM_TX_CANDI[12:9]	4
0xBB00BC64	TRK_LOCAL_TBL [24].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC64	TRK_LOCAL_TBL [24].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC64	TRK_LOCAL_TBL [24].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC64	TRK_LOCAL_TBL [24].TRK_PORT7[5:0]	6
0xBB00BC68	TRK_LOCAL_TBL [24].RESERVED[31:30]	2
0xBB00BC68	TRK_LOCAL_TBL [24].TRK_PORT6[29:24]	6
0xBB00BC68	TRK_LOCAL_TBL [24].TRK_PORT5[23:18]	6
0xBB00BC68	TRK_LOCAL_TBL [24].TRK_PORT4[17:12]	6
0xBB00BC68	TRK_LOCAL_TBL [24].TRK_PORT3[11:6]	6
0xBB00BC68	TRK_LOCAL_TBL [24].TRK_PORT2[5:0]	6
0xBB00BC6C	TRK_LOCAL_TBL [24].RESERVED[31:28]	4
0xBB00BC6C	TRK_LOCAL_TBL [24].TRK_PORT1[27:22]	6
0xBB00BC6C	TRK_LOCAL_TBL [24].TRK_PORT0[21:16]	6
0xBB00BC6C	TRK_LOCAL_TBL [24].SEP_FLOOD_PORT[15:10]	6
0xBB00BC6C	TRK_LOCAL_TBL [24].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC6C	TRK_LOCAL_TBL [24].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC70	TRK_LOCAL_TBL [24].RESERVED[31:30]	2
0xBB00BC70	TRK_LOCAL_TBL [24].AVL_TRK_PORT7[29:24]	6
0xBB00BC70	TRK_LOCAL_TBL [24].AVL_TRK_PORT6[23:18]	6
0xBB00BC70	TRK_LOCAL_TBL [24].AVL_TRK_PORT5[17:12]	6
0xBB00BC70	TRK_LOCAL_TBL [24].AVL_TRK_PORT4[11:6]	6
0xBB00BC70	TRK_LOCAL_TBL [24].AVL_TRK_PORT3[5:0]	6
0xBB00BC74	TRK_LOCAL_TBL [24].RESERVED[31:30]	2
0xBB00BC74	TRK_LOCAL_TBL [24].AVL_TRK_PORT2[29:24]	6
0xBB00BC74	TRK_LOCAL_TBL [24].AVL_TRK_PORT1[23:18]	6
0xBB00BC74	TRK_LOCAL_TBL [24].AVL_TRK_PORT0[17:12]	6
0xBB00BC74	TRK_LOCAL_TBL [24].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC74	TRK_LOCAL_TBL [24].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC78	TRK_LOCAL_TBL [25].RESERVED[31:13]	19
0xBB00BC78	TRK_LOCAL_TBL [25].NUM_TX_CANDI[12:9]	4
0xBB00BC78	TRK_LOCAL_TBL [25].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC78	TRK_LOCAL_TBL [25].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC78	TRK_LOCAL_TBL [25].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC78	TRK_LOCAL_TBL [25].TRK_PORT7[5:0]	6
0xBB00BC7C	TRK_LOCAL_TBL [25].RESERVED[31:30]	2
0xBB00BC7C	TRK_LOCAL_TBL [25].TRK_PORT6[29:24]	6
0xBB00BC7C	TRK_LOCAL_TBL [25].TRK_PORT5[23:18]	6
0xBB00BC7C	TRK_LOCAL_TBL [25].TRK_PORT4[17:12]	6
0xBB00BC7C	TRK_LOCAL_TBL [25].TRK_PORT3[11:6]	6
0xBB00BC7C	TRK_LOCAL_TBL [25].TRK_PORT2[5:0]	6
0xBB00BC80	TRK_LOCAL_TBL [25].RESERVED[31:28]	4
0xBB00BC80	TRK_LOCAL_TBL [25].TRK_PORT1[27:22]	6
0xBB00BC80	TRK_LOCAL_TBL [25].TRK_PORT0[21:16]	6
0xBB00BC80	TRK_LOCAL_TBL [25].SEP_FLOOD_PORT[15:10]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BC80	TRK_LOCAL_TBL [25].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC80	TRK_LOCAL_TBL [25].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC84	TRK_LOCAL_TBL [25].RESERVED[31:30]	2
0xBB00BC84	TRK_LOCAL_TBL [25].AVL_TRK_PORT7[29:24]	6
0xBB00BC84	TRK_LOCAL_TBL [25].AVL_TRK_PORT6[23:18]	6
0xBB00BC84	TRK_LOCAL_TBL [25].AVL_TRK_PORT5[17:12]	6
0xBB00BC84	TRK_LOCAL_TBL [25].AVL_TRK_PORT4[11:6]	6
0xBB00BC84	TRK_LOCAL_TBL [25].AVL_TRK_PORT3[5:0]	6
0xBB00BC88	TRK_LOCAL_TBL [25].RESERVED[31:30]	2
0xBB00BC88	TRK_LOCAL_TBL [25].AVL_TRK_PORT2[29:24]	6
0xBB00BC88	TRK_LOCAL_TBL [25].AVL_TRK_PORT1[23:18]	6
0xBB00BC88	TRK_LOCAL_TBL [25].AVL_TRK_PORT0[17:12]	6
0xBB00BC88	TRK_LOCAL_TBL [25].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC88	TRK_LOCAL_TBL [25].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BC8C	TRK_LOCAL_TBL [26].RESERVED[31:13]	19
0xBB00BC8C	TRK_LOCAL_TBL [26].NUM_TX_CANDI[12:9]	4
0xBB00BC8C	TRK_LOCAL_TBL [26].L2_HASH_MSK_IDX[8:8]	1
0xBB00BC8C	TRK_LOCAL_TBL [26].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BC8C	TRK_LOCAL_TBL [26].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BC8C	TRK_LOCAL_TBL [26].TRK_PORT7[5:0]	6
0xBB00BC90	TRK_LOCAL_TBL [26].RESERVED[31:30]	2
0xBB00BC90	TRK_LOCAL_TBL [26].TRK_PORT6[29:24]	6
0xBB00BC90	TRK_LOCAL_TBL [26].TRK_PORT5[23:18]	6
0xBB00BC90	TRK_LOCAL_TBL [26].TRK_PORT4[17:12]	6
0xBB00BC90	TRK_LOCAL_TBL [26].TRK_PORT3[11:6]	6
0xBB00BC90	TRK_LOCAL_TBL [26].TRK_PORT2[5:0]	6
0xBB00BC94	TRK_LOCAL_TBL [26].RESERVED[31:28]	4
0xBB00BC94	TRK_LOCAL_TBL [26].TRK_PORT1[27:22]	6
0xBB00BC94	TRK_LOCAL_TBL [26].TRK_PORT0[21:16]	6
0xBB00BC94	TRK_LOCAL_TBL [26].SEP_FLOOD_PORT[15:10]	6
0xBB00BC94	TRK_LOCAL_TBL [26].SEP_KWN_MC_PORT[9:4]	6
0xBB00BC94	TRK_LOCAL_TBL [26].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BC98	TRK_LOCAL_TBL [26].RESERVED[31:30]	2
0xBB00BC98	TRK_LOCAL_TBL [26].AVL_TRK_PORT7[29:24]	6
0xBB00BC98	TRK_LOCAL_TBL [26].AVL_TRK_PORT6[23:18]	6
0xBB00BC98	TRK_LOCAL_TBL [26].AVL_TRK_PORTS[17:12]	6
0xBB00BC98	TRK_LOCAL_TBL [26].AVL_TRK_PORT4[11:6]	6
0xBB00BC98	TRK_LOCAL_TBL [26].AVL_TRK_PORT3[5:0]	6
0xBB00BC9C	TRK_LOCAL_TBL [26].RESERVED[31:30]	2
0xBB00BC9C	TRK_LOCAL_TBL [26].AVL_TRK_PORT2[29:24]	6
0xBB00BC9C	TRK_LOCAL_TBL [26].AVL_TRK_PORT1[23:18]	6
0xBB00BC9C	TRK_LOCAL_TBL [26].AVL_TRK_PORT0[17:12]	6
0xBB00BC9C	TRK_LOCAL_TBL [26].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BC9C	TRK_LOCAL_TBL [26].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BCA0	TRK_LOCAL_TBL [27].RESERVED[31:13]	19
0xBB00BCA0	TRK_LOCAL_TBL [27].NUM_TX_CANDI[12:9]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BCA0	TRK_LOCAL_TBL [27].L2_HASH_MSK_IDX[8:8]	1
0xBB00BCA0	TRK_LOCAL_TBL [27].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BCA0	TRK_LOCAL_TBL [27].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BCA0	TRK_LOCAL_TBL [27].TRK_PORT7[5:0]	6
0xBB00BCA4	TRK_LOCAL_TBL [27].RESERVED[31:30]	2
0xBB00BCA4	TRK_LOCAL_TBL [27].TRK_PORT6[29:24]	6
0xBB00BCA4	TRK_LOCAL_TBL [27].TRK_PORT5[23:18]	6
0xBB00BCA4	TRK_LOCAL_TBL [27].TRK_PORT4[17:12]	6
0xBB00BCA4	TRK_LOCAL_TBL [27].TRK_PORT3[11:6]	6
0xBB00BCA4	TRK_LOCAL_TBL [27].TRK_PORT2[5:0]	6
0xBB00BCA8	TRK_LOCAL_TBL [27].RESERVED[31:28]	4
0xBB00BCA8	TRK_LOCAL_TBL [27].TRK_PORT1[27:22]	6
0xBB00BCA8	TRK_LOCAL_TBL [27].TRK_PORT0[21:16]	6
0xBB00BCA8	TRK_LOCAL_TBL [27].SEP_FLOOD_PORT[15:10]	6
0xBB00BCA8	TRK_LOCAL_TBL [27].SEP_KWN_MC_PORT[9:4]	6
0xBB00BCA8	TRK_LOCAL_TBL [27].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BCAC	TRK_LOCAL_TBL [27].RESERVED[31:30]	2
0xBB00BCAC	TRK_LOCAL_TBL [27].AVL_TRK_PORT7[29:24]	6
0xBB00BCAC	TRK_LOCAL_TBL [27].AVL_TRK_PORT6[23:18]	6
0xBB00BCAC	TRK_LOCAL_TBL [27].AVL_TRK_PORT5[17:12]	6
0xBB00BCAC	TRK_LOCAL_TBL [27].AVL_TRK_PORT4[11:6]	6
0xBB00BCAC	TRK_LOCAL_TBL [27].AVL_TRK_PORT3[5:0]	6
0xBB00BCB0	TRK_LOCAL_TBL [27].RESERVED[31:30]	2
0xBB00BCB0	TRK_LOCAL_TBL [27].AVL_TRK_PORT2[29:24]	6
0xBB00BCB0	TRK_LOCAL_TBL [27].AVL_TRK_PORT1[23:18]	6
0xBB00BCB0	TRK_LOCAL_TBL [27].AVL_TRK_PORT0[17:12]	6
0xBB00BCB0	TRK_LOCAL_TBL [27].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BCB0	TRK_LOCAL_TBL [27].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BCB4	TRK_LOCAL_TBL [28].RESERVED[31:13]	19
0xBB00BCB4	TRK_LOCAL_TBL [28].NUM_TX_CANDI[12:9]	4
0xBB00BCB4	TRK_LOCAL_TBL [28].L2_HASH_MSK_IDX[8:8]	1
0xBB00BCB4	TRK_LOCAL_TBL [28].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BCB4	TRK_LOCAL_TBL [28].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BCB4	TRK_LOCAL_TBL [28].TRK_PORT7[5:0]	6
0xBB00BCB8	TRK_LOCAL_TBL [28].RESERVED[31:30]	2
0xBB00BCB8	TRK_LOCAL_TBL [28].TRK_PORT6[29:24]	6
0xBB00BCB8	TRK_LOCAL_TBL [28].TRK_PORT5[23:18]	6
0xBB00BCB8	TRK_LOCAL_TBL [28].TRK_PORT4[17:12]	6
0xBB00BCB8	TRK_LOCAL_TBL [28].TRK_PORT3[11:6]	6
0xBB00BCB8	TRK_LOCAL_TBL [28].TRK_PORT2[5:0]	6
0xBB00BCBC	TRK_LOCAL_TBL [28].RESERVED[31:28]	4
0xBB00BCBC	TRK_LOCAL_TBL [28].TRK_PORT1[27:22]	6
0xBB00BCBC	TRK_LOCAL_TBL [28].TRK_PORT0[21:16]	6
0xBB00BCBC	TRK_LOCAL_TBL [28].SEP_FLOOD_PORT[15:10]	6
0xBB00BCBC	TRK_LOCAL_TBL [28].SEP_KWN_MC_PORT[9:4]	6
0xBB00BCBC	TRK_LOCAL_TBL [28].NUM_AVL_TX_CANDI[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BCC0	TRK_LOCAL_TBL [28].RESERVED[31:30]	2
0xBB00BCC0	TRK_LOCAL_TBL [28].AVL_TRK_PORT7[29:24]	6
0xBB00BCC0	TRK_LOCAL_TBL [28].AVL_TRK_PORT6[23:18]	6
0xBB00BCC0	TRK_LOCAL_TBL [28].AVL_TRK_PORT5[17:12]	6
0xBB00BCC0	TRK_LOCAL_TBL [28].AVL_TRK_PORT4[11:6]	6
0xBB00BCC0	TRK_LOCAL_TBL [28].AVL_TRK_PORT3[5:0]	6
0xBB00BCC4	TRK_LOCAL_TBL [28].RESERVED[31:30]	2
0xBB00BCC4	TRK_LOCAL_TBL [28].AVL_TRK_PORT2[29:24]	6
0xBB00BCC4	TRK_LOCAL_TBL [28].AVL_TRK_PORT1[23:18]	6
0xBB00BCC4	TRK_LOCAL_TBL [28].AVL_TRK_PORT0[17:12]	6
0xBB00BCC4	TRK_LOCAL_TBL [28].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BCC4	TRK_LOCAL_TBL [28].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BCC8	TRK_LOCAL_TBL [29].RESERVED[31:13]	19
0xBB00BCC8	TRK_LOCAL_TBL [29].NUM_TX_CANDI[12:9]	4
0xBB00BCC8	TRK_LOCAL_TBL [29].L2_HASH_MSK_IDX[8:8]	1
0xBB00BCC8	TRK_LOCAL_TBL [29].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BCC8	TRK_LOCAL_TBL [29].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BCC8	TRK_LOCAL_TBL [29].TRK_PORT7[5:0]	6
0xBB00BCCC	TRK_LOCAL_TBL [29].RESERVED[31:30]	2
0xBB00BCCC	TRK_LOCAL_TBL [29].TRK_PORT6[29:24]	6
0xBB00BCCC	TRK_LOCAL_TBL [29].TRK_PORT5[23:18]	6
0xBB00BCCC	TRK_LOCAL_TBL [29].TRK_PORT4[17:12]	6
0xBB00BCCC	TRK_LOCAL_TBL [29].TRK_PORT3[11:6]	6
0xBB00BCCC	TRK_LOCAL_TBL [29].TRK_PORT2[5:0]	6
0xBB00BCD0	TRK_LOCAL_TBL [29].RESERVED[31:28]	4
0xBB00BCD0	TRK_LOCAL_TBL [29].TRK_PORT1[27:22]	6
0xBB00BCD0	TRK_LOCAL_TBL [29].TRK_PORT0[21:16]	6
0xBB00BCD0	TRK_LOCAL_TBL [29].SEP_FLOOD_PORT[15:10]	6
0xBB00BCD0	TRK_LOCAL_TBL [29].SEP_KWN_MC_PORT[9:4]	6
0xBB00BCD0	TRK_LOCAL_TBL [29].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BCD4	TRK_LOCAL_TBL [29].RESERVED[31:30]	2
0xBB00BCD4	TRK_LOCAL_TBL [29].AVL_TRK_PORT7[29:24]	6
0xBB00BCD4	TRK_LOCAL_TBL [29].AVL_TRK_PORT6[23:18]	6
0xBB00BCD4	TRK_LOCAL_TBL [29].AVL_TRK_PORT5[17:12]	6
0xBB00BCD4	TRK_LOCAL_TBL [29].AVL_TRK_PORT4[11:6]	6
0xBB00BCD4	TRK_LOCAL_TBL [29].AVL_TRK_PORT3[5:0]	6
0xBB00BCD8	TRK_LOCAL_TBL [29].RESERVED[31:30]	2
0xBB00BCD8	TRK_LOCAL_TBL [29].AVL_TRK_PORT2[29:24]	6
0xBB00BCD8	TRK_LOCAL_TBL [29].AVL_TRK_PORT1[23:18]	6
0xBB00BCD8	TRK_LOCAL_TBL [29].AVL_TRK_PORT0[17:12]	6
0xBB00BCD8	TRK_LOCAL_TBL [29].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BCD8	TRK_LOCAL_TBL [29].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BCDC	TRK_LOCAL_TBL [30].RESERVED[31:13]	19
0xBB00BCDC	TRK_LOCAL_TBL [30].NUM_TX_CANDI[12:9]	4
0xBB00BCDC	TRK_LOCAL_TBL [30].L2_HASH_MSK_IDX[8:8]	1
0xBB00BCDC	TRK_LOCAL_TBL [30].IP4_HASH_MSK_IDX[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BCDC	TRK_LOCAL_TBL [30].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BCDC	TRK_LOCAL_TBL [30].TRK_PORT7[5:0]	6
0xBB00BCE0	TRK_LOCAL_TBL [30].RESERVED[31:30]	2
0xBB00BCE0	TRK_LOCAL_TBL [30].TRK_PORT6[29:24]	6
0xBB00BCE0	TRK_LOCAL_TBL [30].TRK_PORT5[23:18]	6
0xBB00BCE0	TRK_LOCAL_TBL [30].TRK_PORT4[17:12]	6
0xBB00BCE0	TRK_LOCAL_TBL [30].TRK_PORT3[11:6]	6
0xBB00BCE0	TRK_LOCAL_TBL [30].TRK_PORT2[5:0]	6
0xBB00BCE4	TRK_LOCAL_TBL [30].RESERVED[31:28]	4
0xBB00BCE4	TRK_LOCAL_TBL [30].TRK_PORT1[27:22]	6
0xBB00BCE4	TRK_LOCAL_TBL [30].TRK_PORT0[21:16]	6
0xBB00BCE4	TRK_LOCAL_TBL [30].SEP_FLOOD_PORT[15:10]	6
0xBB00BCE4	TRK_LOCAL_TBL [30].SEP_KWN_MC_PORT[9:4]	6
0xBB00BCE4	TRK_LOCAL_TBL [30].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BCE8	TRK_LOCAL_TBL [30].RESERVED[31:30]	2
0xBB00BCE8	TRK_LOCAL_TBL [30].AVL_TRK_PORT7[29:24]	6
0xBB00BCE8	TRK_LOCAL_TBL [30].AVL_TRK_PORT6[23:18]	6
0xBB00BCE8	TRK_LOCAL_TBL [30].AVL_TRK_PORT5[17:12]	6
0xBB00BCE8	TRK_LOCAL_TBL [30].AVL_TRK_PORT4[11:6]	6
0xBB00BCE8	TRK_LOCAL_TBL [30].AVL_TRK_PORT3[5:0]	6
0xBB00BCEC	TRK_LOCAL_TBL [30].RESERVED[31:30]	2
0xBB00BCEC	TRK_LOCAL_TBL [30].AVL_TRK_PORT2[29:24]	6
0xBB00BCEC	TRK_LOCAL_TBL [30].AVL_TRK_PORT1[23:18]	6
0xBB00BCEC	TRK_LOCAL_TBL [30].AVL_TRK_PORT0[17:12]	6
0xBB00BCEC	TRK_LOCAL_TBL [30].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BCEC	TRK_LOCAL_TBL [30].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BCF0	TRK_LOCAL_TBL [31].RESERVED[31:13]	19
0xBB00BCF0	TRK_LOCAL_TBL [31].NUM_TX_CANDI[12:9]	4
0xBB00BCF0	TRK_LOCAL_TBL [31].L2_HASH_MSK_IDX[8:8]	1
0xBB00BCF0	TRK_LOCAL_TBL [31].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BCF0	TRK_LOCAL_TBL [31].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BCF0	TRK_LOCAL_TBL [31].TRK_PORT7[5:0]	6
0xBB00BCF4	TRK_LOCAL_TBL [31].RESERVED[31:30]	2
0xBB00BCF4	TRK_LOCAL_TBL [31].TRK_PORT6[29:24]	6
0xBB00BCF4	TRK_LOCAL_TBL [31].TRK_PORT5[23:18]	6
0xBB00BCF4	TRK_LOCAL_TBL [31].TRK_PORT4[17:12]	6
0xBB00BCF4	TRK_LOCAL_TBL [31].TRK_PORT3[11:6]	6
0xBB00BCF4	TRK_LOCAL_TBL [31].TRK_PORT2[5:0]	6
0xBB00BCF8	TRK_LOCAL_TBL [31].RESERVED[31:28]	4
0xBB00BCF8	TRK_LOCAL_TBL [31].TRK_PORT1[27:22]	6
0xBB00BCF8	TRK_LOCAL_TBL [31].TRK_PORT0[21:16]	6
0xBB00BCF8	TRK_LOCAL_TBL [31].SEP_FLOOD_PORT[15:10]	6
0xBB00BCF8	TRK_LOCAL_TBL [31].SEP_KWN_MC_PORT[9:4]	6
0xBB00BCF8	TRK_LOCAL_TBL [31].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BCFC	TRK_LOCAL_TBL [31].RESERVED[31:30]	2
0xBB00BCFC	TRK_LOCAL_TBL [31].AVL_TRK_PORT7[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BCFC	TRK_LOCAL_TBL [31].AVL_TRK_PORT6[23:18]	6
0xBB00BCFC	TRK_LOCAL_TBL [31].AVL_TRK_PORT5[17:12]	6
0xBB00BCFC	TRK_LOCAL_TBL [31].AVL_TRK_PORT4[11:6]	6
0xBB00BCFC	TRK_LOCAL_TBL [31].AVL_TRK_PORT3[5:0]	6
0xBB00BD00	TRK_LOCAL_TBL [31].RESERVED[31:30]	2
0xBB00BD00	TRK_LOCAL_TBL [31].AVL_TRK_PORT2[29:24]	6
0xBB00BD00	TRK_LOCAL_TBL [31].AVL_TRK_PORT1[23:18]	6
0xBB00BD00	TRK_LOCAL_TBL [31].AVL_TRK_PORT0[17:12]	6
0xBB00BD00	TRK_LOCAL_TBL [31].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD00	TRK_LOCAL_TBL [31].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD04	TRK_LOCAL_TBL [32].RESERVED[31:13]	19
0xBB00BD04	TRK_LOCAL_TBL [32].NUM_TX_CANDI[12:9]	4
0xBB00BD04	TRK_LOCAL_TBL [32].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD04	TRK_LOCAL_TBL [32].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD04	TRK_LOCAL_TBL [32].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD04	TRK_LOCAL_TBL [32].TRK_PORT7[5:0]	6
0xBB00BD08	TRK_LOCAL_TBL [32].RESERVED[31:30]	2
0xBB00BD08	TRK_LOCAL_TBL [32].TRK_PORT6[29:24]	6
0xBB00BD08	TRK_LOCAL_TBL [32].TRK_PORT5[23:18]	6
0xBB00BD08	TRK_LOCAL_TBL [32].TRK_PORT4[17:12]	6
0xBB00BD08	TRK_LOCAL_TBL [32].TRK_PORT3[11:6]	6
0xBB00BD08	TRK_LOCAL_TBL [32].TRK_PORT2[5:0]	6
0xBB00BD0C	TRK_LOCAL_TBL [32].RESERVED[31:28]	4
0xBB00BD0C	TRK_LOCAL_TBL [32].TRK_PORT1[27:22]	6
0xBB00BD0C	TRK_LOCAL_TBL [32].TRK_PORT0[21:16]	6
0xBB00BD0C	TRK_LOCAL_TBL [32].SEP_FLOOD_PORT[15:10]	6
0xBB00BD0C	TRK_LOCAL_TBL [32].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD0C	TRK_LOCAL_TBL [32].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD10	TRK_LOCAL_TBL [32].RESERVED[31:30]	2
0xBB00BD10	TRK_LOCAL_TBL [32].AVL_TRK_PORT7[29:24]	6
0xBB00BD10	TRK_LOCAL_TBL [32].AVL_TRK_PORT6[23:18]	6
0xBB00BD10	TRK_LOCAL_TBL [32].AVL_TRK_PORT5[17:12]	6
0xBB00BD10	TRK_LOCAL_TBL [32].AVL_TRK_PORT4[11:6]	6
0xBB00BD10	TRK_LOCAL_TBL [32].AVL_TRK_PORT3[5:0]	6
0xBB00BD14	TRK_LOCAL_TBL [32].RESERVED[31:30]	2
0xBB00BD14	TRK_LOCAL_TBL [32].AVL_TRK_PORT2[29:24]	6
0xBB00BD14	TRK_LOCAL_TBL [32].AVL_TRK_PORT1[23:18]	6
0xBB00BD14	TRK_LOCAL_TBL [32].AVL_TRK_PORT0[17:12]	6
0xBB00BD14	TRK_LOCAL_TBL [32].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD14	TRK_LOCAL_TBL [32].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD18	TRK_LOCAL_TBL [33].RESERVED[31:13]	19
0xBB00BD18	TRK_LOCAL_TBL [33].NUM_TX_CANDI[12:9]	4
0xBB00BD18	TRK_LOCAL_TBL [33].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD18	TRK_LOCAL_TBL [33].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD18	TRK_LOCAL_TBL [33].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD18	TRK_LOCAL_TBL [33].TRK_PORT7[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BD1C	TRK_LOCAL_TBL [33].RESERVED[31:30]	2
0xBB00BD1C	TRK_LOCAL_TBL [33].TRK_PORT6[29:24]	6
0xBB00BD1C	TRK_LOCAL_TBL [33].TRK_PORT5[23:18]	6
0xBB00BD1C	TRK_LOCAL_TBL [33].TRK_PORT4[17:12]	6
0xBB00BD1C	TRK_LOCAL_TBL [33].TRK_PORT3[11:6]	6
0xBB00BD1C	TRK_LOCAL_TBL [33].TRK_PORT2[5:0]	6
0xBB00BD20	TRK_LOCAL_TBL [33].RESERVED[31:28]	4
0xBB00BD20	TRK_LOCAL_TBL [33].TRK_PORT1[27:22]	6
0xBB00BD20	TRK_LOCAL_TBL [33].TRK_PORT0[21:16]	6
0xBB00BD20	TRK_LOCAL_TBL [33].SEP_FLOOD_PORT[15:10]	6
0xBB00BD20	TRK_LOCAL_TBL [33].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD20	TRK_LOCAL_TBL [33].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD24	TRK_LOCAL_TBL [33].RESERVED[31:30]	2
0xBB00BD24	TRK_LOCAL_TBL [33].AVL_TRK_PORT7[29:24]	6
0xBB00BD24	TRK_LOCAL_TBL [33].AVL_TRK_PORT6[23:18]	6
0xBB00BD24	TRK_LOCAL_TBL [33].AVL_TRK_PORT5[17:12]	6
0xBB00BD24	TRK_LOCAL_TBL [33].AVL_TRK_PORT4[11:6]	6
0xBB00BD24	TRK_LOCAL_TBL [33].AVL_TRK_PORT3[5:0]	6
0xBB00BD28	TRK_LOCAL_TBL [33].RESERVED[31:30]	2
0xBB00BD28	TRK_LOCAL_TBL [33].AVL_TRK_PORT2[29:24]	6
0xBB00BD28	TRK_LOCAL_TBL [33].AVL_TRK_PORT1[23:18]	6
0xBB00BD28	TRK_LOCAL_TBL [33].AVL_TRK_PORT0[17:12]	6
0xBB00BD28	TRK_LOCAL_TBL [33].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD28	TRK_LOCAL_TBL [33].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD2C	TRK_LOCAL_TBL [34].RESERVED[31:13]	19
0xBB00BD2C	TRK_LOCAL_TBL [34].NUM_TX_CANDI[12:9]	4
0xBB00BD2C	TRK_LOCAL_TBL [34].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD2C	TRK_LOCAL_TBL [34].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD2C	TRK_LOCAL_TBL [34].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD2C	TRK_LOCAL_TBL [34].TRK_PORT7[5:0]	6
0xBB00BD30	TRK_LOCAL_TBL [34].RESERVED[31:30]	2
0xBB00BD30	TRK_LOCAL_TBL [34].TRK_PORT6[29:24]	6
0xBB00BD30	TRK_LOCAL_TBL [34].TRK_PORT5[23:18]	6
0xBB00BD30	TRK_LOCAL_TBL [34].TRK_PORT4[17:12]	6
0xBB00BD30	TRK_LOCAL_TBL [34].TRK_PORT3[11:6]	6
0xBB00BD30	TRK_LOCAL_TBL [34].TRK_PORT2[5:0]	6
0xBB00BD34	TRK_LOCAL_TBL [34].RESERVED[31:28]	4
0xBB00BD34	TRK_LOCAL_TBL [34].TRK_PORT1[27:22]	6
0xBB00BD34	TRK_LOCAL_TBL [34].TRK_PORT0[21:16]	6
0xBB00BD34	TRK_LOCAL_TBL [34].SEP_FLOOD_PORT[15:10]	6
0xBB00BD34	TRK_LOCAL_TBL [34].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD34	TRK_LOCAL_TBL [34].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD38	TRK_LOCAL_TBL [34].RESERVED[31:30]	2
0xBB00BD38	TRK_LOCAL_TBL [34].AVL_TRK_PORT7[29:24]	6
0xBB00BD38	TRK_LOCAL_TBL [34].AVL_TRK_PORT6[23:18]	6
0xBB00BD38	TRK_LOCAL_TBL [34].AVL_TRK_PORT5[17:12]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BD38	TRK_LOCAL_TBL [34].AVL_TRK_PORT4[11:6]	6
0xBB00BD38	TRK_LOCAL_TBL [34].AVL_TRK_PORT3[5:0]	6
0xBB00BD3C	TRK_LOCAL_TBL [34].RESERVED[31:30]	2
0xBB00BD3C	TRK_LOCAL_TBL [34].AVL_TRK_PORT2[29:24]	6
0xBB00BD3C	TRK_LOCAL_TBL [34].AVL_TRK_PORT1[23:18]	6
0xBB00BD3C	TRK_LOCAL_TBL [34].AVL_TRK_PORT0[17:12]	6
0xBB00BD3C	TRK_LOCAL_TBL [34].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD3C	TRK_LOCAL_TBL [34].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD40	TRK_LOCAL_TBL [35].RESERVED[31:13]	19
0xBB00BD40	TRK_LOCAL_TBL [35].NUM_TX_CANDI[12:9]	4
0xBB00BD40	TRK_LOCAL_TBL [35].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD40	TRK_LOCAL_TBL [35].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD40	TRK_LOCAL_TBL [35].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD40	TRK_LOCAL_TBL [35].TRK_PORT7[5:0]	6
0xBB00BD44	TRK_LOCAL_TBL [35].RESERVED[31:30]	2
0xBB00BD44	TRK_LOCAL_TBL [35].TRK_PORT6[29:24]	6
0xBB00BD44	TRK_LOCAL_TBL [35].TRK_PORT5[23:18]	6
0xBB00BD44	TRK_LOCAL_TBL [35].TRK_PORT4[17:12]	6
0xBB00BD44	TRK_LOCAL_TBL [35].TRK_PORT3[11:6]	6
0xBB00BD44	TRK_LOCAL_TBL [35].TRK_PORT2[5:0]	6
0xBB00BD48	TRK_LOCAL_TBL [35].RESERVED[31:28]	4
0xBB00BD48	TRK_LOCAL_TBL [35].TRK_PORT1[27:22]	6
0xBB00BD48	TRK_LOCAL_TBL [35].TRK_PORT0[21:16]	6
0xBB00BD48	TRK_LOCAL_TBL [35].SEP_FLOOD_PORT[15:10]	6
0xBB00BD48	TRK_LOCAL_TBL [35].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD48	TRK_LOCAL_TBL [35].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD4C	TRK_LOCAL_TBL [35].RESERVED[31:30]	2
0xBB00BD4C	TRK_LOCAL_TBL [35].AVL_TRK_PORT7[29:24]	6
0xBB00BD4C	TRK_LOCAL_TBL [35].AVL_TRK_PORT6[23:18]	6
0xBB00BD4C	TRK_LOCAL_TBL [35].AVL_TRK_PORT5[17:12]	6
0xBB00BD4C	TRK_LOCAL_TBL [35].AVL_TRK_PORT4[11:6]	6
0xBB00BD4C	TRK_LOCAL_TBL [35].AVL_TRK_PORT3[5:0]	6
0xBB00BD50	TRK_LOCAL_TBL [35].RESERVED[31:30]	2
0xBB00BD50	TRK_LOCAL_TBL [35].AVL_TRK_PORT2[29:24]	6
0xBB00BD50	TRK_LOCAL_TBL [35].AVL_TRK_PORT1[23:18]	6
0xBB00BD50	TRK_LOCAL_TBL [35].AVL_TRK_PORT0[17:12]	6
0xBB00BD50	TRK_LOCAL_TBL [35].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD50	TRK_LOCAL_TBL [35].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD54	TRK_LOCAL_TBL [36].RESERVED[31:13]	19
0xBB00BD54	TRK_LOCAL_TBL [36].NUM_TX_CANDI[12:9]	4
0xBB00BD54	TRK_LOCAL_TBL [36].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD54	TRK_LOCAL_TBL [36].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD54	TRK_LOCAL_TBL [36].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD54	TRK_LOCAL_TBL [36].TRK_PORT7[5:0]	6
0xBB00BD58	TRK_LOCAL_TBL [36].RESERVED[31:30]	2
0xBB00BD58	TRK_LOCAL_TBL [36].TRK_PORT6[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BD58	TRK_LOCAL_TBL [36].TRK_PORT5[23:18]	6
0xBB00BD58	TRK_LOCAL_TBL [36].TRK_PORT4[17:12]	6
0xBB00BD58	TRK_LOCAL_TBL [36].TRK_PORT3[11:6]	6
0xBB00BD58	TRK_LOCAL_TBL [36].TRK_PORT2[5:0]	6
0xBB00BD5C	TRK_LOCAL_TBL [36].RESERVED[31:28]	4
0xBB00BD5C	TRK_LOCAL_TBL [36].TRK_PORT1[27:22]	6
0xBB00BD5C	TRK_LOCAL_TBL [36].TRK_PORT0[21:16]	6
0xBB00BD5C	TRK_LOCAL_TBL [36].SEP_FLOOD_PORT[15:10]	6
0xBB00BD5C	TRK_LOCAL_TBL [36].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD5C	TRK_LOCAL_TBL [36].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD60	TRK_LOCAL_TBL [36].RESERVED[31:30]	2
0xBB00BD60	TRK_LOCAL_TBL [36].AVL_TRK_PORT7[29:24]	6
0xBB00BD60	TRK_LOCAL_TBL [36].AVL_TRK_PORT6[23:18]	6
0xBB00BD60	TRK_LOCAL_TBL [36].AVL_TRK_PORT5[17:12]	6
0xBB00BD60	TRK_LOCAL_TBL [36].AVL_TRK_PORT4[11:6]	6
0xBB00BD60	TRK_LOCAL_TBL [36].AVL_TRK_PORT3[5:0]	6
0xBB00BD64	TRK_LOCAL_TBL [36].RESERVED[31:30]	2
0xBB00BD64	TRK_LOCAL_TBL [36].AVL_TRK_PORT2[29:24]	6
0xBB00BD64	TRK_LOCAL_TBL [36].AVL_TRK_PORT1[23:18]	6
0xBB00BD64	TRK_LOCAL_TBL [36].AVL_TRK_PORT0[17:12]	6
0xBB00BD64	TRK_LOCAL_TBL [36].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD64	TRK_LOCAL_TBL [36].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD68	TRK_LOCAL_TBL [37].RESERVED[31:13]	19
0xBB00BD68	TRK_LOCAL_TBL [37].NUM_TX_CANDI[12:9]	4
0xBB00BD68	TRK_LOCAL_TBL [37].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD68	TRK_LOCAL_TBL [37].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD68	TRK_LOCAL_TBL [37].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD68	TRK_LOCAL_TBL [37].TRK_PORT7[5:0]	6
0xBB00BD6C	TRK_LOCAL_TBL [37].RESERVED[31:30]	2
0xBB00BD6C	TRK_LOCAL_TBL [37].TRK_PORT6[29:24]	6
0xBB00BD6C	TRK_LOCAL_TBL [37].TRK_PORT5[23:18]	6
0xBB00BD6C	TRK_LOCAL_TBL [37].TRK_PORT4[17:12]	6
0xBB00BD6C	TRK_LOCAL_TBL [37].TRK_PORT3[11:6]	6
0xBB00BD6C	TRK_LOCAL_TBL [37].TRK_PORT2[5:0]	6
0xBB00BD70	TRK_LOCAL_TBL [37].RESERVED[31:28]	4
0xBB00BD70	TRK_LOCAL_TBL [37].TRK_PORT1[27:22]	6
0xBB00BD70	TRK_LOCAL_TBL [37].TRK_PORT0[21:16]	6
0xBB00BD70	TRK_LOCAL_TBL [37].SEP_FLOOD_PORT[15:10]	6
0xBB00BD70	TRK_LOCAL_TBL [37].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD70	TRK_LOCAL_TBL [37].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD74	TRK_LOCAL_TBL [37].RESERVED[31:30]	2
0xBB00BD74	TRK_LOCAL_TBL [37].AVL_TRK_PORT7[29:24]	6
0xBB00BD74	TRK_LOCAL_TBL [37].AVL_TRK_PORT6[23:18]	6
0xBB00BD74	TRK_LOCAL_TBL [37].AVL_TRK_PORT5[17:12]	6
0xBB00BD74	TRK_LOCAL_TBL [37].AVL_TRK_PORT4[11:6]	6
0xBB00BD74	TRK_LOCAL_TBL [37].AVL_TRK_PORT3[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BD78	TRK_LOCAL_TBL [37].RESERVED[31:30]	2
0xBB00BD78	TRK_LOCAL_TBL [37].AVL_TRK_PORT2[29:24]	6
0xBB00BD78	TRK_LOCAL_TBL [37].AVL_TRK_PORT1[23:18]	6
0xBB00BD78	TRK_LOCAL_TBL [37].AVL_TRK_PORT0[17:12]	6
0xBB00BD78	TRK_LOCAL_TBL [37].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD78	TRK_LOCAL_TBL [37].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD7C	TRK_LOCAL_TBL [38].RESERVED[31:13]	19
0xBB00BD7C	TRK_LOCAL_TBL [38].NUM_TX_CANDI[12:9]	4
0xBB00BD7C	TRK_LOCAL_TBL [38].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD7C	TRK_LOCAL_TBL [38].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD7C	TRK_LOCAL_TBL [38].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD7C	TRK_LOCAL_TBL [38].TRK_PORT7[5:0]	6
0xBB00BD80	TRK_LOCAL_TBL [38].RESERVED[31:30]	2
0xBB00BD80	TRK_LOCAL_TBL [38].TRK_PORT6[29:24]	6
0xBB00BD80	TRK_LOCAL_TBL [38].TRK_PORT5[23:18]	6
0xBB00BD80	TRK_LOCAL_TBL [38].TRK_PORT4[17:12]	6
0xBB00BD80	TRK_LOCAL_TBL [38].TRK_PORT3[11:6]	6
0xBB00BD80	TRK_LOCAL_TBL [38].TRK_PORT2[5:0]	6
0xBB00BD84	TRK_LOCAL_TBL [38].RESERVED[31:28]	4
0xBB00BD84	TRK_LOCAL_TBL [38].TRK_PORT1[27:22]	6
0xBB00BD84	TRK_LOCAL_TBL [38].TRK_PORT0[21:16]	6
0xBB00BD84	TRK_LOCAL_TBL [38].SEP_FLOOD_PORT[15:10]	6
0xBB00BD84	TRK_LOCAL_TBL [38].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD84	TRK_LOCAL_TBL [38].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD88	TRK_LOCAL_TBL [38].RESERVED[31:30]	2
0xBB00BD88	TRK_LOCAL_TBL [38].AVL_TRK_PORT7[29:24]	6
0xBB00BD88	TRK_LOCAL_TBL [38].AVL_TRK_PORT6[23:18]	6
0xBB00BD88	TRK_LOCAL_TBL [38].AVL_TRK_PORT5[17:12]	6
0xBB00BD88	TRK_LOCAL_TBL [38].AVL_TRK_PORT4[11:6]	6
0xBB00BD88	TRK_LOCAL_TBL [38].AVL_TRK_PORT3[5:0]	6
0xBB00BD8C	TRK_LOCAL_TBL [38].RESERVED[31:30]	2
0xBB00BD8C	TRK_LOCAL_TBL [38].AVL_TRK_PORT2[29:24]	6
0xBB00BD8C	TRK_LOCAL_TBL [38].AVL_TRK_PORT1[23:18]	6
0xBB00BD8C	TRK_LOCAL_TBL [38].AVL_TRK_PORT0[17:12]	6
0xBB00BD8C	TRK_LOCAL_TBL [38].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BD8C	TRK_LOCAL_TBL [38].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BD90	TRK_LOCAL_TBL [39].RESERVED[31:13]	19
0xBB00BD90	TRK_LOCAL_TBL [39].NUM_TX_CANDI[12:9]	4
0xBB00BD90	TRK_LOCAL_TBL [39].L2_HASH_MSK_IDX[8:8]	1
0xBB00BD90	TRK_LOCAL_TBL [39].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BD90	TRK_LOCAL_TBL [39].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BD90	TRK_LOCAL_TBL [39].TRK_PORT7[5:0]	6
0xBB00BD94	TRK_LOCAL_TBL [39].RESERVED[31:30]	2
0xBB00BD94	TRK_LOCAL_TBL [39].TRK_PORT6[29:24]	6
0xBB00BD94	TRK_LOCAL_TBL [39].TRK_PORT5[23:18]	6
0xBB00BD94	TRK_LOCAL_TBL [39].TRK_PORT4[17:12]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BD94	TRK_LOCAL_TBL [39].TRK_PORT3[11:6]	6
0xBB00BD94	TRK_LOCAL_TBL [39].TRK_PORT2[5:0]	6
0xBB00BD98	TRK_LOCAL_TBL [39].RESERVED[31:28]	4
0xBB00BD98	TRK_LOCAL_TBL [39].TRK_PORT1[27:22]	6
0xBB00BD98	TRK_LOCAL_TBL [39].TRK_PORT0[21:16]	6
0xBB00BD98	TRK_LOCAL_TBL [39].SEP_FLOOD_PORT[15:10]	6
0xBB00BD98	TRK_LOCAL_TBL [39].SEP_KWN_MC_PORT[9:4]	6
0xBB00BD98	TRK_LOCAL_TBL [39].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BD9C	TRK_LOCAL_TBL [39].RESERVED[31:30]	2
0xBB00BD9C	TRK_LOCAL_TBL [39].AVL_TRK_PORT7[29:24]	6
0xBB00BD9C	TRK_LOCAL_TBL [39].AVL_TRK_PORT6[23:18]	6
0xBB00BD9C	TRK_LOCAL_TBL [39].AVL_TRK_PORT5[17:12]	6
0xBB00BD9C	TRK_LOCAL_TBL [39].AVL_TRK_PORT4[11:6]	6
0xBB00BD9C	TRK_LOCAL_TBL [39].AVL_TRK_PORT3[5:0]	6
0xBB00BDA0	TRK_LOCAL_TBL [39].RESERVED[31:30]	2
0xBB00BDA0	TRK_LOCAL_TBL [39].AVL_TRK_PORT2[29:24]	6
0xBB00BDA0	TRK_LOCAL_TBL [39].AVL_TRK_PORT1[23:18]	6
0xBB00BDA0	TRK_LOCAL_TBL [39].AVL_TRK_PORT0[17:12]	6
0xBB00BDA0	TRK_LOCAL_TBL [39].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BDA0	TRK_LOCAL_TBL [39].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BDA4	TRK_LOCAL_TBL [40].RESERVED[31:13]	19
0xBB00BDA4	TRK_LOCAL_TBL [40].NUM_TX_CANDI[12:9]	4
0xBB00BDA4	TRK_LOCAL_TBL [40].L2_HASH_MSK_IDX[8:8]	1
0xBB00BDA4	TRK_LOCAL_TBL [40].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BDA4	TRK_LOCAL_TBL [40].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BDA4	TRK_LOCAL_TBL [40].TRK_PORT7[5:0]	6
0xBB00BDA8	TRK_LOCAL_TBL [40].RESERVED[31:30]	2
0xBB00BDA8	TRK_LOCAL_TBL [40].TRK_PORT6[29:24]	6
0xBB00BDA8	TRK_LOCAL_TBL [40].TRK_PORT5[23:18]	6
0xBB00BDA8	TRK_LOCAL_TBL [40].TRK_PORT4[17:12]	6
0xBB00BDA8	TRK_LOCAL_TBL [40].TRK_PORT3[11:6]	6
0xBB00BDA8	TRK_LOCAL_TBL [40].TRK_PORT2[5:0]	6
0xBB00BDAC	TRK_LOCAL_TBL [40].RESERVED[31:28]	4
0xBB00BDAC	TRK_LOCAL_TBL [40].TRK_PORT1[27:22]	6
0xBB00BDAC	TRK_LOCAL_TBL [40].TRK_PORT0[21:16]	6
0xBB00BDAC	TRK_LOCAL_TBL [40].SEP_FLOOD_PORT[15:10]	6
0xBB00BDAC	TRK_LOCAL_TBL [40].SEP_KWN_MC_PORT[9:4]	6
0xBB00BDAC	TRK_LOCAL_TBL [40].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BDB0	TRK_LOCAL_TBL [40].RESERVED[31:30]	2
0xBB00BDB0	TRK_LOCAL_TBL [40].AVL_TRK_PORT7[29:24]	6
0xBB00BDB0	TRK_LOCAL_TBL [40].AVL_TRK_PORT6[23:18]	6
0xBB00BDB0	TRK_LOCAL_TBL [40].AVL_TRK_PORT5[17:12]	6
0xBB00BDB0	TRK_LOCAL_TBL [40].AVL_TRK_PORT4[11:6]	6
0xBB00BDB0	TRK_LOCAL_TBL [40].AVL_TRK_PORT3[5:0]	6
0xBB00BDB4	TRK_LOCAL_TBL [40].RESERVED[31:30]	2
0xBB00BDB4	TRK_LOCAL_TBL [40].AVL_TRK_PORT2[29:24]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BDB4	TRK_LOCAL_TBL [40].AVL_TRK_PORT1[23:18]	6
0xBB00BDB4	TRK_LOCAL_TBL [40].AVL_TRK_PORT0[17:12]	6
0xBB00BDB4	TRK_LOCAL_TBL [40].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BDB4	TRK_LOCAL_TBL [40].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BDB8	TRK_LOCAL_TBL [41].RESERVED[31:13]	19
0xBB00BDB8	TRK_LOCAL_TBL [41].NUM_TX_CANDI[12:9]	4
0xBB00BDB8	TRK_LOCAL_TBL [41].L2_HASH_MSK_IDX[8:8]	1
0xBB00BDB8	TRK_LOCAL_TBL [41].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BDB8	TRK_LOCAL_TBL [41].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BDB8	TRK_LOCAL_TBL [41].TRK_PORT7[5:0]	6
0xBB00BDBC	TRK_LOCAL_TBL [41].RESERVED[31:30]	2
0xBB00BDBC	TRK_LOCAL_TBL [41].TRK_PORT6[29:24]	6
0xBB00BDBC	TRK_LOCAL_TBL [41].TRK_PORT5[23:18]	6
0xBB00BDBC	TRK_LOCAL_TBL [41].TRK_PORT4[17:12]	6
0xBB00BDBC	TRK_LOCAL_TBL [41].TRK_PORT3[11:6]	6
0xBB00BDBC	TRK_LOCAL_TBL [41].TRK_PORT2[5:0]	6
0xBB00BDC0	TRK_LOCAL_TBL [41].RESERVED[31:28]	4
0xBB00BDC0	TRK_LOCAL_TBL [41].TRK_PORT1[27:22]	6
0xBB00BDC0	TRK_LOCAL_TBL [41].TRK_PORT0[21:16]	6
0xBB00BDC0	TRK_LOCAL_TBL [41].SEP_FLOOD_PORT[15:10]	6
0xBB00BDC0	TRK_LOCAL_TBL [41].SEP_KWN_MC_PORT[9:4]	6
0xBB00BDC0	TRK_LOCAL_TBL [41].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BDC4	TRK_LOCAL_TBL [41].RESERVED[31:30]	2
0xBB00BDC4	TRK_LOCAL_TBL [41].AVL_TRK_PORT7[29:24]	6
0xBB00BDC4	TRK_LOCAL_TBL [41].AVL_TRK_PORT6[23:18]	6
0xBB00BDC4	TRK_LOCAL_TBL [41].AVL_TRK_PORT5[17:12]	6
0xBB00BDC4	TRK_LOCAL_TBL [41].AVL_TRK_PORT4[11:6]	6
0xBB00BDC4	TRK_LOCAL_TBL [41].AVL_TRK_PORT3[5:0]	6
0xBB00BDC8	TRK_LOCAL_TBL [41].RESERVED[31:30]	2
0xBB00BDC8	TRK_LOCAL_TBL [41].AVL_TRK_PORT2[29:24]	6
0xBB00BDC8	TRK_LOCAL_TBL [41].AVL_TRK_PORT1[23:18]	6
0xBB00BDC8	TRK_LOCAL_TBL [41].AVL_TRK_PORT0[17:12]	6
0xBB00BDC8	TRK_LOCAL_TBL [41].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BDC8	TRK_LOCAL_TBL [41].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BDCC	TRK_LOCAL_TBL [42].RESERVED[31:13]	19
0xBB00BDCC	TRK_LOCAL_TBL [42].NUM_TX_CANDI[12:9]	4
0xBB00BDCC	TRK_LOCAL_TBL [42].L2_HASH_MSK_IDX[8:8]	1
0xBB00BDCC	TRK_LOCAL_TBL [42].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BDCC	TRK_LOCAL_TBL [42].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BDCC	TRK_LOCAL_TBL [42].TRK_PORT7[5:0]	6
0xBB00BDD0	TRK_LOCAL_TBL [42].RESERVED[31:30]	2
0xBB00BDD0	TRK_LOCAL_TBL [42].TRK_PORT6[29:24]	6
0xBB00BDD0	TRK_LOCAL_TBL [42].TRK_PORT5[23:18]	6
0xBB00BDD0	TRK_LOCAL_TBL [42].TRK_PORT4[17:12]	6
0xBB00BDD0	TRK_LOCAL_TBL [42].TRK_PORT3[11:6]	6
0xBB00BDD0	TRK_LOCAL_TBL [42].TRK_PORT2[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BDD4	TRK_LOCAL_TBL [42].RESERVED[31:28]	4
0xBB00BDD4	TRK_LOCAL_TBL [42].TRK_PORT1[27:22]	6
0xBB00BDD4	TRK_LOCAL_TBL [42].TRK_PORT0[21:16]	6
0xBB00BDD4	TRK_LOCAL_TBL [42].SEP_FLOOD_PORT[15:10]	6
0xBB00BDD4	TRK_LOCAL_TBL [42].SEP_KWN_MC_PORT[9:4]	6
0xBB00BDD4	TRK_LOCAL_TBL [42].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BDD8	TRK_LOCAL_TBL [42].RESERVED[31:30]	2
0xBB00BDD8	TRK_LOCAL_TBL [42].AVL_TRK_PORT7[29:24]	6
0xBB00BDD8	TRK_LOCAL_TBL [42].AVL_TRK_PORT6[23:18]	6
0xBB00BDD8	TRK_LOCAL_TBL [42].AVL_TRK_PORT5[17:12]	6
0xBB00BDD8	TRK_LOCAL_TBL [42].AVL_TRK_PORT4[11:6]	6
0xBB00BDD8	TRK_LOCAL_TBL [42].AVL_TRK_PORT3[5:0]	6
0xBB00BDDC	TRK_LOCAL_TBL [42].RESERVED[31:30]	2
0xBB00BDDC	TRK_LOCAL_TBL [42].AVL_TRK_PORT2[29:24]	6
0xBB00BDDC	TRK_LOCAL_TBL [42].AVL_TRK_PORT1[23:18]	6
0xBB00BDDC	TRK_LOCAL_TBL [42].AVL_TRK_PORT0[17:12]	6
0xBB00BDDC	TRK_LOCAL_TBL [42].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BDDC	TRK_LOCAL_TBL [42].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BDE0	TRK_LOCAL_TBL [43].RESERVED[31:13]	19
0xBB00BDE0	TRK_LOCAL_TBL [43].NUM_TX_CANDI[12:9]	4
0xBB00BDE0	TRK_LOCAL_TBL [43].L2_HASH_MSK_IDX[8:8]	1
0xBB00BDE0	TRK_LOCAL_TBL [43].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BDE0	TRK_LOCAL_TBL [43].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BDE0	TRK_LOCAL_TBL [43].TRK_PORT7[5:0]	6
0xBB00BDE4	TRK_LOCAL_TBL [43].RESERVED[31:30]	2
0xBB00BDE4	TRK_LOCAL_TBL [43].TRK_PORT6[29:24]	6
0xBB00BDE4	TRK_LOCAL_TBL [43].TRK_PORT5[23:18]	6
0xBB00BDE4	TRK_LOCAL_TBL [43].TRK_PORT4[17:12]	6
0xBB00BDE4	TRK_LOCAL_TBL [43].TRK_PORT3[11:6]	6
0xBB00BDE4	TRK_LOCAL_TBL [43].TRK_PORT2[5:0]	6
0xBB00BDE8	TRK_LOCAL_TBL [43].RESERVED[31:28]	4
0xBB00BDE8	TRK_LOCAL_TBL [43].TRK_PORT1[27:22]	6
0xBB00BDE8	TRK_LOCAL_TBL [43].TRK_PORT0[21:16]	6
0xBB00BDE8	TRK_LOCAL_TBL [43].SEP_FLOOD_PORT[15:10]	6
0xBB00BDE8	TRK_LOCAL_TBL [43].SEP_KWN_MC_PORT[9:4]	6
0xBB00BDE8	TRK_LOCAL_TBL [43].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BDEC	TRK_LOCAL_TBL [43].RESERVED[31:30]	2
0xBB00BDEC	TRK_LOCAL_TBL [43].AVL_TRK_PORT7[29:24]	6
0xBB00BDEC	TRK_LOCAL_TBL [43].AVL_TRK_PORT6[23:18]	6
0xBB00BDEC	TRK_LOCAL_TBL [43].AVL_TRK_PORT5[17:12]	6
0xBB00BDEC	TRK_LOCAL_TBL [43].AVL_TRK_PORT4[11:6]	6
0xBB00BDEC	TRK_LOCAL_TBL [43].AVL_TRK_PORT3[5:0]	6
0xBB00BDF0	TRK_LOCAL_TBL [43].RESERVED[31:30]	2
0xBB00BDF0	TRK_LOCAL_TBL [43].AVL_TRK_PORT2[29:24]	6
0xBB00BDF0	TRK_LOCAL_TBL [43].AVL_TRK_PORT1[23:18]	6
0xBB00BDF0	TRK_LOCAL_TBL [43].AVL_TRK_PORT0[17:12]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BDF0	TRK_LOCAL_TBL [43].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BDF0	TRK_LOCAL_TBL [43].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BDF4	TRK_LOCAL_TBL [44].RESERVED[31:13]	19
0xBB00BDF4	TRK_LOCAL_TBL [44].NUM_TX_CANDI[12:9]	4
0xBB00BDF4	TRK_LOCAL_TBL [44].L2_HASH_MSK_IDX[8:8]	1
0xBB00BDF4	TRK_LOCAL_TBL [44].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BDF4	TRK_LOCAL_TBL [44].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BDF4	TRK_LOCAL_TBL [44].TRK_PORT7[5:0]	6
0xBB00BDF8	TRK_LOCAL_TBL [44].RESERVED[31:30]	2
0xBB00BDF8	TRK_LOCAL_TBL [44].TRK_PORT6[29:24]	6
0xBB00BDF8	TRK_LOCAL_TBL [44].TRK_PORT5[23:18]	6
0xBB00BDF8	TRK_LOCAL_TBL [44].TRK_PORT4[17:12]	6
0xBB00BDF8	TRK_LOCAL_TBL [44].TRK_PORT3[11:6]	6
0xBB00BDF8	TRK_LOCAL_TBL [44].TRK_PORT2[5:0]	6
0xBB00BDFA	TRK_LOCAL_TBL [44].RESERVED[31:28]	4
0xBB00BDFA	TRK_LOCAL_TBL [44].TRK_PORT1[27:22]	6
0xBB00BDFA	TRK_LOCAL_TBL [44].TRK_PORT0[21:16]	6
0xBB00BDFA	TRK_LOCAL_TBL [44].SEP_FLOOD_PORT[15:10]	6
0xBB00BDFA	TRK_LOCAL_TBL [44].SEP_KWN_MC_PORT[9:4]	6
0xBB00BDFA	TRK_LOCAL_TBL [44].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE00	TRK_LOCAL_TBL [44].RESERVED[31:30]	2
0xBB00BE00	TRK_LOCAL_TBL [44].AVL_TRK_PORT7[29:24]	6
0xBB00BE00	TRK_LOCAL_TBL [44].AVL_TRK_PORT6[23:18]	6
0xBB00BE00	TRK_LOCAL_TBL [44].AVL_TRK_PORT5[17:12]	6
0xBB00BE00	TRK_LOCAL_TBL [44].AVL_TRK_PORT4[11:6]	6
0xBB00BE00	TRK_LOCAL_TBL [44].AVL_TRK_PORT3[5:0]	6
0xBB00BE04	TRK_LOCAL_TBL [44].RESERVED[31:30]	2
0xBB00BE04	TRK_LOCAL_TBL [44].AVL_TRK_PORT2[29:24]	6
0xBB00BE04	TRK_LOCAL_TBL [44].AVL_TRK_PORT1[23:18]	6
0xBB00BE04	TRK_LOCAL_TBL [44].AVL_TRK_PORT0[17:12]	6
0xBB00BE04	TRK_LOCAL_TBL [44].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE04	TRK_LOCAL_TBL [44].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BE08	TRK_LOCAL_TBL [45].RESERVED[31:13]	19
0xBB00BE08	TRK_LOCAL_TBL [45].NUM_TX_CANDI[12:9]	4
0xBB00BE08	TRK_LOCAL_TBL [45].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE08	TRK_LOCAL_TBL [45].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE08	TRK_LOCAL_TBL [45].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE08	TRK_LOCAL_TBL [45].TRK_PORT7[5:0]	6
0xBB00BE0C	TRK_LOCAL_TBL [45].RESERVED[31:30]	2
0xBB00BE0C	TRK_LOCAL_TBL [45].TRK_PORT6[29:24]	6
0xBB00BE0C	TRK_LOCAL_TBL [45].TRK_PORT5[23:18]	6
0xBB00BE0C	TRK_LOCAL_TBL [45].TRK_PORT4[17:12]	6
0xBB00BE0C	TRK_LOCAL_TBL [45].TRK_PORT3[11:6]	6
0xBB00BE0C	TRK_LOCAL_TBL [45].TRK_PORT2[5:0]	6
0xBB00BE10	TRK_LOCAL_TBL [45].RESERVED[31:28]	4
0xBB00BE10	TRK_LOCAL_TBL [45].TRK_PORT1[27:22]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BE10	TRK_LOCAL_TBL [45].TRK_PORT0[21:16]	6
0xBB00BE10	TRK_LOCAL_TBL [45].SEP_FLOOD_PORT[15:10]	6
0xBB00BE10	TRK_LOCAL_TBL [45].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE10	TRK_LOCAL_TBL [45].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE14	TRK_LOCAL_TBL [45].RESERVED[31:30]	2
0xBB00BE14	TRK_LOCAL_TBL [45].AVL_TRK_PORT7[29:24]	6
0xBB00BE14	TRK_LOCAL_TBL [45].AVL_TRK_PORT6[23:18]	6
0xBB00BE14	TRK_LOCAL_TBL [45].AVL_TRK_PORTS[17:12]	6
0xBB00BE14	TRK_LOCAL_TBL [45].AVL_TRK_PORT4[11:6]	6
0xBB00BE14	TRK_LOCAL_TBL [45].AVL_TRK_PORT3[5:0]	6
0xBB00BE18	TRK_LOCAL_TBL [45].RESERVED[31:30]	2
0xBB00BE18	TRK_LOCAL_TBL [45].AVL_TRK_PORT2[29:24]	6
0xBB00BE18	TRK_LOCAL_TBL [45].AVL_TRK_PORT1[23:18]	6
0xBB00BE18	TRK_LOCAL_TBL [45].AVL_TRK_PORT0[17:12]	6
0xBB00BE18	TRK_LOCAL_TBL [45].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE18	TRK_LOCAL_TBL [45].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BE1C	TRK_LOCAL_TBL [46].RESERVED[31:13]	19
0xBB00BE1C	TRK_LOCAL_TBL [46].NUM_TX_CANDI[12:9]	4
0xBB00BE1C	TRK_LOCAL_TBL [46].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE1C	TRK_LOCAL_TBL [46].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE1C	TRK_LOCAL_TBL [46].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE1C	TRK_LOCAL_TBL [46].TRK_PORT7[5:0]	6
0xBB00BE20	TRK_LOCAL_TBL [46].RESERVED[31:30]	2
0xBB00BE20	TRK_LOCAL_TBL [46].TRK_PORT6[29:24]	6
0xBB00BE20	TRK_LOCAL_TBL [46].TRK_PORT5[23:18]	6
0xBB00BE20	TRK_LOCAL_TBL [46].TRK_PORT4[17:12]	6
0xBB00BE20	TRK_LOCAL_TBL [46].TRK_PORT3[11:6]	6
0xBB00BE20	TRK_LOCAL_TBL [46].TRK_PORT2[5:0]	6
0xBB00BE24	TRK_LOCAL_TBL [46].RESERVED[31:28]	4
0xBB00BE24	TRK_LOCAL_TBL [46].TRK_PORT1[27:22]	6
0xBB00BE24	TRK_LOCAL_TBL [46].TRK_PORT0[21:16]	6
0xBB00BE24	TRK_LOCAL_TBL [46].SEP_FLOOD_PORT[15:10]	6
0xBB00BE24	TRK_LOCAL_TBL [46].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE24	TRK_LOCAL_TBL [46].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE28	TRK_LOCAL_TBL [46].RESERVED[31:30]	2
0xBB00BE28	TRK_LOCAL_TBL [46].AVL_TRK_PORT7[29:24]	6
0xBB00BE28	TRK_LOCAL_TBL [46].AVL_TRK_PORT6[23:18]	6
0xBB00BE28	TRK_LOCAL_TBL [46].AVL_TRK_PORTS[17:12]	6
0xBB00BE28	TRK_LOCAL_TBL [46].AVL_TRK_PORT4[11:6]	6
0xBB00BE28	TRK_LOCAL_TBL [46].AVL_TRK_PORT3[5:0]	6
0xBB00BE2C	TRK_LOCAL_TBL [46].RESERVED[31:30]	2
0xBB00BE2C	TRK_LOCAL_TBL [46].AVL_TRK_PORT2[29:24]	6
0xBB00BE2C	TRK_LOCAL_TBL [46].AVL_TRK_PORT1[23:18]	6
0xBB00BE2C	TRK_LOCAL_TBL [46].AVL_TRK_PORT0[17:12]	6
0xBB00BE2C	TRK_LOCAL_TBL [46].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE2C	TRK_LOCAL_TBL [46].AVL_SEP_KWN_MC_PORT[5:0]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BE30	TRK_LOCAL_TBL [47].RESERVED[31:13]	19
0xBB00BE30	TRK_LOCAL_TBL [47].NUM_TX_CANDI[12:9]	4
0xBB00BE30	TRK_LOCAL_TBL [47].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE30	TRK_LOCAL_TBL [47].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE30	TRK_LOCAL_TBL [47].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE30	TRK_LOCAL_TBL [47].TRK_PORT7[5:0]	6
0xBB00BE34	TRK_LOCAL_TBL [47].RESERVED[31:30]	2
0xBB00BE34	TRK_LOCAL_TBL [47].TRK_PORT6[29:24]	6
0xBB00BE34	TRK_LOCAL_TBL [47].TRK_PORT5[23:18]	6
0xBB00BE34	TRK_LOCAL_TBL [47].TRK_PORT4[17:12]	6
0xBB00BE34	TRK_LOCAL_TBL [47].TRK_PORT3[11:6]	6
0xBB00BE34	TRK_LOCAL_TBL [47].TRK_PORT2[5:0]	6
0xBB00BE38	TRK_LOCAL_TBL [47].RESERVED[31:28]	4
0xBB00BE38	TRK_LOCAL_TBL [47].TRK_PORT1[27:22]	6
0xBB00BE38	TRK_LOCAL_TBL [47].TRK_PORT0[21:16]	6
0xBB00BE38	TRK_LOCAL_TBL [47].SEP_FLOOD_PORT[15:10]	6
0xBB00BE38	TRK_LOCAL_TBL [47].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE38	TRK_LOCAL_TBL [47].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE3C	TRK_LOCAL_TBL [47].RESERVED[31:30]	2
0xBB00BE3C	TRK_LOCAL_TBL [47].AVL_TRK_PORT7[29:24]	6
0xBB00BE3C	TRK_LOCAL_TBL [47].AVL_TRK_PORT6[23:18]	6
0xBB00BE3C	TRK_LOCAL_TBL [47].AVL_TRK_PORT5[17:12]	6
0xBB00BE3C	TRK_LOCAL_TBL [47].AVL_TRK_PORT4[11:6]	6
0xBB00BE3C	TRK_LOCAL_TBL [47].AVL_TRK_PORT3[5:0]	6
0xBB00BE40	TRK_LOCAL_TBL [47].RESERVED[31:30]	2
0xBB00BE40	TRK_LOCAL_TBL [47].AVL_TRK_PORT2[29:24]	6
0xBB00BE40	TRK_LOCAL_TBL [47].AVL_TRK_PORT1[23:18]	6
0xBB00BE40	TRK_LOCAL_TBL [47].AVL_TRK_PORT0[17:12]	6
0xBB00BE40	TRK_LOCAL_TBL [47].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE40	TRK_LOCAL_TBL [47].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BE44	TRK_LOCAL_TBL [48].RESERVED[31:13]	19
0xBB00BE44	TRK_LOCAL_TBL [48].NUM_TX_CANDI[12:9]	4
0xBB00BE44	TRK_LOCAL_TBL [48].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE44	TRK_LOCAL_TBL [48].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE44	TRK_LOCAL_TBL [48].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE44	TRK_LOCAL_TBL [48].TRK_PORT7[5:0]	6
0xBB00BE48	TRK_LOCAL_TBL [48].RESERVED[31:30]	2
0xBB00BE48	TRK_LOCAL_TBL [48].TRK_PORT6[29:24]	6
0xBB00BE48	TRK_LOCAL_TBL [48].TRK_PORT5[23:18]	6
0xBB00BE48	TRK_LOCAL_TBL [48].TRK_PORT4[17:12]	6
0xBB00BE48	TRK_LOCAL_TBL [48].TRK_PORT3[11:6]	6
0xBB00BE48	TRK_LOCAL_TBL [48].TRK_PORT2[5:0]	6
0xBB00BE4C	TRK_LOCAL_TBL [48].RESERVED[31:28]	4
0xBB00BE4C	TRK_LOCAL_TBL [48].TRK_PORT1[27:22]	6
0xBB00BE4C	TRK_LOCAL_TBL [48].TRK_PORT0[21:16]	6
0xBB00BE4C	TRK_LOCAL_TBL [48].SEP_FLOOD_PORT[15:10]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BE4C	TRK_LOCAL_TBL [48].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE4C	TRK_LOCAL_TBL [48].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE50	TRK_LOCAL_TBL [48].RESERVED[31:30]	2
0xBB00BE50	TRK_LOCAL_TBL [48].AVL_TRK_PORT7[29:24]	6
0xBB00BE50	TRK_LOCAL_TBL [48].AVL_TRK_PORT6[23:18]	6
0xBB00BE50	TRK_LOCAL_TBL [48].AVL_TRK_PORT5[17:12]	6
0xBB00BE50	TRK_LOCAL_TBL [48].AVL_TRK_PORT4[11:6]	6
0xBB00BE50	TRK_LOCAL_TBL [48].AVL_TRK_PORT3[5:0]	6
0xBB00BE54	TRK_LOCAL_TBL [48].RESERVED[31:30]	2
0xBB00BE54	TRK_LOCAL_TBL [48].AVL_TRK_PORT2[29:24]	6
0xBB00BE54	TRK_LOCAL_TBL [48].AVL_TRK_PORT1[23:18]	6
0xBB00BE54	TRK_LOCAL_TBL [48].AVL_TRK_PORT0[17:12]	6
0xBB00BE54	TRK_LOCAL_TBL [48].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE54	TRK_LOCAL_TBL [48].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BE58	TRK_LOCAL_TBL [49].RESERVED[31:13]	19
0xBB00BE58	TRK_LOCAL_TBL [49].NUM_TX_CANDI[12:9]	4
0xBB00BE58	TRK_LOCAL_TBL [49].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE58	TRK_LOCAL_TBL [49].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE58	TRK_LOCAL_TBL [49].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE58	TRK_LOCAL_TBL [49].TRK_PORT7[5:0]	6
0xBB00BE5C	TRK_LOCAL_TBL [49].RESERVED[31:30]	2
0xBB00BE5C	TRK_LOCAL_TBL [49].TRK_PORT6[29:24]	6
0xBB00BE5C	TRK_LOCAL_TBL [49].TRK_PORT5[23:18]	6
0xBB00BE5C	TRK_LOCAL_TBL [49].TRK_PORT4[17:12]	6
0xBB00BE5C	TRK_LOCAL_TBL [49].TRK_PORT3[11:6]	6
0xBB00BE5C	TRK_LOCAL_TBL [49].TRK_PORT2[5:0]	6
0xBB00BE60	TRK_LOCAL_TBL [49].RESERVED[31:28]	4
0xBB00BE60	TRK_LOCAL_TBL [49].TRK_PORT1[27:22]	6
0xBB00BE60	TRK_LOCAL_TBL [49].TRK_PORT0[21:16]	6
0xBB00BE60	TRK_LOCAL_TBL [49].SEP_FLOOD_PORT[15:10]	6
0xBB00BE60	TRK_LOCAL_TBL [49].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE60	TRK_LOCAL_TBL [49].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE64	TRK_LOCAL_TBL [49].RESERVED[31:30]	2
0xBB00BE64	TRK_LOCAL_TBL [49].AVL_TRK_PORT7[29:24]	6
0xBB00BE64	TRK_LOCAL_TBL [49].AVL_TRK_PORT6[23:18]	6
0xBB00BE64	TRK_LOCAL_TBL [49].AVL_TRK_PORTS[17:12]	6
0xBB00BE64	TRK_LOCAL_TBL [49].AVL_TRK_PORT4[11:6]	6
0xBB00BE64	TRK_LOCAL_TBL [49].AVL_TRK_PORT3[5:0]	6
0xBB00BE68	TRK_LOCAL_TBL [49].RESERVED[31:30]	2
0xBB00BE68	TRK_LOCAL_TBL [49].AVL_TRK_PORT2[29:24]	6
0xBB00BE68	TRK_LOCAL_TBL [49].AVL_TRK_PORT1[23:18]	6
0xBB00BE68	TRK_LOCAL_TBL [49].AVL_TRK_PORT0[17:12]	6
0xBB00BE68	TRK_LOCAL_TBL [49].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE68	TRK_LOCAL_TBL [49].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BE6C	TRK_LOCAL_TBL [50].RESERVED[31:13]	19
0xBB00BE6C	TRK_LOCAL_TBL [50].NUM_TX_CANDI[12:9]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BE6C	TRK_LOCAL_TBL [50].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE6C	TRK_LOCAL_TBL [50].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE6C	TRK_LOCAL_TBL [50].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE6C	TRK_LOCAL_TBL [50].TRK_PORT7[5:0]	6
0xBB00BE70	TRK_LOCAL_TBL [50].RESERVED[31:30]	2
0xBB00BE70	TRK_LOCAL_TBL [50].TRK_PORT6[29:24]	6
0xBB00BE70	TRK_LOCAL_TBL [50].TRK_PORT5[23:18]	6
0xBB00BE70	TRK_LOCAL_TBL [50].TRK_PORT4[17:12]	6
0xBB00BE70	TRK_LOCAL_TBL [50].TRK_PORT3[11:6]	6
0xBB00BE70	TRK_LOCAL_TBL [50].TRK_PORT2[5:0]	6
0xBB00BE74	TRK_LOCAL_TBL [50].RESERVED[31:28]	4
0xBB00BE74	TRK_LOCAL_TBL [50].TRK_PORT1[27:22]	6
0xBB00BE74	TRK_LOCAL_TBL [50].TRK_PORT0[21:16]	6
0xBB00BE74	TRK_LOCAL_TBL [50].SEP_FLOOD_PORT[15:10]	6
0xBB00BE74	TRK_LOCAL_TBL [50].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE74	TRK_LOCAL_TBL [50].NUM_AVL_TX_CANDI[3:0]	4
0xBB00BE78	TRK_LOCAL_TBL [50].RESERVED[31:30]	2
0xBB00BE78	TRK_LOCAL_TBL [50].AVL_TRK_PORT7[29:24]	6
0xBB00BE78	TRK_LOCAL_TBL [50].AVL_TRK_PORT6[23:18]	6
0xBB00BE78	TRK_LOCAL_TBL [50].AVL_TRK_PORT5[17:12]	6
0xBB00BE78	TRK_LOCAL_TBL [50].AVL_TRK_PORT4[11:6]	6
0xBB00BE78	TRK_LOCAL_TBL [50].AVL_TRK_PORT3[5:0]	6
0xBB00BE7C	TRK_LOCAL_TBL [50].RESERVED[31:30]	2
0xBB00BE7C	TRK_LOCAL_TBL [50].AVL_TRK_PORT2[29:24]	6
0xBB00BE7C	TRK_LOCAL_TBL [50].AVL_TRK_PORT1[23:18]	6
0xBB00BE7C	TRK_LOCAL_TBL [50].AVL_TRK_PORT0[17:12]	6
0xBB00BE7C	TRK_LOCAL_TBL [50].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE7C	TRK_LOCAL_TBL [50].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BE80	TRK_LOCAL_TBL [51].RESERVED[31:13]	19
0xBB00BE80	TRK_LOCAL_TBL [51].NUM_TX_CANDI[12:9]	4
0xBB00BE80	TRK_LOCAL_TBL [51].L2_HASH_MSK_IDX[8:8]	1
0xBB00BE80	TRK_LOCAL_TBL [51].IP4_HASH_MSK_IDX[7:7]	1
0xBB00BE80	TRK_LOCAL_TBL [51].IP6_HASH_MSK_IDX[6:6]	1
0xBB00BE80	TRK_LOCAL_TBL [51].TRK_PORT7[5:0]	6
0xBB00BE84	TRK_LOCAL_TBL [51].RESERVED[31:30]	2
0xBB00BE84	TRK_LOCAL_TBL [51].TRK_PORT6[29:24]	6
0xBB00BE84	TRK_LOCAL_TBL [51].TRK_PORT5[23:18]	6
0xBB00BE84	TRK_LOCAL_TBL [51].TRK_PORT4[17:12]	6
0xBB00BE84	TRK_LOCAL_TBL [51].TRK_PORT3[11:6]	6
0xBB00BE84	TRK_LOCAL_TBL [51].TRK_PORT2[5:0]	6
0xBB00BE88	TRK_LOCAL_TBL [51].RESERVED[31:28]	4
0xBB00BE88	TRK_LOCAL_TBL [51].TRK_PORT1[27:22]	6
0xBB00BE88	TRK_LOCAL_TBL [51].TRK_PORT0[21:16]	6
0xBB00BE88	TRK_LOCAL_TBL [51].SEP_FLOOD_PORT[15:10]	6
0xBB00BE88	TRK_LOCAL_TBL [51].SEP_KWN_MC_PORT[9:4]	6
0xBB00BE88	TRK_LOCAL_TBL [51].NUM_AVL_TX_CANDI[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BE8C	TRK_LOCAL_TBL [51].RESERVED[31:30]	2
0xBB00BE8C	TRK_LOCAL_TBL [51].AVL_TRK_PORT7[29:24]	6
0xBB00BE8C	TRK_LOCAL_TBL [51].AVL_TRK_PORT6[23:18]	6
0xBB00BE8C	TRK_LOCAL_TBL [51].AVL_TRK_PORT5[17:12]	6
0xBB00BE8C	TRK_LOCAL_TBL [51].AVL_TRK_PORT4[11:6]	6
0xBB00BE8C	TRK_LOCAL_TBL [51].AVL_TRK_PORT3[5:0]	6
0xBB00BE90	TRK_LOCAL_TBL [51].RESERVED[31:30]	2
0xBB00BE90	TRK_LOCAL_TBL [51].AVL_TRK_PORT2[29:24]	6
0xBB00BE90	TRK_LOCAL_TBL [51].AVL_TRK_PORT1[23:18]	6
0xBB00BE90	TRK_LOCAL_TBL [51].AVL_TRK_PORT0[17:12]	6
0xBB00BE90	TRK_LOCAL_TBL [51].AVL_SEP_FLOOD_PORT[11:6]	6
0xBB00BE90	TRK_LOCAL_TBL [51].AVL_SEP_KWN_MC_PORT[5:0]	6
0xBB00BA80	TRK_LOCAL_TBL_REFRESH.RESERVED[31:1]	31
0xBB00BA80	TRK_LOCAL_TBL_REFRESH.TRK_LOCAL_TBL_REFRESH[0:0]	1
0xBB00B8D0	TRK_MBR_CTRL [0].RESERVED[31:24]	8
0xBB00B8D0	TRK_MBR_CTRL [0].TRK_PPM_55_32[23:0]	24
0xBB00B8D4	TRK_MBR_CTRL [0].TRK_PPM_31_0[31:0]	32
0xBB00B8D8	TRK_MBR_CTRL [1].RESERVED[31:24]	8
0xBB00B8D8	TRK_MBR_CTRL [1].TRK_PPM_55_32[23:0]	24
0xBB00B8DC	TRK_MBR_CTRL [1].TRK_PPM_31_0[31:0]	32
0xBB00B8E0	TRK_MBR_CTRL [2].RESERVED[31:24]	8
0xBB00B8E0	TRK_MBR_CTRL [2].TRK_PPM_55_32[23:0]	24
0xBB00B8E4	TRK_MBR_CTRL [2].TRK_PPM_31_0[31:0]	32
0xBB00B8E8	TRK_MBR_CTRL [3].RESERVED[31:24]	8
0xBB00B8E8	TRK_MBR_CTRL [3].TRK_PPM_55_32[23:0]	24
0xBB00B8EC	TRK_MBR_CTRL [3].TRK_PPM_31_0[31:0]	32
0xBB00B8F0	TRK_MBR_CTRL [4].RESERVED[31:24]	8
0xBB00B8F0	TRK_MBR_CTRL [4].TRK_PPM_55_32[23:0]	24
0xBB00B8F4	TRK_MBR_CTRL [4].TRK_PPM_31_0[31:0]	32
0xBB00B8F8	TRK_MBR_CTRL [5].RESERVED[31:24]	8
0xBB00B8F8	TRK_MBR_CTRL [5].TRK_PPM_55_32[23:0]	24
0xBB00B8FC	TRK_MBR_CTRL [5].TRK_PPM_31_0[31:0]	32
0xBB00B900	TRK_MBR_CTRL [6].RESERVED[31:24]	8
0xBB00B900	TRK_MBR_CTRL [6].TRK_PPM_55_32[23:0]	24
0xBB00B904	TRK_MBR_CTRL [6].TRK_PPM_31_0[31:0]	32
0xBB00B908	TRK_MBR_CTRL [7].RESERVED[31:24]	8
0xBB00B908	TRK_MBR_CTRL [7].TRK_PPM_55_32[23:0]	24
0xBB00B90C	TRK_MBR_CTRL [7].TRK_PPM_31_0[31:0]	32
0xBB00B910	TRK_MBR_CTRL [8].RESERVED[31:24]	8
0xBB00B910	TRK_MBR_CTRL [8].TRK_PPM_55_32[23:0]	24
0xBB00B914	TRK_MBR_CTRL [8].TRK_PPM_31_0[31:0]	32
0xBB00B918	TRK_MBR_CTRL [9].RESERVED[31:24]	8
0xBB00B918	TRK_MBR_CTRL [9].TRK_PPM_55_32[23:0]	24
0xBB00B91C	TRK_MBR_CTRL [9].TRK_PPM_31_0[31:0]	32
0xBB00B920	TRK_MBR_CTRL [10].RESERVED[31:24]	8
0xBB00B920	TRK_MBR_CTRL [10].TRK_PPM_55_32[23:0]	24

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B924	TRK_MBR_CTRL [10].TRK_PPM_31_0[31:0]	32
0xBB00B928	TRK_MBR_CTRL [11].RESERVED[31:24]	8
0xBB00B928	TRK_MBR_CTRL [11].TRK_PPM_55_32[23:0]	24
0xBB00B92C	TRK_MBR_CTRL [11].TRK_PPM_31_0[31:0]	32
0xBB00B930	TRK_MBR_CTRL [12].RESERVED[31:24]	8
0xBB00B930	TRK_MBR_CTRL [12].TRK_PPM_55_32[23:0]	24
0xBB00B934	TRK_MBR_CTRL [12].TRK_PPM_31_0[31:0]	32
0xBB00B938	TRK_MBR_CTRL [13].RESERVED[31:24]	8
0xBB00B938	TRK_MBR_CTRL [13].TRK_PPM_55_32[23:0]	24
0xBB00B93C	TRK_MBR_CTRL [13].TRK_PPM_31_0[31:0]	32
0xBB00B940	TRK_MBR_CTRL [14].RESERVED[31:24]	8
0xBB00B940	TRK_MBR_CTRL [14].TRK_PPM_55_32[23:0]	24
0xBB00B944	TRK_MBR_CTRL [14].TRK_PPM_31_0[31:0]	32
0xBB00B948	TRK_MBR_CTRL [15].RESERVED[31:24]	8
0xBB00B948	TRK_MBR_CTRL [15].TRK_PPM_55_32[23:0]	24
0xBB00B94C	TRK_MBR_CTRL [15].TRK_PPM_31_0[31:0]	32
0xBB00B950	TRK_MBR_CTRL [16].RESERVED[31:24]	8
0xBB00B950	TRK_MBR_CTRL [16].TRK_PPM_55_32[23:0]	24
0xBB00B954	TRK_MBR_CTRL [16].TRK_PPM_31_0[31:0]	32
0xBB00B958	TRK_MBR_CTRL [17].RESERVED[31:24]	8
0xBB00B958	TRK_MBR_CTRL [17].TRK_PPM_55_32[23:0]	24
0xBB00B95C	TRK_MBR_CTRL [17].TRK_PPM_31_0[31:0]	32
0xBB00B960	TRK_MBR_CTRL [18].RESERVED[31:24]	8
0xBB00B960	TRK_MBR_CTRL [18].TRK_PPM_55_32[23:0]	24
0xBB00B964	TRK_MBR_CTRL [18].TRK_PPM_31_0[31:0]	32
0xBB00B968	TRK_MBR_CTRL [19].RESERVED[31:24]	8
0xBB00B968	TRK_MBR_CTRL [19].TRK_PPM_55_32[23:0]	24
0xBB00B96C	TRK_MBR_CTRL [19].TRK_PPM_31_0[31:0]	32
0xBB00B970	TRK_MBR_CTRL [20].RESERVED[31:24]	8
0xBB00B970	TRK_MBR_CTRL [20].TRK_PPM_55_32[23:0]	24
0xBB00B974	TRK_MBR_CTRL [20].TRK_PPM_31_0[31:0]	32
0xBB00B978	TRK_MBR_CTRL [21].RESERVED[31:24]	8
0xBB00B978	TRK_MBR_CTRL [21].TRK_PPM_55_32[23:0]	24
0xBB00B97C	TRK_MBR_CTRL [21].TRK_PPM_31_0[31:0]	32
0xBB00B980	TRK_MBR_CTRL [22].RESERVED[31:24]	8
0xBB00B980	TRK_MBR_CTRL [22].TRK_PPM_55_32[23:0]	24
0xBB00B984	TRK_MBR_CTRL [22].TRK_PPM_31_0[31:0]	32
0xBB00B988	TRK_MBR_CTRL [23].RESERVED[31:24]	8
0xBB00B988	TRK_MBR_CTRL [23].TRK_PPM_55_32[23:0]	24
0xBB00B98C	TRK_MBR_CTRL [23].TRK_PPM_31_0[31:0]	32
0xBB00B990	TRK_MBR_CTRL [24].RESERVED[31:24]	8
0xBB00B990	TRK_MBR_CTRL [24].TRK_PPM_55_32[23:0]	24
0xBB00B994	TRK_MBR_CTRL [24].TRK_PPM_31_0[31:0]	32
0xBB00B998	TRK_MBR_CTRL [25].RESERVED[31:24]	8
0xBB00B998	TRK_MBR_CTRL [25].TRK_PPM_55_32[23:0]	24
0xBB00B99C	TRK_MBR_CTRL [25].TRK_PPM_31_0[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00B9A0	TRK_MBR_CTRL [26].RESERVED[31:24]	8
0xBB00B9A0	TRK_MBR_CTRL [26].TRK_PPM_55_32[23:0]	24
0xBB00B9A4	TRK_MBR_CTRL [26].TRK_PPM_31_0[31:0]	32
0xBB00B9A8	TRK_MBR_CTRL [27].RESERVED[31:24]	8
0xBB00B9A8	TRK_MBR_CTRL [27].TRK_PPM_55_32[23:0]	24
0xBB00B9AC	TRK_MBR_CTRL [27].TRK_PPM_31_0[31:0]	32
0xBB00B9B0	TRK_MBR_CTRL [28].RESERVED[31:24]	8
0xBB00B9B0	TRK_MBR_CTRL [28].TRK_PPM_55_32[23:0]	24
0xBB00B9B4	TRK_MBR_CTRL [28].TRK_PPM_31_0[31:0]	32
0xBB00B9B8	TRK_MBR_CTRL [29].RESERVED[31:24]	8
0xBB00B9B8	TRK_MBR_CTRL [29].TRK_PPM_55_32[23:0]	24
0xBB00B9BC	TRK_MBR_CTRL [29].TRK_PPM_31_0[31:0]	32
0xBB00B9C0	TRK_MBR_CTRL [30].RESERVED[31:24]	8
0xBB00B9C0	TRK_MBR_CTRL [30].TRK_PPM_55_32[23:0]	24
0xBB00B9C4	TRK_MBR_CTRL [30].TRK_PPM_31_0[31:0]	32
0xBB00B9C8	TRK_MBR_CTRL [31].RESERVED[31:24]	8
0xBB00B9C8	TRK_MBR_CTRL [31].TRK_PPM_55_32[23:0]	24
0xBB00B9CC	TRK_MBR_CTRL [31].TRK_PPM_31_0[31:0]	32
0xBB00B9D0	TRK_MBR_CTRL [32].RESERVED[31:24]	8
0xBB00B9D0	TRK_MBR_CTRL [32].TRK_PPM_55_32[23:0]	24
0xBB00B9D4	TRK_MBR_CTRL [32].TRK_PPM_31_0[31:0]	32
0xBB00B9D8	TRK_MBR_CTRL [33].RESERVED[31:24]	8
0xBB00B9D8	TRK_MBR_CTRL [33].TRK_PPM_55_32[23:0]	24
0xBB00B9DC	TRK_MBR_CTRL [33].TRK_PPM_31_0[31:0]	32
0xBB00B9E0	TRK_MBR_CTRL [34].RESERVED[31:24]	8
0xBB00B9E0	TRK_MBR_CTRL [34].TRK_PPM_55_32[23:0]	24
0xBB00B9E4	TRK_MBR_CTRL [34].TRK_PPM_31_0[31:0]	32
0xBB00B9E8	TRK_MBR_CTRL [35].RESERVED[31:24]	8
0xBB00B9E8	TRK_MBR_CTRL [35].TRK_PPM_55_32[23:0]	24
0xBB00B9EC	TRK_MBR_CTRL [35].TRK_PPM_31_0[31:0]	32
0xBB00B9F0	TRK_MBR_CTRL [36].RESERVED[31:24]	8
0xBB00B9F0	TRK_MBR_CTRL [36].TRK_PPM_55_32[23:0]	24
0xBB00B9F4	TRK_MBR_CTRL [36].TRK_PPM_31_0[31:0]	32
0xBB00B9F8	TRK_MBR_CTRL [37].RESERVED[31:24]	8
0xBB00B9F8	TRK_MBR_CTRL [37].TRK_PPM_55_32[23:0]	24
0xBB00B9FC	TRK_MBR_CTRL [37].TRK_PPM_31_0[31:0]	32
0xBB00BA00	TRK_MBR_CTRL [38].RESERVED[31:24]	8
0xBB00BA00	TRK_MBR_CTRL [38].TRK_PPM_55_32[23:0]	24
0xBB00BA04	TRK_MBR_CTRL [38].TRK_PPM_31_0[31:0]	32
0xBB00BA08	TRK_MBR_CTRL [39].RESERVED[31:24]	8
0xBB00BA08	TRK_MBR_CTRL [39].TRK_PPM_55_32[23:0]	24
0xBB00BA0C	TRK_MBR_CTRL [39].TRK_PPM_31_0[31:0]	32
0xBB00BA10	TRK_MBR_CTRL [40].RESERVED[31:24]	8
0xBB00BA10	TRK_MBR_CTRL [40].TRK_PPM_55_32[23:0]	24
0xBB00BA14	TRK_MBR_CTRL [40].TRK_PPM_31_0[31:0]	32
0xBB00BA18	TRK_MBR_CTRL [41].RESERVED[31:24]	8

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BA18	TRK_MBR_CTRL [41].TRK_PPM_55_32[23:0]	24
0xBB00BA1C	TRK_MBR_CTRL [41].TRK_PPM_31_0[31:0]	32
0xBB00BA20	TRK_MBR_CTRL [42].RESERVED[31:24]	8
0xBB00BA20	TRK_MBR_CTRL [42].TRK_PPM_55_32[23:0]	24
0xBB00BA24	TRK_MBR_CTRL [42].TRK_PPM_31_0[31:0]	32
0xBB00BA28	TRK_MBR_CTRL [43].RESERVED[31:24]	8
0xBB00BA28	TRK_MBR_CTRL [43].TRK_PPM_55_32[23:0]	24
0xBB00BA2C	TRK_MBR_CTRL [43].TRK_PPM_31_0[31:0]	32
0xBB00BA30	TRK_MBR_CTRL [44].RESERVED[31:24]	8
0xBB00BA30	TRK_MBR_CTRL [44].TRK_PPM_55_32[23:0]	24
0xBB00BA34	TRK_MBR_CTRL [44].TRK_PPM_31_0[31:0]	32
0xBB00BA38	TRK_MBR_CTRL [45].RESERVED[31:24]	8
0xBB00BA38	TRK_MBR_CTRL [45].TRK_PPM_55_32[23:0]	24
0xBB00BA3C	TRK_MBR_CTRL [45].TRK_PPM_31_0[31:0]	32
0xBB00BA40	TRK_MBR_CTRL [46].RESERVED[31:24]	8
0xBB00BA40	TRK_MBR_CTRL [46].TRK_PPM_55_32[23:0]	24
0xBB00BA44	TRK_MBR_CTRL [46].TRK_PPM_31_0[31:0]	32
0xBB00BA48	TRK_MBR_CTRL [47].RESERVED[31:24]	8
0xBB00BA48	TRK_MBR_CTRL [47].TRK_PPM_55_32[23:0]	24
0xBB00BA4C	TRK_MBR_CTRL [47].TRK_PPM_31_0[31:0]	32
0xBB00BA50	TRK_MBR_CTRL [48].RESERVED[31:24]	8
0xBB00BA50	TRK_MBR_CTRL [48].TRK_PPM_55_32[23:0]	24
0xBB00BA54	TRK_MBR_CTRL [48].TRK_PPM_31_0[31:0]	32
0xBB00BA58	TRK_MBR_CTRL [49].RESERVED[31:24]	8
0xBB00BA58	TRK_MBR_CTRL [49].TRK_PPM_55_32[23:0]	24
0xBB00BA5C	TRK_MBR_CTRL [49].TRK_PPM_31_0[31:0]	32
0xBB00BA60	TRK_MBR_CTRL [50].RESERVED[31:24]	8
0xBB00BA60	TRK_MBR_CTRL [50].TRK_PPM_55_32[23:0]	24
0xBB00BA64	TRK_MBR_CTRL [50].TRK_PPM_31_0[31:0]	32
0xBB00BA68	TRK_MBR_CTRL [51].RESERVED[31:24]	8
0xBB00BA68	TRK_MBR_CTRL [51].TRK_PPM_55_32[23:0]	24
0xBB00BA6C	TRK_MBR_CTRL [51].TRK_PPM_31_0[31:0]	32
0xBB00BA7C	TRK_SHFT_CTRL.RESERVED[31:30]	2
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_FLOW_LABEL[29:27]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_PROTO[26:24]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_L4DPORT[23:21]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_L4SPORT[20:18]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_DIP[17:15]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_SIP[14:12]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_VLAN[11:9]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_DMAC[8:6]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_SMAC[5:3]	3
0xBB00BA7C	TRK_SHFT_CTRL.SHIFT_BITS_SPP[2:0]	3
0xBB00BE94	TRK_STK_CTRL [0].RESERVED[31:19]	13
0xBB00BE94	TRK_STK_CTRL [0].L2_HASH_MSK_IDX[18:18]	1
0xBB00BE94	TRK_STK_CTRL [0].IP4_HASH_MSK_IDX[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00BE94	TRK_STK_CTRL [0].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BE94	TRK_STK_CTRL [0].STK_TRK_PPM[15:0]	16
0xBB00BE98	TRK_STK_CTRL [1].RESERVED[31:19]	13
0xBB00BE98	TRK_STK_CTRL [1].L2_HASH_MSK_IDX[18:18]	1
0xBB00BE98	TRK_STK_CTRL [1].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BE98	TRK_STK_CTRL [1].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BE98	TRK_STK_CTRL [1].STK_TRK_PPM[15:0]	16
0xBB00BE9C	TRK_STK_CTRL [2].RESERVED[31:19]	13
0xBB00BE9C	TRK_STK_CTRL [2].L2_HASH_MSK_IDX[18:18]	1
0xBB00BE9C	TRK_STK_CTRL [2].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BE9C	TRK_STK_CTRL [2].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BE9C	TRK_STK_CTRL [2].STK_TRK_PPM[15:0]	16
0xBB00BEA0	TRK_STK_CTRL [3].RESERVED[31:19]	13
0xBB00BEA0	TRK_STK_CTRL [3].L2_HASH_MSK_IDX[18:18]	1
0xBB00BEA0	TRK_STK_CTRL [3].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BEA0	TRK_STK_CTRL [3].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BEA0	TRK_STK_CTRL [3].STK_TRK_PPM[15:0]	16
0xBB00BEA4	TRK_STK_CTRL [4].RESERVED[31:19]	13
0xBB00BEA4	TRK_STK_CTRL [4].L2_HASH_MSK_IDX[18:18]	1
0xBB00BEA4	TRK_STK_CTRL [4].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BEA4	TRK_STK_CTRL [4].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BEA4	TRK_STK_CTRL [4].STK_TRK_PPM[15:0]	16
0xBB00BEA8	TRK_STK_CTRL [5].RESERVED[31:19]	13
0xBB00BEA8	TRK_STK_CTRL [5].L2_HASH_MSK_IDX[18:18]	1
0xBB00BEA8	TRK_STK_CTRL [5].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BEA8	TRK_STK_CTRL [5].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BEA8	TRK_STK_CTRL [5].STK_TRK_PPM[15:0]	16
0xBB00BEAC	TRK_STK_CTRL [6].RESERVED[31:19]	13
0xBB00BEAC	TRK_STK_CTRL [6].L2_HASH_MSK_IDX[18:18]	1
0xBB00BEAC	TRK_STK_CTRL [6].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BEAC	TRK_STK_CTRL [6].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BEAC	TRK_STK_CTRL [6].STK_TRK_PPM[15:0]	16
0xBB00BEB0	TRK_STK_CTRL [7].RESERVED[31:19]	13
0xBB00BEB0	TRK_STK_CTRL [7].L2_HASH_MSK_IDX[18:18]	1
0xBB00BEB0	TRK_STK_CTRL [7].IP4_HASH_MSK_IDX[17:17]	1
0xBB00BEB0	TRK_STK_CTRL [7].IP6_HASH_MSK_IDX[16:16]	1
0xBB00BEB0	TRK_STK_CTRL [7].STK_TRK_PPM[15:0]	16
0xBB00F2CC	TUNNEL_6RD_DOMAIN [0].PREFIX_HI[31:0]	32
0xBB00F2D0	TUNNEL_6RD_DOMAIN [0].PREFIX_LO[31:1]	31
0xBB00F2D0	TUNNEL_6RD_DOMAIN [0].RESERVED[0:11]	-10
0xBB00F2D0	TUNNEL_6RD_DOMAIN [0].IPV4_MASK_LEN[10:6]	5
0xBB00F2D0	TUNNEL_6RD_DOMAIN [0].PREFIX_LEN[5:0]	6
0xBB00F2D8	TUNNEL_6RD_DOMAIN [1].PREFIX_HI[31:0]	32
0xBB00F2DC	TUNNEL_6RD_DOMAIN [1].PREFIX_LO[31:1]	31
0xBB00F2DC	TUNNEL_6RD_DOMAIN [1].RESERVED[0:11]	-10
0xBB00F2DC	TUNNEL_6RD_DOMAIN [1].IPV4_MASK_LEN[10:6]	5

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00F2DC	TUNNEL_6RD_DOMAIN [1].PREFIX_LEN[5:0]	6
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.RESERVED[31:17]	15
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_6RD_DIP_FAIL_TRAP[16:15]	2
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_6RD_DIP_CHK[14:14]	1
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_6TO4_DIP_FAIL_TRAP[13:12]	2
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_6TO4_DIP_CHK[11:11]	1
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_6TO4_SIP_FAIL_TRAP[10:9]	2
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_6TO4_SIP_CHK[8:8]	1
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_ISATAP_SIP_FAIL_TRAP[7:6]	2
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_ISATAP_SIP_MAP_CHK[5:5]	1
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_ISATAP_SIP_TYPE_CHK[4:4]	1
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_IP6_SIP_FAIL_TRAP[3:2]	2
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_IP6_SIP_IP4MAP_CHK[1:1]	1
0xBB00F2C4	TUNNEL_IP_ADDR_CHK_CTRL.TT_IP6_SIP_IP4CMPT_CHK[0:0]	1
0xBB004F48	TUNNEL_IP_IDENTIFICATION.RESERVED[31:16]	16
0xBB004F48	TUNNEL_IP_IDENTIFICATION.ID[15:0]	16
0xBB000100	TUNNEL_QOS_PROFILE [0].RESERVED[31:18]	14
0xBB000100	TUNNEL_QOS_PROFILE [0].OPRI[17:15]	3
0xBB000100	TUNNEL_QOS_PROFILE [0].OPRI_SRC[14:13]	2
0xBB000100	TUNNEL_QOS_PROFILE [0].IPRI[12:10]	3
0xBB000100	TUNNEL_QOS_PROFILE [0].IPRI_SRC[9:8]	2
0xBB000100	TUNNEL_QOS_PROFILE [0].DSCP[7:2]	6
0xBB000100	TUNNEL_QOS_PROFILE [0].DSCP_SRC[1:0]	2
0xBB000104	TUNNEL_QOS_PROFILE [1].RESERVED[31:18]	14
0xBB000104	TUNNEL_QOS_PROFILE [1].OPRI[17:15]	3
0xBB000104	TUNNEL_QOS_PROFILE [1].OPRI_SRC[14:13]	2
0xBB000104	TUNNEL_QOS_PROFILE [1].IPRI[12:10]	3
0xBB000104	TUNNEL_QOS_PROFILE [1].IPRI_SRC[9:8]	2
0xBB000104	TUNNEL_QOS_PROFILE [1].DSCP[7:2]	6
0xBB000104	TUNNEL_QOS_PROFILE [1].DSCP_SRC[1:0]	2
0xBB000108	TUNNEL_QOS_PROFILE [2].RESERVED[31:18]	14
0xBB000108	TUNNEL_QOS_PROFILE [2].OPRI[17:15]	3
0xBB000108	TUNNEL_QOS_PROFILE [2].OPRI_SRC[14:13]	2
0xBB000108	TUNNEL_QOS_PROFILE [2].IPRI[12:10]	3
0xBB000108	TUNNEL_QOS_PROFILE [2].IPRI_SRC[9:8]	2
0xBB000108	TUNNEL_QOS_PROFILE [2].DSCP[7:2]	6
0xBB000108	TUNNEL_QOS_PROFILE [2].DSCP_SRC[1:0]	2
0xBB00010C	TUNNEL_QOS_PROFILE [3].RESERVED[31:18]	14
0xBB00010C	TUNNEL_QOS_PROFILE [3].OPRI[17:15]	3
0xBB00010C	TUNNEL_QOS_PROFILE [3].OPRI_SRC[14:13]	2
0xBB00010C	TUNNEL_QOS_PROFILE [3].IPRI[12:10]	3
0xBB00010C	TUNNEL_QOS_PROFILE [3].IPRI_SRC[9:8]	2
0xBB00010C	TUNNEL_QOS_PROFILE [3].DSCP[7:2]	6
0xBB00010C	TUNNEL_QOS_PROFILE [3].DSCP_SRC[1:0]	2
0xBB000110	TUNNEL_QOS_PROFILE [4].RESERVED[31:18]	14
0xBB000110	TUNNEL_QOS_PROFILE [4].OPRI[17:15]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000110	TUNNEL_QOS_PROFILE [4].OPRI_SRC[14:13]	2
0xBB000110	TUNNEL_QOS_PROFILE [4].IPRI[12:10]	3
0xBB000110	TUNNEL_QOS_PROFILE [4].IPRI_SRC[9:8]	2
0xBB000110	TUNNEL_QOS_PROFILE [4].DSCP[7:2]	6
0xBB000110	TUNNEL_QOS_PROFILE [4].DSCP_SRC[1:0]	2
0xBB000114	TUNNEL_QOS_PROFILE [5].RESERVED[31:18]	14
0xBB000114	TUNNEL_QOS_PROFILE [5].OPRI[17:15]	3
0xBB000114	TUNNEL_QOS_PROFILE [5].OPRI_SRC[14:13]	2
0xBB000114	TUNNEL_QOS_PROFILE [5].IPRI[12:10]	3
0xBB000114	TUNNEL_QOS_PROFILE [5].IPRI_SRC[9:8]	2
0xBB000114	TUNNEL_QOS_PROFILE [5].DSCP[7:2]	6
0xBB000114	TUNNEL_QOS_PROFILE [5].DSCP_SRC[1:0]	2
0xBB000118	TUNNEL_QOS_PROFILE [6].RESERVED[31:18]	14
0xBB000118	TUNNEL_QOS_PROFILE [6].OPRI[17:15]	3
0xBB000118	TUNNEL_QOS_PROFILE [6].OPRI_SRC[14:13]	2
0xBB000118	TUNNEL_QOS_PROFILE [6].IPRI[12:10]	3
0xBB000118	TUNNEL_QOS_PROFILE [6].IPRI_SRC[9:8]	2
0xBB000118	TUNNEL_QOS_PROFILE [6].DSCP[7:2]	6
0xBB000118	TUNNEL_QOS_PROFILE [6].DSCP_SRC[1:0]	2
0xBB00011C	TUNNEL_QOS_PROFILE [7].RESERVED[31:18]	14
0xBB00011C	TUNNEL_QOS_PROFILE [7].OPRI[17:15]	3
0xBB00011C	TUNNEL_QOS_PROFILE [7].OPRI_SRC[14:13]	2
0xBB00011C	TUNNEL_QOS_PROFILE [7].IPRI[12:10]	3
0xBB00011C	TUNNEL_QOS_PROFILE [7].IPRI_SRC[9:8]	2
0xBB00011C	TUNNEL_QOS_PROFILE [7].DSCP[7:2]	6
0xBB00011C	TUNNEL_QOS_PROFILE [7].DSCP_SRC[1:0]	2
0xBB000120	TUNNEL_QOS_PROFILE [8].RESERVED[31:18]	14
0xBB000120	TUNNEL_QOS_PROFILE [8].OPRI[17:15]	3
0xBB000120	TUNNEL_QOS_PROFILE [8].OPRI_SRC[14:13]	2
0xBB000120	TUNNEL_QOS_PROFILE [8].IPRI[12:10]	3
0xBB000120	TUNNEL_QOS_PROFILE [8].IPRI_SRC[9:8]	2
0xBB000120	TUNNEL_QOS_PROFILE [8].DSCP[7:2]	6
0xBB000120	TUNNEL_QOS_PROFILE [8].DSCP_SRC[1:0]	2
0xBB000124	TUNNEL_QOS_PROFILE [9].RESERVED[31:18]	14
0xBB000124	TUNNEL_QOS_PROFILE [9].OPRI[17:15]	3
0xBB000124	TUNNEL_QOS_PROFILE [9].OPRI_SRC[14:13]	2
0xBB000124	TUNNEL_QOS_PROFILE [9].IPRI[12:10]	3
0xBB000124	TUNNEL_QOS_PROFILE [9].IPRI_SRC[9:8]	2
0xBB000124	TUNNEL_QOS_PROFILE [9].DSCP[7:2]	6
0xBB000124	TUNNEL_QOS_PROFILE [9].DSCP_SRC[1:0]	2
0xBB000128	TUNNEL_QOS_PROFILE [10].RESERVED[31:18]	14
0xBB000128	TUNNEL_QOS_PROFILE [10].OPRI[17:15]	3
0xBB000128	TUNNEL_QOS_PROFILE [10].OPRI_SRC[14:13]	2
0xBB000128	TUNNEL_QOS_PROFILE [10].IPRI[12:10]	3
0xBB000128	TUNNEL_QOS_PROFILE [10].IPRI_SRC[9:8]	2
0xBB000128	TUNNEL_QOS_PROFILE [10].DSCP[7:2]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000128	TUNNEL_QOS_PROFILE [10].DSCP_SRC[1:0]	2
0xBB00012C	TUNNEL_QOS_PROFILE [11].RESERVED[31:18]	14
0xBB00012C	TUNNEL_QOS_PROFILE [11].OPRI[17:15]	3
0xBB00012C	TUNNEL_QOS_PROFILE [11].OPRI_SRC[14:13]	2
0xBB00012C	TUNNEL_QOS_PROFILE [11].IPRI[12:10]	3
0xBB00012C	TUNNEL_QOS_PROFILE [11].IPRI_SRC[9:8]	2
0xBB00012C	TUNNEL_QOS_PROFILE [11].DSCP[7:2]	6
0xBB00012C	TUNNEL_QOS_PROFILE [11].DSCP_SRC[1:0]	2
0xBB000130	TUNNEL_QOS_PROFILE [12].RESERVED[31:18]	14
0xBB000130	TUNNEL_QOS_PROFILE [12].OPRI[17:15]	3
0xBB000130	TUNNEL_QOS_PROFILE [12].OPRI_SRC[14:13]	2
0xBB000130	TUNNEL_QOS_PROFILE [12].IPRI[12:10]	3
0xBB000130	TUNNEL_QOS_PROFILE [12].IPRI_SRC[9:8]	2
0xBB000130	TUNNEL_QOS_PROFILE [12].DSCP[7:2]	6
0xBB000130	TUNNEL_QOS_PROFILE [12].DSCP_SRC[1:0]	2
0xBB000134	TUNNEL_QOS_PROFILE [13].RESERVED[31:18]	14
0xBB000134	TUNNEL_QOS_PROFILE [13].OPRI[17:15]	3
0xBB000134	TUNNEL_QOS_PROFILE [13].OPRI_SRC[14:13]	2
0xBB000134	TUNNEL_QOS_PROFILE [13].IPRI[12:10]	3
0xBB000134	TUNNEL_QOS_PROFILE [13].IPRI_SRC[9:8]	2
0xBB000134	TUNNEL_QOS_PROFILE [13].DSCP[7:2]	6
0xBB000134	TUNNEL_QOS_PROFILE [13].DSCP_SRC[1:0]	2
0xBB000138	TUNNEL_QOS_PROFILE [14].RESERVED[31:18]	14
0xBB000138	TUNNEL_QOS_PROFILE [14].OPRI[17:15]	3
0xBB000138	TUNNEL_QOS_PROFILE [14].OPRI_SRC[14:13]	2
0xBB000138	TUNNEL_QOS_PROFILE [14].IPRI[12:10]	3
0xBB000138	TUNNEL_QOS_PROFILE [14].IPRI_SRC[9:8]	2
0xBB000138	TUNNEL_QOS_PROFILE [14].DSCP[7:2]	6
0xBB000138	TUNNEL_QOS_PROFILE [14].DSCP_SRC[1:0]	2
0xBB00013C	TUNNEL_QOS_PROFILE [15].RESERVED[31:18]	14
0xBB00013C	TUNNEL_QOS_PROFILE [15].OPRI[17:15]	3
0xBB00013C	TUNNEL_QOS_PROFILE [15].OPRI_SRC[14:13]	2
0xBB00013C	TUNNEL_QOS_PROFILE [15].IPRI[12:10]	3
0xBB00013C	TUNNEL_QOS_PROFILE [15].IPRI_SRC[9:8]	2
0xBB00013C	TUNNEL_QOS_PROFILE [15].DSCP[7:2]	6
0xBB00013C	TUNNEL_QOS_PROFILE [15].DSCP_SRC[1:0]	2
0xBB000140	TUNNEL_QOS_PROFILE [16].RESERVED[31:18]	14
0xBB000140	TUNNEL_QOS_PROFILE [16].OPR[17:15]	3
0xBB000140	TUNNEL_QOS_PROFILE [16].OPR_SRC[14:13]	2
0xBB000140	TUNNEL_QOS_PROFILE [16].IPRI[12:10]	3
0xBB000140	TUNNEL_QOS_PROFILE [16].IPRI_SRC[9:8]	2
0xBB000140	TUNNEL_QOS_PROFILE [16].DSCP[7:2]	6
0xBB000140	TUNNEL_QOS_PROFILE [16].DSCP_SRC[1:0]	2
0xBB000144	TUNNEL_QOS_PROFILE [17].RESERVED[31:18]	14
0xBB000144	TUNNEL_QOS_PROFILE [17].OPRI[17:15]	3
0xBB000144	TUNNEL_QOS_PROFILE [17].OPRI_SRC[14:13]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000144	TUNNEL_QOS_PROFILE [17].IPRI[12:10]	3
0xBB000144	TUNNEL_QOS_PROFILE [17].IPRI_SRC[9:8]	2
0xBB000144	TUNNEL_QOS_PROFILE [17].DSCP[7:2]	6
0xBB000144	TUNNEL_QOS_PROFILE [17].DSCP_SRC[1:0]	2
0xBB000148	TUNNEL_QOS_PROFILE [18].RESERVED[31:18]	14
0xBB000148	TUNNEL_QOS_PROFILE [18].OPRI[17:15]	3
0xBB000148	TUNNEL_QOS_PROFILE [18].OPRI_SRC[14:13]	2
0xBB000148	TUNNEL_QOS_PROFILE [18].IPRI[12:10]	3
0xBB000148	TUNNEL_QOS_PROFILE [18].IPRI_SRC[9:8]	2
0xBB000148	TUNNEL_QOS_PROFILE [18].DSCP[7:2]	6
0xBB000148	TUNNEL_QOS_PROFILE [18].DSCP_SRC[1:0]	2
0xBB00014C	TUNNEL_QOS_PROFILE [19].RESERVED[31:18]	14
0xBB00014C	TUNNEL_QOS_PROFILE [19].OPRI[17:15]	3
0xBB00014C	TUNNEL_QOS_PROFILE [19].OPRI_SRC[14:13]	2
0xBB00014C	TUNNEL_QOS_PROFILE [19].IPRI[12:10]	3
0xBB00014C	TUNNEL_QOS_PROFILE [19].IPRI_SRC[9:8]	2
0xBB00014C	TUNNEL_QOS_PROFILE [19].DSCP[7:2]	6
0xBB00014C	TUNNEL_QOS_PROFILE [19].DSCP_SRC[1:0]	2
0xBB000150	TUNNEL_QOS_PROFILE [20].RESERVED[31:18]	14
0xBB000150	TUNNEL_QOS_PROFILE [20].OPRI[17:15]	3
0xBB000150	TUNNEL_QOS_PROFILE [20].OPRI_SRC[14:13]	2
0xBB000150	TUNNEL_QOS_PROFILE [20].IPRI[12:10]	3
0xBB000150	TUNNEL_QOS_PROFILE [20].IPRI_SRC[9:8]	2
0xBB000150	TUNNEL_QOS_PROFILE [20].DSCP[7:2]	6
0xBB000150	TUNNEL_QOS_PROFILE [20].DSCP_SRC[1:0]	2
0xBB000154	TUNNEL_QOS_PROFILE [21].RESERVED[31:18]	14
0xBB000154	TUNNEL_QOS_PROFILE [21].OPRI[17:15]	3
0xBB000154	TUNNEL_QOS_PROFILE [21].OPRI_SRC[14:13]	2
0xBB000154	TUNNEL_QOS_PROFILE [21].IPRI[12:10]	3
0xBB000154	TUNNEL_QOS_PROFILE [21].IPRI_SRC[9:8]	2
0xBB000154	TUNNEL_QOS_PROFILE [21].DSCP[7:2]	6
0xBB000154	TUNNEL_QOS_PROFILE [21].DSCP_SRC[1:0]	2
0xBB000158	TUNNEL_QOS_PROFILE [22].RESERVED[31:18]	14
0xBB000158	TUNNEL_QOS_PROFILE [22].OPRI[17:15]	3
0xBB000158	TUNNEL_QOS_PROFILE [22].OPRI_SRC[14:13]	2
0xBB000158	TUNNEL_QOS_PROFILE [22].IPRI[12:10]	3
0xBB000158	TUNNEL_QOS_PROFILE [22].IPRI_SRC[9:8]	2
0xBB000158	TUNNEL_QOS_PROFILE [22].DSCP[7:2]	6
0xBB000158	TUNNEL_QOS_PROFILE [22].DSCP_SRC[1:0]	2
0xBB00015C	TUNNEL_QOS_PROFILE [23].RESERVED[31:18]	14
0xBB00015C	TUNNEL_QOS_PROFILE [23].OPRI[17:15]	3
0xBB00015C	TUNNEL_QOS_PROFILE [23].OPRI_SRC[14:13]	2
0xBB00015C	TUNNEL_QOS_PROFILE [23].IPRI[12:10]	3
0xBB00015C	TUNNEL_QOS_PROFILE [23].IPRI_SRC[9:8]	2
0xBB00015C	TUNNEL_QOS_PROFILE [23].DSCP[7:2]	6
0xBB00015C	TUNNEL_QOS_PROFILE [23].DSCP_SRC[1:0]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000160	TUNNEL_QOS_PROFILE [24].RESERVED[31:18]	14
0xBB000160	TUNNEL_QOS_PROFILE [24].OPRI[17:15]	3
0xBB000160	TUNNEL_QOS_PROFILE [24].OPRI_SRC[14:13]	2
0xBB000160	TUNNEL_QOS_PROFILE [24].IPRI[12:10]	3
0xBB000160	TUNNEL_QOS_PROFILE [24].IPRI_SRC[9:8]	2
0xBB000160	TUNNEL_QOS_PROFILE [24].DSCP[7:2]	6
0xBB000160	TUNNEL_QOS_PROFILE [24].DSCP_SRC[1:0]	2
0xBB000164	TUNNEL_QOS_PROFILE [25].RESERVED[31:18]	14
0xBB000164	TUNNEL_QOS_PROFILE [25].OPRI[17:15]	3
0xBB000164	TUNNEL_QOS_PROFILE [25].OPRI_SRC[14:13]	2
0xBB000164	TUNNEL_QOS_PROFILE [25].IPRI[12:10]	3
0xBB000164	TUNNEL_QOS_PROFILE [25].IPRI_SRC[9:8]	2
0xBB000164	TUNNEL_QOS_PROFILE [25].DSCP[7:2]	6
0xBB000164	TUNNEL_QOS_PROFILE [25].DSCP_SRC[1:0]	2
0xBB000168	TUNNEL_QOS_PROFILE [26].RESERVED[31:18]	14
0xBB000168	TUNNEL_QOS_PROFILE [26].OPRI[17:15]	3
0xBB000168	TUNNEL_QOS_PROFILE [26].OPRI_SRC[14:13]	2
0xBB000168	TUNNEL_QOS_PROFILE [26].IPRI[12:10]	3
0xBB000168	TUNNEL_QOS_PROFILE [26].IPRI_SRC[9:8]	2
0xBB000168	TUNNEL_QOS_PROFILE [26].DSCP[7:2]	6
0xBB000168	TUNNEL_QOS_PROFILE [26].DSCP_SRC[1:0]	2
0xBB00016C	TUNNEL_QOS_PROFILE [27].RESERVED[31:18]	14
0xBB00016C	TUNNEL_QOS_PROFILE [27].OPRI[17:15]	3
0xBB00016C	TUNNEL_QOS_PROFILE [27].OPRI_SRC[14:13]	2
0xBB00016C	TUNNEL_QOS_PROFILE [27].IPRI[12:10]	3
0xBB00016C	TUNNEL_QOS_PROFILE [27].IPRI_SRC[9:8]	2
0xBB00016C	TUNNEL_QOS_PROFILE [27].DSCP[7:2]	6
0xBB00016C	TUNNEL_QOS_PROFILE [27].DSCP_SRC[1:0]	2
0xBB000170	TUNNEL_QOS_PROFILE [28].RESERVED[31:18]	14
0xBB000170	TUNNEL_QOS_PROFILE [28].OPRI[17:15]	3
0xBB000170	TUNNEL_QOS_PROFILE [28].OPRI_SRC[14:13]	2
0xBB000170	TUNNEL_QOS_PROFILE [28].IPRI[12:10]	3
0xBB000170	TUNNEL_QOS_PROFILE [28].IPRI_SRC[9:8]	2
0xBB000170	TUNNEL_QOS_PROFILE [28].DSCP[7:2]	6
0xBB000170	TUNNEL_QOS_PROFILE [28].DSCP_SRC[1:0]	2
0xBB000174	TUNNEL_QOS_PROFILE [29].RESERVED[31:18]	14
0xBB000174	TUNNEL_QOS_PROFILE [29].OPRI[17:15]	3
0xBB000174	TUNNEL_QOS_PROFILE [29].OPRI_SRC[14:13]	2
0xBB000174	TUNNEL_QOS_PROFILE [29].IPRI[12:10]	3
0xBB000174	TUNNEL_QOS_PROFILE [29].IPRI_SRC[9:8]	2
0xBB000174	TUNNEL_QOS_PROFILE [29].DSCP[7:2]	6
0xBB000174	TUNNEL_QOS_PROFILE [29].DSCP_SRC[1:0]	2
0xBB000178	TUNNEL_QOS_PROFILE [30].RESERVED[31:18]	14
0xBB000178	TUNNEL_QOS_PROFILE [30].OPRI[17:15]	3
0xBB000178	TUNNEL_QOS_PROFILE [30].OPRI_SRC[14:13]	2
0xBB000178	TUNNEL_QOS_PROFILE [30].IPRI[12:10]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000178	TUNNEL_QOS_PROFILE [30].IPRI_SRC[9:8]	2
0xBB000178	TUNNEL_QOS_PROFILE [30].DSCP[7:2]	6
0xBB000178	TUNNEL_QOS_PROFILE [30].DSCP_SRC[1:0]	2
0xBB00017C	TUNNEL_QOS_PROFILE [31].RESERVED[31:18]	14
0xBB00017C	TUNNEL_QOS_PROFILE [31].OPRI[17:15]	3
0xBB00017C	TUNNEL_QOS_PROFILE [31].OPRI_SRC[14:13]	2
0xBB00017C	TUNNEL_QOS_PROFILE [31].IPRI[12:10]	3
0xBB00017C	TUNNEL_QOS_PROFILE [31].IPRI_SRC[9:8]	2
0xBB00017C	TUNNEL_QOS_PROFILE [31].DSCP[7:2]	6
0xBB00017C	TUNNEL_QOS_PROFILE [31].DSCP_SRC[1:0]	2
0xBB000180	TUNNEL_QOS_PROFILE [32].RESERVED[31:18]	14
0xBB000180	TUNNEL_QOS_PROFILE [32].OPRI[17:15]	3
0xBB000180	TUNNEL_QOS_PROFILE [32].OPRI_SRC[14:13]	2
0xBB000180	TUNNEL_QOS_PROFILE [32].IPRI[12:10]	3
0xBB000180	TUNNEL_QOS_PROFILE [32].IPRI_SRC[9:8]	2
0xBB000180	TUNNEL_QOS_PROFILE [32].DSCP[7:2]	6
0xBB000180	TUNNEL_QOS_PROFILE [32].DSCP_SRC[1:0]	2
0xBB000184	TUNNEL_QOS_PROFILE [33].RESERVED[31:18]	14
0xBB000184	TUNNEL_QOS_PROFILE [33].OPRI[17:15]	3
0xBB000184	TUNNEL_QOS_PROFILE [33].OPRI_SRC[14:13]	2
0xBB000184	TUNNEL_QOS_PROFILE [33].IPRI[12:10]	3
0xBB000184	TUNNEL_QOS_PROFILE [33].IPRI_SRC[9:8]	2
0xBB000184	TUNNEL_QOS_PROFILE [33].DSCP[7:2]	6
0xBB000184	TUNNEL_QOS_PROFILE [33].DSCP_SRC[1:0]	2
0xBB000188	TUNNEL_QOS_PROFILE [34].RESERVED[31:18]	14
0xBB000188	TUNNEL_QOS_PROFILE [34].OPRI[17:15]	3
0xBB000188	TUNNEL_QOS_PROFILE [34].OPRI_SRC[14:13]	2
0xBB000188	TUNNEL_QOS_PROFILE [34].IPRI[12:10]	3
0xBB000188	TUNNEL_QOS_PROFILE [34].IPRI_SRC[9:8]	2
0xBB000188	TUNNEL_QOS_PROFILE [34].DSCP[7:2]	6
0xBB000188	TUNNEL_QOS_PROFILE [34].DSCP_SRC[1:0]	2
0xBB00018C	TUNNEL_QOS_PROFILE [35].RESERVED[31:18]	14
0xBB00018C	TUNNEL_QOS_PROFILE [35].OPRI[17:15]	3
0xBB00018C	TUNNEL_QOS_PROFILE [35].OPRI_SRC[14:13]	2
0xBB00018C	TUNNEL_QOS_PROFILE [35].IPRI[12:10]	3
0xBB00018C	TUNNEL_QOS_PROFILE [35].IPRI_SRC[9:8]	2
0xBB00018C	TUNNEL_QOS_PROFILE [35].DSCP[7:2]	6
0xBB00018C	TUNNEL_QOS_PROFILE [35].DSCP_SRC[1:0]	2
0xBB000190	TUNNEL_QOS_PROFILE [36].RESERVED[31:18]	14
0xBB000190	TUNNEL_QOS_PROFILE [36].OPRI[17:15]	3
0xBB000190	TUNNEL_QOS_PROFILE [36].OPRI_SRC[14:13]	2
0xBB000190	TUNNEL_QOS_PROFILE [36].IPRI[12:10]	3
0xBB000190	TUNNEL_QOS_PROFILE [36].IPRI_SRC[9:8]	2
0xBB000190	TUNNEL_QOS_PROFILE [36].DSCP[7:2]	6
0xBB000190	TUNNEL_QOS_PROFILE [36].DSCP_SRC[1:0]	2
0xBB000194	TUNNEL_QOS_PROFILE [37].RESERVED[31:18]	14

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000194	TUNNEL_QOS_PROFILE [37].OPRI[17:15]	3
0xBB000194	TUNNEL_QOS_PROFILE [37].OPRI_SRC[14:13]	2
0xBB000194	TUNNEL_QOS_PROFILE [37].IPRI[12:10]	3
0xBB000194	TUNNEL_QOS_PROFILE [37].IPRI_SRC[9:8]	2
0xBB000194	TUNNEL_QOS_PROFILE [37].DSCP[7:2]	6
0xBB000194	TUNNEL_QOS_PROFILE [37].DSCP_SRC[1:0]	2
0xBB000198	TUNNEL_QOS_PROFILE [38].RESERVED[31:18]	14
0xBB000198	TUNNEL_QOS_PROFILE [38].OPRI[17:15]	3
0xBB000198	TUNNEL_QOS_PROFILE [38].OPRI_SRC[14:13]	2
0xBB000198	TUNNEL_QOS_PROFILE [38].IPRI[12:10]	3
0xBB000198	TUNNEL_QOS_PROFILE [38].IPRI_SRC[9:8]	2
0xBB000198	TUNNEL_QOS_PROFILE [38].DSCP[7:2]	6
0xBB000198	TUNNEL_QOS_PROFILE [38].DSCP_SRC[1:0]	2
0xBB00019C	TUNNEL_QOS_PROFILE [39].RESERVED[31:18]	14
0xBB00019C	TUNNEL_QOS_PROFILE [39].OPRI[17:15]	3
0xBB00019C	TUNNEL_QOS_PROFILE [39].OPRI_SRC[14:13]	2
0xBB00019C	TUNNEL_QOS_PROFILE [39].IPRI[12:10]	3
0xBB00019C	TUNNEL_QOS_PROFILE [39].IPRI_SRC[9:8]	2
0xBB00019C	TUNNEL_QOS_PROFILE [39].DSCP[7:2]	6
0xBB00019C	TUNNEL_QOS_PROFILE [39].DSCP_SRC[1:0]	2
0xBB0001A0	TUNNEL_QOS_PROFILE [40].RESERVED[31:18]	14
0xBB0001A0	TUNNEL_QOS_PROFILE [40].OPRI[17:15]	3
0xBB0001A0	TUNNEL_QOS_PROFILE [40].OPRI_SRC[14:13]	2
0xBB0001A0	TUNNEL_QOS_PROFILE [40].IPRI[12:10]	3
0xBB0001A0	TUNNEL_QOS_PROFILE [40].IPRI_SRC[9:8]	2
0xBB0001A0	TUNNEL_QOS_PROFILE [40].DSCP[7:2]	6
0xBB0001A0	TUNNEL_QOS_PROFILE [40].DSCP_SRC[1:0]	2
0xBB0001A4	TUNNEL_QOS_PROFILE [41].RESERVED[31:18]	14
0xBB0001A4	TUNNEL_QOS_PROFILE [41].OPRI[17:15]	3
0xBB0001A4	TUNNEL_QOS_PROFILE [41].OPRI_SRC[14:13]	2
0xBB0001A4	TUNNEL_QOS_PROFILE [41].IPRI[12:10]	3
0xBB0001A4	TUNNEL_QOS_PROFILE [41].IPRI_SRC[9:8]	2
0xBB0001A4	TUNNEL_QOS_PROFILE [41].DSCP[7:2]	6
0xBB0001A4	TUNNEL_QOS_PROFILE [41].DSCP_SRC[1:0]	2
0xBB0001A8	TUNNEL_QOS_PROFILE [42].RESERVED[31:18]	14
0xBB0001A8	TUNNEL_QOS_PROFILE [42].OPRI[17:15]	3
0xBB0001A8	TUNNEL_QOS_PROFILE [42].OPRI_SRC[14:13]	2
0xBB0001A8	TUNNEL_QOS_PROFILE [42].IPRI[12:10]	3
0xBB0001A8	TUNNEL_QOS_PROFILE [42].IPRI_SRC[9:8]	2
0xBB0001A8	TUNNEL_QOS_PROFILE [42].DSCP[7:2]	6
0xBB0001A8	TUNNEL_QOS_PROFILE [42].DSCP_SRC[1:0]	2
0xBB0001AC	TUNNEL_QOS_PROFILE [43].RESERVED[31:18]	14
0xBB0001AC	TUNNEL_QOS_PROFILE [43].OPRI[17:15]	3
0xBB0001AC	TUNNEL_QOS_PROFILE [43].OPRI_SRC[14:13]	2
0xBB0001AC	TUNNEL_QOS_PROFILE [43].IPRI[12:10]	3
0xBB0001AC	TUNNEL_QOS_PROFILE [43].IPRI_SRC[9:8]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0001AC	TUNNEL_QOS_PROFILE [43].DSCP[7:2]	6
0xBB0001AC	TUNNEL_QOS_PROFILE [43].DSCP_SRC[1:0]	2
0xBB0001B0	TUNNEL_QOS_PROFILE [44].RESERVED[31:18]	14
0xBB0001B0	TUNNEL_QOS_PROFILE [44].OPRI[17:15]	3
0xBB0001B0	TUNNEL_QOS_PROFILE [44].OPRI_SRC[14:13]	2
0xBB0001B0	TUNNEL_QOS_PROFILE [44].IPRI[12:10]	3
0xBB0001B0	TUNNEL_QOS_PROFILE [44].IPRI_SRC[9:8]	2
0xBB0001B0	TUNNEL_QOS_PROFILE [44].DSCP[7:2]	6
0xBB0001B0	TUNNEL_QOS_PROFILE [44].DSCP_SRC[1:0]	2
0xBB0001B4	TUNNEL_QOS_PROFILE [45].RESERVED[31:18]	14
0xBB0001B4	TUNNEL_QOS_PROFILE [45].OPRI[17:15]	3
0xBB0001B4	TUNNEL_QOS_PROFILE [45].OPRI_SRC[14:13]	2
0xBB0001B4	TUNNEL_QOS_PROFILE [45].IPRI[12:10]	3
0xBB0001B4	TUNNEL_QOS_PROFILE [45].IPRI_SRC[9:8]	2
0xBB0001B4	TUNNEL_QOS_PROFILE [45].DSCP[7:2]	6
0xBB0001B4	TUNNEL_QOS_PROFILE [45].DSCP_SRC[1:0]	2
0xBB0001B8	TUNNEL_QOS_PROFILE [46].RESERVED[31:18]	14
0xBB0001B8	TUNNEL_QOS_PROFILE [46].OPRI[17:15]	3
0xBB0001B8	TUNNEL_QOS_PROFILE [46].OPRI_SRC[14:13]	2
0xBB0001B8	TUNNEL_QOS_PROFILE [46].IPRI[12:10]	3
0xBB0001B8	TUNNEL_QOS_PROFILE [46].IPRI_SRC[9:8]	2
0xBB0001B8	TUNNEL_QOS_PROFILE [46].DSCP[7:2]	6
0xBB0001B8	TUNNEL_QOS_PROFILE [46].DSCP_SRC[1:0]	2
0xBB0001BC	TUNNEL_QOS_PROFILE [47].RESERVED[31:18]	14
0xBB0001BC	TUNNEL_QOS_PROFILE [47].OPRI[17:15]	3
0xBB0001BC	TUNNEL_QOS_PROFILE [47].OPRI_SRC[14:13]	2
0xBB0001BC	TUNNEL_QOS_PROFILE [47].IPRI[12:10]	3
0xBB0001BC	TUNNEL_QOS_PROFILE [47].IPRI_SRC[9:8]	2
0xBB0001BC	TUNNEL_QOS_PROFILE [47].DSCP[7:2]	6
0xBB0001BC	TUNNEL_QOS_PROFILE [47].DSCP_SRC[1:0]	2
0xBB0001C0	TUNNEL_QOS_PROFILE [48].RESERVED[31:18]	14
0xBB0001C0	TUNNEL_QOS_PROFILE [48].OPRI[17:15]	3
0xBB0001C0	TUNNEL_QOS_PROFILE [48].OPRI_SRC[14:13]	2
0xBB0001C0	TUNNEL_QOS_PROFILE [48].IPRI[12:10]	3
0xBB0001C0	TUNNEL_QOS_PROFILE [48].IPRI_SRC[9:8]	2
0xBB0001C0	TUNNEL_QOS_PROFILE [48].DSCP[7:2]	6
0xBB0001C0	TUNNEL_QOS_PROFILE [48].DSCP_SRC[1:0]	2
0xBB0001C4	TUNNEL_QOS_PROFILE [49].RESERVED[31:18]	14
0xBB0001C4	TUNNEL_QOS_PROFILE [49].OPRI[17:15]	3
0xBB0001C4	TUNNEL_QOS_PROFILE [49].OPRI_SRC[14:13]	2
0xBB0001C4	TUNNEL_QOS_PROFILE [49].IPRI[12:10]	3
0xBB0001C4	TUNNEL_QOS_PROFILE [49].IPRI_SRC[9:8]	2
0xBB0001C4	TUNNEL_QOS_PROFILE [49].DSCP[7:2]	6
0xBB0001C4	TUNNEL_QOS_PROFILE [49].DSCP_SRC[1:0]	2
0xBB0001C8	TUNNEL_QOS_PROFILE [50].RESERVED[31:18]	14
0xBB0001C8	TUNNEL_QOS_PROFILE [50].OPRI[17:15]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0001C8	TUNNEL_QOS_PROFILE [50].OPRI_SRC[14:13]	2
0xBB0001C8	TUNNEL_QOS_PROFILE [50].IPRI[12:10]	3
0xBB0001C8	TUNNEL_QOS_PROFILE [50].IPRI_SRC[9:8]	2
0xBB0001C8	TUNNEL_QOS_PROFILE [50].DSCP[7:2]	6
0xBB0001C8	TUNNEL_QOS_PROFILE [50].DSCP_SRC[1:0]	2
0xBB0001CC	TUNNEL_QOS_PROFILE [51].RESERVED[31:18]	14
0xBB0001CC	TUNNEL_QOS_PROFILE [51].OPRI[17:15]	3
0xBB0001CC	TUNNEL_QOS_PROFILE [51].OPRI_SRC[14:13]	2
0xBB0001CC	TUNNEL_QOS_PROFILE [51].IPRI[12:10]	3
0xBB0001CC	TUNNEL_QOS_PROFILE [51].IPRI_SRC[9:8]	2
0xBB0001CC	TUNNEL_QOS_PROFILE [51].DSCP[7:2]	6
0xBB0001CC	TUNNEL_QOS_PROFILE [51].DSCP_SRC[1:0]	2
0xBB0001D0	TUNNEL_QOS_PROFILE [52].RESERVED[31:18]	14
0xBB0001D0	TUNNEL_QOS_PROFILE [52].OPRI[17:15]	3
0xBB0001D0	TUNNEL_QOS_PROFILE [52].OPRI_SRC[14:13]	2
0xBB0001D0	TUNNEL_QOS_PROFILE [52].IPRI[12:10]	3
0xBB0001D0	TUNNEL_QOS_PROFILE [52].IPRI_SRC[9:8]	2
0xBB0001D0	TUNNEL_QOS_PROFILE [52].DSCP[7:2]	6
0xBB0001D0	TUNNEL_QOS_PROFILE [52].DSCP_SRC[1:0]	2
0xBB0001D4	TUNNEL_QOS_PROFILE [53].RESERVED[31:18]	14
0xBB0001D4	TUNNEL_QOS_PROFILE [53].OPRI[17:15]	3
0xBB0001D4	TUNNEL_QOS_PROFILE [53].OPRI_SRC[14:13]	2
0xBB0001D4	TUNNEL_QOS_PROFILE [53].IPRI[12:10]	3
0xBB0001D4	TUNNEL_QOS_PROFILE [53].IPRI_SRC[9:8]	2
0xBB0001D4	TUNNEL_QOS_PROFILE [53].DSCP[7:2]	6
0xBB0001D4	TUNNEL_QOS_PROFILE [53].DSCP_SRC[1:0]	2
0xBB0001D8	TUNNEL_QOS_PROFILE [54].RESERVED[31:18]	14
0xBB0001D8	TUNNEL_QOS_PROFILE [54].OPRI[17:15]	3
0xBB0001D8	TUNNEL_QOS_PROFILE [54].OPRI_SRC[14:13]	2
0xBB0001D8	TUNNEL_QOS_PROFILE [54].IPRI[12:10]	3
0xBB0001D8	TUNNEL_QOS_PROFILE [54].IPRI_SRC[9:8]	2
0xBB0001D8	TUNNEL_QOS_PROFILE [54].DSCP[7:2]	6
0xBB0001D8	TUNNEL_QOS_PROFILE [54].DSCP_SRC[1:0]	2
0xBB0001DC	TUNNEL_QOS_PROFILE [55].RESERVED[31:18]	14
0xBB0001DC	TUNNEL_QOS_PROFILE [55].OPRI[17:15]	3
0xBB0001DC	TUNNEL_QOS_PROFILE [55].OPRI_SRC[14:13]	2
0xBB0001DC	TUNNEL_QOS_PROFILE [55].IPRI[12:10]	3
0xBB0001DC	TUNNEL_QOS_PROFILE [55].IPRI_SRC[9:8]	2
0xBB0001DC	TUNNEL_QOS_PROFILE [55].DSCP[7:2]	6
0xBB0001DC	TUNNEL_QOS_PROFILE [55].DSCP_SRC[1:0]	2
0xBB0001E0	TUNNEL_QOS_PROFILE [56].RESERVED[31:18]	14
0xBB0001E0	TUNNEL_QOS_PROFILE [56].OPRI[17:15]	3
0xBB0001E0	TUNNEL_QOS_PROFILE [56].OPRI_SRC[14:13]	2
0xBB0001E0	TUNNEL_QOS_PROFILE [56].IPRI[12:10]	3
0xBB0001E0	TUNNEL_QOS_PROFILE [56].IPRI_SRC[9:8]	2
0xBB0001E0	TUNNEL_QOS_PROFILE [56].DSCP[7:2]	6

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0001E0	TUNNEL_QOS_PROFILE [56].DSCP_SRC[1:0]	2
0xBB0001E4	TUNNEL_QOS_PROFILE [57].RESERVED[31:18]	14
0xBB0001E4	TUNNEL_QOS_PROFILE [57].OPRI[17:15]	3
0xBB0001E4	TUNNEL_QOS_PROFILE [57].OPRI_SRC[14:13]	2
0xBB0001E4	TUNNEL_QOS_PROFILE [57].IPRI[12:10]	3
0xBB0001E4	TUNNEL_QOS_PROFILE [57].IPRI_SRC[9:8]	2
0xBB0001E4	TUNNEL_QOS_PROFILE [57].DSCP[7:2]	6
0xBB0001E4	TUNNEL_QOS_PROFILE [57].DSCP_SRC[1:0]	2
0xBB0001E8	TUNNEL_QOS_PROFILE [58].RESERVED[31:18]	14
0xBB0001E8	TUNNEL_QOS_PROFILE [58].OPRI[17:15]	3
0xBB0001E8	TUNNEL_QOS_PROFILE [58].OPRI_SRC[14:13]	2
0xBB0001E8	TUNNEL_QOS_PROFILE [58].IPRI[12:10]	3
0xBB0001E8	TUNNEL_QOS_PROFILE [58].IPRI_SRC[9:8]	2
0xBB0001E8	TUNNEL_QOS_PROFILE [58].DSCP[7:2]	6
0xBB0001E8	TUNNEL_QOS_PROFILE [58].DSCP_SRC[1:0]	2
0xBB0001EC	TUNNEL_QOS_PROFILE [59].RESERVED[31:18]	14
0xBB0001EC	TUNNEL_QOS_PROFILE [59].OPRI[17:15]	3
0xBB0001EC	TUNNEL_QOS_PROFILE [59].OPRI_SRC[14:13]	2
0xBB0001EC	TUNNEL_QOS_PROFILE [59].IPRI[12:10]	3
0xBB0001EC	TUNNEL_QOS_PROFILE [59].IPRI_SRC[9:8]	2
0xBB0001EC	TUNNEL_QOS_PROFILE [59].DSCP[7:2]	6
0xBB0001EC	TUNNEL_QOS_PROFILE [59].DSCP_SRC[1:0]	2
0xBB0001F0	TUNNEL_QOS_PROFILE [60].RESERVED[31:18]	14
0xBB0001F0	TUNNEL_QOS_PROFILE [60].OPRI[17:15]	3
0xBB0001F0	TUNNEL_QOS_PROFILE [60].OPRI_SRC[14:13]	2
0xBB0001F0	TUNNEL_QOS_PROFILE [60].IPRI[12:10]	3
0xBB0001F0	TUNNEL_QOS_PROFILE [60].IPRI_SRC[9:8]	2
0xBB0001F0	TUNNEL_QOS_PROFILE [60].DSCP[7:2]	6
0xBB0001F0	TUNNEL_QOS_PROFILE [60].DSCP_SRC[1:0]	2
0xBB0001F4	TUNNEL_QOS_PROFILE [61].RESERVED[31:18]	14
0xBB0001F4	TUNNEL_QOS_PROFILE [61].OPRI[17:15]	3
0xBB0001F4	TUNNEL_QOS_PROFILE [61].OPRI_SRC[14:13]	2
0xBB0001F4	TUNNEL_QOS_PROFILE [61].IPRI[12:10]	3
0xBB0001F4	TUNNEL_QOS_PROFILE [61].IPRI_SRC[9:8]	2
0xBB0001F4	TUNNEL_QOS_PROFILE [61].DSCP[7:2]	6
0xBB0001F4	TUNNEL_QOS_PROFILE [61].DSCP_SRC[1:0]	2
0xBB0001F8	TUNNEL_QOS_PROFILE [62].RESERVED[31:18]	14
0xBB0001F8	TUNNEL_QOS_PROFILE [62].OPRI[17:15]	3
0xBB0001F8	TUNNEL_QOS_PROFILE [62].OPRI_SRC[14:13]	2
0xBB0001F8	TUNNEL_QOS_PROFILE [62].IPRI[12:10]	3
0xBB0001F8	TUNNEL_QOS_PROFILE [62].IPRI_SRC[9:8]	2
0xBB0001F8	TUNNEL_QOS_PROFILE [62].DSCP[7:2]	6
0xBB0001F8	TUNNEL_QOS_PROFILE [62].DSCP_SRC[1:0]	2
0xBB0001FC	TUNNEL_QOS_PROFILE [63].RESERVED[31:18]	14
0xBB0001FC	TUNNEL_QOS_PROFILE [63].OPRI[17:15]	3
0xBB0001FC	TUNNEL_QOS_PROFILE [63].OPRI_SRC[14:13]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0001FC	TUNNEL_QOS_PROFILE [63].IPRI[12:10]	3
0xBB0001FC	TUNNEL_QOS_PROFILE [63].IPRI_SRC[9:8]	2
0xBB0001FC	TUNNEL_QOS_PROFILE [63].DSCP[7:2]	6
0xBB0001FC	TUNNEL_QOS_PROFILE [63].DSCP_SRC[1:0]	2
0xBB00F2C8	TUNNEL_ROUTE_CTRL.RESERVED[31:6]	26
0xBB00F2C8	TUNNEL_ROUTE_CTRL.ROUTE_TO_TUNNEL_ACT[5:4]	2
0xBB00F2C8	TUNNEL_ROUTE_CTRL.TTL_FAIL_ACT[3:2]	2
0xBB00F2C8	TUNNEL_ROUTE_CTRL.MTU_FAIL_ACT[1:0]	2
0xBB000528	TUNNEL_VXLAN_CTRL.RESERVED[31:17]	15
0xBB000528	TUNNEL_VXLAN_CTRL.UDP_SPORT_PARSE_EN[16:16]	1
0xBB000528	TUNNEL_VXLAN_CTRL.UDP_PORT[15:0]	16
0xBB0013E8	USXGMII_SUBMODE_CTRL [2].USXGMII_SUBMODE[4:0]	5
0xBB0013E8	USXGMII_SUBMODE_CTRL [3].USXGMII_SUBMODE[9:5]	5
0xBB0013E8	USXGMII_SUBMODE_CTRL [4].USXGMII_SUBMODE[14:10]	5
0xBB0013E8	USXGMII_SUBMODE_CTRL [5].USXGMII_SUBMODE[19:15]	5
0xBB0013E8	USXGMII_SUBMODE_CTRL [6].USXGMII_SUBMODE[24:20]	5
0xBB0013E8	USXGMII_SUBMODE_CTRL [7].USXGMII_SUBMODE[29:25]	5
0xBB0013EC	USXGMII_SUBMODE_CTRL [8].USXGMII_SUBMODE[4:0]	5
0xBB0013EC	USXGMII_SUBMODE_CTRL [9].USXGMII_SUBMODE[9:5]	5
0xBB0013EC	USXGMII_SUBMODE_CTRL [10].USXGMII_SUBMODE[14:10]	5
0xBB0013EC	USXGMII_SUBMODE_CTRL [11].USXGMII_SUBMODE[19:15]	5
0xBB0013EC	USXGMII_SUBMODE_CTRL [12].USXGMII_SUBMODE[24:20]	5
0xBB0013EC	USXGMII_SUBMODE_CTRL [13].USXGMII_SUBMODE[29:25]	5
0xBB0096B0	VLAN_APP_PKT_CTRL.RESERVED[31:18]	14
0xBB0096B0	VLAN_APP_PKT_CTRL.ARPA REP ACT[17:15]	3
0xBB0096B0	VLAN_APP_PKT_CTRL.ARPA REQ ACT[14:12]	3
0xBB0096B0	VLAN_APP_PKT_CTRL.DHCP6_ACT[11:9]	3
0xBB0096B0	VLAN_APP_PKT_CTRL.DHCP_ACT[8:6]	3
0xBB0096B0	VLAN_APP_PKT_CTRL.MLD_ACT[5:3]	3
0xBB0096B0	VLAN_APP_PKT_CTRL.IGMP_ACT[2:0]	3
0xBB0094E4	VLAN_CTRL.RESERVED[31:31]	1
0xBB0094E4	VLAN_CTRL.STP_LEAK_EN[30:30]	1
0xBB0094E4	VLAN_CTRL.LEAK_EN[29:29]	1
0xBB0094E4	VLAN_CTRL.SVL_FID_SRC[28:28]	1
0xBB0094E4	VLAN_CTRL.MC_SVL_FID[27:16]	12
0xBB0094E4	VLAN_CTRL.UC_SVL_FID[15:4]	12
0xBB0094E4	VLAN_CTRL.OCFI_ACT[3:2]	2
0xBB0094E4	VLAN_CTRL.ICFI_ACT[1:0]	2
0xBB004944	VLAN_EGR_VID_RNG_CHK_SET_0 [0].RESERVED[31:25]	7
0xBB004944	VLAN_EGR_VID_RNG_CHK_SET_0 [0].TYPE[24:24]	1
0xBB004944	VLAN_EGR_VID_RNG_CHK_SET_0 [0].UPPER[23:12]	12
0xBB004944	VLAN_EGR_VID_RNG_CHK_SET_0 [0].LOWER[11:0]	12
0xBB004948	VLAN_EGR_VID_RNG_CHK_SET_0 [1].RESERVED[31:25]	7
0xBB004948	VLAN_EGR_VID_RNG_CHK_SET_0 [1].TYPE[24:24]	1
0xBB004948	VLAN_EGR_VID_RNG_CHK_SET_0 [1].UPPER[23:12]	12
0xBB004948	VLAN_EGR_VID_RNG_CHK_SET_0 [1].LOWER[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00494C	VLAN_EGR_VID_RNG_CHK_SET_0 [2].RESERVED[31:25]	7
0xBB00494C	VLAN_EGR_VID_RNG_CHK_SET_0 [2].TYPE[24:24]	1
0xBB00494C	VLAN_EGR_VID_RNG_CHK_SET_0 [2].UPPER[23:12]	12
0xBB00494C	VLAN_EGR_VID_RNG_CHK_SET_0 [2].LOWER[11:0]	12
0xBB004950	VLAN_EGR_VID_RNG_CHK_SET_0 [3].RESERVED[31:25]	7
0xBB004950	VLAN_EGR_VID_RNG_CHK_SET_0 [3].TYPE[24:24]	1
0xBB004950	VLAN_EGR_VID_RNG_CHK_SET_0 [3].UPPER[23:12]	12
0xBB004950	VLAN_EGR_VID_RNG_CHK_SET_0 [3].LOWER[11:0]	12
0xBB004954	VLAN_EGR_VID_RNG_CHK_SET_0 [4].RESERVED[31:25]	7
0xBB004954	VLAN_EGR_VID_RNG_CHK_SET_0 [4].TYPE[24:24]	1
0xBB004954	VLAN_EGR_VID_RNG_CHK_SET_0 [4].UPPER[23:12]	12
0xBB004954	VLAN_EGR_VID_RNG_CHK_SET_0 [4].LOWER[11:0]	12
0xBB004958	VLAN_EGR_VID_RNG_CHK_SET_0 [5].RESERVED[31:25]	7
0xBB004958	VLAN_EGR_VID_RNG_CHK_SET_0 [5].TYPE[24:24]	1
0xBB004958	VLAN_EGR_VID_RNG_CHK_SET_0 [5].UPPER[23:12]	12
0xBB004958	VLAN_EGR_VID_RNG_CHK_SET_0 [5].LOWER[11:0]	12
0xBB00495C	VLAN_EGR_VID_RNG_CHK_SET_0 [6].RESERVED[31:25]	7
0xBB00495C	VLAN_EGR_VID_RNG_CHK_SET_0 [6].TYPE[24:24]	1
0xBB00495C	VLAN_EGR_VID_RNG_CHK_SET_0 [6].UPPER[23:12]	12
0xBB00495C	VLAN_EGR_VID_RNG_CHK_SET_0 [6].LOWER[11:0]	12
0xBB004960	VLAN_EGR_VID_RNG_CHK_SET_0 [7].RESERVED[31:25]	7
0xBB004960	VLAN_EGR_VID_RNG_CHK_SET_0 [7].TYPE[24:24]	1
0xBB004960	VLAN_EGR_VID_RNG_CHK_SET_0 [7].UPPER[23:12]	12
0xBB004960	VLAN_EGR_VID_RNG_CHK_SET_0 [7].LOWER[11:0]	12
0xBB004964	VLAN_EGR_VID_RNG_CHK_SET_0 [8].RESERVED[31:25]	7
0xBB004964	VLAN_EGR_VID_RNG_CHK_SET_0 [8].TYPE[24:24]	1
0xBB004964	VLAN_EGR_VID_RNG_CHK_SET_0 [8].UPPER[23:12]	12
0xBB004964	VLAN_EGR_VID_RNG_CHK_SET_0 [8].LOWER[11:0]	12
0xBB004968	VLAN_EGR_VID_RNG_CHK_SET_0 [9].RESERVED[31:25]	7
0xBB004968	VLAN_EGR_VID_RNG_CHK_SET_0 [9].TYPE[24:24]	1
0xBB004968	VLAN_EGR_VID_RNG_CHK_SET_0 [9].UPPER[23:12]	12
0xBB004968	VLAN_EGR_VID_RNG_CHK_SET_0 [9].LOWER[11:0]	12
0xBB00496C	VLAN_EGR_VID_RNG_CHK_SET_0 [10].RESERVED[31:25]	7
0xBB00496C	VLAN_EGR_VID_RNG_CHK_SET_0 [10].TYPE[24:24]	1
0xBB00496C	VLAN_EGR_VID_RNG_CHK_SET_0 [10].UPPER[23:12]	12
0xBB00496C	VLAN_EGR_VID_RNG_CHK_SET_0 [10].LOWER[11:0]	12
0xBB004970	VLAN_EGR_VID_RNG_CHK_SET_0 [11].RESERVED[31:25]	7
0xBB004970	VLAN_EGR_VID_RNG_CHK_SET_0 [11].TYPE[24:24]	1
0xBB004970	VLAN_EGR_VID_RNG_CHK_SET_0 [11].UPPER[23:12]	12
0xBB004970	VLAN_EGR_VID_RNG_CHK_SET_0 [11].LOWER[11:0]	12
0xBB004974	VLAN_EGR_VID_RNG_CHK_SET_0 [12].RESERVED[31:25]	7
0xBB004974	VLAN_EGR_VID_RNG_CHK_SET_0 [12].TYPE[24:24]	1
0xBB004974	VLAN_EGR_VID_RNG_CHK_SET_0 [12].UPPER[23:12]	12
0xBB004974	VLAN_EGR_VID_RNG_CHK_SET_0 [12].LOWER[11:0]	12
0xBB004978	VLAN_EGR_VID_RNG_CHK_SET_0 [13].RESERVED[31:25]	7
0xBB004978	VLAN_EGR_VID_RNG_CHK_SET_0 [13].TYPE[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004978	VLAN_EGR_VID_RNG_CHK_SET_0 [13].UPPER[23:12]	12
0xBB004978	VLAN_EGR_VID_RNG_CHK_SET_0 [13].LOWER[11:0]	12
0xBB00497C	VLAN_EGR_VID_RNG_CHK_SET_0 [14].RESERVED[31:25]	7
0xBB00497C	VLAN_EGR_VID_RNG_CHK_SET_0 [14].TYPE[24:24]	1
0xBB00497C	VLAN_EGR_VID_RNG_CHK_SET_0 [14].UPPER[23:12]	12
0xBB00497C	VLAN_EGR_VID_RNG_CHK_SET_0 [14].LOWER[11:0]	12
0xBB004980	VLAN_EGR_VID_RNG_CHK_SET_0 [15].RESERVED[31:25]	7
0xBB004980	VLAN_EGR_VID_RNG_CHK_SET_0 [15].TYPE[24:24]	1
0xBB004980	VLAN_EGR_VID_RNG_CHK_SET_0 [15].UPPER[23:12]	12
0xBB004980	VLAN_EGR_VID_RNG_CHK_SET_0 [15].LOWER[11:0]	12
0xBB004984	VLAN_EGR_VID_RNG_CHK_SET_0 [16].RESERVED[31:25]	7
0xBB004984	VLAN_EGR_VID_RNG_CHK_SET_0 [16].TYPE[24:24]	1
0xBB004984	VLAN_EGR_VID_RNG_CHK_SET_0 [16].UPPER[23:12]	12
0xBB004984	VLAN_EGR_VID_RNG_CHK_SET_0 [16].LOWER[11:0]	12
0xBB004988	VLAN_EGR_VID_RNG_CHK_SET_0 [17].RESERVED[31:25]	7
0xBB004988	VLAN_EGR_VID_RNG_CHK_SET_0 [17].TYPE[24:24]	1
0xBB004988	VLAN_EGR_VID_RNG_CHK_SET_0 [17].UPPER[23:12]	12
0xBB004988	VLAN_EGR_VID_RNG_CHK_SET_0 [17].LOWER[11:0]	12
0xBB00498C	VLAN_EGR_VID_RNG_CHK_SET_0 [18].RESERVED[31:25]	7
0xBB00498C	VLAN_EGR_VID_RNG_CHK_SET_0 [18].TYPE[24:24]	1
0xBB00498C	VLAN_EGR_VID_RNG_CHK_SET_0 [18].UPPER[23:12]	12
0xBB00498C	VLAN_EGR_VID_RNG_CHK_SET_0 [18].LOWER[11:0]	12
0xBB004990	VLAN_EGR_VID_RNG_CHK_SET_0 [19].RESERVED[31:25]	7
0xBB004990	VLAN_EGR_VID_RNG_CHK_SET_0 [19].TYPE[24:24]	1
0xBB004990	VLAN_EGR_VID_RNG_CHK_SET_0 [19].UPPER[23:12]	12
0xBB004990	VLAN_EGR_VID_RNG_CHK_SET_0 [19].LOWER[11:0]	12
0xBB004994	VLAN_EGR_VID_RNG_CHK_SET_0 [20].RESERVED[31:25]	7
0xBB004994	VLAN_EGR_VID_RNG_CHK_SET_0 [20].TYPE[24:24]	1
0xBB004994	VLAN_EGR_VID_RNG_CHK_SET_0 [20].UPPER[23:12]	12
0xBB004994	VLAN_EGR_VID_RNG_CHK_SET_0 [20].LOWER[11:0]	12
0xBB004998	VLAN_EGR_VID_RNG_CHK_SET_0 [21].RESERVED[31:25]	7
0xBB004998	VLAN_EGR_VID_RNG_CHK_SET_0 [21].TYPE[24:24]	1
0xBB004998	VLAN_EGR_VID_RNG_CHK_SET_0 [21].UPPER[23:12]	12
0xBB004998	VLAN_EGR_VID_RNG_CHK_SET_0 [21].LOWER[11:0]	12
0xBB00499C	VLAN_EGR_VID_RNG_CHK_SET_0 [22].RESERVED[31:25]	7
0xBB00499C	VLAN_EGR_VID_RNG_CHK_SET_0 [22].TYPE[24:24]	1
0xBB00499C	VLAN_EGR_VID_RNG_CHK_SET_0 [22].UPPER[23:12]	12
0xBB00499C	VLAN_EGR_VID_RNG_CHK_SET_0 [22].LOWER[11:0]	12
0xBB0049A0	VLAN_EGR_VID_RNG_CHK_SET_0 [23].RESERVED[31:25]	7
0xBB0049A0	VLAN_EGR_VID_RNG_CHK_SET_0 [23].TYPE[24:24]	1
0xBB0049A0	VLAN_EGR_VID_RNG_CHK_SET_0 [23].UPPER[23:12]	12
0xBB0049A0	VLAN_EGR_VID_RNG_CHK_SET_0 [23].LOWER[11:0]	12
0xBB0049A4	VLAN_EGR_VID_RNG_CHK_SET_0 [24].RESERVED[31:25]	7
0xBB0049A4	VLAN_EGR_VID_RNG_CHK_SET_0 [24].TYPE[24:24]	1
0xBB0049A4	VLAN_EGR_VID_RNG_CHK_SET_0 [24].UPPER[23:12]	12
0xBB0049A4	VLAN_EGR_VID_RNG_CHK_SET_0 [24].LOWER[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0049A8	VLAN_EGR_VID_RNG_CHK_SET_0 [25].RESERVED[31:25]	7
0xBB0049A8	VLAN_EGR_VID_RNG_CHK_SET_0 [25].TYPE[24:24]	1
0xBB0049A8	VLAN_EGR_VID_RNG_CHK_SET_0 [25].UPPER[23:12]	12
0xBB0049A8	VLAN_EGR_VID_RNG_CHK_SET_0 [25].LOWER[11:0]	12
0xBB0049AC	VLAN_EGR_VID_RNG_CHK_SET_0 [26].RESERVED[31:25]	7
0xBB0049AC	VLAN_EGR_VID_RNG_CHK_SET_0 [26].TYPE[24:24]	1
0xBB0049AC	VLAN_EGR_VID_RNG_CHK_SET_0 [26].UPPER[23:12]	12
0xBB0049AC	VLAN_EGR_VID_RNG_CHK_SET_0 [26].LOWER[11:0]	12
0xBB0049B0	VLAN_EGR_VID_RNG_CHK_SET_0 [27].RESERVED[31:25]	7
0xBB0049B0	VLAN_EGR_VID_RNG_CHK_SET_0 [27].TYPE[24:24]	1
0xBB0049B0	VLAN_EGR_VID_RNG_CHK_SET_0 [27].UPPER[23:12]	12
0xBB0049B0	VLAN_EGR_VID_RNG_CHK_SET_0 [27].LOWER[11:0]	12
0xBB0049B4	VLAN_EGR_VID_RNG_CHK_SET_0 [28].RESERVED[31:25]	7
0xBB0049B4	VLAN_EGR_VID_RNG_CHK_SET_0 [28].TYPE[24:24]	1
0xBB0049B4	VLAN_EGR_VID_RNG_CHK_SET_0 [28].UPPER[23:12]	12
0xBB0049B4	VLAN_EGR_VID_RNG_CHK_SET_0 [28].LOWER[11:0]	12
0xBB0049B8	VLAN_EGR_VID_RNG_CHK_SET_0 [29].RESERVED[31:25]	7
0xBB0049B8	VLAN_EGR_VID_RNG_CHK_SET_0 [29].TYPE[24:24]	1
0xBB0049B8	VLAN_EGR_VID_RNG_CHK_SET_0 [29].UPPER[23:12]	12
0xBB0049B8	VLAN_EGR_VID_RNG_CHK_SET_0 [29].LOWER[11:0]	12
0xBB0049BC	VLAN_EGR_VID_RNG_CHK_SET_0 [30].RESERVED[31:25]	7
0xBB0049BC	VLAN_EGR_VID_RNG_CHK_SET_0 [30].TYPE[24:24]	1
0xBB0049BC	VLAN_EGR_VID_RNG_CHK_SET_0 [30].UPPER[23:12]	12
0xBB0049BC	VLAN_EGR_VID_RNG_CHK_SET_0 [30].LOWER[11:0]	12
0xBB0049C0	VLAN_EGR_VID_RNG_CHK_SET_0 [31].RESERVED[31:25]	7
0xBB0049C0	VLAN_EGR_VID_RNG_CHK_SET_0 [31].TYPE[24:24]	1
0xBB0049C0	VLAN_EGR_VID_RNG_CHK_SET_0 [31].UPPER[23:12]	12
0xBB0049C0	VLAN_EGR_VID_RNG_CHK_SET_0 [31].LOWER[11:0]	12
0xBB0049C4	VLAN_EGR_VID_RNG_CHK_SET_1 [0].RESERVED[31:25]	7
0xBB0049C4	VLAN_EGR_VID_RNG_CHK_SET_1 [0].TYPE[24:24]	1
0xBB0049C4	VLAN_EGR_VID_RNG_CHK_SET_1 [0].UPPER[23:12]	12
0xBB0049C4	VLAN_EGR_VID_RNG_CHK_SET_1 [0].LOWER[11:0]	12
0xBB0049C8	VLAN_EGR_VID_RNG_CHK_SET_1 [1].RESERVED[31:25]	7
0xBB0049C8	VLAN_EGR_VID_RNG_CHK_SET_1 [1].TYPE[24:24]	1
0xBB0049C8	VLAN_EGR_VID_RNG_CHK_SET_1 [1].UPPER[23:12]	12
0xBB0049C8	VLAN_EGR_VID_RNG_CHK_SET_1 [1].LOWER[11:0]	12
0xBB0049CC	VLAN_EGR_VID_RNG_CHK_SET_1 [2].RESERVED[31:25]	7
0xBB0049CC	VLAN_EGR_VID_RNG_CHK_SET_1 [2].TYPE[24:24]	1
0xBB0049CC	VLAN_EGR_VID_RNG_CHK_SET_1 [2].UPPER[23:12]	12
0xBB0049CC	VLAN_EGR_VID_RNG_CHK_SET_1 [2].LOWER[11:0]	12
0xBB0049D0	VLAN_EGR_VID_RNG_CHK_SET_1 [3].RESERVED[31:25]	7
0xBB0049D0	VLAN_EGR_VID_RNG_CHK_SET_1 [3].TYPE[24:24]	1
0xBB0049D0	VLAN_EGR_VID_RNG_CHK_SET_1 [3].UPPER[23:12]	12
0xBB0049D0	VLAN_EGR_VID_RNG_CHK_SET_1 [3].LOWER[11:0]	12
0xBB0049D4	VLAN_EGR_VID_RNG_CHK_SET_1 [4].RESERVED[31:25]	7
0xBB0049D4	VLAN_EGR_VID_RNG_CHK_SET_1 [4].TYPE[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0049D4	VLAN_EGR_VID_RNG_CHK_SET_1 [4].UPPER[23:12]	12
0xBB0049D4	VLAN_EGR_VID_RNG_CHK_SET_1 [4].LOWER[11:0]	12
0xBB0049D8	VLAN_EGR_VID_RNG_CHK_SET_1 [5].RESERVED[31:25]	7
0xBB0049D8	VLAN_EGR_VID_RNG_CHK_SET_1 [5].TYPE[24:24]	1
0xBB0049D8	VLAN_EGR_VID_RNG_CHK_SET_1 [5].UPPER[23:12]	12
0xBB0049D8	VLAN_EGR_VID_RNG_CHK_SET_1 [5].LOWER[11:0]	12
0xBB0049DC	VLAN_EGR_VID_RNG_CHK_SET_1 [6].RESERVED[31:25]	7
0xBB0049DC	VLAN_EGR_VID_RNG_CHK_SET_1 [6].TYPE[24:24]	1
0xBB0049DC	VLAN_EGR_VID_RNG_CHK_SET_1 [6].UPPER[23:12]	12
0xBB0049DC	VLAN_EGR_VID_RNG_CHK_SET_1 [6].LOWER[11:0]	12
0xBB0049E0	VLAN_EGR_VID_RNG_CHK_SET_1 [7].RESERVED[31:25]	7
0xBB0049E0	VLAN_EGR_VID_RNG_CHK_SET_1 [7].TYPE[24:24]	1
0xBB0049E0	VLAN_EGR_VID_RNG_CHK_SET_1 [7].UPPER[23:12]	12
0xBB0049E0	VLAN_EGR_VID_RNG_CHK_SET_1 [7].LOWER[11:0]	12
0xBB0049E4	VLAN_EGR_VID_RNG_CHK_SET_1 [8].RESERVED[31:25]	7
0xBB0049E4	VLAN_EGR_VID_RNG_CHK_SET_1 [8].TYPE[24:24]	1
0xBB0049E4	VLAN_EGR_VID_RNG_CHK_SET_1 [8].UPPER[23:12]	12
0xBB0049E4	VLAN_EGR_VID_RNG_CHK_SET_1 [8].LOWER[11:0]	12
0xBB0049E8	VLAN_EGR_VID_RNG_CHK_SET_1 [9].RESERVED[31:25]	7
0xBB0049E8	VLAN_EGR_VID_RNG_CHK_SET_1 [9].TYPE[24:24]	1
0xBB0049E8	VLAN_EGR_VID_RNG_CHK_SET_1 [9].UPPER[23:12]	12
0xBB0049E8	VLAN_EGR_VID_RNG_CHK_SET_1 [9].LOWER[11:0]	12
0xBB0049FC	VLAN_EGR_VID_RNG_CHK_SET_1 [10].RESERVED[31:25]	7
0xBB0049FC	VLAN_EGR_VID_RNG_CHK_SET_1 [10].TYPE[24:24]	1
0xBB0049EC	VLAN_EGR_VID_RNG_CHK_SET_1 [10].UPPER[23:12]	12
0xBB0049EC	VLAN_EGR_VID_RNG_CHK_SET_1 [10].LOWER[11:0]	12
0xBB0049F0	VLAN_EGR_VID_RNG_CHK_SET_1 [11].RESERVED[31:25]	7
0xBB0049F0	VLAN_EGR_VID_RNG_CHK_SET_1 [11].TYPE[24:24]	1
0xBB0049F0	VLAN_EGR_VID_RNG_CHK_SET_1 [11].UPPER[23:12]	12
0xBB0049F0	VLAN_EGR_VID_RNG_CHK_SET_1 [11].LOWER[11:0]	12
0xBB0049F4	VLAN_EGR_VID_RNG_CHK_SET_1 [12].RESERVED[31:25]	7
0xBB0049F4	VLAN_EGR_VID_RNG_CHK_SET_1 [12].TYPE[24:24]	1
0xBB0049F4	VLAN_EGR_VID_RNG_CHK_SET_1 [12].UPPER[23:12]	12
0xBB0049F4	VLAN_EGR_VID_RNG_CHK_SET_1 [12].LOWER[11:0]	12
0xBB0049F8	VLAN_EGR_VID_RNG_CHK_SET_1 [13].RESERVED[31:25]	7
0xBB0049F8	VLAN_EGR_VID_RNG_CHK_SET_1 [13].TYPE[24:24]	1
0xBB0049F8	VLAN_EGR_VID_RNG_CHK_SET_1 [13].UPPER[23:12]	12
0xBB0049F8	VLAN_EGR_VID_RNG_CHK_SET_1 [13].LOWER[11:0]	12
0xBB0049FC	VLAN_EGR_VID_RNG_CHK_SET_1 [14].RESERVED[31:25]	7
0xBB0049FC	VLAN_EGR_VID_RNG_CHK_SET_1 [14].TYPE[24:24]	1
0xBB0049FC	VLAN_EGR_VID_RNG_CHK_SET_1 [14].UPPER[23:12]	12
0xBB0049FC	VLAN_EGR_VID_RNG_CHK_SET_1 [14].LOWER[11:0]	12
0xBB004A00	VLAN_EGR_VID_RNG_CHK_SET_1 [15].RESERVED[31:25]	7
0xBB004A00	VLAN_EGR_VID_RNG_CHK_SET_1 [15].TYPE[24:24]	1
0xBB004A00	VLAN_EGR_VID_RNG_CHK_SET_1 [15].UPPER[23:12]	12
0xBB004A00	VLAN_EGR_VID_RNG_CHK_SET_1 [15].LOWER[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004A04	VLAN_EGR_VID_RNG_CHK_SET_1 [16].RESERVED[31:25]	7
0xBB004A04	VLAN_EGR_VID_RNG_CHK_SET_1 [16].TYPE[24:24]	1
0xBB004A04	VLAN_EGR_VID_RNG_CHK_SET_1 [16].UPPER[23:12]	12
0xBB004A04	VLAN_EGR_VID_RNG_CHK_SET_1 [16].LOWER[11:0]	12
0xBB004A08	VLAN_EGR_VID_RNG_CHK_SET_1 [17].RESERVED[31:25]	7
0xBB004A08	VLAN_EGR_VID_RNG_CHK_SET_1 [17].TYPE[24:24]	1
0xBB004A08	VLAN_EGR_VID_RNG_CHK_SET_1 [17].UPPER[23:12]	12
0xBB004A08	VLAN_EGR_VID_RNG_CHK_SET_1 [17].LOWER[11:0]	12
0xBB004A0C	VLAN_EGR_VID_RNG_CHK_SET_1 [18].RESERVED[31:25]	7
0xBB004A0C	VLAN_EGR_VID_RNG_CHK_SET_1 [18].TYPE[24:24]	1
0xBB004A0C	VLAN_EGR_VID_RNG_CHK_SET_1 [18].UPPER[23:12]	12
0xBB004A0C	VLAN_EGR_VID_RNG_CHK_SET_1 [18].LOWER[11:0]	12
0xBB004A10	VLAN_EGR_VID_RNG_CHK_SET_1 [19].RESERVED[31:25]	7
0xBB004A10	VLAN_EGR_VID_RNG_CHK_SET_1 [19].TYPE[24:24]	1
0xBB004A10	VLAN_EGR_VID_RNG_CHK_SET_1 [19].UPPER[23:12]	12
0xBB004A10	VLAN_EGR_VID_RNG_CHK_SET_1 [19].LOWER[11:0]	12
0xBB004A14	VLAN_EGR_VID_RNG_CHK_SET_1 [20].RESERVED[31:25]	7
0xBB004A14	VLAN_EGR_VID_RNG_CHK_SET_1 [20].TYPE[24:24]	1
0xBB004A14	VLAN_EGR_VID_RNG_CHK_SET_1 [20].UPPER[23:12]	12
0xBB004A14	VLAN_EGR_VID_RNG_CHK_SET_1 [20].LOWER[11:0]	12
0xBB004A18	VLAN_EGR_VID_RNG_CHK_SET_1 [21].RESERVED[31:25]	7
0xBB004A18	VLAN_EGR_VID_RNG_CHK_SET_1 [21].TYPE[24:24]	1
0xBB004A18	VLAN_EGR_VID_RNG_CHK_SET_1 [21].UPPER[23:12]	12
0xBB004A18	VLAN_EGR_VID_RNG_CHK_SET_1 [21].LOWER[11:0]	12
0xBB004A1C	VLAN_EGR_VID_RNG_CHK_SET_1 [22].RESERVED[31:25]	7
0xBB004A1C	VLAN_EGR_VID_RNG_CHK_SET_1 [22].TYPE[24:24]	1
0xBB004A1C	VLAN_EGR_VID_RNG_CHK_SET_1 [22].UPPER[23:12]	12
0xBB004A1C	VLAN_EGR_VID_RNG_CHK_SET_1 [22].LOWER[11:0]	12
0xBB004A20	VLAN_EGR_VID_RNG_CHK_SET_1 [23].RESERVED[31:25]	7
0xBB004A20	VLAN_EGR_VID_RNG_CHK_SET_1 [23].TYPE[24:24]	1
0xBB004A20	VLAN_EGR_VID_RNG_CHK_SET_1 [23].UPPER[23:12]	12
0xBB004A20	VLAN_EGR_VID_RNG_CHK_SET_1 [23].LOWER[11:0]	12
0xBB004A24	VLAN_EGR_VID_RNG_CHK_SET_1 [24].RESERVED[31:25]	7
0xBB004A24	VLAN_EGR_VID_RNG_CHK_SET_1 [24].TYPE[24:24]	1
0xBB004A24	VLAN_EGR_VID_RNG_CHK_SET_1 [24].UPPER[23:12]	12
0xBB004A24	VLAN_EGR_VID_RNG_CHK_SET_1 [24].LOWER[11:0]	12
0xBB004A28	VLAN_EGR_VID_RNG_CHK_SET_1 [25].RESERVED[31:25]	7
0xBB004A28	VLAN_EGR_VID_RNG_CHK_SET_1 [25].TYPE[24:24]	1
0xBB004A28	VLAN_EGR_VID_RNG_CHK_SET_1 [25].UPPER[23:12]	12
0xBB004A28	VLAN_EGR_VID_RNG_CHK_SET_1 [25].LOWER[11:0]	12
0xBB004A2C	VLAN_EGR_VID_RNG_CHK_SET_1 [26].RESERVED[31:25]	7
0xBB004A2C	VLAN_EGR_VID_RNG_CHK_SET_1 [26].TYPE[24:24]	1
0xBB004A2C	VLAN_EGR_VID_RNG_CHK_SET_1 [26].UPPER[23:12]	12
0xBB004A2C	VLAN_EGR_VID_RNG_CHK_SET_1 [26].LOWER[11:0]	12
0xBB004A30	VLAN_EGR_VID_RNG_CHK_SET_1 [27].RESERVED[31:25]	7
0xBB004A30	VLAN_EGR_VID_RNG_CHK_SET_1 [27].TYPE[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004A30	VLAN_EGR_VID_RNG_CHK_SET_1 [27].UPPER[23:12]	12
0xBB004A30	VLAN_EGR_VID_RNG_CHK_SET_1 [27].LOWER[11:0]	12
0xBB004A34	VLAN_EGR_VID_RNG_CHK_SET_1 [28].RESERVED[31:25]	7
0xBB004A34	VLAN_EGR_VID_RNG_CHK_SET_1 [28].TYPE[24:24]	1
0xBB004A34	VLAN_EGR_VID_RNG_CHK_SET_1 [28].UPPER[23:12]	12
0xBB004A34	VLAN_EGR_VID_RNG_CHK_SET_1 [28].LOWER[11:0]	12
0xBB004A38	VLAN_EGR_VID_RNG_CHK_SET_1 [29].RESERVED[31:25]	7
0xBB004A38	VLAN_EGR_VID_RNG_CHK_SET_1 [29].TYPE[24:24]	1
0xBB004A38	VLAN_EGR_VID_RNG_CHK_SET_1 [29].UPPER[23:12]	12
0xBB004A38	VLAN_EGR_VID_RNG_CHK_SET_1 [29].LOWER[11:0]	12
0xBB004A3C	VLAN_EGR_VID_RNG_CHK_SET_1 [30].RESERVED[31:25]	7
0xBB004A3C	VLAN_EGR_VID_RNG_CHK_SET_1 [30].TYPE[24:24]	1
0xBB004A3C	VLAN_EGR_VID_RNG_CHK_SET_1 [30].UPPER[23:12]	12
0xBB004A3C	VLAN_EGR_VID_RNG_CHK_SET_1 [30].LOWER[11:0]	12
0xBB004A40	VLAN_EGR_VID_RNG_CHK_SET_1 [31].RESERVED[31:25]	7
0xBB004A40	VLAN_EGR_VID_RNG_CHK_SET_1 [31].TYPE[24:24]	1
0xBB004A40	VLAN_EGR_VID_RNG_CHK_SET_1 [31].UPPER[23:12]	12
0xBB004A40	VLAN_EGR_VID_RNG_CHK_SET_1 [31].LOWER[11:0]	12
0xBB004A44	VLAN_EGR_VID_RNG_CHK_SET_2 [0].RESERVED[31:25]	7
0xBB004A44	VLAN_EGR_VID_RNG_CHK_SET_2 [0].TYPE[24:24]	1
0xBB004A44	VLAN_EGR_VID_RNG_CHK_SET_2 [0].UPPER[23:12]	12
0xBB004A44	VLAN_EGR_VID_RNG_CHK_SET_2 [0].LOWER[11:0]	12
0xBB004A48	VLAN_EGR_VID_RNG_CHK_SET_2 [1].RESERVED[31:25]	7
0xBB004A48	VLAN_EGR_VID_RNG_CHK_SET_2 [1].TYPE[24:24]	1
0xBB004A48	VLAN_EGR_VID_RNG_CHK_SET_2 [1].UPPER[23:12]	12
0xBB004A48	VLAN_EGR_VID_RNG_CHK_SET_2 [1].LOWER[11:0]	12
0xBB004A4C	VLAN_EGR_VID_RNG_CHK_SET_2 [2].RESERVED[31:25]	7
0xBB004A4C	VLAN_EGR_VID_RNG_CHK_SET_2 [2].TYPE[24:24]	1
0xBB004A4C	VLAN_EGR_VID_RNG_CHK_SET_2 [2].UPPER[23:12]	12
0xBB004A4C	VLAN_EGR_VID_RNG_CHK_SET_2 [2].LOWER[11:0]	12
0xBB004A50	VLAN_EGR_VID_RNG_CHK_SET_2 [3].RESERVED[31:25]	7
0xBB004A50	VLAN_EGR_VID_RNG_CHK_SET_2 [3].TYPE[24:24]	1
0xBB004A50	VLAN_EGR_VID_RNG_CHK_SET_2 [3].UPPER[23:12]	12
0xBB004A50	VLAN_EGR_VID_RNG_CHK_SET_2 [3].LOWER[11:0]	12
0xBB004A54	VLAN_EGR_VID_RNG_CHK_SET_2 [4].RESERVED[31:25]	7
0xBB004A54	VLAN_EGR_VID_RNG_CHK_SET_2 [4].TYPE[24:24]	1
0xBB004A54	VLAN_EGR_VID_RNG_CHK_SET_2 [4].UPPER[23:12]	12
0xBB004A54	VLAN_EGR_VID_RNG_CHK_SET_2 [4].LOWER[11:0]	12
0xBB004A58	VLAN_EGR_VID_RNG_CHK_SET_2 [5].RESERVED[31:25]	7
0xBB004A58	VLAN_EGR_VID_RNG_CHK_SET_2 [5].TYPE[24:24]	1
0xBB004A58	VLAN_EGR_VID_RNG_CHK_SET_2 [5].UPPER[23:12]	12
0xBB004A58	VLAN_EGR_VID_RNG_CHK_SET_2 [5].LOWER[11:0]	12
0xBB004A5C	VLAN_EGR_VID_RNG_CHK_SET_2 [6].RESERVED[31:25]	7
0xBB004A5C	VLAN_EGR_VID_RNG_CHK_SET_2 [6].TYPE[24:24]	1
0xBB004A5C	VLAN_EGR_VID_RNG_CHK_SET_2 [6].UPPER[23:12]	12
0xBB004A5C	VLAN_EGR_VID_RNG_CHK_SET_2 [6].LOWER[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004A60	VLAN_EGR_VID_RNG_CHK_SET_2 [7].RESERVED[31:25]	7
0xBB004A60	VLAN_EGR_VID_RNG_CHK_SET_2 [7].TYPE[24:24]	1
0xBB004A60	VLAN_EGR_VID_RNG_CHK_SET_2 [7].UPPER[23:12]	12
0xBB004A60	VLAN_EGR_VID_RNG_CHK_SET_2 [7].LOWER[11:0]	12
0xBB004A64	VLAN_EGR_VID_RNG_CHK_SET_2 [8].RESERVED[31:25]	7
0xBB004A64	VLAN_EGR_VID_RNG_CHK_SET_2 [8].TYPE[24:24]	1
0xBB004A64	VLAN_EGR_VID_RNG_CHK_SET_2 [8].UPPER[23:12]	12
0xBB004A64	VLAN_EGR_VID_RNG_CHK_SET_2 [8].LOWER[11:0]	12
0xBB004A68	VLAN_EGR_VID_RNG_CHK_SET_2 [9].RESERVED[31:25]	7
0xBB004A68	VLAN_EGR_VID_RNG_CHK_SET_2 [9].TYPE[24:24]	1
0xBB004A68	VLAN_EGR_VID_RNG_CHK_SET_2 [9].UPPER[23:12]	12
0xBB004A68	VLAN_EGR_VID_RNG_CHK_SET_2 [9].LOWER[11:0]	12
0xBB004A6C	VLAN_EGR_VID_RNG_CHK_SET_2 [10].RESERVED[31:25]	7
0xBB004A6C	VLAN_EGR_VID_RNG_CHK_SET_2 [10].TYPE[24:24]	1
0xBB004A6C	VLAN_EGR_VID_RNG_CHK_SET_2 [10].UPPER[23:12]	12
0xBB004A6C	VLAN_EGR_VID_RNG_CHK_SET_2 [10].LOWER[11:0]	12
0xBB004A70	VLAN_EGR_VID_RNG_CHK_SET_2 [11].RESERVED[31:25]	7
0xBB004A70	VLAN_EGR_VID_RNG_CHK_SET_2 [11].TYPE[24:24]	1
0xBB004A70	VLAN_EGR_VID_RNG_CHK_SET_2 [11].UPPER[23:12]	12
0xBB004A70	VLAN_EGR_VID_RNG_CHK_SET_2 [11].LOWER[11:0]	12
0xBB004A74	VLAN_EGR_VID_RNG_CHK_SET_2 [12].RESERVED[31:25]	7
0xBB004A74	VLAN_EGR_VID_RNG_CHK_SET_2 [12].TYPE[24:24]	1
0xBB004A74	VLAN_EGR_VID_RNG_CHK_SET_2 [12].UPPER[23:12]	12
0xBB004A74	VLAN_EGR_VID_RNG_CHK_SET_2 [12].LOWER[11:0]	12
0xBB004A78	VLAN_EGR_VID_RNG_CHK_SET_2 [13].RESERVED[31:25]	7
0xBB004A78	VLAN_EGR_VID_RNG_CHK_SET_2 [13].TYPE[24:24]	1
0xBB004A78	VLAN_EGR_VID_RNG_CHK_SET_2 [13].UPPER[23:12]	12
0xBB004A78	VLAN_EGR_VID_RNG_CHK_SET_2 [13].LOWER[11:0]	12
0xBB004A7C	VLAN_EGR_VID_RNG_CHK_SET_2 [14].RESERVED[31:25]	7
0xBB004A7C	VLAN_EGR_VID_RNG_CHK_SET_2 [14].TYPE[24:24]	1
0xBB004A7C	VLAN_EGR_VID_RNG_CHK_SET_2 [14].UPPER[23:12]	12
0xBB004A7C	VLAN_EGR_VID_RNG_CHK_SET_2 [14].LOWER[11:0]	12
0xBB004A80	VLAN_EGR_VID_RNG_CHK_SET_2 [15].RESERVED[31:25]	7
0xBB004A80	VLAN_EGR_VID_RNG_CHK_SET_2 [15].TYPE[24:24]	1
0xBB004A80	VLAN_EGR_VID_RNG_CHK_SET_2 [15].UPPER[23:12]	12
0xBB004A80	VLAN_EGR_VID_RNG_CHK_SET_2 [15].LOWER[11:0]	12
0xBB004A84	VLAN_EGR_VID_RNG_CHK_SET_2 [16].RESERVED[31:25]	7
0xBB004A84	VLAN_EGR_VID_RNG_CHK_SET_2 [16].TYPE[24:24]	1
0xBB004A84	VLAN_EGR_VID_RNG_CHK_SET_2 [16].UPPER[23:12]	12
0xBB004A84	VLAN_EGR_VID_RNG_CHK_SET_2 [16].LOWER[11:0]	12
0xBB004A88	VLAN_EGR_VID_RNG_CHK_SET_2 [17].RESERVED[31:25]	7
0xBB004A88	VLAN_EGR_VID_RNG_CHK_SET_2 [17].TYPE[24:24]	1
0xBB004A88	VLAN_EGR_VID_RNG_CHK_SET_2 [17].UPPER[23:12]	12
0xBB004A88	VLAN_EGR_VID_RNG_CHK_SET_2 [17].LOWER[11:0]	12
0xBB004A8C	VLAN_EGR_VID_RNG_CHK_SET_2 [18].RESERVED[31:25]	7
0xBB004A8C	VLAN_EGR_VID_RNG_CHK_SET_2 [18].TYPE[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004A8C	VLAN_EGR_VID_RNG_CHK_SET_2 [18].UPPER[23:12]	12
0xBB004A8C	VLAN_EGR_VID_RNG_CHK_SET_2 [18].LOWER[11:0]	12
0xBB004A90	VLAN_EGR_VID_RNG_CHK_SET_2 [19].RESERVED[31:25]	7
0xBB004A90	VLAN_EGR_VID_RNG_CHK_SET_2 [19].TYPE[24:24]	1
0xBB004A90	VLAN_EGR_VID_RNG_CHK_SET_2 [19].UPPER[23:12]	12
0xBB004A90	VLAN_EGR_VID_RNG_CHK_SET_2 [19].LOWER[11:0]	12
0xBB004A94	VLAN_EGR_VID_RNG_CHK_SET_2 [20].RESERVED[31:25]	7
0xBB004A94	VLAN_EGR_VID_RNG_CHK_SET_2 [20].TYPE[24:24]	1
0xBB004A94	VLAN_EGR_VID_RNG_CHK_SET_2 [20].UPPER[23:12]	12
0xBB004A94	VLAN_EGR_VID_RNG_CHK_SET_2 [20].LOWER[11:0]	12
0xBB004A98	VLAN_EGR_VID_RNG_CHK_SET_2 [21].RESERVED[31:25]	7
0xBB004A98	VLAN_EGR_VID_RNG_CHK_SET_2 [21].TYPE[24:24]	1
0xBB004A98	VLAN_EGR_VID_RNG_CHK_SET_2 [21].UPPER[23:12]	12
0xBB004A98	VLAN_EGR_VID_RNG_CHK_SET_2 [21].LOWER[11:0]	12
0xBB004A9C	VLAN_EGR_VID_RNG_CHK_SET_2 [22].RESERVED[31:25]	7
0xBB004A9C	VLAN_EGR_VID_RNG_CHK_SET_2 [22].TYPE[24:24]	1
0xBB004A9C	VLAN_EGR_VID_RNG_CHK_SET_2 [22].UPPER[23:12]	12
0xBB004A9C	VLAN_EGR_VID_RNG_CHK_SET_2 [22].LOWER[11:0]	12
0xBB004AA0	VLAN_EGR_VID_RNG_CHK_SET_2 [23].RESERVED[31:25]	7
0xBB004AA0	VLAN_EGR_VID_RNG_CHK_SET_2 [23].TYPE[24:24]	1
0xBB004AA0	VLAN_EGR_VID_RNG_CHK_SET_2 [23].UPPER[23:12]	12
0xBB004AA0	VLAN_EGR_VID_RNG_CHK_SET_2 [23].LOWER[11:0]	12
0xBB004AA4	VLAN_EGR_VID_RNG_CHK_SET_2 [24].RESERVED[31:25]	7
0xBB004AA4	VLAN_EGR_VID_RNG_CHK_SET_2 [24].TYPE[24:24]	1
0xBB004AA4	VLAN_EGR_VID_RNG_CHK_SET_2 [24].UPPER[23:12]	12
0xBB004AA4	VLAN_EGR_VID_RNG_CHK_SET_2 [24].LOWER[11:0]	12
0xBB004AA8	VLAN_EGR_VID_RNG_CHK_SET_2 [25].RESERVED[31:25]	7
0xBB004AA8	VLAN_EGR_VID_RNG_CHK_SET_2 [25].TYPE[24:24]	1
0xBB004AA8	VLAN_EGR_VID_RNG_CHK_SET_2 [25].UPPER[23:12]	12
0xBB004AA8	VLAN_EGR_VID_RNG_CHK_SET_2 [25].LOWER[11:0]	12
0xBB004AAC	VLAN_EGR_VID_RNG_CHK_SET_2 [26].RESERVED[31:25]	7
0xBB004AAC	VLAN_EGR_VID_RNG_CHK_SET_2 [26].TYPE[24:24]	1
0xBB004AAC	VLAN_EGR_VID_RNG_CHK_SET_2 [26].UPPER[23:12]	12
0xBB004AAC	VLAN_EGR_VID_RNG_CHK_SET_2 [26].LOWER[11:0]	12
0xBB004AB0	VLAN_EGR_VID_RNG_CHK_SET_2 [27].RESERVED[31:25]	7
0xBB004AB0	VLAN_EGR_VID_RNG_CHK_SET_2 [27].TYPE[24:24]	1
0xBB004AB0	VLAN_EGR_VID_RNG_CHK_SET_2 [27].UPPER[23:12]	12
0xBB004AB0	VLAN_EGR_VID_RNG_CHK_SET_2 [27].LOWER[11:0]	12
0xBB004AB4	VLAN_EGR_VID_RNG_CHK_SET_2 [28].RESERVED[31:25]	7
0xBB004AB4	VLAN_EGR_VID_RNG_CHK_SET_2 [28].TYPE[24:24]	1
0xBB004AB4	VLAN_EGR_VID_RNG_CHK_SET_2 [28].UPPER[23:12]	12
0xBB004AB4	VLAN_EGR_VID_RNG_CHK_SET_2 [28].LOWER[11:0]	12
0xBB004AB8	VLAN_EGR_VID_RNG_CHK_SET_2 [29].RESERVED[31:25]	7
0xBB004AB8	VLAN_EGR_VID_RNG_CHK_SET_2 [29].TYPE[24:24]	1
0xBB004AB8	VLAN_EGR_VID_RNG_CHK_SET_2 [29].UPPER[23:12]	12
0xBB004AB8	VLAN_EGR_VID_RNG_CHK_SET_2 [29].LOWER[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004ABC	VLAN_EGR_VID_RNG_CHK_SET_2 [30].RESERVED[31:25]	7
0xBB004ABC	VLAN_EGR_VID_RNG_CHK_SET_2 [30].TYPE[24:24]	1
0xBB004ABC	VLAN_EGR_VID_RNG_CHK_SET_2 [30].UPPER[23:12]	12
0xBB004ABC	VLAN_EGR_VID_RNG_CHK_SET_2 [30].LOWER[11:0]	12
0xBB004AC0	VLAN_EGR_VID_RNG_CHK_SET_2 [31].RESERVED[31:25]	7
0xBB004AC0	VLAN_EGR_VID_RNG_CHK_SET_2 [31].TYPE[24:24]	1
0xBB004AC0	VLAN_EGR_VID_RNG_CHK_SET_2 [31].UPPER[23:12]	12
0xBB004AC0	VLAN_EGR_VID_RNG_CHK_SET_2 [31].LOWER[11:0]	12
0xBB004AC4	VLAN_EGR_VID_RNG_CHK_SET_3 [0].RESERVED[31:25]	7
0xBB004AC4	VLAN_EGR_VID_RNG_CHK_SET_3 [0].TYPE[24:24]	1
0xBB004AC4	VLAN_EGR_VID_RNG_CHK_SET_3 [0].UPPER[23:12]	12
0xBB004AC4	VLAN_EGR_VID_RNG_CHK_SET_3 [0].LOWER[11:0]	12
0xBB004AC8	VLAN_EGR_VID_RNG_CHK_SET_3 [1].RESERVED[31:25]	7
0xBB004AC8	VLAN_EGR_VID_RNG_CHK_SET_3 [1].TYPE[24:24]	1
0xBB004AC8	VLAN_EGR_VID_RNG_CHK_SET_3 [1].UPPER[23:12]	12
0xBB004AC8	VLAN_EGR_VID_RNG_CHK_SET_3 [1].LOWER[11:0]	12
0xBB004ACC	VLAN_EGR_VID_RNG_CHK_SET_3 [2].RESERVED[31:25]	7
0xBB004ACC	VLAN_EGR_VID_RNG_CHK_SET_3 [2].TYPE[24:24]	1
0xBB004ACC	VLAN_EGR_VID_RNG_CHK_SET_3 [2].UPPER[23:12]	12
0xBB004ACC	VLAN_EGR_VID_RNG_CHK_SET_3 [2].LOWER[11:0]	12
0xBB004AD0	VLAN_EGR_VID_RNG_CHK_SET_3 [3].RESERVED[31:25]	7
0xBB004AD0	VLAN_EGR_VID_RNG_CHK_SET_3 [3].TYPE[24:24]	1
0xBB004AD0	VLAN_EGR_VID_RNG_CHK_SET_3 [3].UPPER[23:12]	12
0xBB004AD0	VLAN_EGR_VID_RNG_CHK_SET_3 [3].LOWER[11:0]	12
0xBB004AD4	VLAN_EGR_VID_RNG_CHK_SET_3 [4].RESERVED[31:25]	7
0xBB004AD4	VLAN_EGR_VID_RNG_CHK_SET_3 [4].TYPE[24:24]	1
0xBB004AD4	VLAN_EGR_VID_RNG_CHK_SET_3 [4].UPPER[23:12]	12
0xBB004AD4	VLAN_EGR_VID_RNG_CHK_SET_3 [4].LOWER[11:0]	12
0xBB004AD8	VLAN_EGR_VID_RNG_CHK_SET_3 [5].RESERVED[31:25]	7
0xBB004AD8	VLAN_EGR_VID_RNG_CHK_SET_3 [5].TYPE[24:24]	1
0xBB004AD8	VLAN_EGR_VID_RNG_CHK_SET_3 [5].UPPER[23:12]	12
0xBB004AD8	VLAN_EGR_VID_RNG_CHK_SET_3 [5].LOWER[11:0]	12
0xBB004ADC	VLAN_EGR_VID_RNG_CHK_SET_3 [6].RESERVED[31:25]	7
0xBB004ADC	VLAN_EGR_VID_RNG_CHK_SET_3 [6].TYPE[24:24]	1
0xBB004ADC	VLAN_EGR_VID_RNG_CHK_SET_3 [6].UPPER[23:12]	12
0xBB004ADC	VLAN_EGR_VID_RNG_CHK_SET_3 [6].LOWER[11:0]	12
0xBB004AE0	VLAN_EGR_VID_RNG_CHK_SET_3 [7].RESERVED[31:25]	7
0xBB004AE0	VLAN_EGR_VID_RNG_CHK_SET_3 [7].TYPE[24:24]	1
0xBB004AE0	VLAN_EGR_VID_RNG_CHK_SET_3 [7].UPPER[23:12]	12
0xBB004AE0	VLAN_EGR_VID_RNG_CHK_SET_3 [7].LOWER[11:0]	12
0xBB004AE4	VLAN_EGR_VID_RNG_CHK_SET_3 [8].RESERVED[31:25]	7
0xBB004AE4	VLAN_EGR_VID_RNG_CHK_SET_3 [8].TYPE[24:24]	1
0xBB004AE4	VLAN_EGR_VID_RNG_CHK_SET_3 [8].UPPER[23:12]	12
0xBB004AE4	VLAN_EGR_VID_RNG_CHK_SET_3 [8].LOWER[11:0]	12
0xBB004AE8	VLAN_EGR_VID_RNG_CHK_SET_3 [9].RESERVED[31:25]	7
0xBB004AE8	VLAN_EGR_VID_RNG_CHK_SET_3 [9].TYPE[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004AE8	VLAN_EGR_VID_RNG_CHK_SET_3 [9].UPPER[23:12]	12
0xBB004AE8	VLAN_EGR_VID_RNG_CHK_SET_3 [9].LOWER[11:0]	12
0xBB004AEC	VLAN_EGR_VID_RNG_CHK_SET_3 [10].RESERVED[31:25]	7
0xBB004AEC	VLAN_EGR_VID_RNG_CHK_SET_3 [10].TYPE[24:24]	1
0xBB004AEC	VLAN_EGR_VID_RNG_CHK_SET_3 [10].UPPER[23:12]	12
0xBB004AEC	VLAN_EGR_VID_RNG_CHK_SET_3 [10].LOWER[11:0]	12
0xBB004AF0	VLAN_EGR_VID_RNG_CHK_SET_3 [11].RESERVED[31:25]	7
0xBB004AF0	VLAN_EGR_VID_RNG_CHK_SET_3 [11].TYPE[24:24]	1
0xBB004AF0	VLAN_EGR_VID_RNG_CHK_SET_3 [11].UPPER[23:12]	12
0xBB004AF0	VLAN_EGR_VID_RNG_CHK_SET_3 [11].LOWER[11:0]	12
0xBB004AF4	VLAN_EGR_VID_RNG_CHK_SET_3 [12].RESERVED[31:25]	7
0xBB004AF4	VLAN_EGR_VID_RNG_CHK_SET_3 [12].TYPE[24:24]	1
0xBB004AF4	VLAN_EGR_VID_RNG_CHK_SET_3 [12].UPPER[23:12]	12
0xBB004AF4	VLAN_EGR_VID_RNG_CHK_SET_3 [12].LOWER[11:0]	12
0xBB004AF8	VLAN_EGR_VID_RNG_CHK_SET_3 [13].RESERVED[31:25]	7
0xBB004AF8	VLAN_EGR_VID_RNG_CHK_SET_3 [13].TYPE[24:24]	1
0xBB004AF8	VLAN_EGR_VID_RNG_CHK_SET_3 [13].UPPER[23:12]	12
0xBB004AF8	VLAN_EGR_VID_RNG_CHK_SET_3 [13].LOWER[11:0]	12
0xBB004AFC	VLAN_EGR_VID_RNG_CHK_SET_3 [14].RESERVED[31:25]	7
0xBB004AFC	VLAN_EGR_VID_RNG_CHK_SET_3 [14].TYPE[24:24]	1
0xBB004AFC	VLAN_EGR_VID_RNG_CHK_SET_3 [14].UPPER[23:12]	12
0xBB004AFC	VLAN_EGR_VID_RNG_CHK_SET_3 [14].LOWER[11:0]	12
0xBB004B00	VLAN_EGR_VID_RNG_CHK_SET_3 [15].RESERVED[31:25]	7
0xBB004B00	VLAN_EGR_VID_RNG_CHK_SET_3 [15].TYPE[24:24]	1
0xBB004B00	VLAN_EGR_VID_RNG_CHK_SET_3 [15].UPPER[23:12]	12
0xBB004B00	VLAN_EGR_VID_RNG_CHK_SET_3 [15].LOWER[11:0]	12
0xBB004B04	VLAN_EGR_VID_RNG_CHK_SET_3 [16].RESERVED[31:25]	7
0xBB004B04	VLAN_EGR_VID_RNG_CHK_SET_3 [16].TYPE[24:24]	1
0xBB004B04	VLAN_EGR_VID_RNG_CHK_SET_3 [16].UPPER[23:12]	12
0xBB004B04	VLAN_EGR_VID_RNG_CHK_SET_3 [16].LOWER[11:0]	12
0xBB004B08	VLAN_EGR_VID_RNG_CHK_SET_3 [17].RESERVED[31:25]	7
0xBB004B08	VLAN_EGR_VID_RNG_CHK_SET_3 [17].TYPE[24:24]	1
0xBB004B08	VLAN_EGR_VID_RNG_CHK_SET_3 [17].UPPER[23:12]	12
0xBB004B08	VLAN_EGR_VID_RNG_CHK_SET_3 [17].LOWER[11:0]	12
0xBB004B0C	VLAN_EGR_VID_RNG_CHK_SET_3 [18].RESERVED[31:25]	7
0xBB004B0C	VLAN_EGR_VID_RNG_CHK_SET_3 [18].TYPE[24:24]	1
0xBB004B0C	VLAN_EGR_VID_RNG_CHK_SET_3 [18].UPPER[23:12]	12
0xBB004B0C	VLAN_EGR_VID_RNG_CHK_SET_3 [18].LOWER[11:0]	12
0xBB004B10	VLAN_EGR_VID_RNG_CHK_SET_3 [19].RESERVED[31:25]	7
0xBB004B10	VLAN_EGR_VID_RNG_CHK_SET_3 [19].TYPE[24:24]	1
0xBB004B10	VLAN_EGR_VID_RNG_CHK_SET_3 [19].UPPER[23:12]	12
0xBB004B10	VLAN_EGR_VID_RNG_CHK_SET_3 [19].LOWER[11:0]	12
0xBB004B14	VLAN_EGR_VID_RNG_CHK_SET_3 [20].RESERVED[31:25]	7
0xBB004B14	VLAN_EGR_VID_RNG_CHK_SET_3 [20].TYPE[24:24]	1
0xBB004B14	VLAN_EGR_VID_RNG_CHK_SET_3 [20].UPPER[23:12]	12
0xBB004B14	VLAN_EGR_VID_RNG_CHK_SET_3 [20].LOWER[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004B18	VLAN_EGR_VID_RNG_CHK_SET_3 [21].RESERVED[31:25]	7
0xBB004B18	VLAN_EGR_VID_RNG_CHK_SET_3 [21].TYPE[24:24]	1
0xBB004B18	VLAN_EGR_VID_RNG_CHK_SET_3 [21].UPPER[23:12]	12
0xBB004B18	VLAN_EGR_VID_RNG_CHK_SET_3 [21].LOWER[11:0]	12
0xBB004B1C	VLAN_EGR_VID_RNG_CHK_SET_3 [22].RESERVED[31:25]	7
0xBB004B1C	VLAN_EGR_VID_RNG_CHK_SET_3 [22].TYPE[24:24]	1
0xBB004B1C	VLAN_EGR_VID_RNG_CHK_SET_3 [22].UPPER[23:12]	12
0xBB004B1C	VLAN_EGR_VID_RNG_CHK_SET_3 [22].LOWER[11:0]	12
0xBB004B20	VLAN_EGR_VID_RNG_CHK_SET_3 [23].RESERVED[31:25]	7
0xBB004B20	VLAN_EGR_VID_RNG_CHK_SET_3 [23].TYPE[24:24]	1
0xBB004B20	VLAN_EGR_VID_RNG_CHK_SET_3 [23].UPPER[23:12]	12
0xBB004B20	VLAN_EGR_VID_RNG_CHK_SET_3 [23].LOWER[11:0]	12
0xBB004B24	VLAN_EGR_VID_RNG_CHK_SET_3 [24].RESERVED[31:25]	7
0xBB004B24	VLAN_EGR_VID_RNG_CHK_SET_3 [24].TYPE[24:24]	1
0xBB004B24	VLAN_EGR_VID_RNG_CHK_SET_3 [24].UPPER[23:12]	12
0xBB004B24	VLAN_EGR_VID_RNG_CHK_SET_3 [24].LOWER[11:0]	12
0xBB004B28	VLAN_EGR_VID_RNG_CHK_SET_3 [25].RESERVED[31:25]	7
0xBB004B28	VLAN_EGR_VID_RNG_CHK_SET_3 [25].TYPE[24:24]	1
0xBB004B28	VLAN_EGR_VID_RNG_CHK_SET_3 [25].UPPER[23:12]	12
0xBB004B28	VLAN_EGR_VID_RNG_CHK_SET_3 [25].LOWER[11:0]	12
0xBB004B2C	VLAN_EGR_VID_RNG_CHK_SET_3 [26].RESERVED[31:25]	7
0xBB004B2C	VLAN_EGR_VID_RNG_CHK_SET_3 [26].TYPE[24:24]	1
0xBB004B2C	VLAN_EGR_VID_RNG_CHK_SET_3 [26].UPPER[23:12]	12
0xBB004B2C	VLAN_EGR_VID_RNG_CHK_SET_3 [26].LOWER[11:0]	12
0xBB004B30	VLAN_EGR_VID_RNG_CHK_SET_3 [27].RESERVED[31:25]	7
0xBB004B30	VLAN_EGR_VID_RNG_CHK_SET_3 [27].TYPE[24:24]	1
0xBB004B30	VLAN_EGR_VID_RNG_CHK_SET_3 [27].UPPER[23:12]	12
0xBB004B30	VLAN_EGR_VID_RNG_CHK_SET_3 [27].LOWER[11:0]	12
0xBB004B34	VLAN_EGR_VID_RNG_CHK_SET_3 [28].RESERVED[31:25]	7
0xBB004B34	VLAN_EGR_VID_RNG_CHK_SET_3 [28].TYPE[24:24]	1
0xBB004B34	VLAN_EGR_VID_RNG_CHK_SET_3 [28].UPPER[23:12]	12
0xBB004B34	VLAN_EGR_VID_RNG_CHK_SET_3 [28].LOWER[11:0]	12
0xBB004B38	VLAN_EGR_VID_RNG_CHK_SET_3 [29].RESERVED[31:25]	7
0xBB004B38	VLAN_EGR_VID_RNG_CHK_SET_3 [29].TYPE[24:24]	1
0xBB004B38	VLAN_EGR_VID_RNG_CHK_SET_3 [29].UPPER[23:12]	12
0xBB004B38	VLAN_EGR_VID_RNG_CHK_SET_3 [29].LOWER[11:0]	12
0xBB004B3C	VLAN_EGR_VID_RNG_CHK_SET_3 [30].RESERVED[31:25]	7
0xBB004B3C	VLAN_EGR_VID_RNG_CHK_SET_3 [30].TYPE[24:24]	1
0xBB004B3C	VLAN_EGR_VID_RNG_CHK_SET_3 [30].UPPER[23:12]	12
0xBB004B3C	VLAN_EGR_VID_RNG_CHK_SET_3 [30].LOWER[11:0]	12
0xBB004B40	VLAN_EGR_VID_RNG_CHK_SET_3 [31].RESERVED[31:25]	7
0xBB004B40	VLAN_EGR_VID_RNG_CHK_SET_3 [31].TYPE[24:24]	1
0xBB004B40	VLAN_EGR_VID_RNG_CHK_SET_3 [31].UPPER[23:12]	12
0xBB004B40	VLAN_EGR_VID_RNG_CHK_SET_3 [31].LOWER[11:0]	12
0xBB00051C	VLAN_ETAG_TPID_CTRL.RESERVED[31:16]	16
0xBB00051C	VLAN_ETAG_TPID_CTRL.ETPID[15:0]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [0].HIT[0:0]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [1].HIT[1:1]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [2].HIT[2:2]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [3].HIT[3:3]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [4].HIT[4:4]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [5].HIT[5:5]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [6].HIT[6:6]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [7].HIT[7:7]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [8].HIT[8:8]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [9].HIT[9:9]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [10].HIT[10:10]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [11].HIT[11:11]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [12].HIT[12:12]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [13].HIT[13:13]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [14].HIT[14:14]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [15].HIT[15:15]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [16].HIT[16:16]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [17].HIT[17:17]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [18].HIT[18:18]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [19].HIT[19:19]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [20].HIT[20:20]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [21].HIT[21:21]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [22].HIT[22:22]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [23].HIT[23:23]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [24].HIT[24:24]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [25].HIT[25:25]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [26].HIT[26:26]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [27].HIT[27:27]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [28].HIT[28:28]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [29].HIT[29:29]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [30].HIT[30:30]	1
0xBB004C28	VLAN_EVC_ENTRY_INDICATION [31].HIT[31:31]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [32].HIT[0:0]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [33].HIT[1:1]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [34].HIT[2:2]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [35].HIT[3:3]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [36].HIT[4:4]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [37].HIT[5:5]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [38].HIT[6:6]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [39].HIT[7:7]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [40].HIT[8:8]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [41].HIT[9:9]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [42].HIT[10:10]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [43].HIT[11:11]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [44].HIT[12:12]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [45].HIT[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [46].HIT[14:14]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [47].HIT[15:15]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [48].HIT[16:16]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [49].HIT[17:17]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [50].HIT[18:18]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [51].HIT[19:19]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [52].HIT[20:20]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [53].HIT[21:21]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [54].HIT[22:22]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [55].HIT[23:23]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [56].HIT[24:24]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [57].HIT[25:25]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [58].HIT[26:26]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [59].HIT[27:27]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [60].HIT[28:28]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [61].HIT[29:29]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [62].HIT[30:30]	1
0xBB004C2C	VLAN_EVC_ENTRY_INDICATION [63].HIT[31:31]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [64].HIT[0:0]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [65].HIT[1:1]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [66].HIT[2:2]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [67].HIT[3:3]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [68].HIT[4:4]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [69].HIT[5:5]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [70].HIT[6:6]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [71].HIT[7:7]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [72].HIT[8:8]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [73].HIT[9:9]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [74].HIT[10:10]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [75].HIT[11:11]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [76].HIT[12:12]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [77].HIT[13:13]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [78].HIT[14:14]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [79].HIT[15:15]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [80].HIT[16:16]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [81].HIT[17:17]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [82].HIT[18:18]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [83].HIT[19:19]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [84].HIT[20:20]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [85].HIT[21:21]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [86].HIT[22:22]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [87].HIT[23:23]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [88].HIT[24:24]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [89].HIT[25:25]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [90].HIT[26:26]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [91].HIT[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [92].HIT[28:28]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [93].HIT[29:29]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [94].HIT[30:30]	1
0xBB004C30	VLAN_EVC_ENTRY_INDICATION [95].HIT[31:31]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [96].HIT[0:0]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [97].HIT[1:1]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [98].HIT[2:2]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [99].HIT[3:3]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [100].HIT[4:4]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [101].HIT[5:5]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [102].HIT[6:6]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [103].HIT[7:7]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [104].HIT[8:8]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [105].HIT[9:9]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [106].HIT[10:10]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [107].HIT[11:11]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [108].HIT[12:12]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [109].HIT[13:13]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [110].HIT[14:14]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [111].HIT[15:15]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [112].HIT[16:16]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [113].HIT[17:17]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [114].HIT[18:18]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [115].HIT[19:19]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [116].HIT[20:20]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [117].HIT[21:21]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [118].HIT[22:22]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [119].HIT[23:23]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [120].HIT[24:24]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [121].HIT[25:25]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [122].HIT[26:26]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [123].HIT[27:27]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [124].HIT[28:28]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [125].HIT[29:29]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [126].HIT[30:30]	1
0xBB004C34	VLAN_EVC_ENTRY_INDICATION [127].HIT[31:31]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [128].HIT[0:0]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [129].HIT[1:1]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [130].HIT[2:2]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [131].HIT[3:3]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [132].HIT[4:4]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [133].HIT[5:5]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [134].HIT[6:6]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [135].HIT[7:7]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [136].HIT[8:8]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [137].HIT[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [138].HIT[10:10]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [139].HIT[11:11]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [140].HIT[12:12]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [141].HIT[13:13]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [142].HIT[14:14]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [143].HIT[15:15]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [144].HIT[16:16]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [145].HIT[17:17]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [146].HIT[18:18]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [147].HIT[19:19]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [148].HIT[20:20]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [149].HIT[21:21]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [150].HIT[22:22]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [151].HIT[23:23]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [152].HIT[24:24]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [153].HIT[25:25]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [154].HIT[26:26]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [155].HIT[27:27]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [156].HIT[28:28]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [157].HIT[29:29]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [158].HIT[30:30]	1
0xBB004C38	VLAN_EVC_ENTRY_INDICATION [159].HIT[31:31]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [160].HIT[0:0]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [161].HIT[1:1]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [162].HIT[2:2]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [163].HIT[3:3]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [164].HIT[4:4]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [165].HIT[5:5]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [166].HIT[6:6]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [167].HIT[7:7]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [168].HIT[8:8]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [169].HIT[9:9]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [170].HIT[10:10]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [171].HIT[11:11]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [172].HIT[12:12]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [173].HIT[13:13]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [174].HIT[14:14]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [175].HIT[15:15]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [176].HIT[16:16]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [177].HIT[17:17]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [178].HIT[18:18]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [179].HIT[19:19]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [180].HIT[20:20]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [181].HIT[21:21]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [182].HIT[22:22]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [183].HIT[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [184].HIT[24:24]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [185].HIT[25:25]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [186].HIT[26:26]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [187].HIT[27:27]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [188].HIT[28:28]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [189].HIT[29:29]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [190].HIT[30:30]	1
0xBB004C3C	VLAN_EVC_ENTRY_INDICATION [191].HIT[31:31]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [192].HIT[0:0]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [193].HIT[1:1]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [194].HIT[2:2]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [195].HIT[3:3]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [196].HIT[4:4]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [197].HIT[5:5]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [198].HIT[6:6]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [199].HIT[7:7]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [200].HIT[8:8]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [201].HIT[9:9]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [202].HIT[10:10]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [203].HIT[11:11]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [204].HIT[12:12]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [205].HIT[13:13]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [206].HIT[14:14]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [207].HIT[15:15]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [208].HIT[16:16]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [209].HIT[17:17]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [210].HIT[18:18]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [211].HIT[19:19]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [212].HIT[20:20]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [213].HIT[21:21]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [214].HIT[22:22]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [215].HIT[23:23]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [216].HIT[24:24]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [217].HIT[25:25]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [218].HIT[26:26]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [219].HIT[27:27]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [220].HIT[28:28]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [221].HIT[29:29]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [222].HIT[30:30]	1
0xBB004C40	VLAN_EVC_ENTRY_INDICATION [223].HIT[31:31]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [224].HIT[0:0]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [225].HIT[1:1]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [226].HIT[2:2]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [227].HIT[3:3]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [228].HIT[4:4]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [229].HIT[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [230].HIT[6:6]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [231].HIT[7:7]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [232].HIT[8:8]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [233].HIT[9:9]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [234].HIT[10:10]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [235].HIT[11:11]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [236].HIT[12:12]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [237].HIT[13:13]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [238].HIT[14:14]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [239].HIT[15:15]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [240].HIT[16:16]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [241].HIT[17:17]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [242].HIT[18:18]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [243].HIT[19:19]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [244].HIT[20:20]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [245].HIT[21:21]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [246].HIT[22:22]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [247].HIT[23:23]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [248].HIT[24:24]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [249].HIT[25:25]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [250].HIT[26:26]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [251].HIT[27:27]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [252].HIT[28:28]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [253].HIT[29:29]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [254].HIT[30:30]	1
0xBB004C44	VLAN_EVC_ENTRY_INDICATION [255].HIT[31:31]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [256].HIT[0:0]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [257].HIT[1:1]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [258].HIT[2:2]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [259].HIT[3:3]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [260].HIT[4:4]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [261].HIT[5:5]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [262].HIT[6:6]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [263].HIT[7:7]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [264].HIT[8:8]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [265].HIT[9:9]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [266].HIT[10:10]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [267].HIT[11:11]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [268].HIT[12:12]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [269].HIT[13:13]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [270].HIT[14:14]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [271].HIT[15:15]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [272].HIT[16:16]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [273].HIT[17:17]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [274].HIT[18:18]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [275].HIT[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [276].HIT[20:20]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [277].HIT[21:21]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [278].HIT[22:22]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [279].HIT[23:23]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [280].HIT[24:24]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [281].HIT[25:25]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [282].HIT[26:26]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [283].HIT[27:27]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [284].HIT[28:28]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [285].HIT[29:29]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [286].HIT[30:30]	1
0xBB004C48	VLAN_EVC_ENTRY_INDICATION [287].HIT[31:31]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [288].HIT[0:0]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [289].HIT[1:1]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [290].HIT[2:2]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [291].HIT[3:3]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [292].HIT[4:4]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [293].HIT[5:5]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [294].HIT[6:6]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [295].HIT[7:7]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [296].HIT[8:8]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [297].HIT[9:9]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [298].HIT[10:10]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [299].HIT[11:11]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [300].HIT[12:12]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [301].HIT[13:13]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [302].HIT[14:14]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [303].HIT[15:15]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [304].HIT[16:16]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [305].HIT[17:17]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [306].HIT[18:18]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [307].HIT[19:19]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [308].HIT[20:20]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [309].HIT[21:21]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [310].HIT[22:22]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [311].HIT[23:23]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [312].HIT[24:24]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [313].HIT[25:25]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [314].HIT[26:26]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [315].HIT[27:27]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [316].HIT[28:28]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [317].HIT[29:29]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [318].HIT[30:30]	1
0xBB004C4C	VLAN_EVC_ENTRY_INDICATION [319].HIT[31:31]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [320].HIT[0:0]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [321].HIT[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [322].HIT[2:2]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [323].HIT[3:3]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [324].HIT[4:4]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [325].HIT[5:5]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [326].HIT[6:6]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [327].HIT[7:7]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [328].HIT[8:8]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [329].HIT[9:9]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [330].HIT[10:10]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [331].HIT[11:11]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [332].HIT[12:12]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [333].HIT[13:13]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [334].HIT[14:14]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [335].HIT[15:15]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [336].HIT[16:16]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [337].HIT[17:17]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [338].HIT[18:18]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [339].HIT[19:19]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [340].HIT[20:20]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [341].HIT[21:21]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [342].HIT[22:22]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [343].HIT[23:23]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [344].HIT[24:24]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [345].HIT[25:25]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [346].HIT[26:26]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [347].HIT[27:27]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [348].HIT[28:28]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [349].HIT[29:29]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [350].HIT[30:30]	1
0xBB004C50	VLAN_EVC_ENTRY_INDICATION [351].HIT[31:31]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [352].HIT[0:0]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [353].HIT[1:1]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [354].HIT[2:2]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [355].HIT[3:3]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [356].HIT[4:4]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [357].HIT[5:5]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [358].HIT[6:6]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [359].HIT[7:7]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [360].HIT[8:8]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [361].HIT[9:9]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [362].HIT[10:10]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [363].HIT[11:11]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [364].HIT[12:12]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [365].HIT[13:13]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [366].HIT[14:14]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [367].HIT[15:15]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [368].HIT[16:16]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [369].HIT[17:17]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [370].HIT[18:18]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [371].HIT[19:19]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [372].HIT[20:20]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [373].HIT[21:21]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [374].HIT[22:22]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [375].HIT[23:23]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [376].HIT[24:24]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [377].HIT[25:25]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [378].HIT[26:26]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [379].HIT[27:27]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [380].HIT[28:28]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [381].HIT[29:29]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [382].HIT[30:30]	1
0xBB004C54	VLAN_EVC_ENTRY_INDICATION [383].HIT[31:31]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [384].HIT[0:0]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [385].HIT[1:1]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [386].HIT[2:2]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [387].HIT[3:3]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [388].HIT[4:4]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [389].HIT[5:5]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [390].HIT[6:6]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [391].HIT[7:7]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [392].HIT[8:8]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [393].HIT[9:9]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [394].HIT[10:10]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [395].HIT[11:11]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [396].HIT[12:12]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [397].HIT[13:13]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [398].HIT[14:14]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [399].HIT[15:15]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [400].HIT[16:16]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [401].HIT[17:17]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [402].HIT[18:18]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [403].HIT[19:19]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [404].HIT[20:20]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [405].HIT[21:21]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [406].HIT[22:22]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [407].HIT[23:23]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [408].HIT[24:24]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [409].HIT[25:25]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [410].HIT[26:26]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [411].HIT[27:27]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [412].HIT[28:28]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [413].HIT[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [414].HIT[30:30]	1
0xBB004C58	VLAN_EVC_ENTRY_INDICATION [415].HIT[31:31]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [416].HIT[0:0]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [417].HIT[1:1]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [418].HIT[2:2]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [419].HIT[3:3]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [420].HIT[4:4]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [421].HIT[5:5]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [422].HIT[6:6]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [423].HIT[7:7]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [424].HIT[8:8]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [425].HIT[9:9]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [426].HIT[10:10]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [427].HIT[11:11]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [428].HIT[12:12]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [429].HIT[13:13]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [430].HIT[14:14]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [431].HIT[15:15]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [432].HIT[16:16]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [433].HIT[17:17]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [434].HIT[18:18]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [435].HIT[19:19]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [436].HIT[20:20]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [437].HIT[21:21]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [438].HIT[22:22]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [439].HIT[23:23]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [440].HIT[24:24]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [441].HIT[25:25]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [442].HIT[26:26]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [443].HIT[27:27]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [444].HIT[28:28]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [445].HIT[29:29]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [446].HIT[30:30]	1
0xBB004C5C	VLAN_EVC_ENTRY_INDICATION [447].HIT[31:31]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [448].HIT[0:0]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [449].HIT[1:1]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [450].HIT[2:2]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [451].HIT[3:3]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [452].HIT[4:4]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [453].HIT[5:5]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [454].HIT[6:6]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [455].HIT[7:7]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [456].HIT[8:8]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [457].HIT[9:9]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [458].HIT[10:10]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [459].HIT[11:11]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [460].HIT[12:12]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [461].HIT[13:13]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [462].HIT[14:14]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [463].HIT[15:15]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [464].HIT[16:16]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [465].HIT[17:17]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [466].HIT[18:18]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [467].HIT[19:19]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [468].HIT[20:20]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [469].HIT[21:21]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [470].HIT[22:22]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [471].HIT[23:23]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [472].HIT[24:24]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [473].HIT[25:25]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [474].HIT[26:26]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [475].HIT[27:27]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [476].HIT[28:28]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [477].HIT[29:29]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [478].HIT[30:30]	1
0xBB004C60	VLAN_EVC_ENTRY_INDICATION [479].HIT[31:31]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [480].HIT[0:0]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [481].HIT[1:1]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [482].HIT[2:2]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [483].HIT[3:3]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [484].HIT[4:4]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [485].HIT[5:5]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [486].HIT[6:6]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [487].HIT[7:7]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [488].HIT[8:8]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [489].HIT[9:9]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [490].HIT[10:10]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [491].HIT[11:11]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [492].HIT[12:12]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [493].HIT[13:13]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [494].HIT[14:14]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [495].HIT[15:15]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [496].HIT[16:16]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [497].HIT[17:17]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [498].HIT[18:18]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [499].HIT[19:19]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [500].HIT[20:20]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [501].HIT[21:21]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [502].HIT[22:22]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [503].HIT[23:23]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [504].HIT[24:24]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [505].HIT[25:25]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [506].HIT[26:26]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [507].HIT[27:27]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [508].HIT[28:28]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [509].HIT[29:29]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [510].HIT[30:30]	1
0xBB004C64	VLAN_EVC_ENTRY_INDICATION [511].HIT[31:31]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [512].HIT[0:0]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [513].HIT[1:1]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [514].HIT[2:2]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [515].HIT[3:3]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [516].HIT[4:4]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [517].HIT[5:5]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [518].HIT[6:6]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [519].HIT[7:7]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [520].HIT[8:8]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [521].HIT[9:9]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [522].HIT[10:10]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [523].HIT[11:11]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [524].HIT[12:12]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [525].HIT[13:13]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [526].HIT[14:14]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [527].HIT[15:15]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [528].HIT[16:16]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [529].HIT[17:17]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [530].HIT[18:18]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [531].HIT[19:19]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [532].HIT[20:20]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [533].HIT[21:21]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [534].HIT[22:22]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [535].HIT[23:23]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [536].HIT[24:24]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [537].HIT[25:25]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [538].HIT[26:26]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [539].HIT[27:27]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [540].HIT[28:28]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [541].HIT[29:29]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [542].HIT[30:30]	1
0xBB004C68	VLAN_EVC_ENTRY_INDICATION [543].HIT[31:31]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [544].HIT[0:0]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [545].HIT[1:1]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [546].HIT[2:2]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [547].HIT[3:3]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [548].HIT[4:4]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [549].HIT[5:5]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [550].HIT[6:6]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [551].HIT[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [552].HIT[8:8]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [553].HIT[9:9]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [554].HIT[10:10]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [555].HIT[11:11]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [556].HIT[12:12]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [557].HIT[13:13]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [558].HIT[14:14]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [559].HIT[15:15]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [560].HIT[16:16]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [561].HIT[17:17]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [562].HIT[18:18]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [563].HIT[19:19]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [564].HIT[20:20]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [565].HIT[21:21]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [566].HIT[22:22]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [567].HIT[23:23]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [568].HIT[24:24]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [569].HIT[25:25]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [570].HIT[26:26]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [571].HIT[27:27]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [572].HIT[28:28]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [573].HIT[29:29]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [574].HIT[30:30]	1
0xBB004C6C	VLAN_EVC_ENTRY_INDICATION [575].HIT[31:31]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [576].HIT[0:0]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [577].HIT[1:1]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [578].HIT[2:2]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [579].HIT[3:3]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [580].HIT[4:4]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [581].HIT[5:5]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [582].HIT[6:6]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [583].HIT[7:7]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [584].HIT[8:8]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [585].HIT[9:9]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [586].HIT[10:10]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [587].HIT[11:11]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [588].HIT[12:12]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [589].HIT[13:13]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [590].HIT[14:14]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [591].HIT[15:15]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [592].HIT[16:16]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [593].HIT[17:17]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [594].HIT[18:18]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [595].HIT[19:19]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [596].HIT[20:20]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [597].HIT[21:21]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [598].HIT[22:22]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [599].HIT[23:23]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [600].HIT[24:24]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [601].HIT[25:25]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [602].HIT[26:26]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [603].HIT[27:27]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [604].HIT[28:28]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [605].HIT[29:29]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [606].HIT[30:30]	1
0xBB004C70	VLAN_EVC_ENTRY_INDICATION [607].HIT[31:31]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [608].HIT[0:0]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [609].HIT[1:1]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [610].HIT[2:2]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [611].HIT[3:3]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [612].HIT[4:4]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [613].HIT[5:5]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [614].HIT[6:6]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [615].HIT[7:7]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [616].HIT[8:8]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [617].HIT[9:9]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [618].HIT[10:10]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [619].HIT[11:11]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [620].HIT[12:12]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [621].HIT[13:13]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [622].HIT[14:14]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [623].HIT[15:15]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [624].HIT[16:16]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [625].HIT[17:17]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [626].HIT[18:18]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [627].HIT[19:19]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [628].HIT[20:20]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [629].HIT[21:21]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [630].HIT[22:22]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [631].HIT[23:23]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [632].HIT[24:24]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [633].HIT[25:25]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [634].HIT[26:26]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [635].HIT[27:27]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [636].HIT[28:28]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [637].HIT[29:29]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [638].HIT[30:30]	1
0xBB004C74	VLAN_EVC_ENTRY_INDICATION [639].HIT[31:31]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [640].HIT[0:0]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [641].HIT[1:1]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [642].HIT[2:2]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [643].HIT[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [644].HIT[4:4]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [645].HIT[5:5]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [646].HIT[6:6]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [647].HIT[7:7]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [648].HIT[8:8]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [649].HIT[9:9]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [650].HIT[10:10]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [651].HIT[11:11]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [652].HIT[12:12]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [653].HIT[13:13]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [654].HIT[14:14]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [655].HIT[15:15]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [656].HIT[16:16]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [657].HIT[17:17]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [658].HIT[18:18]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [659].HIT[19:19]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [660].HIT[20:20]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [661].HIT[21:21]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [662].HIT[22:22]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [663].HIT[23:23]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [664].HIT[24:24]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [665].HIT[25:25]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [666].HIT[26:26]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [667].HIT[27:27]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [668].HIT[28:28]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [669].HIT[29:29]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [670].HIT[30:30]	1
0xBB004C78	VLAN_EVC_ENTRY_INDICATION [671].HIT[31:31]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [672].HIT[0:0]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [673].HIT[1:1]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [674].HIT[2:2]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [675].HIT[3:3]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [676].HIT[4:4]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [677].HIT[5:5]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [678].HIT[6:6]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [679].HIT[7:7]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [680].HIT[8:8]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [681].HIT[9:9]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [682].HIT[10:10]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [683].HIT[11:11]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [684].HIT[12:12]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [685].HIT[13:13]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [686].HIT[14:14]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [687].HIT[15:15]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [688].HIT[16:16]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [689].HIT[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [690].HIT[18:18]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [691].HIT[19:19]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [692].HIT[20:20]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [693].HIT[21:21]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [694].HIT[22:22]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [695].HIT[23:23]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [696].HIT[24:24]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [697].HIT[25:25]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [698].HIT[26:26]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [699].HIT[27:27]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [700].HIT[28:28]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [701].HIT[29:29]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [702].HIT[30:30]	1
0xBB004C7C	VLAN_EVC_ENTRY_INDICATION [703].HIT[31:31]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [704].HIT[0:0]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [705].HIT[1:1]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [706].HIT[2:2]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [707].HIT[3:3]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [708].HIT[4:4]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [709].HIT[5:5]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [710].HIT[6:6]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [711].HIT[7:7]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [712].HIT[8:8]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [713].HIT[9:9]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [714].HIT[10:10]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [715].HIT[11:11]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [716].HIT[12:12]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [717].HIT[13:13]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [718].HIT[14:14]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [719].HIT[15:15]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [720].HIT[16:16]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [721].HIT[17:17]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [722].HIT[18:18]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [723].HIT[19:19]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [724].HIT[20:20]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [725].HIT[21:21]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [726].HIT[22:22]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [727].HIT[23:23]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [728].HIT[24:24]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [729].HIT[25:25]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [730].HIT[26:26]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [731].HIT[27:27]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [732].HIT[28:28]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [733].HIT[29:29]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [734].HIT[30:30]	1
0xBB004C80	VLAN_EVC_ENTRY_INDICATION [735].HIT[31:31]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [736].HIT[0:0]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [737].HIT[1:1]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [738].HIT[2:2]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [739].HIT[3:3]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [740].HIT[4:4]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [741].HIT[5:5]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [742].HIT[6:6]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [743].HIT[7:7]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [744].HIT[8:8]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [745].HIT[9:9]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [746].HIT[10:10]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [747].HIT[11:11]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [748].HIT[12:12]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [749].HIT[13:13]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [750].HIT[14:14]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [751].HIT[15:15]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [752].HIT[16:16]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [753].HIT[17:17]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [754].HIT[18:18]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [755].HIT[19:19]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [756].HIT[20:20]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [757].HIT[21:21]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [758].HIT[22:22]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [759].HIT[23:23]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [760].HIT[24:24]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [761].HIT[25:25]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [762].HIT[26:26]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [763].HIT[27:27]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [764].HIT[28:28]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [765].HIT[29:29]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [766].HIT[30:30]	1
0xBB004C84	VLAN_EVC_ENTRY_INDICATION [767].HIT[31:31]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [768].HIT[0:0]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [769].HIT[1:1]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [770].HIT[2:2]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [771].HIT[3:3]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [772].HIT[4:4]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [773].HIT[5:5]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [774].HIT[6:6]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [775].HIT[7:7]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [776].HIT[8:8]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [777].HIT[9:9]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [778].HIT[10:10]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [779].HIT[11:11]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [780].HIT[12:12]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [781].HIT[13:13]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [782].HIT[14:14]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [783].HIT[15:15]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [784].HIT[16:16]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [785].HIT[17:17]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [786].HIT[18:18]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [787].HIT[19:19]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [788].HIT[20:20]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [789].HIT[21:21]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [790].HIT[22:22]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [791].HIT[23:23]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [792].HIT[24:24]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [793].HIT[25:25]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [794].HIT[26:26]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [795].HIT[27:27]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [796].HIT[28:28]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [797].HIT[29:29]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [798].HIT[30:30]	1
0xBB004C88	VLAN_EVC_ENTRY_INDICATION [799].HIT[31:31]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [800].HIT[0:0]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [801].HIT[1:1]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [802].HIT[2:2]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [803].HIT[3:3]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [804].HIT[4:4]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [805].HIT[5:5]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [806].HIT[6:6]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [807].HIT[7:7]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [808].HIT[8:8]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [809].HIT[9:9]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [810].HIT[10:10]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [811].HIT[11:11]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [812].HIT[12:12]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [813].HIT[13:13]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [814].HIT[14:14]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [815].HIT[15:15]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [816].HIT[16:16]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [817].HIT[17:17]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [818].HIT[18:18]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [819].HIT[19:19]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [820].HIT[20:20]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [821].HIT[21:21]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [822].HIT[22:22]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [823].HIT[23:23]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [824].HIT[24:24]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [825].HIT[25:25]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [826].HIT[26:26]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [827].HIT[27:27]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [828].HIT[28:28]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [829].HIT[29:29]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [830].HIT[30:30]	1
0xBB004C8C	VLAN_EVC_ENTRY_INDICATION [831].HIT[31:31]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [832].HIT[0:0]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [833].HIT[1:1]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [834].HIT[2:2]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [835].HIT[3:3]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [836].HIT[4:4]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [837].HIT[5:5]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [838].HIT[6:6]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [839].HIT[7:7]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [840].HIT[8:8]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [841].HIT[9:9]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [842].HIT[10:10]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [843].HIT[11:11]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [844].HIT[12:12]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [845].HIT[13:13]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [846].HIT[14:14]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [847].HIT[15:15]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [848].HIT[16:16]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [849].HIT[17:17]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [850].HIT[18:18]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [851].HIT[19:19]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [852].HIT[20:20]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [853].HIT[21:21]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [854].HIT[22:22]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [855].HIT[23:23]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [856].HIT[24:24]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [857].HIT[25:25]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [858].HIT[26:26]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [859].HIT[27:27]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [860].HIT[28:28]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [861].HIT[29:29]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [862].HIT[30:30]	1
0xBB004C90	VLAN_EVC_ENTRY_INDICATION [863].HIT[31:31]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [864].HIT[0:0]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [865].HIT[1:1]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [866].HIT[2:2]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [867].HIT[3:3]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [868].HIT[4:4]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [869].HIT[5:5]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [870].HIT[6:6]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [871].HIT[7:7]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [872].HIT[8:8]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [873].HIT[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [874].HIT[10:10]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [875].HIT[11:11]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [876].HIT[12:12]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [877].HIT[13:13]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [878].HIT[14:14]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [879].HIT[15:15]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [880].HIT[16:16]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [881].HIT[17:17]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [882].HIT[18:18]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [883].HIT[19:19]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [884].HIT[20:20]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [885].HIT[21:21]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [886].HIT[22:22]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [887].HIT[23:23]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [888].HIT[24:24]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [889].HIT[25:25]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [890].HIT[26:26]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [891].HIT[27:27]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [892].HIT[28:28]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [893].HIT[29:29]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [894].HIT[30:30]	1
0xBB004C94	VLAN_EVC_ENTRY_INDICATION [895].HIT[31:31]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [896].HIT[0:0]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [897].HIT[1:1]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [898].HIT[2:2]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [899].HIT[3:3]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [900].HIT[4:4]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [901].HIT[5:5]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [902].HIT[6:6]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [903].HIT[7:7]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [904].HIT[8:8]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [905].HIT[9:9]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [906].HIT[10:10]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [907].HIT[11:11]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [908].HIT[12:12]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [909].HIT[13:13]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [910].HIT[14:14]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [911].HIT[15:15]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [912].HIT[16:16]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [913].HIT[17:17]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [914].HIT[18:18]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [915].HIT[19:19]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [916].HIT[20:20]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [917].HIT[21:21]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [918].HIT[22:22]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [919].HIT[23:23]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [920].HIT[24:24]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [921].HIT[25:25]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [922].HIT[26:26]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [923].HIT[27:27]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [924].HIT[28:28]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [925].HIT[29:29]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [926].HIT[30:30]	1
0xBB004C98	VLAN_EVC_ENTRY_INDICATION [927].HIT[31:31]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [928].HIT[0:0]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [929].HIT[1:1]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [930].HIT[2:2]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [931].HIT[3:3]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [932].HIT[4:4]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [933].HIT[5:5]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [934].HIT[6:6]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [935].HIT[7:7]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [936].HIT[8:8]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [937].HIT[9:9]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [938].HIT[10:10]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [939].HIT[11:11]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [940].HIT[12:12]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [941].HIT[13:13]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [942].HIT[14:14]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [943].HIT[15:15]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [944].HIT[16:16]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [945].HIT[17:17]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [946].HIT[18:18]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [947].HIT[19:19]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [948].HIT[20:20]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [949].HIT[21:21]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [950].HIT[22:22]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [951].HIT[23:23]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [952].HIT[24:24]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [953].HIT[25:25]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [954].HIT[26:26]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [955].HIT[27:27]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [956].HIT[28:28]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [957].HIT[29:29]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [958].HIT[30:30]	1
0xBB004C9C	VLAN_EVC_ENTRY_INDICATION [959].HIT[31:31]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [960].HIT[0:0]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [961].HIT[1:1]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [962].HIT[2:2]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [963].HIT[3:3]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [964].HIT[4:4]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [965].HIT[5:5]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [966].HIT[6:6]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [967].HIT[7:7]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [968].HIT[8:8]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [969].HIT[9:9]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [970].HIT[10:10]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [971].HIT[11:11]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [972].HIT[12:12]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [973].HIT[13:13]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [974].HIT[14:14]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [975].HIT[15:15]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [976].HIT[16:16]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [977].HIT[17:17]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [978].HIT[18:18]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [979].HIT[19:19]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [980].HIT[20:20]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [981].HIT[21:21]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [982].HIT[22:22]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [983].HIT[23:23]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [984].HIT[24:24]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [985].HIT[25:25]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [986].HIT[26:26]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [987].HIT[27:27]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [988].HIT[28:28]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [989].HIT[29:29]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [990].HIT[30:30]	1
0xBB004CA0	VLAN_EVC_ENTRY_INDICATION [991].HIT[31:31]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [992].HIT[0:0]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [993].HIT[1:1]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [994].HIT[2:2]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [995].HIT[3:3]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [996].HIT[4:4]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [997].HIT[5:5]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [998].HIT[6:6]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [999].HIT[7:7]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1000].HIT[8:8]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1001].HIT[9:9]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1002].HIT[10:10]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1003].HIT[11:11]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1004].HIT[12:12]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1005].HIT[13:13]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1006].HIT[14:14]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1007].HIT[15:15]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1008].HIT[16:16]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1009].HIT[17:17]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1010].HIT[18:18]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1011].HIT[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1012].HIT[20:20]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1013].HIT[21:21]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1014].HIT[22:22]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1015].HIT[23:23]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1016].HIT[24:24]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1017].HIT[25:25]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1018].HIT[26:26]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1019].HIT[27:27]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1020].HIT[28:28]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1021].HIT[29:29]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1022].HIT[30:30]	1
0xBB004CA4	VLAN_EVC_ENTRY_INDICATION [1023].HIT[31:31]	1
0xBB004CA8	VLAN_EVC_ENTRY_INDICATION_AGGR.HIT_MSK[31:0]	32
0xBB009C00	VLAN_IGR_VID_RNG_CHK_SET_0 [0].RESERVED[31:25]	7
0xBB009C00	VLAN_IGR_VID_RNG_CHK_SET_0 [0].TYPE[24:24]	1
0xBB009C00	VLAN_IGR_VID_RNG_CHK_SET_0 [0].UPPER[23:12]	12
0xBB009C00	VLAN_IGR_VID_RNG_CHK_SET_0 [0].LOWER[11:0]	12
0xBB009C04	VLAN_IGR_VID_RNG_CHK_SET_0 [1].RESERVED[31:25]	7
0xBB009C04	VLAN_IGR_VID_RNG_CHK_SET_0 [1].TYPE[24:24]	1
0xBB009C04	VLAN_IGR_VID_RNG_CHK_SET_0 [1].UPPER[23:12]	12
0xBB009C04	VLAN_IGR_VID_RNG_CHK_SET_0 [1].LOWER[11:0]	12
0xBB009C08	VLAN_IGR_VID_RNG_CHK_SET_0 [2].RESERVED[31:25]	7
0xBB009C08	VLAN_IGR_VID_RNG_CHK_SET_0 [2].TYPE[24:24]	1
0xBB009C08	VLAN_IGR_VID_RNG_CHK_SET_0 [2].UPPER[23:12]	12
0xBB009C08	VLAN_IGR_VID_RNG_CHK_SET_0 [2].LOWER[11:0]	12
0xBB009C0C	VLAN_IGR_VID_RNG_CHK_SET_0 [3].RESERVED[31:25]	7
0xBB009C0C	VLAN_IGR_VID_RNG_CHK_SET_0 [3].TYPE[24:24]	1
0xBB009C0C	VLAN_IGR_VID_RNG_CHK_SET_0 [3].UPPER[23:12]	12
0xBB009C0C	VLAN_IGR_VID_RNG_CHK_SET_0 [3].LOWER[11:0]	12
0xBB009C10	VLAN_IGR_VID_RNG_CHK_SET_0 [4].RESERVED[31:25]	7
0xBB009C10	VLAN_IGR_VID_RNG_CHK_SET_0 [4].TYPE[24:24]	1
0xBB009C10	VLAN_IGR_VID_RNG_CHK_SET_0 [4].UPPER[23:12]	12
0xBB009C10	VLAN_IGR_VID_RNG_CHK_SET_0 [4].LOWER[11:0]	12
0xBB009C14	VLAN_IGR_VID_RNG_CHK_SET_0 [5].RESERVED[31:25]	7
0xBB009C14	VLAN_IGR_VID_RNG_CHK_SET_0 [5].TYPE[24:24]	1
0xBB009C14	VLAN_IGR_VID_RNG_CHK_SET_0 [5].UPPER[23:12]	12
0xBB009C14	VLAN_IGR_VID_RNG_CHK_SET_0 [5].LOWER[11:0]	12
0xBB009C18	VLAN_IGR_VID_RNG_CHK_SET_0 [6].RESERVED[31:25]	7
0xBB009C18	VLAN_IGR_VID_RNG_CHK_SET_0 [6].TYPE[24:24]	1
0xBB009C18	VLAN_IGR_VID_RNG_CHK_SET_0 [6].UPPER[23:12]	12
0xBB009C18	VLAN_IGR_VID_RNG_CHK_SET_0 [6].LOWER[11:0]	12
0xBB009C1C	VLAN_IGR_VID_RNG_CHK_SET_0 [7].RESERVED[31:25]	7
0xBB009C1C	VLAN_IGR_VID_RNG_CHK_SET_0 [7].TYPE[24:24]	1
0xBB009C1C	VLAN_IGR_VID_RNG_CHK_SET_0 [7].UPPER[23:12]	12
0xBB009C1C	VLAN_IGR_VID_RNG_CHK_SET_0 [7].LOWER[11:0]	12
0xBB009C20	VLAN_IGR_VID_RNG_CHK_SET_0 [8].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009C20	VLAN_IGR_VID_RNG_CHK_SET_0 [8].TYPE[24:24]	1
0xBB009C20	VLAN_IGR_VID_RNG_CHK_SET_0 [8].UPPER[23:12]	12
0xBB009C20	VLAN_IGR_VID_RNG_CHK_SET_0 [8].LOWER[11:0]	12
0xBB009C24	VLAN_IGR_VID_RNG_CHK_SET_0 [9].RESERVED[31:25]	7
0xBB009C24	VLAN_IGR_VID_RNG_CHK_SET_0 [9].TYPE[24:24]	1
0xBB009C24	VLAN_IGR_VID_RNG_CHK_SET_0 [9].UPPER[23:12]	12
0xBB009C24	VLAN_IGR_VID_RNG_CHK_SET_0 [9].LOWER[11:0]	12
0xBB009C28	VLAN_IGR_VID_RNG_CHK_SET_0 [10].RESERVED[31:25]	7
0xBB009C28	VLAN_IGR_VID_RNG_CHK_SET_0 [10].TYPE[24:24]	1
0xBB009C28	VLAN_IGR_VID_RNG_CHK_SET_0 [10].UPPER[23:12]	12
0xBB009C28	VLAN_IGR_VID_RNG_CHK_SET_0 [10].LOWER[11:0]	12
0xBB009C2C	VLAN_IGR_VID_RNG_CHK_SET_0 [11].RESERVED[31:25]	7
0xBB009C2C	VLAN_IGR_VID_RNG_CHK_SET_0 [11].TYPE[24:24]	1
0xBB009C2C	VLAN_IGR_VID_RNG_CHK_SET_0 [11].UPPER[23:12]	12
0xBB009C2C	VLAN_IGR_VID_RNG_CHK_SET_0 [11].LOWER[11:0]	12
0xBB009C30	VLAN_IGR_VID_RNG_CHK_SET_0 [12].RESERVED[31:25]	7
0xBB009C30	VLAN_IGR_VID_RNG_CHK_SET_0 [12].TYPE[24:24]	1
0xBB009C30	VLAN_IGR_VID_RNG_CHK_SET_0 [12].UPPER[23:12]	12
0xBB009C30	VLAN_IGR_VID_RNG_CHK_SET_0 [12].LOWER[11:0]	12
0xBB009C34	VLAN_IGR_VID_RNG_CHK_SET_0 [13].RESERVED[31:25]	7
0xBB009C34	VLAN_IGR_VID_RNG_CHK_SET_0 [13].TYPE[24:24]	1
0xBB009C34	VLAN_IGR_VID_RNG_CHK_SET_0 [13].UPPER[23:12]	12
0xBB009C34	VLAN_IGR_VID_RNG_CHK_SET_0 [13].LOWER[11:0]	12
0xBB009C38	VLAN_IGR_VID_RNG_CHK_SET_0 [14].RESERVED[31:25]	7
0xBB009C38	VLAN_IGR_VID_RNG_CHK_SET_0 [14].TYPE[24:24]	1
0xBB009C38	VLAN_IGR_VID_RNG_CHK_SET_0 [14].UPPER[23:12]	12
0xBB009C38	VLAN_IGR_VID_RNG_CHK_SET_0 [14].LOWER[11:0]	12
0xBB009C3C	VLAN_IGR_VID_RNG_CHK_SET_0 [15].RESERVED[31:25]	7
0xBB009C3C	VLAN_IGR_VID_RNG_CHK_SET_0 [15].TYPE[24:24]	1
0xBB009C3C	VLAN_IGR_VID_RNG_CHK_SET_0 [15].UPPER[23:12]	12
0xBB009C3C	VLAN_IGR_VID_RNG_CHK_SET_0 [15].LOWER[11:0]	12
0xBB009C40	VLAN_IGR_VID_RNG_CHK_SET_0 [16].RESERVED[31:25]	7
0xBB009C40	VLAN_IGR_VID_RNG_CHK_SET_0 [16].TYPE[24:24]	1
0xBB009C40	VLAN_IGR_VID_RNG_CHK_SET_0 [16].UPPER[23:12]	12
0xBB009C40	VLAN_IGR_VID_RNG_CHK_SET_0 [16].LOWER[11:0]	12
0xBB009C44	VLAN_IGR_VID_RNG_CHK_SET_0 [17].RESERVED[31:25]	7
0xBB009C44	VLAN_IGR_VID_RNG_CHK_SET_0 [17].TYPE[24:24]	1
0xBB009C44	VLAN_IGR_VID_RNG_CHK_SET_0 [17].UPPER[23:12]	12
0xBB009C44	VLAN_IGR_VID_RNG_CHK_SET_0 [17].LOWER[11:0]	12
0xBB009C48	VLAN_IGR_VID_RNG_CHK_SET_0 [18].RESERVED[31:25]	7
0xBB009C48	VLAN_IGR_VID_RNG_CHK_SET_0 [18].TYPE[24:24]	1
0xBB009C48	VLAN_IGR_VID_RNG_CHK_SET_0 [18].UPPER[23:12]	12
0xBB009C48	VLAN_IGR_VID_RNG_CHK_SET_0 [18].LOWER[11:0]	12
0xBB009C4C	VLAN_IGR_VID_RNG_CHK_SET_0 [19].RESERVED[31:25]	7
0xBB009C4C	VLAN_IGR_VID_RNG_CHK_SET_0 [19].TYPE[24:24]	1
0xBB009C4C	VLAN_IGR_VID_RNG_CHK_SET_0 [19].UPPER[23:12]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009C4C	VLAN_IGR_VID_RNG_CHK_SET_0 [19].LOWER[11:0]	12
0xBB009C50	VLAN_IGR_VID_RNG_CHK_SET_0 [20].RESERVED[31:25]	7
0xBB009C50	VLAN_IGR_VID_RNG_CHK_SET_0 [20].TYPE[24:24]	1
0xBB009C50	VLAN_IGR_VID_RNG_CHK_SET_0 [20].UPPER[23:12]	12
0xBB009C50	VLAN_IGR_VID_RNG_CHK_SET_0 [20].LOWER[11:0]	12
0xBB009C54	VLAN_IGR_VID_RNG_CHK_SET_0 [21].RESERVED[31:25]	7
0xBB009C54	VLAN_IGR_VID_RNG_CHK_SET_0 [21].TYPE[24:24]	1
0xBB009C54	VLAN_IGR_VID_RNG_CHK_SET_0 [21].UPPER[23:12]	12
0xBB009C54	VLAN_IGR_VID_RNG_CHK_SET_0 [21].LOWER[11:0]	12
0xBB009C58	VLAN_IGR_VID_RNG_CHK_SET_0 [22].RESERVED[31:25]	7
0xBB009C58	VLAN_IGR_VID_RNG_CHK_SET_0 [22].TYPE[24:24]	1
0xBB009C58	VLAN_IGR_VID_RNG_CHK_SET_0 [22].UPPER[23:12]	12
0xBB009C58	VLAN_IGR_VID_RNG_CHK_SET_0 [22].LOWER[11:0]	12
0xBB009C5C	VLAN_IGR_VID_RNG_CHK_SET_0 [23].RESERVED[31:25]	7
0xBB009C5C	VLAN_IGR_VID_RNG_CHK_SET_0 [23].TYPE[24:24]	1
0xBB009C5C	VLAN_IGR_VID_RNG_CHK_SET_0 [23].UPPER[23:12]	12
0xBB009C5C	VLAN_IGR_VID_RNG_CHK_SET_0 [23].LOWER[11:0]	12
0xBB009C60	VLAN_IGR_VID_RNG_CHK_SET_0 [24].RESERVED[31:25]	7
0xBB009C60	VLAN_IGR_VID_RNG_CHK_SET_0 [24].TYPE[24:24]	1
0xBB009C60	VLAN_IGR_VID_RNG_CHK_SET_0 [24].UPPER[23:12]	12
0xBB009C60	VLAN_IGR_VID_RNG_CHK_SET_0 [24].LOWER[11:0]	12
0xBB009C64	VLAN_IGR_VID_RNG_CHK_SET_0 [25].RESERVED[31:25]	7
0xBB009C64	VLAN_IGR_VID_RNG_CHK_SET_0 [25].TYPE[24:24]	1
0xBB009C64	VLAN_IGR_VID_RNG_CHK_SET_0 [25].UPPER[23:12]	12
0xBB009C64	VLAN_IGR_VID_RNG_CHK_SET_0 [25].LOWER[11:0]	12
0xBB009C68	VLAN_IGR_VID_RNG_CHK_SET_0 [26].RESERVED[31:25]	7
0xBB009C68	VLAN_IGR_VID_RNG_CHK_SET_0 [26].TYPE[24:24]	1
0xBB009C68	VLAN_IGR_VID_RNG_CHK_SET_0 [26].UPPER[23:12]	12
0xBB009C68	VLAN_IGR_VID_RNG_CHK_SET_0 [26].LOWER[11:0]	12
0xBB009C6C	VLAN_IGR_VID_RNG_CHK_SET_0 [27].RESERVED[31:25]	7
0xBB009C6C	VLAN_IGR_VID_RNG_CHK_SET_0 [27].TYPE[24:24]	1
0xBB009C6C	VLAN_IGR_VID_RNG_CHK_SET_0 [27].UPPER[23:12]	12
0xBB009C6C	VLAN_IGR_VID_RNG_CHK_SET_0 [27].LOWER[11:0]	12
0xBB009C70	VLAN_IGR_VID_RNG_CHK_SET_0 [28].RESERVED[31:25]	7
0xBB009C70	VLAN_IGR_VID_RNG_CHK_SET_0 [28].TYPE[24:24]	1
0xBB009C70	VLAN_IGR_VID_RNG_CHK_SET_0 [28].UPPER[23:12]	12
0xBB009C70	VLAN_IGR_VID_RNG_CHK_SET_0 [28].LOWER[11:0]	12
0xBB009C74	VLAN_IGR_VID_RNG_CHK_SET_0 [29].RESERVED[31:25]	7
0xBB009C74	VLAN_IGR_VID_RNG_CHK_SET_0 [29].TYPE[24:24]	1
0xBB009C74	VLAN_IGR_VID_RNG_CHK_SET_0 [29].UPPER[23:12]	12
0xBB009C74	VLAN_IGR_VID_RNG_CHK_SET_0 [29].LOWER[11:0]	12
0xBB009C78	VLAN_IGR_VID_RNG_CHK_SET_0 [30].RESERVED[31:25]	7
0xBB009C78	VLAN_IGR_VID_RNG_CHK_SET_0 [30].TYPE[24:24]	1
0xBB009C78	VLAN_IGR_VID_RNG_CHK_SET_0 [30].UPPER[23:12]	12
0xBB009C78	VLAN_IGR_VID_RNG_CHK_SET_0 [30].LOWER[11:0]	12
0xBB009C7C	VLAN_IGR_VID_RNG_CHK_SET_0 [31].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009C7C	VLAN_IGR_VID_RNG_CHK_SET_0 [31].TYPE[24:24]	1
0xBB009C7C	VLAN_IGR_VID_RNG_CHK_SET_0 [31].UPPER[23:12]	12
0xBB009C7C	VLAN_IGR_VID_RNG_CHK_SET_0 [31].LOWER[11:0]	12
0xBB009C80	VLAN_IGR_VID_RNG_CHK_SET_1 [0].RESERVED[31:25]	7
0xBB009C80	VLAN_IGR_VID_RNG_CHK_SET_1 [0].TYPE[24:24]	1
0xBB009C80	VLAN_IGR_VID_RNG_CHK_SET_1 [0].UPPER[23:12]	12
0xBB009C80	VLAN_IGR_VID_RNG_CHK_SET_1 [0].LOWER[11:0]	12
0xBB009C84	VLAN_IGR_VID_RNG_CHK_SET_1 [1].RESERVED[31:25]	7
0xBB009C84	VLAN_IGR_VID_RNG_CHK_SET_1 [1].TYPE[24:24]	1
0xBB009C84	VLAN_IGR_VID_RNG_CHK_SET_1 [1].UPPER[23:12]	12
0xBB009C84	VLAN_IGR_VID_RNG_CHK_SET_1 [1].LOWER[11:0]	12
0xBB009C88	VLAN_IGR_VID_RNG_CHK_SET_1 [2].RESERVED[31:25]	7
0xBB009C88	VLAN_IGR_VID_RNG_CHK_SET_1 [2].TYPE[24:24]	1
0xBB009C88	VLAN_IGR_VID_RNG_CHK_SET_1 [2].UPPER[23:12]	12
0xBB009C88	VLAN_IGR_VID_RNG_CHK_SET_1 [2].LOWER[11:0]	12
0xBB009C8C	VLAN_IGR_VID_RNG_CHK_SET_1 [3].RESERVED[31:25]	7
0xBB009C8C	VLAN_IGR_VID_RNG_CHK_SET_1 [3].TYPE[24:24]	1
0xBB009C8C	VLAN_IGR_VID_RNG_CHK_SET_1 [3].UPPER[23:12]	12
0xBB009C8C	VLAN_IGR_VID_RNG_CHK_SET_1 [3].LOWER[11:0]	12
0xBB009C90	VLAN_IGR_VID_RNG_CHK_SET_1 [4].RESERVED[31:25]	7
0xBB009C90	VLAN_IGR_VID_RNG_CHK_SET_1 [4].TYPE[24:24]	1
0xBB009C90	VLAN_IGR_VID_RNG_CHK_SET_1 [4].UPPER[23:12]	12
0xBB009C90	VLAN_IGR_VID_RNG_CHK_SET_1 [4].LOWER[11:0]	12
0xBB009C94	VLAN_IGR_VID_RNG_CHK_SET_1 [5].RESERVED[31:25]	7
0xBB009C94	VLAN_IGR_VID_RNG_CHK_SET_1 [5].TYPE[24:24]	1
0xBB009C94	VLAN_IGR_VID_RNG_CHK_SET_1 [5].UPPER[23:12]	12
0xBB009C94	VLAN_IGR_VID_RNG_CHK_SET_1 [5].LOWER[11:0]	12
0xBB009C98	VLAN_IGR_VID_RNG_CHK_SET_1 [6].RESERVED[31:25]	7
0xBB009C98	VLAN_IGR_VID_RNG_CHK_SET_1 [6].TYPE[24:24]	1
0xBB009C98	VLAN_IGR_VID_RNG_CHK_SET_1 [6].UPPER[23:12]	12
0xBB009C98	VLAN_IGR_VID_RNG_CHK_SET_1 [6].LOWER[11:0]	12
0xBB009C9C	VLAN_IGR_VID_RNG_CHK_SET_1 [7].RESERVED[31:25]	7
0xBB009C9C	VLAN_IGR_VID_RNG_CHK_SET_1 [7].TYPE[24:24]	1
0xBB009C9C	VLAN_IGR_VID_RNG_CHK_SET_1 [7].UPPER[23:12]	12
0xBB009C9C	VLAN_IGR_VID_RNG_CHK_SET_1 [7].LOWER[11:0]	12
0xBB009CA0	VLAN_IGR_VID_RNG_CHK_SET_1 [8].RESERVED[31:25]	7
0xBB009CA0	VLAN_IGR_VID_RNG_CHK_SET_1 [8].TYPE[24:24]	1
0xBB009CA0	VLAN_IGR_VID_RNG_CHK_SET_1 [8].UPPER[23:12]	12
0xBB009CA0	VLAN_IGR_VID_RNG_CHK_SET_1 [8].LOWER[11:0]	12
0xBB009CA4	VLAN_IGR_VID_RNG_CHK_SET_1 [9].RESERVED[31:25]	7
0xBB009CA4	VLAN_IGR_VID_RNG_CHK_SET_1 [9].TYPE[24:24]	1
0xBB009CA4	VLAN_IGR_VID_RNG_CHK_SET_1 [9].UPPER[23:12]	12
0xBB009CA4	VLAN_IGR_VID_RNG_CHK_SET_1 [9].LOWER[11:0]	12
0xBB009CA8	VLAN_IGR_VID_RNG_CHK_SET_1 [10].RESERVED[31:25]	7
0xBB009CA8	VLAN_IGR_VID_RNG_CHK_SET_1 [10].TYPE[24:24]	1
0xBB009CA8	VLAN_IGR_VID_RNG_CHK_SET_1 [10].UPPER[23:12]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009CA8	VLAN_IGR_VID_RNG_CHK_SET_1 [10].LOWER[11:0]	12
0xBB009CAC	VLAN_IGR_VID_RNG_CHK_SET_1 [11].RESERVED[31:25]	7
0xBB009CAC	VLAN_IGR_VID_RNG_CHK_SET_1 [11].TYPE[24:24]	1
0xBB009CAC	VLAN_IGR_VID_RNG_CHK_SET_1 [11].UPPER[23:12]	12
0xBB009CAC	VLAN_IGR_VID_RNG_CHK_SET_1 [11].LOWER[11:0]	12
0xBB009CB0	VLAN_IGR_VID_RNG_CHK_SET_1 [12].RESERVED[31:25]	7
0xBB009CB0	VLAN_IGR_VID_RNG_CHK_SET_1 [12].TYPE[24:24]	1
0xBB009CB0	VLAN_IGR_VID_RNG_CHK_SET_1 [12].UPPER[23:12]	12
0xBB009CB0	VLAN_IGR_VID_RNG_CHK_SET_1 [12].LOWER[11:0]	12
0xBB009CB4	VLAN_IGR_VID_RNG_CHK_SET_1 [13].RESERVED[31:25]	7
0xBB009CB4	VLAN_IGR_VID_RNG_CHK_SET_1 [13].TYPE[24:24]	1
0xBB009CB4	VLAN_IGR_VID_RNG_CHK_SET_1 [13].UPPER[23:12]	12
0xBB009CB4	VLAN_IGR_VID_RNG_CHK_SET_1 [13].LOWER[11:0]	12
0xBB009CB8	VLAN_IGR_VID_RNG_CHK_SET_1 [14].RESERVED[31:25]	7
0xBB009CB8	VLAN_IGR_VID_RNG_CHK_SET_1 [14].TYPE[24:24]	1
0xBB009CB8	VLAN_IGR_VID_RNG_CHK_SET_1 [14].UPPER[23:12]	12
0xBB009CB8	VLAN_IGR_VID_RNG_CHK_SET_1 [14].LOWER[11:0]	12
0xBB009CBC	VLAN_IGR_VID_RNG_CHK_SET_1 [15].RESERVED[31:25]	7
0xBB009CBC	VLAN_IGR_VID_RNG_CHK_SET_1 [15].TYPE[24:24]	1
0xBB009CBC	VLAN_IGR_VID_RNG_CHK_SET_1 [15].UPPER[23:12]	12
0xBB009CBC	VLAN_IGR_VID_RNG_CHK_SET_1 [15].LOWER[11:0]	12
0xBB009CC0	VLAN_IGR_VID_RNG_CHK_SET_1 [16].RESERVED[31:25]	7
0xBB009CC0	VLAN_IGR_VID_RNG_CHK_SET_1 [16].TYPE[24:24]	1
0xBB009CC0	VLAN_IGR_VID_RNG_CHK_SET_1 [16].UPPER[23:12]	12
0xBB009CC0	VLAN_IGR_VID_RNG_CHK_SET_1 [16].LOWER[11:0]	12
0xBB009CC4	VLAN_IGR_VID_RNG_CHK_SET_1 [17].RESERVED[31:25]	7
0xBB009CC4	VLAN_IGR_VID_RNG_CHK_SET_1 [17].TYPE[24:24]	1
0xBB009CC4	VLAN_IGR_VID_RNG_CHK_SET_1 [17].UPPER[23:12]	12
0xBB009CC4	VLAN_IGR_VID_RNG_CHK_SET_1 [17].LOWER[11:0]	12
0xBB009CC8	VLAN_IGR_VID_RNG_CHK_SET_1 [18].RESERVED[31:25]	7
0xBB009CC8	VLAN_IGR_VID_RNG_CHK_SET_1 [18].TYPE[24:24]	1
0xBB009CC8	VLAN_IGR_VID_RNG_CHK_SET_1 [18].UPPER[23:12]	12
0xBB009CC8	VLAN_IGR_VID_RNG_CHK_SET_1 [18].LOWER[11:0]	12
0xBB009CCC	VLAN_IGR_VID_RNG_CHK_SET_1 [19].RESERVED[31:25]	7
0xBB009CCC	VLAN_IGR_VID_RNG_CHK_SET_1 [19].TYPE[24:24]	1
0xBB009CCC	VLAN_IGR_VID_RNG_CHK_SET_1 [19].UPPER[23:12]	12
0xBB009CCC	VLAN_IGR_VID_RNG_CHK_SET_1 [19].LOWER[11:0]	12
0xBB009CD0	VLAN_IGR_VID_RNG_CHK_SET_1 [20].RESERVED[31:25]	7
0xBB009CD0	VLAN_IGR_VID_RNG_CHK_SET_1 [20].TYPE[24:24]	1
0xBB009CD0	VLAN_IGR_VID_RNG_CHK_SET_1 [20].UPPER[23:12]	12
0xBB009CD0	VLAN_IGR_VID_RNG_CHK_SET_1 [20].LOWER[11:0]	12
0xBB009CD4	VLAN_IGR_VID_RNG_CHK_SET_1 [21].RESERVED[31:25]	7
0xBB009CD4	VLAN_IGR_VID_RNG_CHK_SET_1 [21].TYPE[24:24]	1
0xBB009CD4	VLAN_IGR_VID_RNG_CHK_SET_1 [21].UPPER[23:12]	12
0xBB009CD4	VLAN_IGR_VID_RNG_CHK_SET_1 [21].LOWER[11:0]	12
0xBB009CD8	VLAN_IGR_VID_RNG_CHK_SET_1 [22].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009CD8	VLAN_IGR_VID_RNG_CHK_SET_1 [22].TYPE[24:24]	1
0xBB009CD8	VLAN_IGR_VID_RNG_CHK_SET_1 [22].UPPER[23:12]	12
0xBB009CD8	VLAN_IGR_VID_RNG_CHK_SET_1 [22].LOWER[11:0]	12
0xBB009CDC	VLAN_IGR_VID_RNG_CHK_SET_1 [23].RESERVED[31:25]	7
0xBB009CDC	VLAN_IGR_VID_RNG_CHK_SET_1 [23].TYPE[24:24]	1
0xBB009CDC	VLAN_IGR_VID_RNG_CHK_SET_1 [23].UPPER[23:12]	12
0xBB009CDC	VLAN_IGR_VID_RNG_CHK_SET_1 [23].LOWER[11:0]	12
0xBB009CE0	VLAN_IGR_VID_RNG_CHK_SET_1 [24].RESERVED[31:25]	7
0xBB009CE0	VLAN_IGR_VID_RNG_CHK_SET_1 [24].TYPE[24:24]	1
0xBB009CE0	VLAN_IGR_VID_RNG_CHK_SET_1 [24].UPPER[23:12]	12
0xBB009CE0	VLAN_IGR_VID_RNG_CHK_SET_1 [24].LOWER[11:0]	12
0xBB009CE4	VLAN_IGR_VID_RNG_CHK_SET_1 [25].RESERVED[31:25]	7
0xBB009CE4	VLAN_IGR_VID_RNG_CHK_SET_1 [25].TYPE[24:24]	1
0xBB009CE4	VLAN_IGR_VID_RNG_CHK_SET_1 [25].UPPER[23:12]	12
0xBB009CE4	VLAN_IGR_VID_RNG_CHK_SET_1 [25].LOWER[11:0]	12
0xBB009CE8	VLAN_IGR_VID_RNG_CHK_SET_1 [26].RESERVED[31:25]	7
0xBB009CE8	VLAN_IGR_VID_RNG_CHK_SET_1 [26].TYPE[24:24]	1
0xBB009CE8	VLAN_IGR_VID_RNG_CHK_SET_1 [26].UPPER[23:12]	12
0xBB009CE8	VLAN_IGR_VID_RNG_CHK_SET_1 [26].LOWER[11:0]	12
0xBB009CEC	VLAN_IGR_VID_RNG_CHK_SET_1 [27].RESERVED[31:25]	7
0xBB009CEC	VLAN_IGR_VID_RNG_CHK_SET_1 [27].TYPE[24:24]	1
0xBB009CEC	VLAN_IGR_VID_RNG_CHK_SET_1 [27].UPPER[23:12]	12
0xBB009CEC	VLAN_IGR_VID_RNG_CHK_SET_1 [27].LOWER[11:0]	12
0xBB009CF0	VLAN_IGR_VID_RNG_CHK_SET_1 [28].RESERVED[31:25]	7
0xBB009CF0	VLAN_IGR_VID_RNG_CHK_SET_1 [28].TYPE[24:24]	1
0xBB009CF0	VLAN_IGR_VID_RNG_CHK_SET_1 [28].UPPER[23:12]	12
0xBB009CF0	VLAN_IGR_VID_RNG_CHK_SET_1 [28].LOWER[11:0]	12
0xBB009CF4	VLAN_IGR_VID_RNG_CHK_SET_1 [29].RESERVED[31:25]	7
0xBB009CF4	VLAN_IGR_VID_RNG_CHK_SET_1 [29].TYPE[24:24]	1
0xBB009CF4	VLAN_IGR_VID_RNG_CHK_SET_1 [29].UPPER[23:12]	12
0xBB009CF4	VLAN_IGR_VID_RNG_CHK_SET_1 [29].LOWER[11:0]	12
0xBB009CF8	VLAN_IGR_VID_RNG_CHK_SET_1 [30].RESERVED[31:25]	7
0xBB009CF8	VLAN_IGR_VID_RNG_CHK_SET_1 [30].TYPE[24:24]	1
0xBB009CF8	VLAN_IGR_VID_RNG_CHK_SET_1 [30].UPPER[23:12]	12
0xBB009CF8	VLAN_IGR_VID_RNG_CHK_SET_1 [30].LOWER[11:0]	12
0xBB009CFC	VLAN_IGR_VID_RNG_CHK_SET_1 [31].RESERVED[31:25]	7
0xBB009CFC	VLAN_IGR_VID_RNG_CHK_SET_1 [31].TYPE[24:24]	1
0xBB009CFC	VLAN_IGR_VID_RNG_CHK_SET_1 [31].UPPER[23:12]	12
0xBB009CFC	VLAN_IGR_VID_RNG_CHK_SET_1 [31].LOWER[11:0]	12
0xBB009D00	VLAN_IGR_VID_RNG_CHK_SET_2 [0].RESERVED[31:25]	7
0xBB009D00	VLAN_IGR_VID_RNG_CHK_SET_2 [0].TYPE[24:24]	1
0xBB009D00	VLAN_IGR_VID_RNG_CHK_SET_2 [0].UPPER[23:12]	12
0xBB009D00	VLAN_IGR_VID_RNG_CHK_SET_2 [0].LOWER[11:0]	12
0xBB009D04	VLAN_IGR_VID_RNG_CHK_SET_2 [1].RESERVED[31:25]	7
0xBB009D04	VLAN_IGR_VID_RNG_CHK_SET_2 [1].TYPE[24:24]	1
0xBB009D04	VLAN_IGR_VID_RNG_CHK_SET_2 [1].UPPER[23:12]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009D04	VLAN_IGR_VID_RNG_CHK_SET_2 [1].LOWER[11:0]	12
0xBB009D08	VLAN_IGR_VID_RNG_CHK_SET_2 [2].RESERVED[31:25]	7
0xBB009D08	VLAN_IGR_VID_RNG_CHK_SET_2 [2].TYPE[24:24]	1
0xBB009D08	VLAN_IGR_VID_RNG_CHK_SET_2 [2].UPPER[23:12]	12
0xBB009D08	VLAN_IGR_VID_RNG_CHK_SET_2 [2].LOWER[11:0]	12
0xBB009D0C	VLAN_IGR_VID_RNG_CHK_SET_2 [3].RESERVED[31:25]	7
0xBB009D0C	VLAN_IGR_VID_RNG_CHK_SET_2 [3].TYPE[24:24]	1
0xBB009D0C	VLAN_IGR_VID_RNG_CHK_SET_2 [3].UPPER[23:12]	12
0xBB009D0C	VLAN_IGR_VID_RNG_CHK_SET_2 [3].LOWER[11:0]	12
0xBB009D10	VLAN_IGR_VID_RNG_CHK_SET_2 [4].RESERVED[31:25]	7
0xBB009D10	VLAN_IGR_VID_RNG_CHK_SET_2 [4].TYPE[24:24]	1
0xBB009D10	VLAN_IGR_VID_RNG_CHK_SET_2 [4].UPPER[23:12]	12
0xBB009D10	VLAN_IGR_VID_RNG_CHK_SET_2 [4].LOWER[11:0]	12
0xBB009D14	VLAN_IGR_VID_RNG_CHK_SET_2 [5].RESERVED[31:25]	7
0xBB009D14	VLAN_IGR_VID_RNG_CHK_SET_2 [5].TYPE[24:24]	1
0xBB009D14	VLAN_IGR_VID_RNG_CHK_SET_2 [5].UPPER[23:12]	12
0xBB009D14	VLAN_IGR_VID_RNG_CHK_SET_2 [5].LOWER[11:0]	12
0xBB009D18	VLAN_IGR_VID_RNG_CHK_SET_2 [6].RESERVED[31:25]	7
0xBB009D18	VLAN_IGR_VID_RNG_CHK_SET_2 [6].TYPE[24:24]	1
0xBB009D18	VLAN_IGR_VID_RNG_CHK_SET_2 [6].UPPER[23:12]	12
0xBB009D18	VLAN_IGR_VID_RNG_CHK_SET_2 [6].LOWER[11:0]	12
0xBB009D1C	VLAN_IGR_VID_RNG_CHK_SET_2 [7].RESERVED[31:25]	7
0xBB009D1C	VLAN_IGR_VID_RNG_CHK_SET_2 [7].TYPE[24:24]	1
0xBB009D1C	VLAN_IGR_VID_RNG_CHK_SET_2 [7].UPPER[23:12]	12
0xBB009D1C	VLAN_IGR_VID_RNG_CHK_SET_2 [7].LOWER[11:0]	12
0xBB009D20	VLAN_IGR_VID_RNG_CHK_SET_2 [8].RESERVED[31:25]	7
0xBB009D20	VLAN_IGR_VID_RNG_CHK_SET_2 [8].TYPE[24:24]	1
0xBB009D20	VLAN_IGR_VID_RNG_CHK_SET_2 [8].UPPER[23:12]	12
0xBB009D20	VLAN_IGR_VID_RNG_CHK_SET_2 [8].LOWER[11:0]	12
0xBB009D24	VLAN_IGR_VID_RNG_CHK_SET_2 [9].RESERVED[31:25]	7
0xBB009D24	VLAN_IGR_VID_RNG_CHK_SET_2 [9].TYPE[24:24]	1
0xBB009D24	VLAN_IGR_VID_RNG_CHK_SET_2 [9].UPPER[23:12]	12
0xBB009D24	VLAN_IGR_VID_RNG_CHK_SET_2 [9].LOWER[11:0]	12
0xBB009D28	VLAN_IGR_VID_RNG_CHK_SET_2 [10].RESERVED[31:25]	7
0xBB009D28	VLAN_IGR_VID_RNG_CHK_SET_2 [10].TYPE[24:24]	1
0xBB009D28	VLAN_IGR_VID_RNG_CHK_SET_2 [10].UPPER[23:12]	12
0xBB009D28	VLAN_IGR_VID_RNG_CHK_SET_2 [10].LOWER[11:0]	12
0xBB009D2C	VLAN_IGR_VID_RNG_CHK_SET_2 [11].RESERVED[31:25]	7
0xBB009D2C	VLAN_IGR_VID_RNG_CHK_SET_2 [11].TYPE[24:24]	1
0xBB009D2C	VLAN_IGR_VID_RNG_CHK_SET_2 [11].UPPER[23:12]	12
0xBB009D2C	VLAN_IGR_VID_RNG_CHK_SET_2 [11].LOWER[11:0]	12
0xBB009D30	VLAN_IGR_VID_RNG_CHK_SET_2 [12].RESERVED[31:25]	7
0xBB009D30	VLAN_IGR_VID_RNG_CHK_SET_2 [12].TYPE[24:24]	1
0xBB009D30	VLAN_IGR_VID_RNG_CHK_SET_2 [12].UPPER[23:12]	12
0xBB009D30	VLAN_IGR_VID_RNG_CHK_SET_2 [12].LOWER[11:0]	12
0xBB009D34	VLAN_IGR_VID_RNG_CHK_SET_2 [13].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009D34	VLAN_IGR_VID_RNG_CHK_SET_2 [13].TYPE[24:24]	1
0xBB009D34	VLAN_IGR_VID_RNG_CHK_SET_2 [13].UPPER[23:12]	12
0xBB009D34	VLAN_IGR_VID_RNG_CHK_SET_2 [13].LOWER[11:0]	12
0xBB009D38	VLAN_IGR_VID_RNG_CHK_SET_2 [14].RESERVED[31:25]	7
0xBB009D38	VLAN_IGR_VID_RNG_CHK_SET_2 [14].TYPE[24:24]	1
0xBB009D38	VLAN_IGR_VID_RNG_CHK_SET_2 [14].UPPER[23:12]	12
0xBB009D38	VLAN_IGR_VID_RNG_CHK_SET_2 [14].LOWER[11:0]	12
0xBB009D3C	VLAN_IGR_VID_RNG_CHK_SET_2 [15].RESERVED[31:25]	7
0xBB009D3C	VLAN_IGR_VID_RNG_CHK_SET_2 [15].TYPE[24:24]	1
0xBB009D3C	VLAN_IGR_VID_RNG_CHK_SET_2 [15].UPPER[23:12]	12
0xBB009D3C	VLAN_IGR_VID_RNG_CHK_SET_2 [15].LOWER[11:0]	12
0xBB009D40	VLAN_IGR_VID_RNG_CHK_SET_2 [16].RESERVED[31:25]	7
0xBB009D40	VLAN_IGR_VID_RNG_CHK_SET_2 [16].TYPE[24:24]	1
0xBB009D40	VLAN_IGR_VID_RNG_CHK_SET_2 [16].UPPER[23:12]	12
0xBB009D40	VLAN_IGR_VID_RNG_CHK_SET_2 [16].LOWER[11:0]	12
0xBB009D44	VLAN_IGR_VID_RNG_CHK_SET_2 [17].RESERVED[31:25]	7
0xBB009D44	VLAN_IGR_VID_RNG_CHK_SET_2 [17].TYPE[24:24]	1
0xBB009D44	VLAN_IGR_VID_RNG_CHK_SET_2 [17].UPPER[23:12]	12
0xBB009D44	VLAN_IGR_VID_RNG_CHK_SET_2 [17].LOWER[11:0]	12
0xBB009D48	VLAN_IGR_VID_RNG_CHK_SET_2 [18].RESERVED[31:25]	7
0xBB009D48	VLAN_IGR_VID_RNG_CHK_SET_2 [18].TYPE[24:24]	1
0xBB009D48	VLAN_IGR_VID_RNG_CHK_SET_2 [18].UPPER[23:12]	12
0xBB009D48	VLAN_IGR_VID_RNG_CHK_SET_2 [18].LOWER[11:0]	12
0xBB009D4C	VLAN_IGR_VID_RNG_CHK_SET_2 [19].RESERVED[31:25]	7
0xBB009D4C	VLAN_IGR_VID_RNG_CHK_SET_2 [19].TYPE[24:24]	1
0xBB009D4C	VLAN_IGR_VID_RNG_CHK_SET_2 [19].UPPER[23:12]	12
0xBB009D4C	VLAN_IGR_VID_RNG_CHK_SET_2 [19].LOWER[11:0]	12
0xBB009D50	VLAN_IGR_VID_RNG_CHK_SET_2 [20].RESERVED[31:25]	7
0xBB009D50	VLAN_IGR_VID_RNG_CHK_SET_2 [20].TYPE[24:24]	1
0xBB009D50	VLAN_IGR_VID_RNG_CHK_SET_2 [20].UPPER[23:12]	12
0xBB009D50	VLAN_IGR_VID_RNG_CHK_SET_2 [20].LOWER[11:0]	12
0xBB009D54	VLAN_IGR_VID_RNG_CHK_SET_2 [21].RESERVED[31:25]	7
0xBB009D54	VLAN_IGR_VID_RNG_CHK_SET_2 [21].TYPE[24:24]	1
0xBB009D54	VLAN_IGR_VID_RNG_CHK_SET_2 [21].UPPER[23:12]	12
0xBB009D54	VLAN_IGR_VID_RNG_CHK_SET_2 [21].LOWER[11:0]	12
0xBB009D58	VLAN_IGR_VID_RNG_CHK_SET_2 [22].RESERVED[31:25]	7
0xBB009D58	VLAN_IGR_VID_RNG_CHK_SET_2 [22].TYPE[24:24]	1
0xBB009D58	VLAN_IGR_VID_RNG_CHK_SET_2 [22].UPPER[23:12]	12
0xBB009D58	VLAN_IGR_VID_RNG_CHK_SET_2 [22].LOWER[11:0]	12
0xBB009D5C	VLAN_IGR_VID_RNG_CHK_SET_2 [23].RESERVED[31:25]	7
0xBB009D5C	VLAN_IGR_VID_RNG_CHK_SET_2 [23].TYPE[24:24]	1
0xBB009D5C	VLAN_IGR_VID_RNG_CHK_SET_2 [23].UPPER[23:12]	12
0xBB009D5C	VLAN_IGR_VID_RNG_CHK_SET_2 [23].LOWER[11:0]	12
0xBB009D60	VLAN_IGR_VID_RNG_CHK_SET_2 [24].RESERVED[31:25]	7
0xBB009D60	VLAN_IGR_VID_RNG_CHK_SET_2 [24].TYPE[24:24]	1
0xBB009D60	VLAN_IGR_VID_RNG_CHK_SET_2 [24].UPPER[23:12]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009D60	VLAN_IGR_VID_RNG_CHK_SET_2 [24].LOWER[11:0]	12
0xBB009D64	VLAN_IGR_VID_RNG_CHK_SET_2 [25].RESERVED[31:25]	7
0xBB009D64	VLAN_IGR_VID_RNG_CHK_SET_2 [25].TYPE[24:24]	1
0xBB009D64	VLAN_IGR_VID_RNG_CHK_SET_2 [25].UPPER[23:12]	12
0xBB009D64	VLAN_IGR_VID_RNG_CHK_SET_2 [25].LOWER[11:0]	12
0xBB009D68	VLAN_IGR_VID_RNG_CHK_SET_2 [26].RESERVED[31:25]	7
0xBB009D68	VLAN_IGR_VID_RNG_CHK_SET_2 [26].TYPE[24:24]	1
0xBB009D68	VLAN_IGR_VID_RNG_CHK_SET_2 [26].UPPER[23:12]	12
0xBB009D68	VLAN_IGR_VID_RNG_CHK_SET_2 [26].LOWER[11:0]	12
0xBB009D6C	VLAN_IGR_VID_RNG_CHK_SET_2 [27].RESERVED[31:25]	7
0xBB009D6C	VLAN_IGR_VID_RNG_CHK_SET_2 [27].TYPE[24:24]	1
0xBB009D6C	VLAN_IGR_VID_RNG_CHK_SET_2 [27].UPPER[23:12]	12
0xBB009D6C	VLAN_IGR_VID_RNG_CHK_SET_2 [27].LOWER[11:0]	12
0xBB009D70	VLAN_IGR_VID_RNG_CHK_SET_2 [28].RESERVED[31:25]	7
0xBB009D70	VLAN_IGR_VID_RNG_CHK_SET_2 [28].TYPE[24:24]	1
0xBB009D70	VLAN_IGR_VID_RNG_CHK_SET_2 [28].UPPER[23:12]	12
0xBB009D70	VLAN_IGR_VID_RNG_CHK_SET_2 [28].LOWER[11:0]	12
0xBB009D74	VLAN_IGR_VID_RNG_CHK_SET_2 [29].RESERVED[31:25]	7
0xBB009D74	VLAN_IGR_VID_RNG_CHK_SET_2 [29].TYPE[24:24]	1
0xBB009D74	VLAN_IGR_VID_RNG_CHK_SET_2 [29].UPPER[23:12]	12
0xBB009D74	VLAN_IGR_VID_RNG_CHK_SET_2 [29].LOWER[11:0]	12
0xBB009D78	VLAN_IGR_VID_RNG_CHK_SET_2 [30].RESERVED[31:25]	7
0xBB009D78	VLAN_IGR_VID_RNG_CHK_SET_2 [30].TYPE[24:24]	1
0xBB009D78	VLAN_IGR_VID_RNG_CHK_SET_2 [30].UPPER[23:12]	12
0xBB009D78	VLAN_IGR_VID_RNG_CHK_SET_2 [30].LOWER[11:0]	12
0xBB009D7C	VLAN_IGR_VID_RNG_CHK_SET_2 [31].RESERVED[31:25]	7
0xBB009D7C	VLAN_IGR_VID_RNG_CHK_SET_2 [31].TYPE[24:24]	1
0xBB009D7C	VLAN_IGR_VID_RNG_CHK_SET_2 [31].UPPER[23:12]	12
0xBB009D7C	VLAN_IGR_VID_RNG_CHK_SET_2 [31].LOWER[11:0]	12
0xBB009D80	VLAN_IGR_VID_RNG_CHK_SET_3 [0].RESERVED[31:25]	7
0xBB009D80	VLAN_IGR_VID_RNG_CHK_SET_3 [0].TYPE[24:24]	1
0xBB009D80	VLAN_IGR_VID_RNG_CHK_SET_3 [0].UPPER[23:12]	12
0xBB009D80	VLAN_IGR_VID_RNG_CHK_SET_3 [0].LOWER[11:0]	12
0xBB009D84	VLAN_IGR_VID_RNG_CHK_SET_3 [1].RESERVED[31:25]	7
0xBB009D84	VLAN_IGR_VID_RNG_CHK_SET_3 [1].TYPE[24:24]	1
0xBB009D84	VLAN_IGR_VID_RNG_CHK_SET_3 [1].UPPER[23:12]	12
0xBB009D84	VLAN_IGR_VID_RNG_CHK_SET_3 [1].LOWER[11:0]	12
0xBB009D88	VLAN_IGR_VID_RNG_CHK_SET_3 [2].RESERVED[31:25]	7
0xBB009D88	VLAN_IGR_VID_RNG_CHK_SET_3 [2].TYPE[24:24]	1
0xBB009D88	VLAN_IGR_VID_RNG_CHK_SET_3 [2].UPPER[23:12]	12
0xBB009D88	VLAN_IGR_VID_RNG_CHK_SET_3 [2].LOWER[11:0]	12
0xBB009D8C	VLAN_IGR_VID_RNG_CHK_SET_3 [3].RESERVED[31:25]	7
0xBB009D8C	VLAN_IGR_VID_RNG_CHK_SET_3 [3].TYPE[24:24]	1
0xBB009D8C	VLAN_IGR_VID_RNG_CHK_SET_3 [3].UPPER[23:12]	12
0xBB009D8C	VLAN_IGR_VID_RNG_CHK_SET_3 [3].LOWER[11:0]	12
0xBB009D90	VLAN_IGR_VID_RNG_CHK_SET_3 [4].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009D90	VLAN_IGR_VID_RNG_CHK_SET_3 [4].TYPE[24:24]	1
0xBB009D90	VLAN_IGR_VID_RNG_CHK_SET_3 [4].UPPER[23:12]	12
0xBB009D90	VLAN_IGR_VID_RNG_CHK_SET_3 [4].LOWER[11:0]	12
0xBB009D94	VLAN_IGR_VID_RNG_CHK_SET_3 [5].RESERVED[31:25]	7
0xBB009D94	VLAN_IGR_VID_RNG_CHK_SET_3 [5].TYPE[24:24]	1
0xBB009D94	VLAN_IGR_VID_RNG_CHK_SET_3 [5].UPPER[23:12]	12
0xBB009D94	VLAN_IGR_VID_RNG_CHK_SET_3 [5].LOWER[11:0]	12
0xBB009D98	VLAN_IGR_VID_RNG_CHK_SET_3 [6].RESERVED[31:25]	7
0xBB009D98	VLAN_IGR_VID_RNG_CHK_SET_3 [6].TYPE[24:24]	1
0xBB009D98	VLAN_IGR_VID_RNG_CHK_SET_3 [6].UPPER[23:12]	12
0xBB009D98	VLAN_IGR_VID_RNG_CHK_SET_3 [6].LOWER[11:0]	12
0xBB009D9C	VLAN_IGR_VID_RNG_CHK_SET_3 [7].RESERVED[31:25]	7
0xBB009D9C	VLAN_IGR_VID_RNG_CHK_SET_3 [7].TYPE[24:24]	1
0xBB009D9C	VLAN_IGR_VID_RNG_CHK_SET_3 [7].UPPER[23:12]	12
0xBB009D9C	VLAN_IGR_VID_RNG_CHK_SET_3 [7].LOWER[11:0]	12
0xBB009DA0	VLAN_IGR_VID_RNG_CHK_SET_3 [8].RESERVED[31:25]	7
0xBB009DA0	VLAN_IGR_VID_RNG_CHK_SET_3 [8].TYPE[24:24]	1
0xBB009DA0	VLAN_IGR_VID_RNG_CHK_SET_3 [8].UPPER[23:12]	12
0xBB009DA0	VLAN_IGR_VID_RNG_CHK_SET_3 [8].LOWER[11:0]	12
0xBB009DA4	VLAN_IGR_VID_RNG_CHK_SET_3 [9].RESERVED[31:25]	7
0xBB009DA4	VLAN_IGR_VID_RNG_CHK_SET_3 [9].TYPE[24:24]	1
0xBB009DA4	VLAN_IGR_VID_RNG_CHK_SET_3 [9].UPPER[23:12]	12
0xBB009DA4	VLAN_IGR_VID_RNG_CHK_SET_3 [9].LOWER[11:0]	12
0xBB009DA8	VLAN_IGR_VID_RNG_CHK_SET_3 [10].RESERVED[31:25]	7
0xBB009DA8	VLAN_IGR_VID_RNG_CHK_SET_3 [10].TYPE[24:24]	1
0xBB009DA8	VLAN_IGR_VID_RNG_CHK_SET_3 [10].UPPER[23:12]	12
0xBB009DA8	VLAN_IGR_VID_RNG_CHK_SET_3 [10].LOWER[11:0]	12
0xBB009DAC	VLAN_IGR_VID_RNG_CHK_SET_3 [11].RESERVED[31:25]	7
0xBB009DAC	VLAN_IGR_VID_RNG_CHK_SET_3 [11].TYPE[24:24]	1
0xBB009DAC	VLAN_IGR_VID_RNG_CHK_SET_3 [11].UPPER[23:12]	12
0xBB009DAC	VLAN_IGR_VID_RNG_CHK_SET_3 [11].LOWER[11:0]	12
0xBB009DB0	VLAN_IGR_VID_RNG_CHK_SET_3 [12].RESERVED[31:25]	7
0xBB009DB0	VLAN_IGR_VID_RNG_CHK_SET_3 [12].TYPE[24:24]	1
0xBB009DB0	VLAN_IGR_VID_RNG_CHK_SET_3 [12].UPPER[23:12]	12
0xBB009DB0	VLAN_IGR_VID_RNG_CHK_SET_3 [12].LOWER[11:0]	12
0xBB009DB4	VLAN_IGR_VID_RNG_CHK_SET_3 [13].RESERVED[31:25]	7
0xBB009DB4	VLAN_IGR_VID_RNG_CHK_SET_3 [13].TYPE[24:24]	1
0xBB009DB4	VLAN_IGR_VID_RNG_CHK_SET_3 [13].UPPER[23:12]	12
0xBB009DB4	VLAN_IGR_VID_RNG_CHK_SET_3 [13].LOWER[11:0]	12
0xBB009DB8	VLAN_IGR_VID_RNG_CHK_SET_3 [14].RESERVED[31:25]	7
0xBB009DB8	VLAN_IGR_VID_RNG_CHK_SET_3 [14].TYPE[24:24]	1
0xBB009DB8	VLAN_IGR_VID_RNG_CHK_SET_3 [14].UPPER[23:12]	12
0xBB009DB8	VLAN_IGR_VID_RNG_CHK_SET_3 [14].LOWER[11:0]	12
0xBB009DBC	VLAN_IGR_VID_RNG_CHK_SET_3 [15].RESERVED[31:25]	7
0xBB009DBC	VLAN_IGR_VID_RNG_CHK_SET_3 [15].TYPE[24:24]	1
0xBB009DBC	VLAN_IGR_VID_RNG_CHK_SET_3 [15].UPPER[23:12]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009DBC	VLAN_IGR_VID_RNG_CHK_SET_3 [15].LOWER[11:0]	12
0xBB009DC0	VLAN_IGR_VID_RNG_CHK_SET_3 [16].RESERVED[31:25]	7
0xBB009DC0	VLAN_IGR_VID_RNG_CHK_SET_3 [16].TYPE[24:24]	1
0xBB009DC0	VLAN_IGR_VID_RNG_CHK_SET_3 [16].UPPER[23:12]	12
0xBB009DC0	VLAN_IGR_VID_RNG_CHK_SET_3 [16].LOWER[11:0]	12
0xBB009DC4	VLAN_IGR_VID_RNG_CHK_SET_3 [17].RESERVED[31:25]	7
0xBB009DC4	VLAN_IGR_VID_RNG_CHK_SET_3 [17].TYPE[24:24]	1
0xBB009DC4	VLAN_IGR_VID_RNG_CHK_SET_3 [17].UPPER[23:12]	12
0xBB009DC4	VLAN_IGR_VID_RNG_CHK_SET_3 [17].LOWER[11:0]	12
0xBB009DC8	VLAN_IGR_VID_RNG_CHK_SET_3 [18].RESERVED[31:25]	7
0xBB009DC8	VLAN_IGR_VID_RNG_CHK_SET_3 [18].TYPE[24:24]	1
0xBB009DC8	VLAN_IGR_VID_RNG_CHK_SET_3 [18].UPPER[23:12]	12
0xBB009DC8	VLAN_IGR_VID_RNG_CHK_SET_3 [18].LOWER[11:0]	12
0xBB009DCC	VLAN_IGR_VID_RNG_CHK_SET_3 [19].RESERVED[31:25]	7
0xBB009DCC	VLAN_IGR_VID_RNG_CHK_SET_3 [19].TYPE[24:24]	1
0xBB009DCC	VLAN_IGR_VID_RNG_CHK_SET_3 [19].UPPER[23:12]	12
0xBB009DCC	VLAN_IGR_VID_RNG_CHK_SET_3 [19].LOWER[11:0]	12
0xBB009DD0	VLAN_IGR_VID_RNG_CHK_SET_3 [20].RESERVED[31:25]	7
0xBB009DD0	VLAN_IGR_VID_RNG_CHK_SET_3 [20].TYPE[24:24]	1
0xBB009DD0	VLAN_IGR_VID_RNG_CHK_SET_3 [20].UPPER[23:12]	12
0xBB009DD0	VLAN_IGR_VID_RNG_CHK_SET_3 [20].LOWER[11:0]	12
0xBB009DD4	VLAN_IGR_VID_RNG_CHK_SET_3 [21].RESERVED[31:25]	7
0xBB009DD4	VLAN_IGR_VID_RNG_CHK_SET_3 [21].TYPE[24:24]	1
0xBB009DD4	VLAN_IGR_VID_RNG_CHK_SET_3 [21].UPPER[23:12]	12
0xBB009DD4	VLAN_IGR_VID_RNG_CHK_SET_3 [21].LOWER[11:0]	12
0xBB009DD8	VLAN_IGR_VID_RNG_CHK_SET_3 [22].RESERVED[31:25]	7
0xBB009DD8	VLAN_IGR_VID_RNG_CHK_SET_3 [22].TYPE[24:24]	1
0xBB009DD8	VLAN_IGR_VID_RNG_CHK_SET_3 [22].UPPER[23:12]	12
0xBB009DD8	VLAN_IGR_VID_RNG_CHK_SET_3 [22].LOWER[11:0]	12
0xBB009DDC	VLAN_IGR_VID_RNG_CHK_SET_3 [23].RESERVED[31:25]	7
0xBB009DDC	VLAN_IGR_VID_RNG_CHK_SET_3 [23].TYPE[24:24]	1
0xBB009DDC	VLAN_IGR_VID_RNG_CHK_SET_3 [23].UPPER[23:12]	12
0xBB009DDC	VLAN_IGR_VID_RNG_CHK_SET_3 [23].LOWER[11:0]	12
0xBB009DE0	VLAN_IGR_VID_RNG_CHK_SET_3 [24].RESERVED[31:25]	7
0xBB009DE0	VLAN_IGR_VID_RNG_CHK_SET_3 [24].TYPE[24:24]	1
0xBB009DE0	VLAN_IGR_VID_RNG_CHK_SET_3 [24].UPPER[23:12]	12
0xBB009DE0	VLAN_IGR_VID_RNG_CHK_SET_3 [24].LOWER[11:0]	12
0xBB009DE4	VLAN_IGR_VID_RNG_CHK_SET_3 [25].RESERVED[31:25]	7
0xBB009DE4	VLAN_IGR_VID_RNG_CHK_SET_3 [25].TYPE[24:24]	1
0xBB009DE4	VLAN_IGR_VID_RNG_CHK_SET_3 [25].UPPER[23:12]	12
0xBB009DE4	VLAN_IGR_VID_RNG_CHK_SET_3 [25].LOWER[11:0]	12
0xBB009DE8	VLAN_IGR_VID_RNG_CHK_SET_3 [26].RESERVED[31:25]	7
0xBB009DE8	VLAN_IGR_VID_RNG_CHK_SET_3 [26].TYPE[24:24]	1
0xBB009DE8	VLAN_IGR_VID_RNG_CHK_SET_3 [26].UPPER[23:12]	12
0xBB009DE8	VLAN_IGR_VID_RNG_CHK_SET_3 [26].LOWER[11:0]	12
0xBB009DEC	VLAN_IGR_VID_RNG_CHK_SET_3 [27].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009DEC	VLAN_IGR_VID_RNG_CHK_SET_3 [27].TYPE[24:24]	1
0xBB009DEC	VLAN_IGR_VID_RNG_CHK_SET_3 [27].UPPER[23:12]	12
0xBB009DEC	VLAN_IGR_VID_RNG_CHK_SET_3 [27].LOWER[11:0]	12
0xBB009DF0	VLAN_IGR_VID_RNG_CHK_SET_3 [28].RESERVED[31:25]	7
0xBB009DF0	VLAN_IGR_VID_RNG_CHK_SET_3 [28].TYPE[24:24]	1
0xBB009DF0	VLAN_IGR_VID_RNG_CHK_SET_3 [28].UPPER[23:12]	12
0xBB009DF0	VLAN_IGR_VID_RNG_CHK_SET_3 [28].LOWER[11:0]	12
0xBB009DF4	VLAN_IGR_VID_RNG_CHK_SET_3 [29].RESERVED[31:25]	7
0xBB009DF4	VLAN_IGR_VID_RNG_CHK_SET_3 [29].TYPE[24:24]	1
0xBB009DF4	VLAN_IGR_VID_RNG_CHK_SET_3 [29].UPPER[23:12]	12
0xBB009DF4	VLAN_IGR_VID_RNG_CHK_SET_3 [29].LOWER[11:0]	12
0xBB009DF8	VLAN_IGR_VID_RNG_CHK_SET_3 [30].RESERVED[31:25]	7
0xBB009DF8	VLAN_IGR_VID_RNG_CHK_SET_3 [30].TYPE[24:24]	1
0xBB009DF8	VLAN_IGR_VID_RNG_CHK_SET_3 [30].UPPER[23:12]	12
0xBB009DF8	VLAN_IGR_VID_RNG_CHK_SET_3 [30].LOWER[11:0]	12
0xBB009DFC	VLAN_IGR_VID_RNG_CHK_SET_3 [31].RESERVED[31:25]	7
0xBB009DFC	VLAN_IGR_VID_RNG_CHK_SET_3 [31].TYPE[24:24]	1
0xBB009DFC	VLAN_IGR_VID_RNG_CHK_SET_3 [31].UPPER[23:12]	12
0xBB009DFC	VLAN_IGR_VID_RNG_CHK_SET_3 [31].LOWER[11:0]	12
0xBB009E00	VLAN_IVC_BLK_CTRL [0].MODE[1:0]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [1].MODE[3:2]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [2].MODE[5:4]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [3].MODE[7:6]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [4].MODE[9:8]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [5].MODE[11:10]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [6].MODE[13:12]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [7].MODE[15:14]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [8].MODE[17:16]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [9].MODE[19:18]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [10].MODE[21:20]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [11].MODE[23:22]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [12].MODE[25:24]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [13].MODE[27:26]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [14].MODE[29:28]	2
0xBB009E00	VLAN_IVC_BLK_CTRL [15].MODE[31:30]	2
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [0].HIT[0:0]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [1].HIT[1:1]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [2].HIT[2:2]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [3].HIT[3:3]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [4].HIT[4:4]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [5].HIT[5:5]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [6].HIT[6:6]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [7].HIT[7:7]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [8].HIT[8:8]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [9].HIT[9:9]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [10].HIT[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [11].HIT[11:11]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [12].HIT[12:12]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [13].HIT[13:13]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [14].HIT[14:14]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [15].HIT[15:15]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [16].HIT[16:16]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [17].HIT[17:17]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [18].HIT[18:18]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [19].HIT[19:19]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [20].HIT[20:20]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [21].HIT[21:21]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [22].HIT[22:22]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [23].HIT[23:23]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [24].HIT[24:24]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [25].HIT[25:25]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [26].HIT[26:26]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [27].HIT[27:27]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [28].HIT[28:28]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [29].HIT[29:29]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [30].HIT[30:30]	1
0xBB009EE8	VLAN_IVC_ENTRY_INDICATION [31].HIT[31:31]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [32].HIT[0:0]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [33].HIT[1:1]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [34].HIT[2:2]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [35].HIT[3:3]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [36].HIT[4:4]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [37].HIT[5:5]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [38].HIT[6:6]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [39].HIT[7:7]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [40].HIT[8:8]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [41].HIT[9:9]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [42].HIT[10:10]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [43].HIT[11:11]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [44].HIT[12:12]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [45].HIT[13:13]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [46].HIT[14:14]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [47].HIT[15:15]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [48].HIT[16:16]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [49].HIT[17:17]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [50].HIT[18:18]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [51].HIT[19:19]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [52].HIT[20:20]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [53].HIT[21:21]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [54].HIT[22:22]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [55].HIT[23:23]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [56].HIT[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [58].HIT[26:26]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [59].HIT[27:27]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [60].HIT[28:28]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [61].HIT[29:29]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [62].HIT[30:30]	1
0xBB009EEC	VLAN_IVC_ENTRY_INDICATION [63].HIT[31:31]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [64].HIT[0:0]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [65].HIT[1:1]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [66].HIT[2:2]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [67].HIT[3:3]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [68].HIT[4:4]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [69].HIT[5:5]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [70].HIT[6:6]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [71].HIT[7:7]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [72].HIT[8:8]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [73].HIT[9:9]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [74].HIT[10:10]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [75].HIT[11:11]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [76].HIT[12:12]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [77].HIT[13:13]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [78].HIT[14:14]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [79].HIT[15:15]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [80].HIT[16:16]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [81].HIT[17:17]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [82].HIT[18:18]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [83].HIT[19:19]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [84].HIT[20:20]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [85].HIT[21:21]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [86].HIT[22:22]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [87].HIT[23:23]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [88].HIT[24:24]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [89].HIT[25:25]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [90].HIT[26:26]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [91].HIT[27:27]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [92].HIT[28:28]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [93].HIT[29:29]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [94].HIT[30:30]	1
0xBB009EF0	VLAN_IVC_ENTRY_INDICATION [95].HIT[31:31]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [96].HIT[0:0]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [97].HIT[1:1]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [98].HIT[2:2]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [99].HIT[3:3]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [100].HIT[4:4]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [101].HIT[5:5]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [102].HIT[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [103].HIT[7:7]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [104].HIT[8:8]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [105].HIT[9:9]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [106].HIT[10:10]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [107].HIT[11:11]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [108].HIT[12:12]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [109].HIT[13:13]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [110].HIT[14:14]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [111].HIT[15:15]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [112].HIT[16:16]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [113].HIT[17:17]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [114].HIT[18:18]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [115].HIT[19:19]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [116].HIT[20:20]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [117].HIT[21:21]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [118].HIT[22:22]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [119].HIT[23:23]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [120].HIT[24:24]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [121].HIT[25:25]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [122].HIT[26:26]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [123].HIT[27:27]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [124].HIT[28:28]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [125].HIT[29:29]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [126].HIT[30:30]	1
0xBB009EF4	VLAN_IVC_ENTRY_INDICATION [127].HIT[31:31]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [128].HIT[0:0]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [129].HIT[1:1]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [130].HIT[2:2]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [131].HIT[3:3]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [132].HIT[4:4]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [133].HIT[5:5]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [134].HIT[6:6]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [135].HIT[7:7]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [136].HIT[8:8]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [137].HIT[9:9]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [138].HIT[10:10]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [139].HIT[11:11]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [140].HIT[12:12]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [141].HIT[13:13]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [142].HIT[14:14]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [143].HIT[15:15]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [144].HIT[16:16]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [145].HIT[17:17]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [146].HIT[18:18]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [147].HIT[19:19]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [148].HIT[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [149].HIT[21:21]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [150].HIT[22:22]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [151].HIT[23:23]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [152].HIT[24:24]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [153].HIT[25:25]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [154].HIT[26:26]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [155].HIT[27:27]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [156].HIT[28:28]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [157].HIT[29:29]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [158].HIT[30:30]	1
0xBB009EF8	VLAN_IVC_ENTRY_INDICATION [159].HIT[31:31]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [160].HIT[0:0]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [161].HIT[1:1]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [162].HIT[2:2]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [163].HIT[3:3]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [164].HIT[4:4]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [165].HIT[5:5]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [166].HIT[6:6]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [167].HIT[7:7]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [168].HIT[8:8]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [169].HIT[9:9]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [170].HIT[10:10]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [171].HIT[11:11]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [172].HIT[12:12]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [173].HIT[13:13]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [174].HIT[14:14]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [175].HIT[15:15]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [176].HIT[16:16]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [177].HIT[17:17]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [178].HIT[18:18]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [179].HIT[19:19]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [180].HIT[20:20]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [181].HIT[21:21]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [182].HIT[22:22]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [183].HIT[23:23]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [184].HIT[24:24]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [185].HIT[25:25]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [186].HIT[26:26]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [187].HIT[27:27]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [188].HIT[28:28]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [189].HIT[29:29]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [190].HIT[30:30]	1
0xBB009EFC	VLAN_IVC_ENTRY_INDICATION [191].HIT[31:31]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [192].HIT[0:0]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [193].HIT[1:1]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [194].HIT[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [195].HIT[3:3]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [196].HIT[4:4]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [197].HIT[5:5]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [198].HIT[6:6]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [199].HIT[7:7]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [200].HIT[8:8]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [201].HIT[9:9]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [202].HIT[10:10]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [203].HIT[11:11]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [204].HIT[12:12]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [205].HIT[13:13]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [206].HIT[14:14]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [207].HIT[15:15]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [208].HIT[16:16]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [209].HIT[17:17]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [210].HIT[18:18]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [211].HIT[19:19]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [212].HIT[20:20]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [213].HIT[21:21]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [214].HIT[22:22]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [215].HIT[23:23]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [216].HIT[24:24]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [217].HIT[25:25]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [218].HIT[26:26]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [219].HIT[27:27]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [220].HIT[28:28]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [221].HIT[29:29]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [222].HIT[30:30]	1
0xBB009F00	VLAN_IVC_ENTRY_INDICATION [223].HIT[31:31]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [224].HIT[0:0]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [225].HIT[1:1]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [226].HIT[2:2]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [227].HIT[3:3]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [228].HIT[4:4]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [229].HIT[5:5]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [230].HIT[6:6]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [231].HIT[7:7]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [232].HIT[8:8]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [233].HIT[9:9]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [234].HIT[10:10]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [235].HIT[11:11]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [236].HIT[12:12]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [237].HIT[13:13]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [238].HIT[14:14]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [239].HIT[15:15]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [240].HIT[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [241].HIT[17:17]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [242].HIT[18:18]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [243].HIT[19:19]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [244].HIT[20:20]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [245].HIT[21:21]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [246].HIT[22:22]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [247].HIT[23:23]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [248].HIT[24:24]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [249].HIT[25:25]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [250].HIT[26:26]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [251].HIT[27:27]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [252].HIT[28:28]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [253].HIT[29:29]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [254].HIT[30:30]	1
0xBB009F04	VLAN_IVC_ENTRY_INDICATION [255].HIT[31:31]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [256].HIT[0:0]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [257].HIT[1:1]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [258].HIT[2:2]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [259].HIT[3:3]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [260].HIT[4:4]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [261].HIT[5:5]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [262].HIT[6:6]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [263].HIT[7:7]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [264].HIT[8:8]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [265].HIT[9:9]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [266].HIT[10:10]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [267].HIT[11:11]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [268].HIT[12:12]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [269].HIT[13:13]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [270].HIT[14:14]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [271].HIT[15:15]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [272].HIT[16:16]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [273].HIT[17:17]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [274].HIT[18:18]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [275].HIT[19:19]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [276].HIT[20:20]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [277].HIT[21:21]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [278].HIT[22:22]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [279].HIT[23:23]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [280].HIT[24:24]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [281].HIT[25:25]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [282].HIT[26:26]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [283].HIT[27:27]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [284].HIT[28:28]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [285].HIT[29:29]	1
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [286].HIT[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F08	VLAN_IVC_ENTRY_INDICATION [287].HIT[31:31]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [288].HIT[0:0]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [289].HIT[1:1]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [290].HIT[2:2]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [291].HIT[3:3]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [292].HIT[4:4]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [293].HIT[5:5]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [294].HIT[6:6]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [295].HIT[7:7]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [296].HIT[8:8]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [297].HIT[9:9]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [298].HIT[10:10]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [299].HIT[11:11]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [300].HIT[12:12]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [301].HIT[13:13]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [302].HIT[14:14]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [303].HIT[15:15]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [304].HIT[16:16]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [305].HIT[17:17]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [306].HIT[18:18]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [307].HIT[19:19]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [308].HIT[20:20]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [309].HIT[21:21]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [310].HIT[22:22]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [311].HIT[23:23]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [312].HIT[24:24]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [313].HIT[25:25]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [314].HIT[26:26]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [315].HIT[27:27]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [316].HIT[28:28]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [317].HIT[29:29]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [318].HIT[30:30]	1
0xBB009F0C	VLAN_IVC_ENTRY_INDICATION [319].HIT[31:31]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [320].HIT[0:0]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [321].HIT[1:1]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [322].HIT[2:2]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [323].HIT[3:3]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [324].HIT[4:4]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [325].HIT[5:5]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [326].HIT[6:6]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [327].HIT[7:7]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [328].HIT[8:8]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [329].HIT[9:9]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [330].HIT[10:10]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [331].HIT[11:11]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [332].HIT[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [333].HIT[13:13]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [334].HIT[14:14]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [335].HIT[15:15]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [336].HIT[16:16]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [337].HIT[17:17]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [338].HIT[18:18]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [339].HIT[19:19]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [340].HIT[20:20]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [341].HIT[21:21]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [342].HIT[22:22]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [343].HIT[23:23]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [344].HIT[24:24]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [345].HIT[25:25]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [346].HIT[26:26]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [347].HIT[27:27]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [348].HIT[28:28]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [349].HIT[29:29]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [350].HIT[30:30]	1
0xBB009F10	VLAN_IVC_ENTRY_INDICATION [351].HIT[31:31]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [352].HIT[0:0]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [353].HIT[1:1]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [354].HIT[2:2]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [355].HIT[3:3]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [356].HIT[4:4]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [357].HIT[5:5]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [358].HIT[6:6]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [359].HIT[7:7]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [360].HIT[8:8]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [361].HIT[9:9]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [362].HIT[10:10]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [363].HIT[11:11]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [364].HIT[12:12]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [365].HIT[13:13]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [366].HIT[14:14]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [367].HIT[15:15]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [368].HIT[16:16]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [369].HIT[17:17]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [370].HIT[18:18]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [371].HIT[19:19]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [372].HIT[20:20]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [373].HIT[21:21]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [374].HIT[22:22]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [375].HIT[23:23]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [376].HIT[24:24]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [377].HIT[25:25]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [378].HIT[26:26]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [379].HIT[27:27]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [380].HIT[28:28]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [381].HIT[29:29]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [382].HIT[30:30]	1
0xBB009F14	VLAN_IVC_ENTRY_INDICATION [383].HIT[31:31]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [384].HIT[0:0]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [385].HIT[1:1]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [386].HIT[2:2]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [387].HIT[3:3]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [388].HIT[4:4]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [389].HIT[5:5]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [390].HIT[6:6]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [391].HIT[7:7]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [392].HIT[8:8]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [393].HIT[9:9]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [394].HIT[10:10]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [395].HIT[11:11]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [396].HIT[12:12]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [397].HIT[13:13]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [398].HIT[14:14]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [399].HIT[15:15]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [400].HIT[16:16]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [401].HIT[17:17]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [402].HIT[18:18]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [403].HIT[19:19]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [404].HIT[20:20]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [405].HIT[21:21]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [406].HIT[22:22]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [407].HIT[23:23]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [408].HIT[24:24]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [409].HIT[25:25]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [410].HIT[26:26]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [411].HIT[27:27]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [412].HIT[28:28]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [413].HIT[29:29]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [414].HIT[30:30]	1
0xBB009F18	VLAN_IVC_ENTRY_INDICATION [415].HIT[31:31]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [416].HIT[0:0]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [417].HIT[1:1]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [418].HIT[2:2]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [419].HIT[3:3]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [420].HIT[4:4]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [421].HIT[5:5]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [422].HIT[6:6]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [423].HIT[7:7]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [424].HIT[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [425].HIT[9:9]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [426].HIT[10:10]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [427].HIT[11:11]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [428].HIT[12:12]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [429].HIT[13:13]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [430].HIT[14:14]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [431].HIT[15:15]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [432].HIT[16:16]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [433].HIT[17:17]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [434].HIT[18:18]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [435].HIT[19:19]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [436].HIT[20:20]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [437].HIT[21:21]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [438].HIT[22:22]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [439].HIT[23:23]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [440].HIT[24:24]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [441].HIT[25:25]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [442].HIT[26:26]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [443].HIT[27:27]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [444].HIT[28:28]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [445].HIT[29:29]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [446].HIT[30:30]	1
0xBB009F1C	VLAN_IVC_ENTRY_INDICATION [447].HIT[31:31]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [448].HIT[0:0]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [449].HIT[1:1]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [450].HIT[2:2]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [451].HIT[3:3]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [452].HIT[4:4]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [453].HIT[5:5]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [454].HIT[6:6]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [455].HIT[7:7]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [456].HIT[8:8]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [457].HIT[9:9]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [458].HIT[10:10]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [459].HIT[11:11]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [460].HIT[12:12]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [461].HIT[13:13]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [462].HIT[14:14]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [463].HIT[15:15]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [464].HIT[16:16]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [465].HIT[17:17]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [466].HIT[18:18]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [467].HIT[19:19]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [468].HIT[20:20]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [469].HIT[21:21]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [470].HIT[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [471].HIT[23:23]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [472].HIT[24:24]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [473].HIT[25:25]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [474].HIT[26:26]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [475].HIT[27:27]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [476].HIT[28:28]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [477].HIT[29:29]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [478].HIT[30:30]	1
0xBB009F20	VLAN_IVC_ENTRY_INDICATION [479].HIT[31:31]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [480].HIT[0:0]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [481].HIT[1:1]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [482].HIT[2:2]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [483].HIT[3:3]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [484].HIT[4:4]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [485].HIT[5:5]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [486].HIT[6:6]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [487].HIT[7:7]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [488].HIT[8:8]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [489].HIT[9:9]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [490].HIT[10:10]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [491].HIT[11:11]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [492].HIT[12:12]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [493].HIT[13:13]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [494].HIT[14:14]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [495].HIT[15:15]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [496].HIT[16:16]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [497].HIT[17:17]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [498].HIT[18:18]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [499].HIT[19:19]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [500].HIT[20:20]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [501].HIT[21:21]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [502].HIT[22:22]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [503].HIT[23:23]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [504].HIT[24:24]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [505].HIT[25:25]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [506].HIT[26:26]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [507].HIT[27:27]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [508].HIT[28:28]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [509].HIT[29:29]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [510].HIT[30:30]	1
0xBB009F24	VLAN_IVC_ENTRY_INDICATION [511].HIT[31:31]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [512].HIT[0:0]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [513].HIT[1:1]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [514].HIT[2:2]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [515].HIT[3:3]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [516].HIT[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [517].HIT[5:5]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [518].HIT[6:6]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [519].HIT[7:7]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [520].HIT[8:8]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [521].HIT[9:9]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [522].HIT[10:10]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [523].HIT[11:11]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [524].HIT[12:12]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [525].HIT[13:13]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [526].HIT[14:14]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [527].HIT[15:15]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [528].HIT[16:16]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [529].HIT[17:17]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [530].HIT[18:18]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [531].HIT[19:19]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [532].HIT[20:20]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [533].HIT[21:21]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [534].HIT[22:22]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [535].HIT[23:23]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [536].HIT[24:24]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [537].HIT[25:25]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [538].HIT[26:26]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [539].HIT[27:27]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [540].HIT[28:28]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [541].HIT[29:29]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [542].HIT[30:30]	1
0xBB009F28	VLAN_IVC_ENTRY_INDICATION [543].HIT[31:31]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [544].HIT[0:0]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [545].HIT[1:1]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [546].HIT[2:2]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [547].HIT[3:3]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [548].HIT[4:4]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [549].HIT[5:5]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [550].HIT[6:6]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [551].HIT[7:7]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [552].HIT[8:8]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [553].HIT[9:9]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [554].HIT[10:10]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [555].HIT[11:11]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [556].HIT[12:12]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [557].HIT[13:13]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [558].HIT[14:14]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [559].HIT[15:15]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [560].HIT[16:16]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [561].HIT[17:17]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [562].HIT[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [563].HIT[19:19]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [564].HIT[20:20]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [565].HIT[21:21]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [566].HIT[22:22]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [567].HIT[23:23]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [568].HIT[24:24]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [569].HIT[25:25]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [570].HIT[26:26]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [571].HIT[27:27]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [572].HIT[28:28]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [573].HIT[29:29]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [574].HIT[30:30]	1
0xBB009F2C	VLAN_IVC_ENTRY_INDICATION [575].HIT[31:31]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [576].HIT[0:0]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [577].HIT[1:1]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [578].HIT[2:2]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [579].HIT[3:3]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [580].HIT[4:4]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [581].HIT[5:5]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [582].HIT[6:6]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [583].HIT[7:7]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [584].HIT[8:8]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [585].HIT[9:9]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [586].HIT[10:10]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [587].HIT[11:11]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [588].HIT[12:12]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [589].HIT[13:13]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [590].HIT[14:14]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [591].HIT[15:15]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [592].HIT[16:16]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [593].HIT[17:17]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [594].HIT[18:18]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [595].HIT[19:19]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [596].HIT[20:20]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [597].HIT[21:21]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [598].HIT[22:22]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [599].HIT[23:23]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [600].HIT[24:24]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [601].HIT[25:25]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [602].HIT[26:26]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [603].HIT[27:27]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [604].HIT[28:28]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [605].HIT[29:29]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [606].HIT[30:30]	1
0xBB009F30	VLAN_IVC_ENTRY_INDICATION [607].HIT[31:31]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [608].HIT[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [609].HIT[1:1]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [610].HIT[2:2]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [611].HIT[3:3]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [612].HIT[4:4]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [613].HIT[5:5]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [614].HIT[6:6]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [615].HIT[7:7]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [616].HIT[8:8]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [617].HIT[9:9]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [618].HIT[10:10]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [619].HIT[11:11]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [620].HIT[12:12]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [621].HIT[13:13]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [622].HIT[14:14]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [623].HIT[15:15]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [624].HIT[16:16]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [625].HIT[17:17]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [626].HIT[18:18]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [627].HIT[19:19]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [628].HIT[20:20]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [629].HIT[21:21]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [630].HIT[22:22]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [631].HIT[23:23]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [632].HIT[24:24]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [633].HIT[25:25]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [634].HIT[26:26]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [635].HIT[27:27]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [636].HIT[28:28]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [637].HIT[29:29]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [638].HIT[30:30]	1
0xBB009F34	VLAN_IVC_ENTRY_INDICATION [639].HIT[31:31]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [640].HIT[0:0]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [641].HIT[1:1]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [642].HIT[2:2]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [643].HIT[3:3]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [644].HIT[4:4]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [645].HIT[5:5]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [646].HIT[6:6]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [647].HIT[7:7]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [648].HIT[8:8]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [649].HIT[9:9]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [650].HIT[10:10]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [651].HIT[11:11]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [652].HIT[12:12]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [653].HIT[13:13]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [654].HIT[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [655].HIT[15:15]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [656].HIT[16:16]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [657].HIT[17:17]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [658].HIT[18:18]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [659].HIT[19:19]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [660].HIT[20:20]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [661].HIT[21:21]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [662].HIT[22:22]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [663].HIT[23:23]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [664].HIT[24:24]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [665].HIT[25:25]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [666].HIT[26:26]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [667].HIT[27:27]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [668].HIT[28:28]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [669].HIT[29:29]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [670].HIT[30:30]	1
0xBB009F38	VLAN_IVC_ENTRY_INDICATION [671].HIT[31:31]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [672].HIT[0:0]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [673].HIT[1:1]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [674].HIT[2:2]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [675].HIT[3:3]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [676].HIT[4:4]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [677].HIT[5:5]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [678].HIT[6:6]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [679].HIT[7:7]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [680].HIT[8:8]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [681].HIT[9:9]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [682].HIT[10:10]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [683].HIT[11:11]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [684].HIT[12:12]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [685].HIT[13:13]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [686].HIT[14:14]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [687].HIT[15:15]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [688].HIT[16:16]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [689].HIT[17:17]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [690].HIT[18:18]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [691].HIT[19:19]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [692].HIT[20:20]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [693].HIT[21:21]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [694].HIT[22:22]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [695].HIT[23:23]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [696].HIT[24:24]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [697].HIT[25:25]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [698].HIT[26:26]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [699].HIT[27:27]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [700].HIT[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [701].HIT[29:29]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [702].HIT[30:30]	1
0xBB009F3C	VLAN_IVC_ENTRY_INDICATION [703].HIT[31:31]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [704].HIT[0:0]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [705].HIT[1:1]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [706].HIT[2:2]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [707].HIT[3:3]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [708].HIT[4:4]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [709].HIT[5:5]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [710].HIT[6:6]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [711].HIT[7:7]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [712].HIT[8:8]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [713].HIT[9:9]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [714].HIT[10:10]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [715].HIT[11:11]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [716].HIT[12:12]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [717].HIT[13:13]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [718].HIT[14:14]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [719].HIT[15:15]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [720].HIT[16:16]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [721].HIT[17:17]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [722].HIT[18:18]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [723].HIT[19:19]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [724].HIT[20:20]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [725].HIT[21:21]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [726].HIT[22:22]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [727].HIT[23:23]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [728].HIT[24:24]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [729].HIT[25:25]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [730].HIT[26:26]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [731].HIT[27:27]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [732].HIT[28:28]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [733].HIT[29:29]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [734].HIT[30:30]	1
0xBB009F40	VLAN_IVC_ENTRY_INDICATION [735].HIT[31:31]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [736].HIT[0:0]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [737].HIT[1:1]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [738].HIT[2:2]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [739].HIT[3:3]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [740].HIT[4:4]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [741].HIT[5:5]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [742].HIT[6:6]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [743].HIT[7:7]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [744].HIT[8:8]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [745].HIT[9:9]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [746].HIT[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [747].HIT[11:11]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [748].HIT[12:12]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [749].HIT[13:13]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [750].HIT[14:14]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [751].HIT[15:15]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [752].HIT[16:16]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [753].HIT[17:17]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [754].HIT[18:18]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [755].HIT[19:19]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [756].HIT[20:20]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [757].HIT[21:21]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [758].HIT[22:22]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [759].HIT[23:23]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [760].HIT[24:24]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [761].HIT[25:25]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [762].HIT[26:26]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [763].HIT[27:27]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [764].HIT[28:28]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [765].HIT[29:29]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [766].HIT[30:30]	1
0xBB009F44	VLAN_IVC_ENTRY_INDICATION [767].HIT[31:31]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [768].HIT[0:0]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [769].HIT[1:1]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [770].HIT[2:2]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [771].HIT[3:3]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [772].HIT[4:4]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [773].HIT[5:5]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [774].HIT[6:6]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [775].HIT[7:7]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [776].HIT[8:8]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [777].HIT[9:9]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [778].HIT[10:10]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [779].HIT[11:11]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [780].HIT[12:12]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [781].HIT[13:13]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [782].HIT[14:14]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [783].HIT[15:15]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [784].HIT[16:16]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [785].HIT[17:17]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [786].HIT[18:18]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [787].HIT[19:19]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [788].HIT[20:20]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [789].HIT[21:21]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [790].HIT[22:22]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [791].HIT[23:23]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [792].HIT[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [793].HIT[25:25]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [794].HIT[26:26]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [795].HIT[27:27]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [796].HIT[28:28]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [797].HIT[29:29]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [798].HIT[30:30]	1
0xBB009F48	VLAN_IVC_ENTRY_INDICATION [799].HIT[31:31]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [800].HIT[0:0]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [801].HIT[1:1]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [802].HIT[2:2]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [803].HIT[3:3]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [804].HIT[4:4]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [805].HIT[5:5]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [806].HIT[6:6]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [807].HIT[7:7]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [808].HIT[8:8]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [809].HIT[9:9]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [810].HIT[10:10]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [811].HIT[11:11]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [812].HIT[12:12]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [813].HIT[13:13]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [814].HIT[14:14]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [815].HIT[15:15]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [816].HIT[16:16]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [817].HIT[17:17]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [818].HIT[18:18]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [819].HIT[19:19]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [820].HIT[20:20]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [821].HIT[21:21]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [822].HIT[22:22]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [823].HIT[23:23]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [824].HIT[24:24]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [825].HIT[25:25]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [826].HIT[26:26]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [827].HIT[27:27]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [828].HIT[28:28]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [829].HIT[29:29]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [830].HIT[30:30]	1
0xBB009F4C	VLAN_IVC_ENTRY_INDICATION [831].HIT[31:31]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [832].HIT[0:0]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [833].HIT[1:1]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [834].HIT[2:2]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [835].HIT[3:3]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [836].HIT[4:4]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [837].HIT[5:5]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [838].HIT[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [839].HIT[7:7]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [840].HIT[8:8]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [841].HIT[9:9]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [842].HIT[10:10]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [843].HIT[11:11]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [844].HIT[12:12]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [845].HIT[13:13]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [846].HIT[14:14]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [847].HIT[15:15]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [848].HIT[16:16]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [849].HIT[17:17]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [850].HIT[18:18]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [851].HIT[19:19]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [852].HIT[20:20]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [853].HIT[21:21]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [854].HIT[22:22]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [855].HIT[23:23]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [856].HIT[24:24]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [857].HIT[25:25]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [858].HIT[26:26]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [859].HIT[27:27]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [860].HIT[28:28]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [861].HIT[29:29]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [862].HIT[30:30]	1
0xBB009F50	VLAN_IVC_ENTRY_INDICATION [863].HIT[31:31]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [864].HIT[0:0]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [865].HIT[1:1]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [866].HIT[2:2]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [867].HIT[3:3]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [868].HIT[4:4]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [869].HIT[5:5]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [870].HIT[6:6]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [871].HIT[7:7]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [872].HIT[8:8]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [873].HIT[9:9]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [874].HIT[10:10]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [875].HIT[11:11]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [876].HIT[12:12]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [877].HIT[13:13]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [878].HIT[14:14]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [879].HIT[15:15]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [880].HIT[16:16]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [881].HIT[17:17]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [882].HIT[18:18]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [883].HIT[19:19]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [884].HIT[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [885].HIT[21:21]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [886].HIT[22:22]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [887].HIT[23:23]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [888].HIT[24:24]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [889].HIT[25:25]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [890].HIT[26:26]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [891].HIT[27:27]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [892].HIT[28:28]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [893].HIT[29:29]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [894].HIT[30:30]	1
0xBB009F54	VLAN_IVC_ENTRY_INDICATION [895].HIT[31:31]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [896].HIT[0:0]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [897].HIT[1:1]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [898].HIT[2:2]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [899].HIT[3:3]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [900].HIT[4:4]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [901].HIT[5:5]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [902].HIT[6:6]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [903].HIT[7:7]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [904].HIT[8:8]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [905].HIT[9:9]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [906].HIT[10:10]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [907].HIT[11:11]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [908].HIT[12:12]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [909].HIT[13:13]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [910].HIT[14:14]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [911].HIT[15:15]	1
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0xBB009F58	VLAN_IVC_ENTRY_INDICATION [913].HIT[17:17]	1
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0xBB009F58	VLAN_IVC_ENTRY_INDICATION [915].HIT[19:19]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [916].HIT[20:20]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [917].HIT[21:21]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [918].HIT[22:22]	1
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0xBB009F58	VLAN_IVC_ENTRY_INDICATION [920].HIT[24:24]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [921].HIT[25:25]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [922].HIT[26:26]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [923].HIT[27:27]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [924].HIT[28:28]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [925].HIT[29:29]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [926].HIT[30:30]	1
0xBB009F58	VLAN_IVC_ENTRY_INDICATION [927].HIT[31:31]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [928].HIT[0:0]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [929].HIT[1:1]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [930].HIT[2:2]	1

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0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [932].HIT[4:4]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [933].HIT[5:5]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [934].HIT[6:6]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [935].HIT[7:7]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [936].HIT[8:8]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [937].HIT[9:9]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [938].HIT[10:10]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [939].HIT[11:11]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [940].HIT[12:12]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [941].HIT[13:13]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [942].HIT[14:14]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [943].HIT[15:15]	1
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0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [946].HIT[18:18]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [947].HIT[19:19]	1
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0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [958].HIT[30:30]	1
0xBB009F5C	VLAN_IVC_ENTRY_INDICATION [959].HIT[31:31]	1
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0xBB009F60	VLAN_IVC_ENTRY_INDICATION [966].HIT[6:6]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [967].HIT[7:7]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [968].HIT[8:8]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [969].HIT[9:9]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [970].HIT[10:10]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [971].HIT[11:11]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [972].HIT[12:12]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [973].HIT[13:13]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [974].HIT[14:14]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [975].HIT[15:15]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [976].HIT[16:16]	1

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0xBB009F60	VLAN_IVC_ENTRY_INDICATION [981].HIT[21:21]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [982].HIT[22:22]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [983].HIT[23:23]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [984].HIT[24:24]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [985].HIT[25:25]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [986].HIT[26:26]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [987].HIT[27:27]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [988].HIT[28:28]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [989].HIT[29:29]	1
0xBB009F60	VLAN_IVC_ENTRY_INDICATION [990].HIT[30:30]	1
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0xBB009F64	VLAN_IVC_ENTRY_INDICATION [995].HIT[3:3]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [996].HIT[4:4]	1
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0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1014].HIT[22:22]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1015].HIT[23:23]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1016].HIT[24:24]	1
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0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1018].HIT[26:26]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1019].HIT[27:27]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1020].HIT[28:28]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1021].HIT[29:29]	1
0xBB009F64	VLAN_IVC_ENTRY_INDICATION [1022].HIT[30:30]	1

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0xBB009F68	VLAN_IVC_ENTRY_INDICATION [1027].HIT[3:3]	1
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0xBB009F68	VLAN_IVC_ENTRY_INDICATION [1033].HIT[9:9]	1
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0xBB009F68	VLAN_IVC_ENTRY_INDICATION [1054].HIT[30:30]	1
0xBB009F68	VLAN_IVC_ENTRY_INDICATION [1055].HIT[31:31]	1
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0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1060].HIT[4:4]	1
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0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1062].HIT[6:6]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1063].HIT[7:7]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1064].HIT[8:8]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1065].HIT[9:9]	1
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0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1067].HIT[11:11]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1068].HIT[12:12]	1

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0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1074].HIT[18:18]	1
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0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1077].HIT[21:21]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1078].HIT[22:22]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1079].HIT[23:23]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1080].HIT[24:24]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1081].HIT[25:25]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1082].HIT[26:26]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1083].HIT[27:27]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1084].HIT[28:28]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1085].HIT[29:29]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1086].HIT[30:30]	1
0xBB009F6C	VLAN_IVC_ENTRY_INDICATION [1087].HIT[31:31]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1088].HIT[0:0]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1089].HIT[1:1]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1090].HIT[2:2]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1091].HIT[3:3]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1092].HIT[4:4]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1093].HIT[5:5]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1094].HIT[6:6]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1095].HIT[7:7]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1096].HIT[8:8]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1097].HIT[9:9]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1098].HIT[10:10]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1099].HIT[11:11]	1
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0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1101].HIT[13:13]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1102].HIT[14:14]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1103].HIT[15:15]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1104].HIT[16:16]	1
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0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1109].HIT[21:21]	1
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0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1111].HIT[23:23]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1112].HIT[24:24]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1113].HIT[25:25]	1
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0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1118].HIT[30:30]	1
0xBB009F70	VLAN_IVC_ENTRY_INDICATION [1119].HIT[31:31]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1120].HIT[0:0]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1121].HIT[1:1]	1
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0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1123].HIT[3:3]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1124].HIT[4:4]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1125].HIT[5:5]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1126].HIT[6:6]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1127].HIT[7:7]	1
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0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1142].HIT[22:22]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1143].HIT[23:23]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1144].HIT[24:24]	1
0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1145].HIT[25:25]	1
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0xBB009F74	VLAN_IVC_ENTRY_INDICATION [1151].HIT[31:31]	1
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0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1159].HIT[7:7]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1163].HIT[11:11]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1164].HIT[12:12]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1165].HIT[13:13]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1166].HIT[14:14]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1167].HIT[15:15]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1168].HIT[16:16]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1169].HIT[17:17]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1170].HIT[18:18]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1171].HIT[19:19]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1172].HIT[20:20]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1173].HIT[21:21]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1174].HIT[22:22]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1175].HIT[23:23]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1176].HIT[24:24]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1177].HIT[25:25]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1178].HIT[26:26]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1179].HIT[27:27]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1180].HIT[28:28]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1181].HIT[29:29]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1182].HIT[30:30]	1
0xBB009F78	VLAN_IVC_ENTRY_INDICATION [1183].HIT[31:31]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1184].HIT[0:0]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1185].HIT[1:1]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1186].HIT[2:2]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1187].HIT[3:3]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1188].HIT[4:4]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1189].HIT[5:5]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1190].HIT[6:6]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1191].HIT[7:7]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1192].HIT[8:8]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1193].HIT[9:9]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1194].HIT[10:10]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1195].HIT[11:11]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1196].HIT[12:12]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1197].HIT[13:13]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1198].HIT[14:14]	1
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0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1201].HIT[17:17]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1202].HIT[18:18]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1203].HIT[19:19]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1204].HIT[20:20]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1205].HIT[21:21]	1
0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1206].HIT[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1211].HIT[27:27]	1
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0xBB009F7C	VLAN_IVC_ENTRY_INDICATION [1215].HIT[31:31]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1216].HIT[0:0]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1217].HIT[1:1]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1218].HIT[2:2]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1219].HIT[3:3]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1220].HIT[4:4]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1221].HIT[5:5]	1
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0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1224].HIT[8:8]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1225].HIT[9:9]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1226].HIT[10:10]	1
0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1227].HIT[11:11]	1
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0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1243].HIT[27:27]	1
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0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1245].HIT[29:29]	1
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0xBB009F80	VLAN_IVC_ENTRY_INDICATION [1247].HIT[31:31]	1
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0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1250].HIT[2:2]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1251].HIT[3:3]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1252].HIT[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1256].HIT[8:8]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1257].HIT[9:9]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1258].HIT[10:10]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1259].HIT[11:11]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1260].HIT[12:12]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1261].HIT[13:13]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1262].HIT[14:14]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1263].HIT[15:15]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1264].HIT[16:16]	1
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0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1266].HIT[18:18]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1267].HIT[19:19]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1268].HIT[20:20]	1
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0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1273].HIT[25:25]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1274].HIT[26:26]	1
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0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1278].HIT[30:30]	1
0xBB009F84	VLAN_IVC_ENTRY_INDICATION [1279].HIT[31:31]	1
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0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1284].HIT[4:4]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1285].HIT[5:5]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1286].HIT[6:6]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1287].HIT[7:7]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1288].HIT[8:8]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1289].HIT[9:9]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1290].HIT[10:10]	1
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0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1297].HIT[17:17]	1
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0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1302].HIT[22:22]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1303].HIT[23:23]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1304].HIT[24:24]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1305].HIT[25:25]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1306].HIT[26:26]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1307].HIT[27:27]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1308].HIT[28:28]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1309].HIT[29:29]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1310].HIT[30:30]	1
0xBB009F88	VLAN_IVC_ENTRY_INDICATION [1311].HIT[31:31]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1312].HIT[0:0]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1313].HIT[1:1]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1314].HIT[2:2]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1315].HIT[3:3]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1316].HIT[4:4]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1317].HIT[5:5]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1318].HIT[6:6]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1319].HIT[7:7]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1320].HIT[8:8]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1321].HIT[9:9]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1322].HIT[10:10]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1323].HIT[11:11]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1324].HIT[12:12]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1325].HIT[13:13]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1326].HIT[14:14]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1327].HIT[15:15]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1328].HIT[16:16]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1329].HIT[17:17]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1330].HIT[18:18]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1331].HIT[19:19]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1332].HIT[20:20]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1333].HIT[21:21]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1334].HIT[22:22]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1335].HIT[23:23]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1336].HIT[24:24]	1
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0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1340].HIT[28:28]	1
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0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1342].HIT[30:30]	1
0xBB009F8C	VLAN_IVC_ENTRY_INDICATION [1343].HIT[31:31]	1
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0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1347].HIT[3:3]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1348].HIT[4:4]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1349].HIT[5:5]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1350].HIT[6:6]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1351].HIT[7:7]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1352].HIT[8:8]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1353].HIT[9:9]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1354].HIT[10:10]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1355].HIT[11:11]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1356].HIT[12:12]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1357].HIT[13:13]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1358].HIT[14:14]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1359].HIT[15:15]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1360].HIT[16:16]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1361].HIT[17:17]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1362].HIT[18:18]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1363].HIT[19:19]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1364].HIT[20:20]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1365].HIT[21:21]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1366].HIT[22:22]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1367].HIT[23:23]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1368].HIT[24:24]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1369].HIT[25:25]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1370].HIT[26:26]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1371].HIT[27:27]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1372].HIT[28:28]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1373].HIT[29:29]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1374].HIT[30:30]	1
0xBB009F90	VLAN_IVC_ENTRY_INDICATION [1375].HIT[31:31]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1376].HIT[0:0]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1377].HIT[1:1]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1378].HIT[2:2]	1
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0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1380].HIT[4:4]	1
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0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1382].HIT[6:6]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1383].HIT[7:7]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1384].HIT[8:8]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1385].HIT[9:9]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1386].HIT[10:10]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1387].HIT[11:11]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1388].HIT[12:12]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1389].HIT[13:13]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1390].HIT[14:14]	1

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0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1395].HIT[19:19]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1396].HIT[20:20]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1397].HIT[21:21]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1398].HIT[22:22]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1399].HIT[23:23]	1
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0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1404].HIT[28:28]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1405].HIT[29:29]	1
0xBB009F94	VLAN_IVC_ENTRY_INDICATION [1406].HIT[30:30]	1
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0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1411].HIT[3:3]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1412].HIT[4:4]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1413].HIT[5:5]	1
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0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1420].HIT[12:12]	1
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0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1424].HIT[16:16]	1
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0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1428].HIT[20:20]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1429].HIT[21:21]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1430].HIT[22:22]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1431].HIT[23:23]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1432].HIT[24:24]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1433].HIT[25:25]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1434].HIT[26:26]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1435].HIT[27:27]	1
0xBB009F98	VLAN_IVC_ENTRY_INDICATION [1436].HIT[28:28]	1

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0xBB009F9C	VLAN_IVC_ENTRY_INDICATION [1441].HIT[1:1]	1
0xBB009F9C	VLAN_IVC_ENTRY_INDICATION [1442].HIT[2:2]	1
0xBB009F9C	VLAN_IVC_ENTRY_INDICATION [1443].HIT[3:3]	1
0xBB009F9C	VLAN_IVC_ENTRY_INDICATION [1444].HIT[4:4]	1
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0xBB009F9C	VLAN_IVC_ENTRY_INDICATION [1447].HIT[7:7]	1
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0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1473].HIT[1:1]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1474].HIT[2:2]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1475].HIT[3:3]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1476].HIT[4:4]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1477].HIT[5:5]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1478].HIT[6:6]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1479].HIT[7:7]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1480].HIT[8:8]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1481].HIT[9:9]	1
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<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1486].HIT[14:14]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1487].HIT[15:15]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1488].HIT[16:16]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1489].HIT[17:17]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1490].HIT[18:18]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1491].HIT[19:19]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1492].HIT[20:20]	1
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0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1502].HIT[30:30]	1
0xBB009FA0	VLAN_IVC_ENTRY_INDICATION [1503].HIT[31:31]	1
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0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1506].HIT[2:2]	1
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0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1508].HIT[4:4]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1509].HIT[5:5]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1510].HIT[6:6]	1
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0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1514].HIT[10:10]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1515].HIT[11:11]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1516].HIT[12:12]	1
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0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1526].HIT[22:22]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1527].HIT[23:23]	1
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0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1533].HIT[29:29]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1534].HIT[30:30]	1
0xBB009FA4	VLAN_IVC_ENTRY_INDICATION [1535].HIT[31:31]	1
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0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1538].HIT[2:2]	1
0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1539].HIT[3:3]	1
0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1540].HIT[4:4]	1
0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1541].HIT[5:5]	1
0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1542].HIT[6:6]	1
0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1543].HIT[7:7]	1
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0xBB009FA8	VLAN_IVC_ENTRY_INDICATION [1546].HIT[10:10]	1
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0xBB009FB0	VLAN_IVC_ENTRY_INDICATION [1603].HIT[3:3]	1
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0xBB009FB0	VLAN_IVC_ENTRY_INDICATION [1606].HIT[6:6]	1
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0xBB009FB0	VLAN_IVC_ENTRY_INDICATION [1608].HIT[8:8]	1
0xBB009FB0	VLAN_IVC_ENTRY_INDICATION [1609].HIT[9:9]	1
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0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1634].HIT[2:2]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1635].HIT[3:3]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1636].HIT[4:4]	1
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0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1643].HIT[11:11]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1644].HIT[12:12]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1645].HIT[13:13]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1646].HIT[14:14]	1
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0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1648].HIT[16:16]	1
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0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1650].HIT[18:18]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1651].HIT[19:19]	1
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0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1654].HIT[22:22]	1
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0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1656].HIT[24:24]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1657].HIT[25:25]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1658].HIT[26:26]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1659].HIT[27:27]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1660].HIT[28:28]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1661].HIT[29:29]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1662].HIT[30:30]	1
0xBB009FB4	VLAN_IVC_ENTRY_INDICATION [1663].HIT[31:31]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1664].HIT[0:0]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1665].HIT[1:1]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1666].HIT[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1667].HIT[3:3]	1
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0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1669].HIT[5:5]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1670].HIT[6:6]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1671].HIT[7:7]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1672].HIT[8:8]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1673].HIT[9:9]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1674].HIT[10:10]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1675].HIT[11:11]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1676].HIT[12:12]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1677].HIT[13:13]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1678].HIT[14:14]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1679].HIT[15:15]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1680].HIT[16:16]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1681].HIT[17:17]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1682].HIT[18:18]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1683].HIT[19:19]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1684].HIT[20:20]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1685].HIT[21:21]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1686].HIT[22:22]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1687].HIT[23:23]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1688].HIT[24:24]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1689].HIT[25:25]	1
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0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1691].HIT[27:27]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1692].HIT[28:28]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1693].HIT[29:29]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1694].HIT[30:30]	1
0xBB009FB8	VLAN_IVC_ENTRY_INDICATION [1695].HIT[31:31]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1696].HIT[0:0]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1697].HIT[1:1]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1698].HIT[2:2]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1699].HIT[3:3]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1700].HIT[4:4]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1701].HIT[5:5]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1702].HIT[6:6]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1703].HIT[7:7]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1704].HIT[8:8]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1705].HIT[9:9]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1706].HIT[10:10]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1707].HIT[11:11]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1708].HIT[12:12]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1709].HIT[13:13]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1710].HIT[14:14]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1711].HIT[15:15]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1712].HIT[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1715].HIT[19:19]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1716].HIT[20:20]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1717].HIT[21:21]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1718].HIT[22:22]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1719].HIT[23:23]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1720].HIT[24:24]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1721].HIT[25:25]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1722].HIT[26:26]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1723].HIT[27:27]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1724].HIT[28:28]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1725].HIT[29:29]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1726].HIT[30:30]	1
0xBB009FBC	VLAN_IVC_ENTRY_INDICATION [1727].HIT[31:31]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1728].HIT[0:0]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1729].HIT[1:1]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1730].HIT[2:2]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1731].HIT[3:3]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1732].HIT[4:4]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1733].HIT[5:5]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1734].HIT[6:6]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1735].HIT[7:7]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1736].HIT[8:8]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1737].HIT[9:9]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1738].HIT[10:10]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1739].HIT[11:11]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1740].HIT[12:12]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1741].HIT[13:13]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1742].HIT[14:14]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1743].HIT[15:15]	1
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0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1745].HIT[17:17]	1
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0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1747].HIT[19:19]	1
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0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1749].HIT[21:21]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1750].HIT[22:22]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1751].HIT[23:23]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1752].HIT[24:24]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1753].HIT[25:25]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1754].HIT[26:26]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1755].HIT[27:27]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1756].HIT[28:28]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1757].HIT[29:29]	1
0xBB009FC0	VLAN_IVC_ENTRY_INDICATION [1758].HIT[30:30]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1762].HIT[2:2]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1763].HIT[3:3]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1764].HIT[4:4]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1765].HIT[5:5]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1766].HIT[6:6]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1767].HIT[7:7]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1768].HIT[8:8]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1769].HIT[9:9]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1770].HIT[10:10]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1771].HIT[11:11]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1772].HIT[12:12]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1773].HIT[13:13]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1774].HIT[14:14]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1775].HIT[15:15]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1776].HIT[16:16]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1777].HIT[17:17]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1778].HIT[18:18]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1779].HIT[19:19]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1780].HIT[20:20]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1781].HIT[21:21]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1782].HIT[22:22]	1
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0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1784].HIT[24:24]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1785].HIT[25:25]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1786].HIT[26:26]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1787].HIT[27:27]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1788].HIT[28:28]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1789].HIT[29:29]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1790].HIT[30:30]	1
0xBB009FC4	VLAN_IVC_ENTRY_INDICATION [1791].HIT[31:31]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1792].HIT[0:0]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1793].HIT[1:1]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1794].HIT[2:2]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1795].HIT[3:3]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1796].HIT[4:4]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1797].HIT[5:5]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1798].HIT[6:6]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1799].HIT[7:7]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1800].HIT[8:8]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1801].HIT[9:9]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1802].HIT[10:10]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1803].HIT[11:11]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1804].HIT[12:12]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
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0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1808].HIT[16:16]	1
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0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1810].HIT[18:18]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1811].HIT[19:19]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1812].HIT[20:20]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1813].HIT[21:21]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1814].HIT[22:22]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1815].HIT[23:23]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1816].HIT[24:24]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1817].HIT[25:25]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1818].HIT[26:26]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1819].HIT[27:27]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1820].HIT[28:28]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1821].HIT[29:29]	1
0xBB009FC8	VLAN_IVC_ENTRY_INDICATION [1822].HIT[30:30]	1
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0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1824].HIT[0:0]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1825].HIT[1:1]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1826].HIT[2:2]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1827].HIT[3:3]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1828].HIT[4:4]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1829].HIT[5:5]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1830].HIT[6:6]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1831].HIT[7:7]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1832].HIT[8:8]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1833].HIT[9:9]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1834].HIT[10:10]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1835].HIT[11:11]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1836].HIT[12:12]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1837].HIT[13:13]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1838].HIT[14:14]	1
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0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1840].HIT[16:16]	1
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0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1842].HIT[18:18]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1843].HIT[19:19]	1
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0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1845].HIT[21:21]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1846].HIT[22:22]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1847].HIT[23:23]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1848].HIT[24:24]	1
0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1849].HIT[25:25]	1
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0xBB009FCC	VLAN_IVC_ENTRY_INDICATION [1855].HIT[31:31]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1856].HIT[0:0]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1857].HIT[1:1]	1
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0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1859].HIT[3:3]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1860].HIT[4:4]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1861].HIT[5:5]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1862].HIT[6:6]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1863].HIT[7:7]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1864].HIT[8:8]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1865].HIT[9:9]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1866].HIT[10:10]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1867].HIT[11:11]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1868].HIT[12:12]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1869].HIT[13:13]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1870].HIT[14:14]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1871].HIT[15:15]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1872].HIT[16:16]	1
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0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1874].HIT[18:18]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1875].HIT[19:19]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1876].HIT[20:20]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1877].HIT[21:21]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1878].HIT[22:22]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1879].HIT[23:23]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1880].HIT[24:24]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1881].HIT[25:25]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1882].HIT[26:26]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1883].HIT[27:27]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1884].HIT[28:28]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1885].HIT[29:29]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1886].HIT[30:30]	1
0xBB009FD0	VLAN_IVC_ENTRY_INDICATION [1887].HIT[31:31]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1888].HIT[0:0]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1889].HIT[1:1]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1890].HIT[2:2]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1891].HIT[3:3]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1892].HIT[4:4]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1893].HIT[5:5]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1894].HIT[6:6]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1895].HIT[7:7]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1896].HIT[8:8]	1

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0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1903].HIT[15:15]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1904].HIT[16:16]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1905].HIT[17:17]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1906].HIT[18:18]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1907].HIT[19:19]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1908].HIT[20:20]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1909].HIT[21:21]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1910].HIT[22:22]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1911].HIT[23:23]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1912].HIT[24:24]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1913].HIT[25:25]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1914].HIT[26:26]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1915].HIT[27:27]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1916].HIT[28:28]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1917].HIT[29:29]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1918].HIT[30:30]	1
0xBB009FD4	VLAN_IVC_ENTRY_INDICATION [1919].HIT[31:31]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1920].HIT[0:0]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1921].HIT[1:1]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1922].HIT[2:2]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1923].HIT[3:3]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1924].HIT[4:4]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1925].HIT[5:5]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1926].HIT[6:6]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1927].HIT[7:7]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1928].HIT[8:8]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1929].HIT[9:9]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1930].HIT[10:10]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1931].HIT[11:11]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1932].HIT[12:12]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1933].HIT[13:13]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1934].HIT[14:14]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1935].HIT[15:15]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1936].HIT[16:16]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1937].HIT[17:17]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1938].HIT[18:18]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1939].HIT[19:19]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1940].HIT[20:20]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1941].HIT[21:21]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1942].HIT[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1943].HIT[23:23]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1944].HIT[24:24]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1945].HIT[25:25]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1946].HIT[26:26]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1947].HIT[27:27]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1948].HIT[28:28]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1949].HIT[29:29]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1950].HIT[30:30]	1
0xBB009FD8	VLAN_IVC_ENTRY_INDICATION [1951].HIT[31:31]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1952].HIT[0:0]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1953].HIT[1:1]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1954].HIT[2:2]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1955].HIT[3:3]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1956].HIT[4:4]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1957].HIT[5:5]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1958].HIT[6:6]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1959].HIT[7:7]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1960].HIT[8:8]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1961].HIT[9:9]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1962].HIT[10:10]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1963].HIT[11:11]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1964].HIT[12:12]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1965].HIT[13:13]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1966].HIT[14:14]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1967].HIT[15:15]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1968].HIT[16:16]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1969].HIT[17:17]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1970].HIT[18:18]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1971].HIT[19:19]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1972].HIT[20:20]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1973].HIT[21:21]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1974].HIT[22:22]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1975].HIT[23:23]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1976].HIT[24:24]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1977].HIT[25:25]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1978].HIT[26:26]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1979].HIT[27:27]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1980].HIT[28:28]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1981].HIT[29:29]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1982].HIT[30:30]	1
0xBB009FDC	VLAN_IVC_ENTRY_INDICATION [1983].HIT[31:31]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1984].HIT[0:0]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1985].HIT[1:1]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1986].HIT[2:2]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1987].HIT[3:3]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1988].HIT[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1989].HIT[5:5]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1990].HIT[6:6]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1991].HIT[7:7]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1992].HIT[8:8]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1993].HIT[9:9]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1994].HIT[10:10]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1995].HIT[11:11]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1996].HIT[12:12]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1997].HIT[13:13]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1998].HIT[14:14]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [1999].HIT[15:15]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2000].HIT[16:16]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2001].HIT[17:17]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2002].HIT[18:18]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2003].HIT[19:19]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2004].HIT[20:20]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2005].HIT[21:21]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2006].HIT[22:22]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2007].HIT[23:23]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2008].HIT[24:24]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2009].HIT[25:25]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2010].HIT[26:26]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2011].HIT[27:27]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2012].HIT[28:28]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2013].HIT[29:29]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2014].HIT[30:30]	1
0xBB009FE0	VLAN_IVC_ENTRY_INDICATION [2015].HIT[31:31]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2016].HIT[0:0]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2017].HIT[1:1]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2018].HIT[2:2]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2019].HIT[3:3]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2020].HIT[4:4]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2021].HIT[5:5]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2022].HIT[6:6]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2023].HIT[7:7]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2024].HIT[8:8]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2025].HIT[9:9]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2026].HIT[10:10]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2027].HIT[11:11]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2028].HIT[12:12]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2029].HIT[13:13]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2030].HIT[14:14]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2031].HIT[15:15]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2032].HIT[16:16]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2033].HIT[17:17]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2034].HIT[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2035].HIT[19:19]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2036].HIT[20:20]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2037].HIT[21:21]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2038].HIT[22:22]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2039].HIT[23:23]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2040].HIT[24:24]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2041].HIT[25:25]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2042].HIT[26:26]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2043].HIT[27:27]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2044].HIT[28:28]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2045].HIT[29:29]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2046].HIT[30:30]	1
0xBB009FE4	VLAN_IVC_ENTRY_INDICATION [2047].HIT[31:31]	1
0xBB009FE8	VLAN_IVC_ENTRY_INDICATION_AGGR [0].HIT_MSK[31:0]	32
0xBB009FEC	VLAN_IVC_ENTRY_INDICATION_AGGR [1].HIT_MSK[31:0]	32
0xBB0096CC	VLAN_L2TBL_CNVT_CTRL.RESERVED[31:1]	31
0xBB0096CC	VLAN_L2TBL_CNV_T_CTRL.GLB_VID_CNV_T_EN[0:0]	1
0xBB009400	VLAN_PORT_AFT [0].RESERVED[31:4]	28
0xBB009400	VLAN_PORT_AFT [0].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009400	VLAN_PORT_AFT [0].OTAG_TAG_ACCEPT[2:2]	1
0xBB009400	VLAN_PORT_AFT [0].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009400	VLAN_PORT_AFT [0].ITAG_TAG_ACCEPT[0:0]	1
0xBB009404	VLAN_PORT_AFT [1].RESERVED[31:4]	28
0xBB009404	VLAN_PORT_AFT [1].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009404	VLAN_PORT_AFT [1].OTAG_TAG_ACCEPT[2:2]	1
0xBB009404	VLAN_PORT_AFT [1].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009404	VLAN_PORT_AFT [1].ITAG_TAG_ACCEPT[0:0]	1
0xBB009408	VLAN_PORT_AFT [2].RESERVED[31:4]	28
0xBB009408	VLAN_PORT_AFT [2].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009408	VLAN_PORT_AFT [2].OTAG_TAG_ACCEPT[2:2]	1
0xBB009408	VLAN_PORT_AFT [2].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009408	VLAN_PORT_AFT [2].ITAG_TAG_ACCEPT[0:0]	1
0xBB00940C	VLAN_PORT_AFT [3].RESERVED[31:4]	28
0xBB00940C	VLAN_PORT_AFT [3].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00940C	VLAN_PORT_AFT [3].OTAG_TAG_ACCEPT[2:2]	1
0xBB00940C	VLAN_PORT_AFT [3].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00940C	VLAN_PORT_AFT [3].ITAG_TAG_ACCEPT[0:0]	1
0xBB009410	VLAN_PORT_AFT [4].RESERVED[31:4]	28
0xBB009410	VLAN_PORT_AFT [4].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009410	VLAN_PORT_AFT [4].OTAG_TAG_ACCEPT[2:2]	1
0xBB009410	VLAN_PORT_AFT [4].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009410	VLAN_PORT_AFT [4].ITAG_TAG_ACCEPT[0:0]	1
0xBB009414	VLAN_PORT_AFT [5].RESERVED[31:4]	28
0xBB009414	VLAN_PORT_AFT [5].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009414	VLAN_PORT_AFT [5].OTAG_TAG_ACCEPT[2:2]	1
0xBB009414	VLAN_PORT_AFT [5].ITAG_UNTAG_ACCEPT[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009414	VLAN_PORT_AFT [5].ITAG_TAG_ACCEPT[0:0]	1
0xBB009418	VLAN_PORT_AFT [6].RESERVED[31:4]	28
0xBB009418	VLAN_PORT_AFT [6].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009418	VLAN_PORT_AFT [6].OTAG_TAG_ACCEPT[2:2]	1
0xBB009418	VLAN_PORT_AFT [6].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009418	VLAN_PORT_AFT [6].ITAG_TAG_ACCEPT[0:0]	1
0xBB00941C	VLAN_PORT_AFT [7].RESERVED[31:4]	28
0xBB00941C	VLAN_PORT_AFT [7].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00941C	VLAN_PORT_AFT [7].OTAG_TAG_ACCEPT[2:2]	1
0xBB00941C	VLAN_PORT_AFT [7].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00941C	VLAN_PORT_AFT [7].ITAG_TAG_ACCEPT[0:0]	1
0xBB009420	VLAN_PORT_AFT [8].RESERVED[31:4]	28
0xBB009420	VLAN_PORT_AFT [8].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009420	VLAN_PORT_AFT [8].OTAG_TAG_ACCEPT[2:2]	1
0xBB009420	VLAN_PORT_AFT [8].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009420	VLAN_PORT_AFT [8].ITAG_TAG_ACCEPT[0:0]	1
0xBB009424	VLAN_PORT_AFT [9].RESERVED[31:4]	28
0xBB009424	VLAN_PORT_AFT [9].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009424	VLAN_PORT_AFT [9].OTAG_TAG_ACCEPT[2:2]	1
0xBB009424	VLAN_PORT_AFT [9].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009424	VLAN_PORT_AFT [9].ITAG_TAG_ACCEPT[0:0]	1
0xBB009428	VLAN_PORT_AFT [10].RESERVED[31:4]	28
0xBB009428	VLAN_PORT_AFT [10].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009428	VLAN_PORT_AFT [10].OTAG_TAG_ACCEPT[2:2]	1
0xBB009428	VLAN_PORT_AFT [10].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009428	VLAN_PORT_AFT [10].ITAG_TAG_ACCEPT[0:0]	1
0xBB00942C	VLAN_PORT_AFT [11].RESERVED[31:4]	28
0xBB00942C	VLAN_PORT_AFT [11].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00942C	VLAN_PORT_AFT [11].OTAG_TAG_ACCEPT[2:2]	1
0xBB00942C	VLAN_PORT_AFT [11].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00942C	VLAN_PORT_AFT [11].ITAG_TAG_ACCEPT[0:0]	1
0xBB009430	VLAN_PORT_AFT [12].RESERVED[31:4]	28
0xBB009430	VLAN_PORT_AFT [12].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009430	VLAN_PORT_AFT [12].OTAG_TAG_ACCEPT[2:2]	1
0xBB009430	VLAN_PORT_AFT [12].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009430	VLAN_PORT_AFT [12].ITAG_TAG_ACCEPT[0:0]	1
0xBB009434	VLAN_PORT_AFT [13].RESERVED[31:4]	28
0xBB009434	VLAN_PORT_AFT [13].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009434	VLAN_PORT_AFT [13].OTAG_TAG_ACCEPT[2:2]	1
0xBB009434	VLAN_PORT_AFT [13].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009434	VLAN_PORT_AFT [13].ITAG_TAG_ACCEPT[0:0]	1
0xBB009438	VLAN_PORT_AFT [14].RESERVED[31:4]	28
0xBB009438	VLAN_PORT_AFT [14].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009438	VLAN_PORT_AFT [14].OTAG_TAG_ACCEPT[2:2]	1
0xBB009438	VLAN_PORT_AFT [14].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009438	VLAN_PORT_AFT [14].ITAG_TAG_ACCEPT[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00943C	VLAN_PORT_AFT [15].RESERVED[31:4]	28
0xBB00943C	VLAN_PORT_AFT [15].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00943C	VLAN_PORT_AFT [15].OTAG_TAG_ACCEPT[2:2]	1
0xBB00943C	VLAN_PORT_AFT [15].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00943C	VLAN_PORT_AFT [15].ITAG_TAG_ACCEPT[0:0]	1
0xBB009440	VLAN_PORT_AFT [16].RESERVED[31:4]	28
0xBB009440	VLAN_PORT_AFT [16].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009440	VLAN_PORT_AFT [16].OTAG_TAG_ACCEPT[2:2]	1
0xBB009440	VLAN_PORT_AFT [16].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009440	VLAN_PORT_AFT [16].ITAG_TAG_ACCEPT[0:0]	1
0xBB009444	VLAN_PORT_AFT [17].RESERVED[31:4]	28
0xBB009444	VLAN_PORT_AFT [17].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009444	VLAN_PORT_AFT [17].OTAG_TAG_ACCEPT[2:2]	1
0xBB009444	VLAN_PORT_AFT [17].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009444	VLAN_PORT_AFT [17].ITAG_TAG_ACCEPT[0:0]	1
0xBB009448	VLAN_PORT_AFT [18].RESERVED[31:4]	28
0xBB009448	VLAN_PORT_AFT [18].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009448	VLAN_PORT_AFT [18].OTAG_TAG_ACCEPT[2:2]	1
0xBB009448	VLAN_PORT_AFT [18].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009448	VLAN_PORT_AFT [18].ITAG_TAG_ACCEPT[0:0]	1
0xBB00944C	VLAN_PORT_AFT [19].RESERVED[31:4]	28
0xBB00944C	VLAN_PORT_AFT [19].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00944C	VLAN_PORT_AFT [19].OTAG_TAG_ACCEPT[2:2]	1
0xBB00944C	VLAN_PORT_AFT [19].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00944C	VLAN_PORT_AFT [19].ITAG_TAG_ACCEPT[0:0]	1
0xBB009450	VLAN_PORT_AFT [20].RESERVED[31:4]	28
0xBB009450	VLAN_PORT_AFT [20].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009450	VLAN_PORT_AFT [20].OTAG_TAG_ACCEPT[2:2]	1
0xBB009450	VLAN_PORT_AFT [20].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009450	VLAN_PORT_AFT [20].ITAG_TAG_ACCEPT[0:0]	1
0xBB009454	VLAN_PORT_AFT [21].RESERVED[31:4]	28
0xBB009454	VLAN_PORT_AFT [21].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009454	VLAN_PORT_AFT [21].OTAG_TAG_ACCEPT[2:2]	1
0xBB009454	VLAN_PORT_AFT [21].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009454	VLAN_PORT_AFT [21].ITAG_TAG_ACCEPT[0:0]	1
0xBB009458	VLAN_PORT_AFT [22].RESERVED[31:4]	28
0xBB009458	VLAN_PORT_AFT [22].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009458	VLAN_PORT_AFT [22].OTAG_TAG_ACCEPT[2:2]	1
0xBB009458	VLAN_PORT_AFT [22].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009458	VLAN_PORT_AFT [22].ITAG_TAG_ACCEPT[0:0]	1
0xBB00945C	VLAN_PORT_AFT [23].RESERVED[31:4]	28
0xBB00945C	VLAN_PORT_AFT [23].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00945C	VLAN_PORT_AFT [23].OTAG_TAG_ACCEPT[2:2]	1
0xBB00945C	VLAN_PORT_AFT [23].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00945C	VLAN_PORT_AFT [23].ITAG_TAG_ACCEPT[0:0]	1
0xBB009460	VLAN_PORT_AFT [24].RESERVED[31:4]	28

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009460	VLAN_PORT_AFT [24].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009460	VLAN_PORT_AFT [24].OTAG_TAG_ACCEPT[2:2]	1
0xBB009460	VLAN_PORT_AFT [24].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009460	VLAN_PORT_AFT [24].ITAG_TAG_ACCEPT[0:0]	1
0xBB009464	VLAN_PORT_AFT [25].RESERVED[31:4]	28
0xBB009464	VLAN_PORT_AFT [25].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009464	VLAN_PORT_AFT [25].OTAG_TAG_ACCEPT[2:2]	1
0xBB009464	VLAN_PORT_AFT [25].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009464	VLAN_PORT_AFT [25].ITAG_TAG_ACCEPT[0:0]	1
0xBB009468	VLAN_PORT_AFT [26].RESERVED[31:4]	28
0xBB009468	VLAN_PORT_AFT [26].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009468	VLAN_PORT_AFT [26].OTAG_TAG_ACCEPT[2:2]	1
0xBB009468	VLAN_PORT_AFT [26].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009468	VLAN_PORT_AFT [26].ITAG_TAG_ACCEPT[0:0]	1
0xBB00946C	VLAN_PORT_AFT [27].RESERVED[31:4]	28
0xBB00946C	VLAN_PORT_AFT [27].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00946C	VLAN_PORT_AFT [27].OTAG_TAG_ACCEPT[2:2]	1
0xBB00946C	VLAN_PORT_AFT [27].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00946C	VLAN_PORT_AFT [27].ITAG_TAG_ACCEPT[0:0]	1
0xBB009470	VLAN_PORT_AFT [28].RESERVED[31:4]	28
0xBB009470	VLAN_PORT_AFT [28].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009470	VLAN_PORT_AFT [28].OTAG_TAG_ACCEPT[2:2]	1
0xBB009470	VLAN_PORT_AFT [28].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009470	VLAN_PORT_AFT [28].ITAG_TAG_ACCEPT[0:0]	1
0xBB009474	VLAN_PORT_AFT [29].RESERVED[31:4]	28
0xBB009474	VLAN_PORT_AFT [29].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009474	VLAN_PORT_AFT [29].OTAG_TAG_ACCEPT[2:2]	1
0xBB009474	VLAN_PORT_AFT [29].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009474	VLAN_PORT_AFT [29].ITAG_TAG_ACCEPT[0:0]	1
0xBB009478	VLAN_PORT_AFT [30].RESERVED[31:4]	28
0xBB009478	VLAN_PORT_AFT [30].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009478	VLAN_PORT_AFT [30].OTAG_TAG_ACCEPT[2:2]	1
0xBB009478	VLAN_PORT_AFT [30].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009478	VLAN_PORT_AFT [30].ITAG_TAG_ACCEPT[0:0]	1
0xBB00947C	VLAN_PORT_AFT [31].RESERVED[31:4]	28
0xBB00947C	VLAN_PORT_AFT [31].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00947C	VLAN_PORT_AFT [31].OTAG_TAG_ACCEPT[2:2]	1
0xBB00947C	VLAN_PORT_AFT [31].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00947C	VLAN_PORT_AFT [31].ITAG_TAG_ACCEPT[0:0]	1
0xBB009480	VLAN_PORT_AFT [32].RESERVED[31:4]	28
0xBB009480	VLAN_PORT_AFT [32].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009480	VLAN_PORT_AFT [32].OTAG_TAG_ACCEPT[2:2]	1
0xBB009480	VLAN_PORT_AFT [32].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009480	VLAN_PORT_AFT [32].ITAG_TAG_ACCEPT[0:0]	1
0xBB009484	VLAN_PORT_AFT [33].RESERVED[31:4]	28
0xBB009484	VLAN_PORT_AFT [33].OTAG_UNTAG_ACCEPT[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009484	VLAN_PORT_AFT [33].OTAG_TAG_ACCEPT[2:2]	1
0xBB009484	VLAN_PORT_AFT [33].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009484	VLAN_PORT_AFT [33].ITAG_TAG_ACCEPT[0:0]	1
0xBB009488	VLAN_PORT_AFT [34].RESERVED[31:4]	28
0xBB009488	VLAN_PORT_AFT [34].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009488	VLAN_PORT_AFT [34].OTAG_TAG_ACCEPT[2:2]	1
0xBB009488	VLAN_PORT_AFT [34].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009488	VLAN_PORT_AFT [34].ITAG_TAG_ACCEPT[0:0]	1
0xBB00948C	VLAN_PORT_AFT [35].RESERVED[31:4]	28
0xBB00948C	VLAN_PORT_AFT [35].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00948C	VLAN_PORT_AFT [35].OTAG_TAG_ACCEPT[2:2]	1
0xBB00948C	VLAN_PORT_AFT [35].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00948C	VLAN_PORT_AFT [35].ITAG_TAG_ACCEPT[0:0]	1
0xBB009490	VLAN_PORT_AFT [36].RESERVED[31:4]	28
0xBB009490	VLAN_PORT_AFT [36].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009490	VLAN_PORT_AFT [36].OTAG_TAG_ACCEPT[2:2]	1
0xBB009490	VLAN_PORT_AFT [36].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009490	VLAN_PORT_AFT [36].ITAG_TAG_ACCEPT[0:0]	1
0xBB009494	VLAN_PORT_AFT [37].RESERVED[31:4]	28
0xBB009494	VLAN_PORT_AFT [37].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009494	VLAN_PORT_AFT [37].OTAG_TAG_ACCEPT[2:2]	1
0xBB009494	VLAN_PORT_AFT [37].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009494	VLAN_PORT_AFT [37].ITAG_TAG_ACCEPT[0:0]	1
0xBB009498	VLAN_PORT_AFT [38].RESERVED[31:4]	28
0xBB009498	VLAN_PORT_AFT [38].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB009498	VLAN_PORT_AFT [38].OTAG_TAG_ACCEPT[2:2]	1
0xBB009498	VLAN_PORT_AFT [38].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB009498	VLAN_PORT_AFT [38].ITAG_TAG_ACCEPT[0:0]	1
0xBB00949C	VLAN_PORT_AFT [39].RESERVED[31:4]	28
0xBB00949C	VLAN_PORT_AFT [39].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB00949C	VLAN_PORT_AFT [39].OTAG_TAG_ACCEPT[2:2]	1
0xBB00949C	VLAN_PORT_AFT [39].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB00949C	VLAN_PORT_AFT [39].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094A0	VLAN_PORT_AFT [40].RESERVED[31:4]	28
0xBB0094A0	VLAN_PORT_AFT [40].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094A0	VLAN_PORT_AFT [40].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094A0	VLAN_PORT_AFT [40].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094A0	VLAN_PORT_AFT [40].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094A4	VLAN_PORT_AFT [41].RESERVED[31:4]	28
0xBB0094A4	VLAN_PORT_AFT [41].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094A4	VLAN_PORT_AFT [41].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094A4	VLAN_PORT_AFT [41].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094A4	VLAN_PORT_AFT [41].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094A8	VLAN_PORT_AFT [42].RESERVED[31:4]	28
0xBB0094A8	VLAN_PORT_AFT [42].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094A8	VLAN_PORT_AFT [42].OTAG_TAG_ACCEPT[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0094A8	VLAN_PORT_AFT [42].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094A8	VLAN_PORT_AFT [42].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094AC	VLAN_PORT_AFT [43].RESERVED[31:4]	28
0xBB0094AC	VLAN_PORT_AFT [43].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094AC	VLAN_PORT_AFT [43].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094AC	VLAN_PORT_AFT [43].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094AC	VLAN_PORT_AFT [43].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094B0	VLAN_PORT_AFT [44].RESERVED[31:4]	28
0xBB0094B0	VLAN_PORT_AFT [44].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094B0	VLAN_PORT_AFT [44].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094B0	VLAN_PORT_AFT [44].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094B0	VLAN_PORT_AFT [44].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094B4	VLAN_PORT_AFT [45].RESERVED[31:4]	28
0xBB0094B4	VLAN_PORT_AFT [45].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094B4	VLAN_PORT_AFT [45].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094B4	VLAN_PORT_AFT [45].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094B4	VLAN_PORT_AFT [45].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094B8	VLAN_PORT_AFT [46].RESERVED[31:4]	28
0xBB0094B8	VLAN_PORT_AFT [46].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094B8	VLAN_PORT_AFT [46].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094B8	VLAN_PORT_AFT [46].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094B8	VLAN_PORT_AFT [46].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094BC	VLAN_PORT_AFT [47].RESERVED[31:4]	28
0xBB0094BC	VLAN_PORT_AFT [47].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094BC	VLAN_PORT_AFT [47].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094BC	VLAN_PORT_AFT [47].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094BC	VLAN_PORT_AFT [47].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094C0	VLAN_PORT_AFT [48].RESERVED[31:4]	28
0xBB0094C0	VLAN_PORT_AFT [48].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094C0	VLAN_PORT_AFT [48].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094C0	VLAN_PORT_AFT [48].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094C0	VLAN_PORT_AFT [48].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094C4	VLAN_PORT_AFT [49].RESERVED[31:4]	28
0xBB0094C4	VLAN_PORT_AFT [49].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094C4	VLAN_PORT_AFT [49].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094C4	VLAN_PORT_AFT [49].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094C4	VLAN_PORT_AFT [49].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094C8	VLAN_PORT_AFT [50].RESERVED[31:4]	28
0xBB0094C8	VLAN_PORT_AFT [50].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094C8	VLAN_PORT_AFT [50].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094C8	VLAN_PORT_AFT [50].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094C8	VLAN_PORT_AFT [50].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094CC	VLAN_PORT_AFT [51].RESERVED[31:4]	28
0xBB0094CC	VLAN_PORT_AFT [51].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094CC	VLAN_PORT_AFT [51].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094CC	VLAN_PORT_AFT [51].ITAG_UNTAG_ACCEPT[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0094CC	VLAN_PORT_AFT [51].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094D0	VLAN_PORT_AFT [52].RESERVED[31:4]	28
0xBB0094D0	VLAN_PORT_AFT [52].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094D0	VLAN_PORT_AFT [52].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094D0	VLAN_PORT_AFT [52].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094D0	VLAN_PORT_AFT [52].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094D4	VLAN_PORT_AFT [53].RESERVED[31:4]	28
0xBB0094D4	VLAN_PORT_AFT [53].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094D4	VLAN_PORT_AFT [53].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094D4	VLAN_PORT_AFT [53].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094D4	VLAN_PORT_AFT [53].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094D8	VLAN_PORT_AFT [54].RESERVED[31:4]	28
0xBB0094D8	VLAN_PORT_AFT [54].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094D8	VLAN_PORT_AFT [54].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094D8	VLAN_PORT_AFT [54].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094D8	VLAN_PORT_AFT [54].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094DC	VLAN_PORT_AFT [55].RESERVED[31:4]	28
0xBB0094DC	VLAN_PORT_AFT [55].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094DC	VLAN_PORT_AFT [55].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094DC	VLAN_PORT_AFT [55].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094DC	VLAN_PORT_AFT [55].ITAG_TAG_ACCEPT[0:0]	1
0xBB0094E0	VLAN_PORT_AFT [56].RESERVED[31:4]	28
0xBB0094E0	VLAN_PORT_AFT [56].OTAG_UNTAG_ACCEPT[3:3]	1
0xBB0094E0	VLAN_PORT_AFT [56].OTAG_TAG_ACCEPT[2:2]	1
0xBB0094E0	VLAN_PORT_AFT [56].ITAG_UNTAG_ACCEPT[1:1]	1
0xBB0094E0	VLAN_PORT_AFT [56].ITAG_TAG_ACCEPT[0:0]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [0].EGR_FLTR_EN[0:0]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [1].EGR_FLTR_EN[1:1]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [2].EGR_FLTR_EN[2:2]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [3].EGR_FLTR_EN[3:3]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [4].EGR_FLTR_EN[4:4]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [5].EGR_FLTR_EN[5:5]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [6].EGR_FLTR_EN[6:6]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [7].EGR_FLTR_EN[7:7]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [8].EGR_FLTR_EN[8:8]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [9].EGR_FLTR_EN[9:9]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [10].EGR_FLTR_EN[10:10]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [11].EGR_FLTR_EN[11:11]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [12].EGR_FLTR_EN[12:12]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [13].EGR_FLTR_EN[13:13]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [14].EGR_FLTR_EN[14:14]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [15].EGR_FLTR_EN[15:15]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [16].EGR_FLTR_EN[16:16]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [17].EGR_FLTR_EN[17:17]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [18].EGR_FLTR_EN[18:18]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [19].EGR_FLTR_EN[19:19]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0096C4	VLAN_PORT_EGR_FLTR [20].EGR_FLTR_EN[20:20]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [21].EGR_FLTR_EN[21:21]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [22].EGR_FLTR_EN[22:22]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [23].EGR_FLTR_EN[23:23]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [24].EGR_FLTR_EN[24:24]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [25].EGR_FLTR_EN[25:25]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [26].EGR_FLTR_EN[26:26]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [27].EGR_FLTR_EN[27:27]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [28].EGR_FLTR_EN[28:28]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [29].EGR_FLTR_EN[29:29]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [30].EGR_FLTR_EN[30:30]	1
0xBB0096C4	VLAN_PORT_EGR_FLTR [31].EGR_FLTR_EN[31:31]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [32].EGR_FLTR_EN[0:0]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [33].EGR_FLTR_EN[1:1]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [34].EGR_FLTR_EN[2:2]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [35].EGR_FLTR_EN[3:3]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [36].EGR_FLTR_EN[4:4]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [37].EGR_FLTR_EN[5:5]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [38].EGR_FLTR_EN[6:6]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [39].EGR_FLTR_EN[7:7]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [40].EGR_FLTR_EN[8:8]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [41].EGR_FLTR_EN[9:9]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [42].EGR_FLTR_EN[10:10]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [43].EGR_FLTR_EN[11:11]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [44].EGR_FLTR_EN[12:12]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [45].EGR_FLTR_EN[13:13]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [46].EGR_FLTR_EN[14:14]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [47].EGR_FLTR_EN[15:15]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [48].EGR_FLTR_EN[16:16]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [49].EGR_FLTR_EN[17:17]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [50].EGR_FLTR_EN[18:18]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [51].EGR_FLTR_EN[19:19]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [52].EGR_FLTR_EN[20:20]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [53].EGR_FLTR_EN[21:21]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [54].EGR_FLTR_EN[22:22]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [55].EGR_FLTR_EN[23:23]	1
0xBB0096C8	VLAN_PORT_EGR_FLTR [56].EGR_FLTR_EN[24:24]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [0].ETPID_CMP[0:0]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [1].ETPID_CMP[1:1]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [2].ETPID_CMP[2:2]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [3].ETPID_CMP[3:3]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [4].ETPID_CMP[4:4]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [5].ETPID_CMP[5:5]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [6].ETPID_CMP[6:6]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [7].ETPID_CMP[7:7]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [8].ETPID_CMP[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [9].ETPID_CMP[9:9]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [10].ETPID_CMP[10:10]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [11].ETPID_CMP[11:11]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [12].ETPID_CMP[12:12]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [13].ETPID_CMP[13:13]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [14].ETPID_CMP[14:14]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [15].ETPID_CMP[15:15]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [16].ETPID_CMP[16:16]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [17].ETPID_CMP[17:17]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [18].ETPID_CMP[18:18]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [19].ETPID_CMP[19:19]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [20].ETPID_CMP[20:20]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [21].ETPID_CMP[21:21]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [22].ETPID_CMP[22:22]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [23].ETPID_CMP[23:23]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [24].ETPID_CMP[24:24]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [25].ETPID_CMP[25:25]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [26].ETPID_CMP[26:26]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [27].ETPID_CMP[27:27]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [28].ETPID_CMP[28:28]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [29].ETPID_CMP[29:29]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [30].ETPID_CMP[30:30]	1
0xBB000520	VLAN_PORT_ETAG_TPID_CMP [31].ETPID_CMP[31:31]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [32].ETPID_CMP[0:0]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [33].ETPID_CMP[1:1]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [34].ETPID_CMP[2:2]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [35].ETPID_CMP[3:3]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [36].ETPID_CMP[4:4]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [37].ETPID_CMP[5:5]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [38].ETPID_CMP[6:6]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [39].ETPID_CMP[7:7]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [40].ETPID_CMP[8:8]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [41].ETPID_CMP[9:9]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [42].ETPID_CMP[10:10]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [43].ETPID_CMP[11:11]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [44].ETPID_CMP[12:12]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [45].ETPID_CMP[13:13]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [46].ETPID_CMP[14:14]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [47].ETPID_CMP[15:15]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [48].ETPID_CMP[16:16]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [49].ETPID_CMP[17:17]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [50].ETPID_CMP[18:18]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [51].ETPID_CMP[19:19]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [52].ETPID_CMP[20:20]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [53].ETPID_CMP[21:21]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [54].ETPID_CMP[22:22]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [55].ETPID_CMP[23:23]	1
0xBB000524	VLAN_PORT_ETAG_TPID_CMP [56].ETPID_CMP[24:24]	1
0xBB004B44	VLAN_PORT_EVC_CTRL [0].RESERVED[31:5]	27
0xBB004B44	VLAN_PORT_EVC_CTRL [0].VID_RANGE_SET_IDX[4:3]	2
0xBB004B44	VLAN_PORT_EVC_CTRL [0].EVC_EN[2:2]	1
0xBB004B44	VLAN_PORT_EVC_CTRL [0].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B44	VLAN_PORT_EVC_CTRL [0].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B48	VLAN_PORT_EVC_CTRL [1].RESERVED[31:5]	27
0xBB004B48	VLAN_PORT_EVC_CTRL [1].VID_RANGE_SET_IDX[4:3]	2
0xBB004B48	VLAN_PORT_EVC_CTRL [1].EVC_EN[2:2]	1
0xBB004B48	VLAN_PORT_EVC_CTRL [1].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B48	VLAN_PORT_EVC_CTRL [1].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B4C	VLAN_PORT_EVC_CTRL [2].RESERVED[31:5]	27
0xBB004B4C	VLAN_PORT_EVC_CTRL [2].VID_RANGE_SET_IDX[4:3]	2
0xBB004B4C	VLAN_PORT_EVC_CTRL [2].EVC_EN[2:2]	1
0xBB004B4C	VLAN_PORT_EVC_CTRL [2].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B4C	VLAN_PORT_EVC_CTRL [2].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B50	VLAN_PORT_EVC_CTRL [3].RESERVED[31:5]	27
0xBB004B50	VLAN_PORT_EVC_CTRL [3].VID_RANGE_SET_IDX[4:3]	2
0xBB004B50	VLAN_PORT_EVC_CTRL [3].EVC_EN[2:2]	1
0xBB004B50	VLAN_PORT_EVC_CTRL [3].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B50	VLAN_PORT_EVC_CTRL [3].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B54	VLAN_PORT_EVC_CTRL [4].RESERVED[31:5]	27
0xBB004B54	VLAN_PORT_EVC_CTRL [4].VID_RANGE_SET_IDX[4:3]	2
0xBB004B54	VLAN_PORT_EVC_CTRL [4].EVC_EN[2:2]	1
0xBB004B54	VLAN_PORT_EVC_CTRL [4].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B54	VLAN_PORT_EVC_CTRL [4].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B58	VLAN_PORT_EVC_CTRL [5].RESERVED[31:5]	27
0xBB004B58	VLAN_PORT_EVC_CTRL [5].VID_RANGE_SET_IDX[4:3]	2
0xBB004B58	VLAN_PORT_EVC_CTRL [5].EVC_EN[2:2]	1
0xBB004B58	VLAN_PORT_EVC_CTRL [5].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B58	VLAN_PORT_EVC_CTRL [5].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B5C	VLAN_PORT_EVC_CTRL [6].RESERVED[31:5]	27
0xBB004B5C	VLAN_PORT_EVC_CTRL [6].VID_RANGE_SET_IDX[4:3]	2
0xBB004B5C	VLAN_PORT_EVC_CTRL [6].EVC_EN[2:2]	1
0xBB004B5C	VLAN_PORT_EVC_CTRL [6].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B5C	VLAN_PORT_EVC_CTRL [6].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B60	VLAN_PORT_EVC_CTRL [7].RESERVED[31:5]	27
0xBB004B60	VLAN_PORT_EVC_CTRL [7].VID_RANGE_SET_IDX[4:3]	2
0xBB004B60	VLAN_PORT_EVC_CTRL [7].EVC_EN[2:2]	1
0xBB004B60	VLAN_PORT_EVC_CTRL [7].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B60	VLAN_PORT_EVC_CTRL [7].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B64	VLAN_PORT_EVC_CTRL [8].RESERVED[31:5]	27
0xBB004B64	VLAN_PORT_EVC_CTRL [8].VID_RANGE_SET_IDX[4:3]	2
0xBB004B64	VLAN_PORT_EVC_CTRL [8].EVC_EN[2:2]	1
0xBB004B64	VLAN_PORT_EVC_CTRL [8].OTAG_LU_MIS_DROP[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004B64	VLAN_PORT_EVC_CTRL [8].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B68	VLAN_PORT_EVC_CTRL [9].RESERVED[31:5]	27
0xBB004B68	VLAN_PORT_EVC_CTRL [9].VID_RANGE_SET_IDX[4:3]	2
0xBB004B68	VLAN_PORT_EVC_CTRL [9].EVC_EN[2:2]	1
0xBB004B68	VLAN_PORT_EVC_CTRL [9].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B68	VLAN_PORT_EVC_CTRL [9].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B6C	VLAN_PORT_EVC_CTRL [10].RESERVED[31:5]	27
0xBB004B6C	VLAN_PORT_EVC_CTRL [10].VID_RANGE_SET_IDX[4:3]	2
0xBB004B6C	VLAN_PORT_EVC_CTRL [10].EVC_EN[2:2]	1
0xBB004B6C	VLAN_PORT_EVC_CTRL [10].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B6C	VLAN_PORT_EVC_CTRL [10].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B70	VLAN_PORT_EVC_CTRL [11].RESERVED[31:5]	27
0xBB004B70	VLAN_PORT_EVC_CTRL [11].VID_RANGE_SET_IDX[4:3]	2
0xBB004B70	VLAN_PORT_EVC_CTRL [11].EVC_EN[2:2]	1
0xBB004B70	VLAN_PORT_EVC_CTRL [11].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B70	VLAN_PORT_EVC_CTRL [11].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B74	VLAN_PORT_EVC_CTRL [12].RESERVED[31:5]	27
0xBB004B74	VLAN_PORT_EVC_CTRL [12].VID_RANGE_SET_IDX[4:3]	2
0xBB004B74	VLAN_PORT_EVC_CTRL [12].EVC_EN[2:2]	1
0xBB004B74	VLAN_PORT_EVC_CTRL [12].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B74	VLAN_PORT_EVC_CTRL [12].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B78	VLAN_PORT_EVC_CTRL [13].RESERVED[31:5]	27
0xBB004B78	VLAN_PORT_EVC_CTRL [13].VID_RANGE_SET_IDX[4:3]	2
0xBB004B78	VLAN_PORT_EVC_CTRL [13].EVC_EN[2:2]	1
0xBB004B78	VLAN_PORT_EVC_CTRL [13].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B78	VLAN_PORT_EVC_CTRL [13].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B7C	VLAN_PORT_EVC_CTRL [14].RESERVED[31:5]	27
0xBB004B7C	VLAN_PORT_EVC_CTRL [14].VID_RANGE_SET_IDX[4:3]	2
0xBB004B7C	VLAN_PORT_EVC_CTRL [14].EVC_EN[2:2]	1
0xBB004B7C	VLAN_PORT_EVC_CTRL [14].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B7C	VLAN_PORT_EVC_CTRL [14].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B80	VLAN_PORT_EVC_CTRL [15].RESERVED[31:5]	27
0xBB004B80	VLAN_PORT_EVC_CTRL [15].VID_RANGE_SET_IDX[4:3]	2
0xBB004B80	VLAN_PORT_EVC_CTRL [15].EVC_EN[2:2]	1
0xBB004B80	VLAN_PORT_EVC_CTRL [15].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B80	VLAN_PORT_EVC_CTRL [15].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B84	VLAN_PORT_EVC_CTRL [16].RESERVED[31:5]	27
0xBB004B84	VLAN_PORT_EVC_CTRL [16].VID_RANGE_SET_IDX[4:3]	2
0xBB004B84	VLAN_PORT_EVC_CTRL [16].EVC_EN[2:2]	1
0xBB004B84	VLAN_PORT_EVC_CTRL [16].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B84	VLAN_PORT_EVC_CTRL [16].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B88	VLAN_PORT_EVC_CTRL [17].RESERVED[31:5]	27
0xBB004B88	VLAN_PORT_EVC_CTRL [17].VID_RANGE_SET_IDX[4:3]	2
0xBB004B88	VLAN_PORT_EVC_CTRL [17].EVC_EN[2:2]	1
0xBB004B88	VLAN_PORT_EVC_CTRL [17].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B88	VLAN_PORT_EVC_CTRL [17].ITAG_LU_MIS_DROP[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004B8C	VLAN_PORT_EVC_CTRL [18].RESERVED[31:5]	27
0xBB004B8C	VLAN_PORT_EVC_CTRL [18].VID_RANGE_SET_IDX[4:3]	2
0xBB004B8C	VLAN_PORT_EVC_CTRL [18].EVC_EN[2:2]	1
0xBB004B8C	VLAN_PORT_EVC_CTRL [18].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B8C	VLAN_PORT_EVC_CTRL [18].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B90	VLAN_PORT_EVC_CTRL [19].RESERVED[31:5]	27
0xBB004B90	VLAN_PORT_EVC_CTRL [19].VID_RANGE_SET_IDX[4:3]	2
0xBB004B90	VLAN_PORT_EVC_CTRL [19].EVC_EN[2:2]	1
0xBB004B90	VLAN_PORT_EVC_CTRL [19].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B90	VLAN_PORT_EVC_CTRL [19].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B94	VLAN_PORT_EVC_CTRL [20].RESERVED[31:5]	27
0xBB004B94	VLAN_PORT_EVC_CTRL [20].VID_RANGE_SET_IDX[4:3]	2
0xBB004B94	VLAN_PORT_EVC_CTRL [20].EVC_EN[2:2]	1
0xBB004B94	VLAN_PORT_EVC_CTRL [20].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B94	VLAN_PORT_EVC_CTRL [20].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B98	VLAN_PORT_EVC_CTRL [21].RESERVED[31:5]	27
0xBB004B98	VLAN_PORT_EVC_CTRL [21].VID_RANGE_SET_IDX[4:3]	2
0xBB004B98	VLAN_PORT_EVC_CTRL [21].EVC_EN[2:2]	1
0xBB004B98	VLAN_PORT_EVC_CTRL [21].OTAG_LU_MIS_DROP[1:1]	1
0xBB004B98	VLAN_PORT_EVC_CTRL [21].ITAG_LU_MIS_DROP[0:0]	1
0xBB004B9C	VLAN_PORT_EVC_CTRL [22].RESERVED[31:5]	27
0xBB004B9C	VLAN_PORT_EVC_CTRL [22].VID_RANGE_SET_IDX[4:3]	2
0xBB004B9C	VLAN_PORT_EVC_CTRL [22].EVC_EN[2:2]	1
0xBB004B9C	VLAN_PORT_EVC_CTRL [22].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BA0	VLAN_PORT_EVC_CTRL [23].RESERVED[31:5]	27
0xBB004BA0	VLAN_PORT_EVC_CTRL [23].VID_RANGE_SET_IDX[4:3]	2
0xBB004BA0	VLAN_PORT_EVC_CTRL [23].EVC_EN[2:2]	1
0xBB004BA0	VLAN_PORT_EVC_CTRL [23].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BA0	VLAN_PORT_EVC_CTRL [23].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BA4	VLAN_PORT_EVC_CTRL [24].RESERVED[31:5]	27
0xBB004BA4	VLAN_PORT_EVC_CTRL [24].VID_RANGE_SET_IDX[4:3]	2
0xBB004BA4	VLAN_PORT_EVC_CTRL [24].EVC_EN[2:2]	1
0xBB004BA4	VLAN_PORT_EVC_CTRL [24].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BA4	VLAN_PORT_EVC_CTRL [24].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BA8	VLAN_PORT_EVC_CTRL [25].RESERVED[31:5]	27
0xBB004BA8	VLAN_PORT_EVC_CTRL [25].VID_RANGE_SET_IDX[4:3]	2
0xBB004BA8	VLAN_PORT_EVC_CTRL [25].EVC_EN[2:2]	1
0xBB004BA8	VLAN_PORT_EVC_CTRL [25].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BA8	VLAN_PORT_EVC_CTRL [25].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BAC	VLAN_PORT_EVC_CTRL [26].RESERVED[31:5]	27
0xBB004BAC	VLAN_PORT_EVC_CTRL [26].VID_RANGE_SET_IDX[4:3]	2
0xBB004BAC	VLAN_PORT_EVC_CTRL [26].EVC_EN[2:2]	1
0xBB004BAC	VLAN_PORT_EVC_CTRL [26].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BAC	VLAN_PORT_EVC_CTRL [26].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BB0	VLAN_PORT_EVC_CTRL [27].RESERVED[31:5]	27

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004BB0	VLAN_PORT_EVC_CTRL [27].VID_RANGE_SET_IDX[4:3]	2
0xBB004BB0	VLAN_PORT_EVC_CTRL [27].EVC_EN[2:2]	1
0xBB004BB0	VLAN_PORT_EVC_CTRL [27].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BB0	VLAN_PORT_EVC_CTRL [27].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BB4	VLAN_PORT_EVC_CTRL [28].RESERVED[31:5]	27
0xBB004BB4	VLAN_PORT_EVC_CTRL [28].VID_RANGE_SET_IDX[4:3]	2
0xBB004BB4	VLAN_PORT_EVC_CTRL [28].EVC_EN[2:2]	1
0xBB004BB4	VLAN_PORT_EVC_CTRL [28].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BB4	VLAN_PORT_EVC_CTRL [28].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BB8	VLAN_PORT_EVC_CTRL [29].RESERVED[31:5]	27
0xBB004BB8	VLAN_PORT_EVC_CTRL [29].VID_RANGE_SET_IDX[4:3]	2
0xBB004BB8	VLAN_PORT_EVC_CTRL [29].EVC_EN[2:2]	1
0xBB004BB8	VLAN_PORT_EVC_CTRL [29].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BB8	VLAN_PORT_EVC_CTRL [29].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BBC	VLAN_PORT_EVC_CTRL [30].RESERVED[31:5]	27
0xBB004BBC	VLAN_PORT_EVC_CTRL [30].VID_RANGE_SET_IDX[4:3]	2
0xBB004BBC	VLAN_PORT_EVC_CTRL [30].EVC_EN[2:2]	1
0xBB004BBC	VLAN_PORT_EVC_CTRL [30].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BBC	VLAN_PORT_EVC_CTRL [30].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BC0	VLAN_PORT_EVC_CTRL [31].RESERVED[31:5]	27
0xBB004BC0	VLAN_PORT_EVC_CTRL [31].VID_RANGE_SET_IDX[4:3]	2
0xBB004BC0	VLAN_PORT_EVC_CTRL [31].EVC_EN[2:2]	1
0xBB004BC0	VLAN_PORT_EVC_CTRL [31].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BC0	VLAN_PORT_EVC_CTRL [31].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BC4	VLAN_PORT_EVC_CTRL [32].RESERVED[31:5]	27
0xBB004BC4	VLAN_PORT_EVC_CTRL [32].VID_RANGE_SET_IDX[4:3]	2
0xBB004BC4	VLAN_PORT_EVC_CTRL [32].EVC_EN[2:2]	1
0xBB004BC4	VLAN_PORT_EVC_CTRL [32].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BC4	VLAN_PORT_EVC_CTRL [32].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BC8	VLAN_PORT_EVC_CTRL [33].RESERVED[31:5]	27
0xBB004BC8	VLAN_PORT_EVC_CTRL [33].VID_RANGE_SET_IDX[4:3]	2
0xBB004BC8	VLAN_PORT_EVC_CTRL [33].EVC_EN[2:2]	1
0xBB004BC8	VLAN_PORT_EVC_CTRL [33].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BC8	VLAN_PORT_EVC_CTRL [33].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BCC	VLAN_PORT_EVC_CTRL [34].RESERVED[31:5]	27
0xBB004BCC	VLAN_PORT_EVC_CTRL [34].VID_RANGE_SET_IDX[4:3]	2
0xBB004BCC	VLAN_PORT_EVC_CTRL [34].EVC_EN[2:2]	1
0xBB004BCC	VLAN_PORT_EVC_CTRL [34].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BCC	VLAN_PORT_EVC_CTRL [34].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BD0	VLAN_PORT_EVC_CTRL [35].RESERVED[31:5]	27
0xBB004BD0	VLAN_PORT_EVC_CTRL [35].VID_RANGE_SET_IDX[4:3]	2
0xBB004BD0	VLAN_PORT_EVC_CTRL [35].EVC_EN[2:2]	1
0xBB004BD0	VLAN_PORT_EVC_CTRL [35].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BD0	VLAN_PORT_EVC_CTRL [35].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BD4	VLAN_PORT_EVC_CTRL [36].RESERVED[31:5]	27
0xBB004BD4	VLAN_PORT_EVC_CTRL [36].VID_RANGE_SET_IDX[4:3]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004BD4	VLAN_PORT_EVC_CTRL [36].EVC_EN[2:2]	1
0xBB004BD4	VLAN_PORT_EVC_CTRL [36].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BD4	VLAN_PORT_EVC_CTRL [36].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BD8	VLAN_PORT_EVC_CTRL [37].RESERVED[31:5]	27
0xBB004BD8	VLAN_PORT_EVC_CTRL [37].VID_RANGE_SET_IDX[4:3]	2
0xBB004BD8	VLAN_PORT_EVC_CTRL [37].EVC_EN[2:2]	1
0xBB004BD8	VLAN_PORT_EVC_CTRL [37].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BD8	VLAN_PORT_EVC_CTRL [37].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BDC	VLAN_PORT_EVC_CTRL [38].RESERVED[31:5]	27
0xBB004BDC	VLAN_PORT_EVC_CTRL [38].VID_RANGE_SET_IDX[4:3]	2
0xBB004BDC	VLAN_PORT_EVC_CTRL [38].EVC_EN[2:2]	1
0xBB004BDC	VLAN_PORT_EVC_CTRL [38].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BDC	VLAN_PORT_EVC_CTRL [38].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BE0	VLAN_PORT_EVC_CTRL [39].RESERVED[31:5]	27
0xBB004BE0	VLAN_PORT_EVC_CTRL [39].VID_RANGE_SET_IDX[4:3]	2
0xBB004BE0	VLAN_PORT_EVC_CTRL [39].EVC_EN[2:2]	1
0xBB004BE0	VLAN_PORT_EVC_CTRL [39].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BE0	VLAN_PORT_EVC_CTRL [39].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BE4	VLAN_PORT_EVC_CTRL [40].RESERVED[31:5]	27
0xBB004BE4	VLAN_PORT_EVC_CTRL [40].VID_RANGE_SET_IDX[4:3]	2
0xBB004BE4	VLAN_PORT_EVC_CTRL [40].EVC_EN[2:2]	1
0xBB004BE4	VLAN_PORT_EVC_CTRL [40].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BE4	VLAN_PORT_EVC_CTRL [40].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BE8	VLAN_PORT_EVC_CTRL [41].RESERVED[31:5]	27
0xBB004BE8	VLAN_PORT_EVC_CTRL [41].VID_RANGE_SET_IDX[4:3]	2
0xBB004BE8	VLAN_PORT_EVC_CTRL [41].EVC_EN[2:2]	1
0xBB004BE8	VLAN_PORT_EVC_CTRL [41].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BE8	VLAN_PORT_EVC_CTRL [41].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BEC	VLAN_PORT_EVC_CTRL [42].RESERVED[31:5]	27
0xBB004BEC	VLAN_PORT_EVC_CTRL [42].VID_RANGE_SET_IDX[4:3]	2
0xBB004BEC	VLAN_PORT_EVC_CTRL [42].EVC_EN[2:2]	1
0xBB004BEC	VLAN_PORT_EVC_CTRL [42].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BEC	VLAN_PORT_EVC_CTRL [42].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BF0	VLAN_PORT_EVC_CTRL [43].RESERVED[31:5]	27
0xBB004BF0	VLAN_PORT_EVC_CTRL [43].VID_RANGE_SET_IDX[4:3]	2
0xBB004BF0	VLAN_PORT_EVC_CTRL [43].EVC_EN[2:2]	1
0xBB004BF0	VLAN_PORT_EVC_CTRL [43].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BF0	VLAN_PORT_EVC_CTRL [43].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BF4	VLAN_PORT_EVC_CTRL [44].RESERVED[31:5]	27
0xBB004BF4	VLAN_PORT_EVC_CTRL [44].VID_RANGE_SET_IDX[4:3]	2
0xBB004BF4	VLAN_PORT_EVC_CTRL [44].EVC_EN[2:2]	1
0xBB004BF4	VLAN_PORT_EVC_CTRL [44].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BF4	VLAN_PORT_EVC_CTRL [44].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BF8	VLAN_PORT_EVC_CTRL [45].RESERVED[31:5]	27
0xBB004BF8	VLAN_PORT_EVC_CTRL [45].VID_RANGE_SET_IDX[4:3]	2
0xBB004BF8	VLAN_PORT_EVC_CTRL [45].EVC_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004BF8	VLAN_PORT_EVC_CTRL [45].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BF8	VLAN_PORT_EVC_CTRL [45].ITAG_LU_MIS_DROP[0:0]	1
0xBB004BFC	VLAN_PORT_EVC_CTRL [46].RESERVED[31:5]	27
0xBB004BFC	VLAN_PORT_EVC_CTRL [46].VID_RANGE_SET_IDX[4:3]	2
0xBB004BFC	VLAN_PORT_EVC_CTRL [46].EVC_EN[2:2]	1
0xBB004BFC	VLAN_PORT_EVC_CTRL [46].OTAG_LU_MIS_DROP[1:1]	1
0xBB004BFC	VLAN_PORT_EVC_CTRL [46].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C00	VLAN_PORT_EVC_CTRL [47].RESERVED[31:5]	27
0xBB004C00	VLAN_PORT_EVC_CTRL [47].VID_RANGE_SET_IDX[4:3]	2
0xBB004C00	VLAN_PORT_EVC_CTRL [47].EVC_EN[2:2]	1
0xBB004C00	VLAN_PORT_EVC_CTRL [47].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C00	VLAN_PORT_EVC_CTRL [47].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C04	VLAN_PORT_EVC_CTRL [48].RESERVED[31:5]	27
0xBB004C04	VLAN_PORT_EVC_CTRL [48].VID_RANGE_SET_IDX[4:3]	2
0xBB004C04	VLAN_PORT_EVC_CTRL [48].EVC_EN[2:2]	1
0xBB004C04	VLAN_PORT_EVC_CTRL [48].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C04	VLAN_PORT_EVC_CTRL [48].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C08	VLAN_PORT_EVC_CTRL [49].RESERVED[31:5]	27
0xBB004C08	VLAN_PORT_EVC_CTRL [49].VID_RANGE_SET_IDX[4:3]	2
0xBB004C08	VLAN_PORT_EVC_CTRL [49].EVC_EN[2:2]	1
0xBB004C08	VLAN_PORT_EVC_CTRL [49].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C08	VLAN_PORT_EVC_CTRL [49].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C0C	VLAN_PORT_EVC_CTRL [50].RESERVED[31:5]	27
0xBB004C0C	VLAN_PORT_EVC_CTRL [50].VID_RANGE_SET_IDX[4:3]	2
0xBB004C0C	VLAN_PORT_EVC_CTRL [50].EVC_EN[2:2]	1
0xBB004C0C	VLAN_PORT_EVC_CTRL [50].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C0C	VLAN_PORT_EVC_CTRL [50].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C10	VLAN_PORT_EVC_CTRL [51].RESERVED[31:5]	27
0xBB004C10	VLAN_PORT_EVC_CTRL [51].VID_RANGE_SET_IDX[4:3]	2
0xBB004C10	VLAN_PORT_EVC_CTRL [51].EVC_EN[2:2]	1
0xBB004C10	VLAN_PORT_EVC_CTRL [51].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C10	VLAN_PORT_EVC_CTRL [51].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C14	VLAN_PORT_EVC_CTRL [52].RESERVED[31:5]	27
0xBB004C14	VLAN_PORT_EVC_CTRL [52].VID_RANGE_SET_IDX[4:3]	2
0xBB004C14	VLAN_PORT_EVC_CTRL [52].EVC_EN[2:2]	1
0xBB004C14	VLAN_PORT_EVC_CTRL [52].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C14	VLAN_PORT_EVC_CTRL [52].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C18	VLAN_PORT_EVC_CTRL [53].RESERVED[31:5]	27
0xBB004C18	VLAN_PORT_EVC_CTRL [53].VID_RANGE_SET_IDX[4:3]	2
0xBB004C18	VLAN_PORT_EVC_CTRL [53].EVC_EN[2:2]	1
0xBB004C18	VLAN_PORT_EVC_CTRL [53].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C18	VLAN_PORT_EVC_CTRL [53].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C1C	VLAN_PORT_EVC_CTRL [54].RESERVED[31:5]	27
0xBB004C1C	VLAN_PORT_EVC_CTRL [54].VID_RANGE_SET_IDX[4:3]	2
0xBB004C1C	VLAN_PORT_EVC_CTRL [54].EVC_EN[2:2]	1
0xBB004C1C	VLAN_PORT_EVC_CTRL [54].OTAG_LU_MIS_DROP[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004C1C	VLAN_PORT_EVC_CTRL [54].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C20	VLAN_PORT_EVC_CTRL [55].RESERVED[31:5]	27
0xBB004C20	VLAN_PORT_EVC_CTRL [55].VID_RANGE_SET_IDX[4:3]	2
0xBB004C20	VLAN_PORT_EVC_CTRL [55].EVC_EN[2:2]	1
0xBB004C20	VLAN_PORT_EVC_CTRL [55].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C20	VLAN_PORT_EVC_CTRL [55].ITAG_LU_MIS_DROP[0:0]	1
0xBB004C24	VLAN_PORT_EVC_CTRL [56].RESERVED[31:5]	27
0xBB004C24	VLAN_PORT_EVC_CTRL [56].VID_RANGE_SET_IDX[4:3]	2
0xBB004C24	VLAN_PORT_EVC_CTRL [56].EVC_EN[2:2]	1
0xBB004C24	VLAN_PORT_EVC_CTRL [56].OTAG_LU_MIS_DROP[1:1]	1
0xBB004C24	VLAN_PORT_EVC_CTRL [56].ITAG_LU_MIS_DROP[0:0]	1
0xBB0095CC	VLAN_PORT_FWD_CTRL [0].RESERVED[31:4]	28
0xBB0095CC	VLAN_PORT_FWD_CTRL [0].DBL_TAG[3:3]	1
0xBB0095CC	VLAN_PORT_FWD_CTRL [0].OTAG[2:2]	1
0xBB0095CC	VLAN_PORT_FWD_CTRL [0].ITAG[1:1]	1
0xBB0095CC	VLAN_PORT_FWD_CTRL [0].UNTAG[0:0]	1
0xBB0095D0	VLAN_PORT_FWD_CTRL [1].RESERVED[31:4]	28
0xBB0095D0	VLAN_PORT_FWD_CTRL [1].DBL_TAG[3:3]	1
0xBB0095D0	VLAN_PORT_FWD_CTRL [1].OTAG[2:2]	1
0xBB0095D0	VLAN_PORT_FWD_CTRL [1].ITAG[1:1]	1
0xBB0095D0	VLAN_PORT_FWD_CTRL [1].UNTAG[0:0]	1
0xBB0095D4	VLAN_PORT_FWD_CTRL [2].RESERVED[31:4]	28
0xBB0095D4	VLAN_PORT_FWD_CTRL [2].DBL_TAG[3:3]	1
0xBB0095D4	VLAN_PORT_FWD_CTRL [2].OTAG[2:2]	1
0xBB0095D4	VLAN_PORT_FWD_CTRL [2].ITAG[1:1]	1
0xBB0095D4	VLAN_PORT_FWD_CTRL [2].UNTAG[0:0]	1
0xBB0095D8	VLAN_PORT_FWD_CTRL [3].RESERVED[31:4]	28
0xBB0095D8	VLAN_PORT_FWD_CTRL [3].DBL_TAG[3:3]	1
0xBB0095D8	VLAN_PORT_FWD_CTRL [3].OTAG[2:2]	1
0xBB0095D8	VLAN_PORT_FWD_CTRL [3].ITAG[1:1]	1
0xBB0095D8	VLAN_PORT_FWD_CTRL [3].UNTAG[0:0]	1
0xBB0095DC	VLAN_PORT_FWD_CTRL [4].RESERVED[31:4]	28
0xBB0095DC	VLAN_PORT_FWD_CTRL [4].DBL_TAG[3:3]	1
0xBB0095DC	VLAN_PORT_FWD_CTRL [4].OTAG[2:2]	1
0xBB0095DC	VLAN_PORT_FWD_CTRL [4].ITAG[1:1]	1
0xBB0095DC	VLAN_PORT_FWD_CTRL [4].UNTAG[0:0]	1
0xBB0095E0	VLAN_PORT_FWD_CTRL [5].RESERVED[31:4]	28
0xBB0095E0	VLAN_PORT_FWD_CTRL [5].DBL_TAG[3:3]	1
0xBB0095E0	VLAN_PORT_FWD_CTRL [5].OTAG[2:2]	1
0xBB0095E0	VLAN_PORT_FWD_CTRL [5].ITAG[1:1]	1
0xBB0095E0	VLAN_PORT_FWD_CTRL [5].UNTAG[0:0]	1
0xBB0095E4	VLAN_PORT_FWD_CTRL [6].RESERVED[31:4]	28
0xBB0095E4	VLAN_PORT_FWD_CTRL [6].DBL_TAG[3:3]	1
0xBB0095E4	VLAN_PORT_FWD_CTRL [6].OTAG[2:2]	1
0xBB0095E4	VLAN_PORT_FWD_CTRL [6].ITAG[1:1]	1
0xBB0095E4	VLAN_PORT_FWD_CTRL [6].UNTAG[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0095E8	VLAN_PORT_FWD_CTRL [7].RESERVED[31:4]	28
0xBB0095E8	VLAN_PORT_FWD_CTRL [7].DBL_TAG[3:3]	1
0xBB0095E8	VLAN_PORT_FWD_CTRL [7].OTAG[2:2]	1
0xBB0095E8	VLAN_PORT_FWD_CTRL [7].ITAG[1:1]	1
0xBB0095E8	VLAN_PORT_FWD_CTRL [7].UNTAG[0:0]	1
0xBB0095EC	VLAN_PORT_FWD_CTRL [8].RESERVED[31:4]	28
0xBB0095EC	VLAN_PORT_FWD_CTRL [8].DBL_TAG[3:3]	1
0xBB0095EC	VLAN_PORT_FWD_CTRL [8].OTAG[2:2]	1
0xBB0095EC	VLAN_PORT_FWD_CTRL [8].ITAG[1:1]	1
0xBB0095EC	VLAN_PORT_FWD_CTRL [8].UNTAG[0:0]	1
0xBB0095F0	VLAN_PORT_FWD_CTRL [9].RESERVED[31:4]	28
0xBB0095F0	VLAN_PORT_FWD_CTRL [9].DBL_TAG[3:3]	1
0xBB0095F0	VLAN_PORT_FWD_CTRL [9].OTAG[2:2]	1
0xBB0095F0	VLAN_PORT_FWD_CTRL [9].ITAG[1:1]	1
0xBB0095F0	VLAN_PORT_FWD_CTRL [9].UNTAG[0:0]	1
0xBB0095F4	VLAN_PORT_FWD_CTRL [10].RESERVED[31:4]	28
0xBB0095F4	VLAN_PORT_FWD_CTRL [10].DBL_TAG[3:3]	1
0xBB0095F4	VLAN_PORT_FWD_CTRL [10].OTAG[2:2]	1
0xBB0095F4	VLAN_PORT_FWD_CTRL [10].ITAG[1:1]	1
0xBB0095F4	VLAN_PORT_FWD_CTRL [10].UNTAG[0:0]	1
0xBB0095F8	VLAN_PORT_FWD_CTRL [11].RESERVED[31:4]	28
0xBB0095F8	VLAN_PORT_FWD_CTRL [11].DBL_TAG[3:3]	1
0xBB0095F8	VLAN_PORT_FWD_CTRL [11].OTAG[2:2]	1
0xBB0095F8	VLAN_PORT_FWD_CTRL [11].ITAG[1:1]	1
0xBB0095F8	VLAN_PORT_FWD_CTRL [11].UNTAG[0:0]	1
0xBB0095FC	VLAN_PORT_FWD_CTRL [12].RESERVED[31:4]	28
0xBB0095FC	VLAN_PORT_FWD_CTRL [12].DBL_TAG[3:3]	1
0xBB0095FC	VLAN_PORT_FWD_CTRL [12].OTAG[2:2]	1
0xBB0095FC	VLAN_PORT_FWD_CTRL [12].ITAG[1:1]	1
0xBB0095FC	VLAN_PORT_FWD_CTRL [12].UNTAG[0:0]	1
0xBB009600	VLAN_PORT_FWD_CTRL [13].RESERVED[31:4]	28
0xBB009600	VLAN_PORT_FWD_CTRL [13].DBL_TAG[3:3]	1
0xBB009600	VLAN_PORT_FWD_CTRL [13].OTAG[2:2]	1
0xBB009600	VLAN_PORT_FWD_CTRL [13].ITAG[1:1]	1
0xBB009600	VLAN_PORT_FWD_CTRL [13].UNTAG[0:0]	1
0xBB009604	VLAN_PORT_FWD_CTRL [14].RESERVED[31:4]	28
0xBB009604	VLAN_PORT_FWD_CTRL [14].DBL_TAG[3:3]	1
0xBB009604	VLAN_PORT_FWD_CTRL [14].OTAG[2:2]	1
0xBB009604	VLAN_PORT_FWD_CTRL [14].ITAG[1:1]	1
0xBB009604	VLAN_PORT_FWD_CTRL [14].UNTAG[0:0]	1
0xBB009608	VLAN_PORT_FWD_CTRL [15].RESERVED[31:4]	28
0xBB009608	VLAN_PORT_FWD_CTRL [15].DBL_TAG[3:3]	1
0xBB009608	VLAN_PORT_FWD_CTRL [15].OTAG[2:2]	1
0xBB009608	VLAN_PORT_FWD_CTRL [15].ITAG[1:1]	1
0xBB009608	VLAN_PORT_FWD_CTRL [15].UNTAG[0:0]	1
0xBB00960C	VLAN_PORT_FWD_CTRL [16].RESERVED[31:4]	28

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00960C	VLAN_PORT_FWD_CTRL [16].DBL_TAG[3:3]	1
0xBB00960C	VLAN_PORT_FWD_CTRL [16].OTAG[2:2]	1
0xBB00960C	VLAN_PORT_FWD_CTRL [16].ITAG[1:1]	1
0xBB00960C	VLAN_PORT_FWD_CTRL [16].UNTAG[0:0]	1
0xBB009610	VLAN_PORT_FWD_CTRL [17].RESERVED[31:4]	28
0xBB009610	VLAN_PORT_FWD_CTRL [17].DBL_TAG[3:3]	1
0xBB009610	VLAN_PORT_FWD_CTRL [17].OTAG[2:2]	1
0xBB009610	VLAN_PORT_FWD_CTRL [17].ITAG[1:1]	1
0xBB009610	VLAN_PORT_FWD_CTRL [17].UNTAG[0:0]	1
0xBB009614	VLAN_PORT_FWD_CTRL [18].RESERVED[31:4]	28
0xBB009614	VLAN_PORT_FWD_CTRL [18].DBL_TAG[3:3]	1
0xBB009614	VLAN_PORT_FWD_CTRL [18].OTAG[2:2]	1
0xBB009614	VLAN_PORT_FWD_CTRL [18].ITAG[1:1]	1
0xBB009614	VLAN_PORT_FWD_CTRL [18].UNTAG[0:0]	1
0xBB009618	VLAN_PORT_FWD_CTRL [19].RESERVED[31:4]	28
0xBB009618	VLAN_PORT_FWD_CTRL [19].DBL_TAG[3:3]	1
0xBB009618	VLAN_PORT_FWD_CTRL [19].OTAG[2:2]	1
0xBB009618	VLAN_PORT_FWD_CTRL [19].ITAG[1:1]	1
0xBB009618	VLAN_PORT_FWD_CTRL [19].UNTAG[0:0]	1
0xBB00961C	VLAN_PORT_FWD_CTRL [20].RESERVED[31:4]	28
0xBB00961C	VLAN_PORT_FWD_CTRL [20].DBL_TAG[3:3]	1
0xBB00961C	VLAN_PORT_FWD_CTRL [20].OTAG[2:2]	1
0xBB00961C	VLAN_PORT_FWD_CTRL [20].ITAG[1:1]	1
0xBB00961C	VLAN_PORT_FWD_CTRL [20].UNTAG[0:0]	1
0xBB009620	VLAN_PORT_FWD_CTRL [21].RESERVED[31:4]	28
0xBB009620	VLAN_PORT_FWD_CTRL [21].DBL_TAG[3:3]	1
0xBB009620	VLAN_PORT_FWD_CTRL [21].OTAG[2:2]	1
0xBB009620	VLAN_PORT_FWD_CTRL [21].ITAG[1:1]	1
0xBB009620	VLAN_PORT_FWD_CTRL [21].UNTAG[0:0]	1
0xBB009624	VLAN_PORT_FWD_CTRL [22].RESERVED[31:4]	28
0xBB009624	VLAN_PORT_FWD_CTRL [22].DBL_TAG[3:3]	1
0xBB009624	VLAN_PORT_FWD_CTRL [22].OTAG[2:2]	1
0xBB009624	VLAN_PORT_FWD_CTRL [22].ITAG[1:1]	1
0xBB009624	VLAN_PORT_FWD_CTRL [22].UNTAG[0:0]	1
0xBB009628	VLAN_PORT_FWD_CTRL [23].RESERVED[31:4]	28
0xBB009628	VLAN_PORT_FWD_CTRL [23].DBL_TAG[3:3]	1
0xBB009628	VLAN_PORT_FWD_CTRL [23].OTAG[2:2]	1
0xBB009628	VLAN_PORT_FWD_CTRL [23].ITAG[1:1]	1
0xBB009628	VLAN_PORT_FWD_CTRL [23].UNTAG[0:0]	1
0xBB00962C	VLAN_PORT_FWD_CTRL [24].RESERVED[31:4]	28
0xBB00962C	VLAN_PORT_FWD_CTRL [24].DBL_TAG[3:3]	1
0xBB00962C	VLAN_PORT_FWD_CTRL [24].OTAG[2:2]	1
0xBB00962C	VLAN_PORT_FWD_CTRL [24].ITAG[1:1]	1
0xBB00962C	VLAN_PORT_FWD_CTRL [24].UNTAG[0:0]	1
0xBB009630	VLAN_PORT_FWD_CTRL [25].RESERVED[31:4]	28
0xBB009630	VLAN_PORT_FWD_CTRL [25].DBL_TAG[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009630	VLAN_PORT_FWD_CTRL [25].OTAG[2:2]	1
0xBB009630	VLAN_PORT_FWD_CTRL [25].ITAG[1:1]	1
0xBB009630	VLAN_PORT_FWD_CTRL [25].UNTAG[0:0]	1
0xBB009634	VLAN_PORT_FWD_CTRL [26].RESERVED[31:4]	28
0xBB009634	VLAN_PORT_FWD_CTRL [26].DBL_TAG[3:3]	1
0xBB009634	VLAN_PORT_FWD_CTRL [26].OTAG[2:2]	1
0xBB009634	VLAN_PORT_FWD_CTRL [26].ITAG[1:1]	1
0xBB009634	VLAN_PORT_FWD_CTRL [26].UNTAG[0:0]	1
0xBB009638	VLAN_PORT_FWD_CTRL [27].RESERVED[31:4]	28
0xBB009638	VLAN_PORT_FWD_CTRL [27].DBL_TAG[3:3]	1
0xBB009638	VLAN_PORT_FWD_CTRL [27].OTAG[2:2]	1
0xBB009638	VLAN_PORT_FWD_CTRL [27].ITAG[1:1]	1
0xBB009638	VLAN_PORT_FWD_CTRL [27].UNTAG[0:0]	1
0xBB00963C	VLAN_PORT_FWD_CTRL [28].RESERVED[31:4]	28
0xBB00963C	VLAN_PORT_FWD_CTRL [28].DBL_TAG[3:3]	1
0xBB00963C	VLAN_PORT_FWD_CTRL [28].OTAG[2:2]	1
0xBB00963C	VLAN_PORT_FWD_CTRL [28].ITAG[1:1]	1
0xBB00963C	VLAN_PORT_FWD_CTRL [28].UNTAG[0:0]	1
0xBB009640	VLAN_PORT_FWD_CTRL [29].RESERVED[31:4]	28
0xBB009640	VLAN_PORT_FWD_CTRL [29].DBL_TAG[3:3]	1
0xBB009640	VLAN_PORT_FWD_CTRL [29].OTAG[2:2]	1
0xBB009640	VLAN_PORT_FWD_CTRL [29].ITAG[1:1]	1
0xBB009640	VLAN_PORT_FWD_CTRL [29].UNTAG[0:0]	1
0xBB009644	VLAN_PORT_FWD_CTRL [30].RESERVED[31:4]	28
0xBB009644	VLAN_PORT_FWD_CTRL [30].DBL_TAG[3:3]	1
0xBB009644	VLAN_PORT_FWD_CTRL [30].OTAG[2:2]	1
0xBB009644	VLAN_PORT_FWD_CTRL [30].ITAG[1:1]	1
0xBB009644	VLAN_PORT_FWD_CTRL [30].UNTAG[0:0]	1
0xBB009648	VLAN_PORT_FWD_CTRL [31].RESERVED[31:4]	28
0xBB009648	VLAN_PORT_FWD_CTRL [31].DBL_TAG[3:3]	1
0xBB009648	VLAN_PORT_FWD_CTRL [31].OTAG[2:2]	1
0xBB009648	VLAN_PORT_FWD_CTRL [31].ITAG[1:1]	1
0xBB009648	VLAN_PORT_FWD_CTRL [31].UNTAG[0:0]	1
0xBB00964C	VLAN_PORT_FWD_CTRL [32].RESERVED[31:4]	28
0xBB00964C	VLAN_PORT_FWD_CTRL [32].DBL_TAG[3:3]	1
0xBB00964C	VLAN_PORT_FWD_CTRL [32].OTAG[2:2]	1
0xBB00964C	VLAN_PORT_FWD_CTRL [32].ITAG[1:1]	1
0xBB00964C	VLAN_PORT_FWD_CTRL [32].UNTAG[0:0]	1
0xBB009650	VLAN_PORT_FWD_CTRL [33].RESERVED[31:4]	28
0xBB009650	VLAN_PORT_FWD_CTRL [33].DBL_TAG[3:3]	1
0xBB009650	VLAN_PORT_FWD_CTRL [33].OTAG[2:2]	1
0xBB009650	VLAN_PORT_FWD_CTRL [33].ITAG[1:1]	1
0xBB009650	VLAN_PORT_FWD_CTRL [33].UNTAG[0:0]	1
0xBB009654	VLAN_PORT_FWD_CTRL [34].RESERVED[31:4]	28
0xBB009654	VLAN_PORT_FWD_CTRL [34].DBL_TAG[3:3]	1
0xBB009654	VLAN_PORT_FWD_CTRL [34].OTAG[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009654	VLAN_PORT_FWD_CTRL [34].ITAG[1:1]	1
0xBB009654	VLAN_PORT_FWD_CTRL [34].UNTAG[0:0]	1
0xBB009658	VLAN_PORT_FWD_CTRL [35].RESERVED[31:4]	28
0xBB009658	VLAN_PORT_FWD_CTRL [35].DBL_TAG[3:3]	1
0xBB009658	VLAN_PORT_FWD_CTRL [35].OTAG[2:2]	1
0xBB009658	VLAN_PORT_FWD_CTRL [35].ITAG[1:1]	1
0xBB009658	VLAN_PORT_FWD_CTRL [35].UNTAG[0:0]	1
0xBB00965C	VLAN_PORT_FWD_CTRL [36].RESERVED[31:4]	28
0xBB00965C	VLAN_PORT_FWD_CTRL [36].DBL_TAG[3:3]	1
0xBB00965C	VLAN_PORT_FWD_CTRL [36].OTAG[2:2]	1
0xBB00965C	VLAN_PORT_FWD_CTRL [36].ITAG[1:1]	1
0xBB00965C	VLAN_PORT_FWD_CTRL [36].UNTAG[0:0]	1
0xBB009660	VLAN_PORT_FWD_CTRL [37].RESERVED[31:4]	28
0xBB009660	VLAN_PORT_FWD_CTRL [37].DBL_TAG[3:3]	1
0xBB009660	VLAN_PORT_FWD_CTRL [37].OTAG[2:2]	1
0xBB009660	VLAN_PORT_FWD_CTRL [37].ITAG[1:1]	1
0xBB009660	VLAN_PORT_FWD_CTRL [37].UNTAG[0:0]	1
0xBB009664	VLAN_PORT_FWD_CTRL [38].RESERVED[31:4]	28
0xBB009664	VLAN_PORT_FWD_CTRL [38].DBL_TAG[3:3]	1
0xBB009664	VLAN_PORT_FWD_CTRL [38].OTAG[2:2]	1
0xBB009664	VLAN_PORT_FWD_CTRL [38].ITAG[1:1]	1
0xBB009664	VLAN_PORT_FWD_CTRL [38].UNTAG[0:0]	1
0xBB009668	VLAN_PORT_FWD_CTRL [39].RESERVED[31:4]	28
0xBB009668	VLAN_PORT_FWD_CTRL [39].DBL_TAG[3:3]	1
0xBB009668	VLAN_PORT_FWD_CTRL [39].OTAG[2:2]	1
0xBB009668	VLAN_PORT_FWD_CTRL [39].ITAG[1:1]	1
0xBB009668	VLAN_PORT_FWD_CTRL [39].UNTAG[0:0]	1
0xBB00966C	VLAN_PORT_FWD_CTRL [40].RESERVED[31:4]	28
0xBB00966C	VLAN_PORT_FWD_CTRL [40].DBL_TAG[3:3]	1
0xBB00966C	VLAN_PORT_FWD_CTRL [40].OTAG[2:2]	1
0xBB00966C	VLAN_PORT_FWD_CTRL [40].ITAG[1:1]	1
0xBB00966C	VLAN_PORT_FWD_CTRL [40].UNTAG[0:0]	1
0xBB009670	VLAN_PORT_FWD_CTRL [41].RESERVED[31:4]	28
0xBB009670	VLAN_PORT_FWD_CTRL [41].DBL_TAG[3:3]	1
0xBB009670	VLAN_PORT_FWD_CTRL [41].OTAG[2:2]	1
0xBB009670	VLAN_PORT_FWD_CTRL [41].ITAG[1:1]	1
0xBB009670	VLAN_PORT_FWD_CTRL [41].UNTAG[0:0]	1
0xBB009674	VLAN_PORT_FWD_CTRL [42].RESERVED[31:4]	28
0xBB009674	VLAN_PORT_FWD_CTRL [42].DBL_TAG[3:3]	1
0xBB009674	VLAN_PORT_FWD_CTRL [42].OTAG[2:2]	1
0xBB009674	VLAN_PORT_FWD_CTRL [42].ITAG[1:1]	1
0xBB009674	VLAN_PORT_FWD_CTRL [42].UNTAG[0:0]	1
0xBB009678	VLAN_PORT_FWD_CTRL [43].RESERVED[31:4]	28
0xBB009678	VLAN_PORT_FWD_CTRL [43].DBL_TAG[3:3]	1
0xBB009678	VLAN_PORT_FWD_CTRL [43].OTAG[2:2]	1
0xBB009678	VLAN_PORT_FWD_CTRL [43].ITAG[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009678	VLAN_PORT_FWD_CTRL [43].UNTAG[0:0]	1
0xBB00967C	VLAN_PORT_FWD_CTRL [44].RESERVED[31:4]	28
0xBB00967C	VLAN_PORT_FWD_CTRL [44].DBL_TAG[3:3]	1
0xBB00967C	VLAN_PORT_FWD_CTRL [44].OTAG[2:2]	1
0xBB00967C	VLAN_PORT_FWD_CTRL [44].ITAG[1:1]	1
0xBB00967C	VLAN_PORT_FWD_CTRL [44].UNTAG[0:0]	1
0xBB009680	VLAN_PORT_FWD_CTRL [45].RESERVED[31:4]	28
0xBB009680	VLAN_PORT_FWD_CTRL [45].DBL_TAG[3:3]	1
0xBB009680	VLAN_PORT_FWD_CTRL [45].OTAG[2:2]	1
0xBB009680	VLAN_PORT_FWD_CTRL [45].ITAG[1:1]	1
0xBB009680	VLAN_PORT_FWD_CTRL [45].UNTAG[0:0]	1
0xBB009684	VLAN_PORT_FWD_CTRL [46].RESERVED[31:4]	28
0xBB009684	VLAN_PORT_FWD_CTRL [46].DBL_TAG[3:3]	1
0xBB009684	VLAN_PORT_FWD_CTRL [46].OTAG[2:2]	1
0xBB009684	VLAN_PORT_FWD_CTRL [46].ITAG[1:1]	1
0xBB009684	VLAN_PORT_FWD_CTRL [46].UNTAG[0:0]	1
0xBB009688	VLAN_PORT_FWD_CTRL [47].RESERVED[31:4]	28
0xBB009688	VLAN_PORT_FWD_CTRL [47].DBL_TAG[3:3]	1
0xBB009688	VLAN_PORT_FWD_CTRL [47].OTAG[2:2]	1
0xBB009688	VLAN_PORT_FWD_CTRL [47].ITAG[1:1]	1
0xBB009688	VLAN_PORT_FWD_CTRL [47].UNTAG[0:0]	1
0xBB00968C	VLAN_PORT_FWD_CTRL [48].RESERVED[31:4]	28
0xBB00968C	VLAN_PORT_FWD_CTRL [48].DBL_TAG[3:3]	1
0xBB00968C	VLAN_PORT_FWD_CTRL [48].OTAG[2:2]	1
0xBB00968C	VLAN_PORT_FWD_CTRL [48].ITAG[1:1]	1
0xBB00968C	VLAN_PORT_FWD_CTRL [48].UNTAG[0:0]	1
0xBB009690	VLAN_PORT_FWD_CTRL [49].RESERVED[31:4]	28
0xBB009690	VLAN_PORT_FWD_CTRL [49].DBL_TAG[3:3]	1
0xBB009690	VLAN_PORT_FWD_CTRL [49].OTAG[2:2]	1
0xBB009690	VLAN_PORT_FWD_CTRL [49].ITAG[1:1]	1
0xBB009690	VLAN_PORT_FWD_CTRL [49].UNTAG[0:0]	1
0xBB009694	VLAN_PORT_FWD_CTRL [50].RESERVED[31:4]	28
0xBB009694	VLAN_PORT_FWD_CTRL [50].DBL_TAG[3:3]	1
0xBB009694	VLAN_PORT_FWD_CTRL [50].OTAG[2:2]	1
0xBB009694	VLAN_PORT_FWD_CTRL [50].ITAG[1:1]	1
0xBB009694	VLAN_PORT_FWD_CTRL [50].UNTAG[0:0]	1
0xBB009698	VLAN_PORT_FWD_CTRL [51].RESERVED[31:4]	28
0xBB009698	VLAN_PORT_FWD_CTRL [51].DBL_TAG[3:3]	1
0xBB009698	VLAN_PORT_FWD_CTRL [51].OTAG[2:2]	1
0xBB009698	VLAN_PORT_FWD_CTRL [51].ITAG[1:1]	1
0xBB009698	VLAN_PORT_FWD_CTRL [51].UNTAG[0:0]	1
0xBB00969C	VLAN_PORT_FWD_CTRL [52].RESERVED[31:4]	28
0xBB00969C	VLAN_PORT_FWD_CTRL [52].DBL_TAG[3:3]	1
0xBB00969C	VLAN_PORT_FWD_CTRL [52].OTAG[2:2]	1
0xBB00969C	VLAN_PORT_FWD_CTRL [52].ITAG[1:1]	1
0xBB00969C	VLAN_PORT_FWD_CTRL [52].UNTAG[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0096A0	VLAN_PORT_FWD_CTRL [53].RESERVED[31:4]	28
0xBB0096A0	VLAN_PORT_FWD_CTRL [53].DBL_TAG[3:3]	1
0xBB0096A0	VLAN_PORT_FWD_CTRL [53].OTAG[2:2]	1
0xBB0096A0	VLAN_PORT_FWD_CTRL [53].ITAG[1:1]	1
0xBB0096A0	VLAN_PORT_FWD_CTRL [53].UNTAG[0:0]	1
0xBB0096A4	VLAN_PORT_FWD_CTRL [54].RESERVED[31:4]	28
0xBB0096A4	VLAN_PORT_FWD_CTRL [54].DBL_TAG[3:3]	1
0xBB0096A4	VLAN_PORT_FWD_CTRL [54].OTAG[2:2]	1
0xBB0096A4	VLAN_PORT_FWD_CTRL [54].ITAG[1:1]	1
0xBB0096A4	VLAN_PORT_FWD_CTRL [54].UNTAG[0:0]	1
0xBB0096A8	VLAN_PORT_FWD_CTRL [55].RESERVED[31:4]	28
0xBB0096A8	VLAN_PORT_FWD_CTRL [55].DBL_TAG[3:3]	1
0xBB0096A8	VLAN_PORT_FWD_CTRL [55].OTAG[2:2]	1
0xBB0096A8	VLAN_PORT_FWD_CTRL [55].ITAG[1:1]	1
0xBB0096A8	VLAN_PORT_FWD_CTRL [55].UNTAG[0:0]	1
0xBB0096AC	VLAN_PORT_FWD_CTRL [56].RESERVED[31:4]	28
0xBB0096AC	VLAN_PORT_FWD_CTRL [56].DBL_TAG[3:3]	1
0xBB0096AC	VLAN_PORT_FWD_CTRL [56].OTAG[2:2]	1
0xBB0096AC	VLAN_PORT_FWD_CTRL [56].ITAG[1:1]	1
0xBB0096AC	VLAN_PORT_FWD_CTRL [56].UNTAG[0:0]	1
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].RESERVED[31:30]	2
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].SA_LRN_MODE[29:29]	1
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].PRIVATE_VLAN_EN[28:28]	1
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].OPVID_FMT[27:26]	2
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].OPVID[25:14]	12
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].IPVID_FMT[13:12]	2
0xBB0094E8	VLAN_PORT_IGR_CTRL [0].IPVID[11:0]	12
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].RESERVED[31:30]	2
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].SA_LRN_MODE[29:29]	1
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].PRIVATE_VLAN_EN[28:28]	1
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].OPVID_FMT[27:26]	2
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].OPVID[25:14]	12
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].IPVID_FMT[13:12]	2
0xBB0094EC	VLAN_PORT_IGR_CTRL [1].IPVID[11:0]	12
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].RESERVED[31:30]	2
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].SA_LRN_MODE[29:29]	1
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].PRIVATE_VLAN_EN[28:28]	1
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].OPVID_FMT[27:26]	2
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].OPVID[25:14]	12
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].IPVID_FMT[13:12]	2
0xBB0094F0	VLAN_PORT_IGR_CTRL [2].IPVID[11:0]	12
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].RESERVED[31:30]	2
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].SA_LRN_MODE[29:29]	1
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].PRIVATE_VLAN_EN[28:28]	1
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].OPVID_FMT[27:26]	2
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].OPVID[25:14]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].IPVID_FMT[13:12]	2
0xBB0094F4	VLAN_PORT_IGR_CTRL [3].IPVID[11:0]	12
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].RESERVED[31:30]	2
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].SA_LRN_MODE[29:29]	1
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].PRIVATE_VLAN_EN[28:28]	1
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].OPVID_FMT[27:26]	2
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].OPVID[25:14]	12
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].IPVID_FMT[13:12]	2
0xBB0094F8	VLAN_PORT_IGR_CTRL [4].IPVID[11:0]	12
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].RESERVED[31:30]	2
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].SA_LRN_MODE[29:29]	1
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].PRIVATE_VLAN_EN[28:28]	1
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].OPVID_FMT[27:26]	2
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].OPVID[25:14]	12
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].IPVID_FMT[13:12]	2
0xBB0094FC	VLAN_PORT_IGR_CTRL [5].IPVID[11:0]	12
0xBB009500	VLAN_PORT_IGR_CTRL [6].RESERVED[31:30]	2
0xBB009500	VLAN_PORT_IGR_CTRL [6].SA_LRN_MODE[29:29]	1
0xBB009500	VLAN_PORT_IGR_CTRL [6].PRIVATE_VLAN_EN[28:28]	1
0xBB009500	VLAN_PORT_IGR_CTRL [6].OPVID_FMT[27:26]	2
0xBB009500	VLAN_PORT_IGR_CTRL [6].OPVID[25:14]	12
0xBB009500	VLAN_PORT_IGR_CTRL [6].IPVID_FMT[13:12]	2
0xBB009500	VLAN_PORT_IGR_CTRL [6].IPVID[11:0]	12
0xBB009504	VLAN_PORT_IGR_CTRL [7].RESERVED[31:30]	2
0xBB009504	VLAN_PORT_IGR_CTRL [7].SA_LRN_MODE[29:29]	1
0xBB009504	VLAN_PORT_IGR_CTRL [7].PRIVATE_VLAN_EN[28:28]	1
0xBB009504	VLAN_PORT_IGR_CTRL [7].OPVID_FMT[27:26]	2
0xBB009504	VLAN_PORT_IGR_CTRL [7].OPVID[25:14]	12
0xBB009504	VLAN_PORT_IGR_CTRL [7].IPVID_FMT[13:12]	2
0xBB009504	VLAN_PORT_IGR_CTRL [7].IPVID[11:0]	12
0xBB009508	VLAN_PORT_IGR_CTRL [8].RESERVED[31:30]	2
0xBB009508	VLAN_PORT_IGR_CTRL [8].SA_LRN_MODE[29:29]	1
0xBB009508	VLAN_PORT_IGR_CTRL [8].PRIVATE_VLAN_EN[28:28]	1
0xBB009508	VLAN_PORT_IGR_CTRL [8].OPVID_FMT[27:26]	2
0xBB009508	VLAN_PORT_IGR_CTRL [8].OPVID[25:14]	12
0xBB009508	VLAN_PORT_IGR_CTRL [8].IPVID_FMT[13:12]	2
0xBB009508	VLAN_PORT_IGR_CTRL [8].IPVID[11:0]	12
0xBB00950C	VLAN_PORT_IGR_CTRL [9].RESERVED[31:30]	2
0xBB00950C	VLAN_PORT_IGR_CTRL [9].SA_LRN_MODE[29:29]	1
0xBB00950C	VLAN_PORT_IGR_CTRL [9].PRIVATE_VLAN_EN[28:28]	1
0xBB00950C	VLAN_PORT_IGR_CTRL [9].OPVID_FMT[27:26]	2
0xBB00950C	VLAN_PORT_IGR_CTRL [9].OPVID[25:14]	12
0xBB00950C	VLAN_PORT_IGR_CTRL [9].IPVID_FMT[13:12]	2
0xBB00950C	VLAN_PORT_IGR_CTRL [9].IPVID[11:0]	12
0xBB009510	VLAN_PORT_IGR_CTRL [10].RESERVED[31:30]	2
0xBB009510	VLAN_PORT_IGR_CTRL [10].SA_LRN_MODE[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009510	VLAN_PORT_IGR_CTRL [10].PRIVATE_VLAN_EN[28:28]	1
0xBB009510	VLAN_PORT_IGR_CTRL [10].OPVID_FMT[27:26]	2
0xBB009510	VLAN_PORT_IGR_CTRL [10].OPVID[25:14]	12
0xBB009510	VLAN_PORT_IGR_CTRL [10].IPVID_FMT[13:12]	2
0xBB009510	VLAN_PORT_IGR_CTRL [10].IPVID[11:0]	12
0xBB009514	VLAN_PORT_IGR_CTRL [11].RESERVED[31:30]	2
0xBB009514	VLAN_PORT_IGR_CTRL [11].SA_LRN_MODE[29:29]	1
0xBB009514	VLAN_PORT_IGR_CTRL [11].PRIVATE_VLAN_EN[28:28]	1
0xBB009514	VLAN_PORT_IGR_CTRL [11].OPVID_FMT[27:26]	2
0xBB009514	VLAN_PORT_IGR_CTRL [11].OPVID[25:14]	12
0xBB009514	VLAN_PORT_IGR_CTRL [11].IPVID_FMT[13:12]	2
0xBB009514	VLAN_PORT_IGR_CTRL [11].IPVID[11:0]	12
0xBB009518	VLAN_PORT_IGR_CTRL [12].RESERVED[31:30]	2
0xBB009518	VLAN_PORT_IGR_CTRL [12].SA_LRN_MODE[29:29]	1
0xBB009518	VLAN_PORT_IGR_CTRL [12].PRIVATE_VLAN_EN[28:28]	1
0xBB009518	VLAN_PORT_IGR_CTRL [12].OPVID_FMT[27:26]	2
0xBB009518	VLAN_PORT_IGR_CTRL [12].OPVID[25:14]	12
0xBB009518	VLAN_PORT_IGR_CTRL [12].IPVID_FMT[13:12]	2
0xBB009518	VLAN_PORT_IGR_CTRL [12].IPVID[11:0]	12
0xBB00951C	VLAN_PORT_IGR_CTRL [13].RESERVED[31:30]	2
0xBB00951C	VLAN_PORT_IGR_CTRL [13].SA_LRN_MODE[29:29]	1
0xBB00951C	VLAN_PORT_IGR_CTRL [13].PRIVATE_VLAN_EN[28:28]	1
0xBB00951C	VLAN_PORT_IGR_CTRL [13].OPVID_FMT[27:26]	2
0xBB00951C	VLAN_PORT_IGR_CTRL [13].OPVID[25:14]	12
0xBB00951C	VLAN_PORT_IGR_CTRL [13].IPVID_FMT[13:12]	2
0xBB00951C	VLAN_PORT_IGR_CTRL [13].IPVID[11:0]	12
0xBB009520	VLAN_PORT_IGR_CTRL [14].RESERVED[31:30]	2
0xBB009520	VLAN_PORT_IGR_CTRL [14].SA_LRN_MODE[29:29]	1
0xBB009520	VLAN_PORT_IGR_CTRL [14].PRIVATE_VLAN_EN[28:28]	1
0xBB009520	VLAN_PORT_IGR_CTRL [14].OPVID_FMT[27:26]	2
0xBB009520	VLAN_PORT_IGR_CTRL [14].OPVID[25:14]	12
0xBB009520	VLAN_PORT_IGR_CTRL [14].IPVID_FMT[13:12]	2
0xBB009520	VLAN_PORT_IGR_CTRL [14].IPVID[11:0]	12
0xBB009524	VLAN_PORT_IGR_CTRL [15].RESERVED[31:30]	2
0xBB009524	VLAN_PORT_IGR_CTRL [15].SA_LRN_MODE[29:29]	1
0xBB009524	VLAN_PORT_IGR_CTRL [15].PRIVATE_VLAN_EN[28:28]	1
0xBB009524	VLAN_PORT_IGR_CTRL [15].OPVID_FMT[27:26]	2
0xBB009524	VLAN_PORT_IGR_CTRL [15].OPVID[25:14]	12
0xBB009524	VLAN_PORT_IGR_CTRL [15].IPVID_FMT[13:12]	2
0xBB009524	VLAN_PORT_IGR_CTRL [15].IPVID[11:0]	12
0xBB009528	VLAN_PORT_IGR_CTRL [16].RESERVED[31:30]	2
0xBB009528	VLAN_PORT_IGR_CTRL [16].SA_LRN_MODE[29:29]	1
0xBB009528	VLAN_PORT_IGR_CTRL [16].PRIVATE_VLAN_EN[28:28]	1
0xBB009528	VLAN_PORT_IGR_CTRL [16].OPVID_FMT[27:26]	2
0xBB009528	VLAN_PORT_IGR_CTRL [16].OPVID[25:14]	12
0xBB009528	VLAN_PORT_IGR_CTRL [16].IPVID_FMT[13:12]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009528	VLAN_PORT_IGR_CTRL [16].IPVID[11:0]	12
0xBB00952C	VLAN_PORT_IGR_CTRL [17].RESERVED[31:30]	2
0xBB00952C	VLAN_PORT_IGR_CTRL [17].SA_LRN_MODE[29:29]	1
0xBB00952C	VLAN_PORT_IGR_CTRL [17].PRIVATE_VLAN_EN[28:28]	1
0xBB00952C	VLAN_PORT_IGR_CTRL [17].OPVID_FMT[27:26]	2
0xBB00952C	VLAN_PORT_IGR_CTRL [17].OPVID[25:14]	12
0xBB00952C	VLAN_PORT_IGR_CTRL [17].IPVID_FMT[13:12]	2
0xBB00952C	VLAN_PORT_IGR_CTRL [17].IPVID[11:0]	12
0xBB009530	VLAN_PORT_IGR_CTRL [18].RESERVED[31:30]	2
0xBB009530	VLAN_PORT_IGR_CTRL [18].SA_LRN_MODE[29:29]	1
0xBB009530	VLAN_PORT_IGR_CTRL [18].PRIVATE_VLAN_EN[28:28]	1
0xBB009530	VLAN_PORT_IGR_CTRL [18].OPVID_FMT[27:26]	2
0xBB009530	VLAN_PORT_IGR_CTRL [18].OPVID[25:14]	12
0xBB009530	VLAN_PORT_IGR_CTRL [18].IPVID_FMT[13:12]	2
0xBB009530	VLAN_PORT_IGR_CTRL [18].IPVID[11:0]	12
0xBB009534	VLAN_PORT_IGR_CTRL [19].RESERVED[31:30]	2
0xBB009534	VLAN_PORT_IGR_CTRL [19].SA_LRN_MODE[29:29]	1
0xBB009534	VLAN_PORT_IGR_CTRL [19].PRIVATE_VLAN_EN[28:28]	1
0xBB009534	VLAN_PORT_IGR_CTRL [19].OPVID_FMT[27:26]	2
0xBB009534	VLAN_PORT_IGR_CTRL [19].OPVID[25:14]	12
0xBB009534	VLAN_PORT_IGR_CTRL [19].IPVID_FMT[13:12]	2
0xBB009534	VLAN_PORT_IGR_CTRL [19].IPVID[11:0]	12
0xBB009538	VLAN_PORT_IGR_CTRL [20].RESERVED[31:30]	2
0xBB009538	VLAN_PORT_IGR_CTRL [20].SA_LRN_MODE[29:29]	1
0xBB009538	VLAN_PORT_IGR_CTRL [20].PRIVATE_VLAN_EN[28:28]	1
0xBB009538	VLAN_PORT_IGR_CTRL [20].OPVID_FMT[27:26]	2
0xBB009538	VLAN_PORT_IGR_CTRL [20].OPVID[25:14]	12
0xBB009538	VLAN_PORT_IGR_CTRL [20].IPVID_FMT[13:12]	2
0xBB009538	VLAN_PORT_IGR_CTRL [20].IPVID[11:0]	12
0xBB00953C	VLAN_PORT_IGR_CTRL [21].RESERVED[31:30]	2
0xBB00953C	VLAN_PORT_IGR_CTRL [21].SA_LRN_MODE[29:29]	1
0xBB00953C	VLAN_PORT_IGR_CTRL [21].PRIVATE_VLAN_EN[28:28]	1
0xBB00953C	VLAN_PORT_IGR_CTRL [21].OPVID_FMT[27:26]	2
0xBB00953C	VLAN_PORT_IGR_CTRL [21].OPVID[25:14]	12
0xBB00953C	VLAN_PORT_IGR_CTRL [21].IPVID_FMT[13:12]	2
0xBB00953C	VLAN_PORT_IGR_CTRL [21].IPVID[11:0]	12
0xBB009540	VLAN_PORT_IGR_CTRL [22].RESERVED[31:30]	2
0xBB009540	VLAN_PORT_IGR_CTRL [22].SA_LRN_MODE[29:29]	1
0xBB009540	VLAN_PORT_IGR_CTRL [22].PRIVATE_VLAN_EN[28:28]	1
0xBB009540	VLAN_PORT_IGR_CTRL [22].OPVID_FMT[27:26]	2
0xBB009540	VLAN_PORT_IGR_CTRL [22].OPVID[25:14]	12
0xBB009540	VLAN_PORT_IGR_CTRL [22].IPVID_FMT[13:12]	2
0xBB009540	VLAN_PORT_IGR_CTRL [22].IPVID[11:0]	12
0xBB009544	VLAN_PORT_IGR_CTRL [23].RESERVED[31:30]	2
0xBB009544	VLAN_PORT_IGR_CTRL [23].SA_LRN_MODE[29:29]	1
0xBB009544	VLAN_PORT_IGR_CTRL [23].PRIVATE_VLAN_EN[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009544	VLAN_PORT_IGR_CTRL [23].OPVID_FMT[27:26]	2
0xBB009544	VLAN_PORT_IGR_CTRL [23].OPVID[25:14]	12
0xBB009544	VLAN_PORT_IGR_CTRL [23].IPVID_FMT[13:12]	2
0xBB009544	VLAN_PORT_IGR_CTRL [23].IPVID[11:0]	12
0xBB009548	VLAN_PORT_IGR_CTRL [24].RESERVED[31:30]	2
0xBB009548	VLAN_PORT_IGR_CTRL [24].SA_LRN_MODE[29:29]	1
0xBB009548	VLAN_PORT_IGR_CTRL [24].PRIVATE_VLAN_EN[28:28]	1
0xBB009548	VLAN_PORT_IGR_CTRL [24].OPVID_FMT[27:26]	2
0xBB009548	VLAN_PORT_IGR_CTRL [24].OPVID[25:14]	12
0xBB009548	VLAN_PORT_IGR_CTRL [24].IPVID_FMT[13:12]	2
0xBB009548	VLAN_PORT_IGR_CTRL [24].IPVID[11:0]	12
0xBB00954C	VLAN_PORT_IGR_CTRL [25].RESERVED[31:30]	2
0xBB00954C	VLAN_PORT_IGR_CTRL [25].SA_LRN_MODE[29:29]	1
0xBB00954C	VLAN_PORT_IGR_CTRL [25].PRIVATE_VLAN_EN[28:28]	1
0xBB00954C	VLAN_PORT_IGR_CTRL [25].OPVID_FMT[27:26]	2
0xBB00954C	VLAN_PORT_IGR_CTRL [25].OPVID[25:14]	12
0xBB00954C	VLAN_PORT_IGR_CTRL [25].IPVID_FMT[13:12]	2
0xBB00954C	VLAN_PORT_IGR_CTRL [25].IPVID[11:0]	12
0xBB009550	VLAN_PORT_IGR_CTRL [26].RESERVED[31:30]	2
0xBB009550	VLAN_PORT_IGR_CTRL [26].SA_LRN_MODE[29:29]	1
0xBB009550	VLAN_PORT_IGR_CTRL [26].PRIVATE_VLAN_EN[28:28]	1
0xBB009550	VLAN_PORT_IGR_CTRL [26].OPVID_FMT[27:26]	2
0xBB009550	VLAN_PORT_IGR_CTRL [26].OPVID[25:14]	12
0xBB009550	VLAN_PORT_IGR_CTRL [26].IPVID_FMT[13:12]	2
0xBB009550	VLAN_PORT_IGR_CTRL [26].IPVID[11:0]	12
0xBB009554	VLAN_PORT_IGR_CTRL [27].RESERVED[31:30]	2
0xBB009554	VLAN_PORT_IGR_CTRL [27].SA_LRN_MODE[29:29]	1
0xBB009554	VLAN_PORT_IGR_CTRL [27].PRIVATE_VLAN_EN[28:28]	1
0xBB009554	VLAN_PORT_IGR_CTRL [27].OPVID_FMT[27:26]	2
0xBB009554	VLAN_PORT_IGR_CTRL [27].OPVID[25:14]	12
0xBB009554	VLAN_PORT_IGR_CTRL [27].IPVID_FMT[13:12]	2
0xBB009554	VLAN_PORT_IGR_CTRL [27].IPVID[11:0]	12
0xBB009558	VLAN_PORT_IGR_CTRL [28].RESERVED[31:30]	2
0xBB009558	VLAN_PORT_IGR_CTRL [28].SA_LRN_MODE[29:29]	1
0xBB009558	VLAN_PORT_IGR_CTRL [28].PRIVATE_VLAN_EN[28:28]	1
0xBB009558	VLAN_PORT_IGR_CTRL [28].OPVID_FMT[27:26]	2
0xBB009558	VLAN_PORT_IGR_CTRL [28].OPVID[25:14]	12
0xBB009558	VLAN_PORT_IGR_CTRL [28].IPVID_FMT[13:12]	2
0xBB009558	VLAN_PORT_IGR_CTRL [28].IPVID[11:0]	12
0xBB00955C	VLAN_PORT_IGR_CTRL [29].RESERVED[31:30]	2
0xBB00955C	VLAN_PORT_IGR_CTRL [29].SA_LRN_MODE[29:29]	1
0xBB00955C	VLAN_PORT_IGR_CTRL [29].PRIVATE_VLAN_EN[28:28]	1
0xBB00955C	VLAN_PORT_IGR_CTRL [29].OPVID_FMT[27:26]	2
0xBB00955C	VLAN_PORT_IGR_CTRL [29].OPVID[25:14]	12
0xBB00955C	VLAN_PORT_IGR_CTRL [29].IPVID_FMT[13:12]	2
0xBB00955C	VLAN_PORT_IGR_CTRL [29].IPVID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009560	VLAN_PORT_IGR_CTRL [30].RESERVED[31:30]	2
0xBB009560	VLAN_PORT_IGR_CTRL [30].SA_LRN_MODE[29:29]	1
0xBB009560	VLAN_PORT_IGR_CTRL [30].PRIVATE_VLAN_EN[28:28]	1
0xBB009560	VLAN_PORT_IGR_CTRL [30].OPVID_FMT[27:26]	2
0xBB009560	VLAN_PORT_IGR_CTRL [30].OPVID[25:14]	12
0xBB009560	VLAN_PORT_IGR_CTRL [30].IPVID_FMT[13:12]	2
0xBB009560	VLAN_PORT_IGR_CTRL [30].IPVID[11:0]	12
0xBB009564	VLAN_PORT_IGR_CTRL [31].RESERVED[31:30]	2
0xBB009564	VLAN_PORT_IGR_CTRL [31].SA_LRN_MODE[29:29]	1
0xBB009564	VLAN_PORT_IGR_CTRL [31].PRIVATE_VLAN_EN[28:28]	1
0xBB009564	VLAN_PORT_IGR_CTRL [31].OPVID_FMT[27:26]	2
0xBB009564	VLAN_PORT_IGR_CTRL [31].OPVID[25:14]	12
0xBB009564	VLAN_PORT_IGR_CTRL [31].IPVID_FMT[13:12]	2
0xBB009564	VLAN_PORT_IGR_CTRL [31].IPVID[11:0]	12
0xBB009568	VLAN_PORT_IGR_CTRL [32].RESERVED[31:30]	2
0xBB009568	VLAN_PORT_IGR_CTRL [32].SA_LRN_MODE[29:29]	1
0xBB009568	VLAN_PORT_IGR_CTRL [32].PRIVATE_VLAN_EN[28:28]	1
0xBB009568	VLAN_PORT_IGR_CTRL [32].OPVID_FMT[27:26]	2
0xBB009568	VLAN_PORT_IGR_CTRL [32].OPVID[25:14]	12
0xBB009568	VLAN_PORT_IGR_CTRL [32].IPVID_FMT[13:12]	2
0xBB009568	VLAN_PORT_IGR_CTRL [32].IPVID[11:0]	12
0xBB00956C	VLAN_PORT_IGR_CTRL [33].RESERVED[31:30]	2
0xBB00956C	VLAN_PORT_IGR_CTRL [33].SA_LRN_MODE[29:29]	1
0xBB00956C	VLAN_PORT_IGR_CTRL [33].PRIVATE_VLAN_EN[28:28]	1
0xBB00956C	VLAN_PORT_IGR_CTRL [33].OPVID_FMT[27:26]	2
0xBB00956C	VLAN_PORT_IGR_CTRL [33].OPVID[25:14]	12
0xBB00956C	VLAN_PORT_IGR_CTRL [33].IPVID_FMT[13:12]	2
0xBB00956C	VLAN_PORT_IGR_CTRL [33].IPVID[11:0]	12
0xBB009570	VLAN_PORT_IGR_CTRL [34].RESERVED[31:30]	2
0xBB009570	VLAN_PORT_IGR_CTRL [34].SA_LRN_MODE[29:29]	1
0xBB009570	VLAN_PORT_IGR_CTRL [34].PRIVATE_VLAN_EN[28:28]	1
0xBB009570	VLAN_PORT_IGR_CTRL [34].OPVID_FMT[27:26]	2
0xBB009570	VLAN_PORT_IGR_CTRL [34].OPVID[25:14]	12
0xBB009570	VLAN_PORT_IGR_CTRL [34].IPVID_FMT[13:12]	2
0xBB009570	VLAN_PORT_IGR_CTRL [34].IPVID[11:0]	12
0xBB009574	VLAN_PORT_IGR_CTRL [35].RESERVED[31:30]	2
0xBB009574	VLAN_PORT_IGR_CTRL [35].SA_LRN_MODE[29:29]	1
0xBB009574	VLAN_PORT_IGR_CTRL [35].PRIVATE_VLAN_EN[28:28]	1
0xBB009574	VLAN_PORT_IGR_CTRL [35].OPVID_FMT[27:26]	2
0xBB009574	VLAN_PORT_IGR_CTRL [35].OPVID[25:14]	12
0xBB009574	VLAN_PORT_IGR_CTRL [35].IPVID_FMT[13:12]	2
0xBB009574	VLAN_PORT_IGR_CTRL [35].IPVID[11:0]	12
0xBB009578	VLAN_PORT_IGR_CTRL [36].RESERVED[31:30]	2
0xBB009578	VLAN_PORT_IGR_CTRL [36].SA_LRN_MODE[29:29]	1
0xBB009578	VLAN_PORT_IGR_CTRL [36].PRIVATE_VLAN_EN[28:28]	1
0xBB009578	VLAN_PORT_IGR_CTRL [36].OPVID_FMT[27:26]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009578	VLAN_PORT_IGR_CTRL [36].OPVID[25:14]	12
0xBB009578	VLAN_PORT_IGR_CTRL [36].IPVID_FMT[13:12]	2
0xBB009578	VLAN_PORT_IGR_CTRL [36].IPVID[11:0]	12
0xBB00957C	VLAN_PORT_IGR_CTRL [37].RESERVED[31:30]	2
0xBB00957C	VLAN_PORT_IGR_CTRL [37].SA_LRN_MODE[29:29]	1
0xBB00957C	VLAN_PORT_IGR_CTRL [37].PRIVATE_VLAN_EN[28:28]	1
0xBB00957C	VLAN_PORT_IGR_CTRL [37].OPVID_FMT[27:26]	2
0xBB00957C	VLAN_PORT_IGR_CTRL [37].OPVID[25:14]	12
0xBB00957C	VLAN_PORT_IGR_CTRL [37].IPVID_FMT[13:12]	2
0xBB00957C	VLAN_PORT_IGR_CTRL [37].IPVID[11:0]	12
0xBB009580	VLAN_PORT_IGR_CTRL [38].RESERVED[31:30]	2
0xBB009580	VLAN_PORT_IGR_CTRL [38].SA_LRN_MODE[29:29]	1
0xBB009580	VLAN_PORT_IGR_CTRL [38].PRIVATE_VLAN_EN[28:28]	1
0xBB009580	VLAN_PORT_IGR_CTRL [38].OPVID_FMT[27:26]	2
0xBB009580	VLAN_PORT_IGR_CTRL [38].OPVID[25:14]	12
0xBB009580	VLAN_PORT_IGR_CTRL [38].IPVID_FMT[13:12]	2
0xBB009580	VLAN_PORT_IGR_CTRL [38].IPVID[11:0]	12
0xBB009584	VLAN_PORT_IGR_CTRL [39].RESERVED[31:30]	2
0xBB009584	VLAN_PORT_IGR_CTRL [39].SA_LRN_MODE[29:29]	1
0xBB009584	VLAN_PORT_IGR_CTRL [39].PRIVATE_VLAN_EN[28:28]	1
0xBB009584	VLAN_PORT_IGR_CTRL [39].OPVID_FMT[27:26]	2
0xBB009584	VLAN_PORT_IGR_CTRL [39].OPVID[25:14]	12
0xBB009584	VLAN_PORT_IGR_CTRL [39].IPVID_FMT[13:12]	2
0xBB009584	VLAN_PORT_IGR_CTRL [39].IPVID[11:0]	12
0xBB009588	VLAN_PORT_IGR_CTRL [40].RESERVED[31:30]	2
0xBB009588	VLAN_PORT_IGR_CTRL [40].SA_LRN_MODE[29:29]	1
0xBB009588	VLAN_PORT_IGR_CTRL [40].PRIVATE_VLAN_EN[28:28]	1
0xBB009588	VLAN_PORT_IGR_CTRL [40].OPVID_FMT[27:26]	2
0xBB009588	VLAN_PORT_IGR_CTRL [40].OPVID[25:14]	12
0xBB009588	VLAN_PORT_IGR_CTRL [40].IPVID_FMT[13:12]	2
0xBB009588	VLAN_PORT_IGR_CTRL [40].IPVID[11:0]	12
0xBB00958C	VLAN_PORT_IGR_CTRL [41].RESERVED[31:30]	2
0xBB00958C	VLAN_PORT_IGR_CTRL [41].SA_LRN_MODE[29:29]	1
0xBB00958C	VLAN_PORT_IGR_CTRL [41].PRIVATE_VLAN_EN[28:28]	1
0xBB00958C	VLAN_PORT_IGR_CTRL [41].OPVID_FMT[27:26]	2
0xBB00958C	VLAN_PORT_IGR_CTRL [41].OPVID[25:14]	12
0xBB00958C	VLAN_PORT_IGR_CTRL [41].IPVID_FMT[13:12]	2
0xBB00958C	VLAN_PORT_IGR_CTRL [41].IPVID[11:0]	12
0xBB009590	VLAN_PORT_IGR_CTRL [42].RESERVED[31:30]	2
0xBB009590	VLAN_PORT_IGR_CTRL [42].SA_LRN_MODE[29:29]	1
0xBB009590	VLAN_PORT_IGR_CTRL [42].PRIVATE_VLAN_EN[28:28]	1
0xBB009590	VLAN_PORT_IGR_CTRL [42].OPVID_FMT[27:26]	2
0xBB009590	VLAN_PORT_IGR_CTRL [42].OPVID[25:14]	12
0xBB009590	VLAN_PORT_IGR_CTRL [42].IPVID_FMT[13:12]	2
0xBB009590	VLAN_PORT_IGR_CTRL [42].IPVID[11:0]	12
0xBB009594	VLAN_PORT_IGR_CTRL [43].RESERVED[31:30]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009594	VLAN_PORT_IGR_CTRL [43].SA_LRN_MODE[29:29]	1
0xBB009594	VLAN_PORT_IGR_CTRL [43].PRIVATE_VLAN_EN[28:28]	1
0xBB009594	VLAN_PORT_IGR_CTRL [43].OPVID_FMT[27:26]	2
0xBB009594	VLAN_PORT_IGR_CTRL [43].OPVID[25:14]	12
0xBB009594	VLAN_PORT_IGR_CTRL [43].IPVID_FMT[13:12]	2
0xBB009594	VLAN_PORT_IGR_CTRL [43].IPVID[11:0]	12
0xBB009598	VLAN_PORT_IGR_CTRL [44].RESERVED[31:30]	2
0xBB009598	VLAN_PORT_IGR_CTRL [44].SA_LRN_MODE[29:29]	1
0xBB009598	VLAN_PORT_IGR_CTRL [44].PRIVATE_VLAN_EN[28:28]	1
0xBB009598	VLAN_PORT_IGR_CTRL [44].OPVID_FMT[27:26]	2
0xBB009598	VLAN_PORT_IGR_CTRL [44].OPVID[25:14]	12
0xBB009598	VLAN_PORT_IGR_CTRL [44].IPVID_FMT[13:12]	2
0xBB009598	VLAN_PORT_IGR_CTRL [44].IPVID[11:0]	12
0xBB00959C	VLAN_PORT_IGR_CTRL [45].RESERVED[31:30]	2
0xBB00959C	VLAN_PORT_IGR_CTRL [45].SA_LRN_MODE[29:29]	1
0xBB00959C	VLAN_PORT_IGR_CTRL [45].PRIVATE_VLAN_EN[28:28]	1
0xBB00959C	VLAN_PORT_IGR_CTRL [45].OPVID_FMT[27:26]	2
0xBB00959C	VLAN_PORT_IGR_CTRL [45].OPVID[25:14]	12
0xBB00959C	VLAN_PORT_IGR_CTRL [45].IPVID_FMT[13:12]	2
0xBB00959C	VLAN_PORT_IGR_CTRL [45].IPVID[11:0]	12
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].RESERVED[31:30]	2
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].SA_LRN_MODE[29:29]	1
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].PRIVATE_VLAN_EN[28:28]	1
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].OPVID_FMT[27:26]	2
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].OPVID[25:14]	12
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].IPVID_FMT[13:12]	2
0xBB0095A0	VLAN_PORT_IGR_CTRL [46].IPVID[11:0]	12
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].RESERVED[31:30]	2
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].SA_LRN_MODE[29:29]	1
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].PRIVATE_VLAN_EN[28:28]	1
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].OPVID_FMT[27:26]	2
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].OPVID[25:14]	12
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].IPVID_FMT[13:12]	2
0xBB0095A4	VLAN_PORT_IGR_CTRL [47].IPVID[11:0]	12
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].RESERVED[31:30]	2
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].SA_LRN_MODE[29:29]	1
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].PRIVATE_VLAN_EN[28:28]	1
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].OPVID_FMT[27:26]	2
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].OPVID[25:14]	12
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].IPVID_FMT[13:12]	2
0xBB0095A8	VLAN_PORT_IGR_CTRL [48].IPVID[11:0]	12
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].RESERVED[31:30]	2
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].SA_LRN_MODE[29:29]	1
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].PRIVATE_VLAN_EN[28:28]	1
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].OPVID_FMT[27:26]	2
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].OPVID[25:14]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].IPVID_FMT[13:12]	2
0xBB0095AC	VLAN_PORT_IGR_CTRL [49].IPVID[11:0]	12
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].RESERVED[31:30]	2
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].SA_LRN_MODE[29:29]	1
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].PRIVATE_VLAN_EN[28:28]	1
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].OPVID_FMT[27:26]	2
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].OPVID[25:14]	12
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].IPVID_FMT[13:12]	2
0xBB0095B0	VLAN_PORT_IGR_CTRL [50].IPVID[11:0]	12
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].RESERVED[31:30]	2
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].SA_LRN_MODE[29:29]	1
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].PRIVATE_VLAN_EN[28:28]	1
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].OPVID_FMT[27:26]	2
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].OPVID[25:14]	12
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].IPVID_FMT[13:12]	2
0xBB0095B4	VLAN_PORT_IGR_CTRL [51].IPVID[11:0]	12
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].RESERVED[31:30]	2
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].SA_LRN_MODE[29:29]	1
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].PRIVATE_VLAN_EN[28:28]	1
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].OPVID_FMT[27:26]	2
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].OPVID[25:14]	12
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].IPVID_FMT[13:12]	2
0xBB0095B8	VLAN_PORT_IGR_CTRL [52].IPVID[11:0]	12
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].RESERVED[31:30]	2
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].SA_LRN_MODE[29:29]	1
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].PRIVATE_VLAN_EN[28:28]	1
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].OPVID_FMT[27:26]	2
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].OPVID[25:14]	12
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].IPVID_FMT[13:12]	2
0xBB0095BC	VLAN_PORT_IGR_CTRL [53].IPVID[11:0]	12
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].RESERVED[31:30]	2
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].SA_LRN_MODE[29:29]	1
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].PRIVATE_VLAN_EN[28:28]	1
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].OPVID_FMT[27:26]	2
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].OPVID[25:14]	12
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].IPVID_FMT[13:12]	2
0xBB0095C0	VLAN_PORT_IGR_CTRL [54].IPVID[11:0]	12
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].RESERVED[31:30]	2
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].SA_LRN_MODE[29:29]	1
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].PRIVATE_VLAN_EN[28:28]	1
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].OPVID_FMT[27:26]	2
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].OPVID[25:14]	12
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].IPVID_FMT[13:12]	2
0xBB0095C4	VLAN_PORT_IGR_CTRL [55].IPVID[11:0]	12
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].RESERVED[31:30]	2
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].SA_LRN_MODE[29:29]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].PRIVATE_VLAN_EN[28:28]	1
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].OPVID_FMT[27:26]	2
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].OPVID[25:14]	12
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].IPVID_FMT[13:12]	2
0xBB0095C8	VLAN_PORT_IGR_CTRL [56].IPVID[11:0]	12
0xBB0096B4	VLAN_PORT_IGR_FLTR [0].IGR_FLTR_ACT[1:0]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [1].IGR_FLTR_ACT[3:2]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [2].IGR_FLTR_ACT[5:4]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [3].IGR_FLTR_ACT[7:6]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [4].IGR_FLTR_ACT[9:8]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [5].IGR_FLTR_ACT[11:10]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [6].IGR_FLTR_ACT[13:12]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [7].IGR_FLTR_ACT[15:14]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [8].IGR_FLTR_ACT[17:16]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [9].IGR_FLTR_ACT[19:18]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [10].IGR_FLTR_ACT[21:20]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [11].IGR_FLTR_ACT[23:22]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [12].IGR_FLTR_ACT[25:24]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [13].IGR_FLTR_ACT[27:26]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [14].IGR_FLTR_ACT[29:28]	2
0xBB0096B4	VLAN_PORT_IGR_FLTR [15].IGR_FLTR_ACT[31:30]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [16].IGR_FLTR_ACT[1:0]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [17].IGR_FLTR_ACT[3:2]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [18].IGR_FLTR_ACT[5:4]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [19].IGR_FLTR_ACT[7:6]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [20].IGR_FLTR_ACT[9:8]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [21].IGR_FLTR_ACT[11:10]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [22].IGR_FLTR_ACT[13:12]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [23].IGR_FLTR_ACT[15:14]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [24].IGR_FLTR_ACT[17:16]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [25].IGR_FLTR_ACT[19:18]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [26].IGR_FLTR_ACT[21:20]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [27].IGR_FLTR_ACT[23:22]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [28].IGR_FLTR_ACT[25:24]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [29].IGR_FLTR_ACT[27:26]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [30].IGR_FLTR_ACT[29:28]	2
0xBB0096B8	VLAN_PORT_IGR_FLTR [31].IGR_FLTR_ACT[31:30]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [32].IGR_FLTR_ACT[1:0]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [33].IGR_FLTR_ACT[3:2]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [34].IGR_FLTR_ACT[5:4]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [35].IGR_FLTR_ACT[7:6]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [36].IGR_FLTR_ACT[9:8]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [37].IGR_FLTR_ACT[11:10]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [38].IGR_FLTR_ACT[13:12]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [39].IGR_FLTR_ACT[15:14]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [40].IGR_FLTR_ACT[17:16]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0096BC	VLAN_PORT_IGR_FLTR [41].IGR_FLTR_ACT[19:18]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [42].IGR_FLTR_ACT[21:20]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [43].IGR_FLTR_ACT[23:22]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [44].IGR_FLTR_ACT[25:24]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [45].IGR_FLTR_ACT[27:26]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [46].IGR_FLTR_ACT[29:28]	2
0xBB0096BC	VLAN_PORT_IGR_FLTR [47].IGR_FLTR_ACT[31:30]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [48].IGR_FLTR_ACT[1:0]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [49].IGR_FLTR_ACT[3:2]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [50].IGR_FLTR_ACT[5:4]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [51].IGR_FLTR_ACT[7:6]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [52].IGR_FLTR_ACT[9:8]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [53].IGR_FLTR_ACT[11:10]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [54].IGR_FLTR_ACT[13:12]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [55].IGR_FLTR_ACT[15:14]	2
0xBB0096C0	VLAN_PORT_IGR_FLTR [56].IGR_FLTR_ACT[17:16]	2
0xBB006010	VLAN_PORT_ITAG_TPID_CMP_MSK [0].RESERVED[31:4]	28
0xBB006010	VLAN_PORT_ITAG_TPID_CMP_MSK [0].ITPID_CMP_MSK[3:0]	4
0xBB006090	VLAN_PORT_ITAG_TPID_CMP_MSK [1].RESERVED[31:4]	28
0xBB006090	VLAN_PORT_ITAG_TPID_CMP_MSK [1].ITPID_CMP_MSK[3:0]	4
0xBB006110	VLAN_PORT_ITAG_TPID_CMP_MSK [2].RESERVED[31:4]	28
0xBB006110	VLAN_PORT_ITAG_TPID_CMP_MSK [2].ITPID_CMP_MSK[3:0]	4
0xBB006190	VLAN_PORT_ITAG_TPID_CMP_MSK [3].RESERVED[31:4]	28
0xBB006190	VLAN_PORT_ITAG_TPID_CMP_MSK [3].ITPID_CMP_MSK[3:0]	4
0xBB006210	VLAN_PORT_ITAG_TPID_CMP_MSK [4].RESERVED[31:4]	28
0xBB006210	VLAN_PORT_ITAG_TPID_CMP_MSK [4].ITPID_CMP_MSK[3:0]	4
0xBB006290	VLAN_PORT_ITAG_TPID_CMP_MSK [5].RESERVED[31:4]	28
0xBB006290	VLAN_PORT_ITAG_TPID_CMP_MSK [5].ITPID_CMP_MSK[3:0]	4
0xBB006310	VLAN_PORT_ITAG_TPID_CMP_MSK [6].RESERVED[31:4]	28
0xBB006310	VLAN_PORT_ITAG_TPID_CMP_MSK [6].ITPID_CMP_MSK[3:0]	4
0xBB006390	VLAN_PORT_ITAG_TPID_CMP_MSK [7].RESERVED[31:4]	28
0xBB006390	VLAN_PORT_ITAG_TPID_CMP_MSK [7].ITPID_CMP_MSK[3:0]	4
0xBB006410	VLAN_PORT_ITAG_TPID_CMP_MSK [8].RESERVED[31:4]	28
0xBB006410	VLAN_PORT_ITAG_TPID_CMP_MSK [8].ITPID_CMP_MSK[3:0]	4
0xBB006490	VLAN_PORT_ITAG_TPID_CMP_MSK [9].RESERVED[31:4]	28
0xBB006490	VLAN_PORT_ITAG_TPID_CMP_MSK [9].ITPID_CMP_MSK[3:0]	4
0xBB006510	VLAN_PORT_ITAG_TPID_CMP_MSK [10].RESERVED[31:4]	28
0xBB006510	VLAN_PORT_ITAG_TPID_CMP_MSK [10].ITPID_CMP_MSK[3:0]	4
0xBB006590	VLAN_PORT_ITAG_TPID_CMP_MSK [11].RESERVED[31:4]	28
0xBB006590	VLAN_PORT_ITAG_TPID_CMP_MSK [11].ITPID_CMP_MSK[3:0]	4
0xBB006610	VLAN_PORT_ITAG_TPID_CMP_MSK [12].RESERVED[31:4]	28
0xBB006610	VLAN_PORT_ITAG_TPID_CMP_MSK [12].ITPID_CMP_MSK[3:0]	4
0xBB006690	VLAN_PORT_ITAG_TPID_CMP_MSK [13].RESERVED[31:4]	28
0xBB006690	VLAN_PORT_ITAG_TPID_CMP_MSK [13].ITPID_CMP_MSK[3:0]	4
0xBB006710	VLAN_PORT_ITAG_TPID_CMP_MSK [14].RESERVED[31:4]	28
0xBB006710	VLAN_PORT_ITAG_TPID_CMP_MSK [14].ITPID_CMP_MSK[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006790	VLAN_PORT_ITAG_TPID_CMP_MSK [15].RESERVED[31:4]	28
0xBB006790	VLAN_PORT_ITAG_TPID_CMP_MSK [15].ITPID_CMP_MSK[3:0]	4
0xBB006810	VLAN_PORT_ITAG_TPID_CMP_MSK [16].RESERVED[31:4]	28
0xBB006810	VLAN_PORT_ITAG_TPID_CMP_MSK [16].ITPID_CMP_MSK[3:0]	4
0xBB006890	VLAN_PORT_ITAG_TPID_CMP_MSK [17].RESERVED[31:4]	28
0xBB006890	VLAN_PORT_ITAG_TPID_CMP_MSK [17].ITPID_CMP_MSK[3:0]	4
0xBB006910	VLAN_PORT_ITAG_TPID_CMP_MSK [18].RESERVED[31:4]	28
0xBB006910	VLAN_PORT_ITAG_TPID_CMP_MSK [18].ITPID_CMP_MSK[3:0]	4
0xBB006990	VLAN_PORT_ITAG_TPID_CMP_MSK [19].RESERVED[31:4]	28
0xBB006990	VLAN_PORT_ITAG_TPID_CMP_MSK [19].ITPID_CMP_MSK[3:0]	4
0xBB006A10	VLAN_PORT_ITAG_TPID_CMP_MSK [20].RESERVED[31:4]	28
0xBB006A10	VLAN_PORT_ITAG_TPID_CMP_MSK [20].ITPID_CMP_MSK[3:0]	4
0xBB006A90	VLAN_PORT_ITAG_TPID_CMP_MSK [21].RESERVED[31:4]	28
0xBB006A90	VLAN_PORT_ITAG_TPID_CMP_MSK [21].ITPID_CMP_MSK[3:0]	4
0xBB006B10	VLAN_PORT_ITAG_TPID_CMP_MSK [22].RESERVED[31:4]	28
0xBB006B10	VLAN_PORT_ITAG_TPID_CMP_MSK [22].ITPID_CMP_MSK[3:0]	4
0xBB006B90	VLAN_PORT_ITAG_TPID_CMP_MSK [23].RESERVED[31:4]	28
0xBB006B90	VLAN_PORT_ITAG_TPID_CMP_MSK [23].ITPID_CMP_MSK[3:0]	4
0xBB006C10	VLAN_PORT_ITAG_TPID_CMP_MSK [24].RESERVED[31:4]	28
0xBB006C10	VLAN_PORT_ITAG_TPID_CMP_MSK [24].ITPID_CMP_MSK[3:0]	4
0xBB006C90	VLAN_PORT_ITAG_TPID_CMP_MSK [25].RESERVED[31:4]	28
0xBB006C90	VLAN_PORT_ITAG_TPID_CMP_MSK [25].ITPID_CMP_MSK[3:0]	4
0xBB006D10	VLAN_PORT_ITAG_TPID_CMP_MSK [26].RESERVED[31:4]	28
0xBB006D10	VLAN_PORT_ITAG_TPID_CMP_MSK [26].ITPID_CMP_MSK[3:0]	4
0xBB006D90	VLAN_PORT_ITAG_TPID_CMP_MSK [27].RESERVED[31:4]	28
0xBB006D90	VLAN_PORT_ITAG_TPID_CMP_MSK [27].ITPID_CMP_MSK[3:0]	4
0xBB006E10	VLAN_PORT_ITAG_TPID_CMP_MSK [28].RESERVED[31:4]	28
0xBB006E10	VLAN_PORT_ITAG_TPID_CMP_MSK [28].ITPID_CMP_MSK[3:0]	4
0xBB006E90	VLAN_PORT_ITAG_TPID_CMP_MSK [29].RESERVED[31:4]	28
0xBB006E90	VLAN_PORT_ITAG_TPID_CMP_MSK [29].ITPID_CMP_MSK[3:0]	4
0xBB006F10	VLAN_PORT_ITAG_TPID_CMP_MSK [30].RESERVED[31:4]	28
0xBB006F10	VLAN_PORT_ITAG_TPID_CMP_MSK [30].ITPID_CMP_MSK[3:0]	4
0xBB006F90	VLAN_PORT_ITAG_TPID_CMP_MSK [31].RESERVED[31:4]	28
0xBB006F90	VLAN_PORT_ITAG_TPID_CMP_MSK [31].ITPID_CMP_MSK[3:0]	4
0xBB007010	VLAN_PORT_ITAG_TPID_CMP_MSK [32].RESERVED[31:4]	28
0xBB007010	VLAN_PORT_ITAG_TPID_CMP_MSK [32].ITPID_CMP_MSK[3:0]	4
0xBB007090	VLAN_PORT_ITAG_TPID_CMP_MSK [33].RESERVED[31:4]	28
0xBB007090	VLAN_PORT_ITAG_TPID_CMP_MSK [33].ITPID_CMP_MSK[3:0]	4
0xBB007110	VLAN_PORT_ITAG_TPID_CMP_MSK [34].RESERVED[31:4]	28
0xBB007110	VLAN_PORT_ITAG_TPID_CMP_MSK [34].ITPID_CMP_MSK[3:0]	4
0xBB007190	VLAN_PORT_ITAG_TPID_CMP_MSK [35].RESERVED[31:4]	28
0xBB007190	VLAN_PORT_ITAG_TPID_CMP_MSK [35].ITPID_CMP_MSK[3:0]	4
0xBB007210	VLAN_PORT_ITAG_TPID_CMP_MSK [36].RESERVED[31:4]	28
0xBB007210	VLAN_PORT_ITAG_TPID_CMP_MSK [36].ITPID_CMP_MSK[3:0]	4
0xBB007290	VLAN_PORT_ITAG_TPID_CMP_MSK [37].RESERVED[31:4]	28
0xBB007290	VLAN_PORT_ITAG_TPID_CMP_MSK [37].ITPID_CMP_MSK[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007310	VLAN_PORT_ITAG_TPID_CMP_MSK [38].RESERVED[31:4]	28
0xBB007310	VLAN_PORT_ITAG_TPID_CMP_MSK [38].ITPID_CMP_MSK[3:0]	4
0xBB007390	VLAN_PORT_ITAG_TPID_CMP_MSK [39].RESERVED[31:4]	28
0xBB007390	VLAN_PORT_ITAG_TPID_CMP_MSK [39].ITPID_CMP_MSK[3:0]	4
0xBB007410	VLAN_PORT_ITAG_TPID_CMP_MSK [40].RESERVED[31:4]	28
0xBB007410	VLAN_PORT_ITAG_TPID_CMP_MSK [40].ITPID_CMP_MSK[3:0]	4
0xBB007490	VLAN_PORT_ITAG_TPID_CMP_MSK [41].RESERVED[31:4]	28
0xBB007490	VLAN_PORT_ITAG_TPID_CMP_MSK [41].ITPID_CMP_MSK[3:0]	4
0xBB007510	VLAN_PORT_ITAG_TPID_CMP_MSK [42].RESERVED[31:4]	28
0xBB007510	VLAN_PORT_ITAG_TPID_CMP_MSK [42].ITPID_CMP_MSK[3:0]	4
0xBB007590	VLAN_PORT_ITAG_TPID_CMP_MSK [43].RESERVED[31:4]	28
0xBB007590	VLAN_PORT_ITAG_TPID_CMP_MSK [43].ITPID_CMP_MSK[3:0]	4
0xBB007610	VLAN_PORT_ITAG_TPID_CMP_MSK [44].RESERVED[31:4]	28
0xBB007610	VLAN_PORT_ITAG_TPID_CMP_MSK [44].ITPID_CMP_MSK[3:0]	4
0xBB007690	VLAN_PORT_ITAG_TPID_CMP_MSK [45].RESERVED[31:4]	28
0xBB007690	VLAN_PORT_ITAG_TPID_CMP_MSK [45].ITPID_CMP_MSK[3:0]	4
0xBB007710	VLAN_PORT_ITAG_TPID_CMP_MSK [46].RESERVED[31:4]	28
0xBB007710	VLAN_PORT_ITAG_TPID_CMP_MSK [46].ITPID_CMP_MSK[3:0]	4
0xBB007790	VLAN_PORT_ITAG_TPID_CMP_MSK [47].RESERVED[31:4]	28
0xBB007790	VLAN_PORT_ITAG_TPID_CMP_MSK [47].ITPID_CMP_MSK[3:0]	4
0xBB007810	VLAN_PORT_ITAG_TPID_CMP_MSK [48].RESERVED[31:4]	28
0xBB007810	VLAN_PORT_ITAG_TPID_CMP_MSK [48].ITPID_CMP_MSK[3:0]	4
0xBB007890	VLAN_PORT_ITAG_TPID_CMP_MSK [49].RESERVED[31:4]	28
0xBB007890	VLAN_PORT_ITAG_TPID_CMP_MSK [49].ITPID_CMP_MSK[3:0]	4
0xBB007910	VLAN_PORT_ITAG_TPID_CMP_MSK [50].RESERVED[31:4]	28
0xBB007910	VLAN_PORT_ITAG_TPID_CMP_MSK [50].ITPID_CMP_MSK[3:0]	4
0xBB007990	VLAN_PORT_ITAG_TPID_CMP_MSK [51].RESERVED[31:4]	28
0xBB007990	VLAN_PORT_ITAG_TPID_CMP_MSK [51].ITPID_CMP_MSK[3:0]	4
0xBB007A10	VLAN_PORT_ITAG_TPID_CMP_MSK [52].RESERVED[31:4]	28
0xBB007A10	VLAN_PORT_ITAG_TPID_CMP_MSK [52].ITPID_CMP_MSK[3:0]	4
0xBB007A90	VLAN_PORT_ITAG_TPID_CMP_MSK [53].RESERVED[31:4]	28
0xBB007A90	VLAN_PORT_ITAG_TPID_CMP_MSK [53].ITPID_CMP_MSK[3:0]	4
0xBB007B10	VLAN_PORT_ITAG_TPID_CMP_MSK [54].RESERVED[31:4]	28
0xBB007B10	VLAN_PORT_ITAG_TPID_CMP_MSK [54].ITPID_CMP_MSK[3:0]	4
0xBB007B90	VLAN_PORT_ITAG_TPID_CMP_MSK [55].RESERVED[31:4]	28
0xBB007B90	VLAN_PORT_ITAG_TPID_CMP_MSK [55].ITPID_CMP_MSK[3:0]	4
0xBB007C10	VLAN_PORT_ITAG_TPID_CMP_MSK [56].RESERVED[31:4]	28
0xBB007C10	VLAN_PORT_ITAG_TPID_CMP_MSK [56].ITPID_CMP_MSK[3:0]	4
0xBB009E04	VLAN_PORT_IVC_CTRL [0].RESERVED[31:7]	25
0xBB009E04	VLAN_PORT_IVC_CTRL [0].VID_RANGE_SET_IDX[6:5]	2
0xBB009E04	VLAN_PORT_IVC_CTRL [0].IVC_EN[4:4]	1
0xBB009E04	VLAN_PORT_IVC_CTRL [0].IP_VLAN_EN[3:3]	1
0xBB009E04	VLAN_PORT_IVC_CTRL [0].MAC_VLAN_EN[2:2]	1
0xBB009E04	VLAN_PORT_IVC_CTRL [0].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E04	VLAN_PORT_IVC_CTRL [0].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E08	VLAN_PORT_IVC_CTRL [1].RESERVED[31:7]	25

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009E08	VLAN_PORT_IVC_CTRL [1].VID_RANGE_SET_IDX[6:5]	2
0xBB009E08	VLAN_PORT_IVC_CTRL [1].IVC_EN[4:4]	1
0xBB009E08	VLAN_PORT_IVC_CTRL [1].IP_VLAN_EN[3:3]	1
0xBB009E08	VLAN_PORT_IVC_CTRL [1].MAC_VLAN_EN[2:2]	1
0xBB009E08	VLAN_PORT_IVC_CTRL [1].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E08	VLAN_PORT_IVC_CTRL [1].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].RESERVED[31:7]	25
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].VID_RANGE_SET_IDX[6:5]	2
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].IVC_EN[4:4]	1
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].IP_VLAN_EN[3:3]	1
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].MAC_VLAN_EN[2:2]	1
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E0C	VLAN_PORT_IVC_CTRL [2].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E10	VLAN_PORT_IVC_CTRL [3].RESERVED[31:7]	25
0xBB009E10	VLAN_PORT_IVC_CTRL [3].VID_RANGE_SET_IDX[6:5]	2
0xBB009E10	VLAN_PORT_IVC_CTRL [3].IVC_EN[4:4]	1
0xBB009E10	VLAN_PORT_IVC_CTRL [3].IP_VLAN_EN[3:3]	1
0xBB009E10	VLAN_PORT_IVC_CTRL [3].MAC_VLAN_EN[2:2]	1
0xBB009E10	VLAN_PORT_IVC_CTRL [3].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E10	VLAN_PORT_IVC_CTRL [3].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E14	VLAN_PORT_IVC_CTRL [4].RESERVED[31:7]	25
0xBB009E14	VLAN_PORT_IVC_CTRL [4].VID_RANGE_SET_IDX[6:5]	2
0xBB009E14	VLAN_PORT_IVC_CTRL [4].IVC_EN[4:4]	1
0xBB009E14	VLAN_PORT_IVC_CTRL [4].IP_VLAN_EN[3:3]	1
0xBB009E14	VLAN_PORT_IVC_CTRL [4].MAC_VLAN_EN[2:2]	1
0xBB009E14	VLAN_PORT_IVC_CTRL [4].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E14	VLAN_PORT_IVC_CTRL [4].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E18	VLAN_PORT_IVC_CTRL [5].RESERVED[31:7]	25
0xBB009E18	VLAN_PORT_IVC_CTRL [5].VID_RANGE_SET_IDX[6:5]	2
0xBB009E18	VLAN_PORT_IVC_CTRL [5].IVC_EN[4:4]	1
0xBB009E18	VLAN_PORT_IVC_CTRL [5].IP_VLAN_EN[3:3]	1
0xBB009E18	VLAN_PORT_IVC_CTRL [5].MAC_VLAN_EN[2:2]	1
0xBB009E18	VLAN_PORT_IVC_CTRL [5].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E18	VLAN_PORT_IVC_CTRL [5].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].RESERVED[31:7]	25
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].VID_RANGE_SET_IDX[6:5]	2
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].IVC_EN[4:4]	1
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].IP_VLAN_EN[3:3]	1
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].MAC_VLAN_EN[2:2]	1
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E1C	VLAN_PORT_IVC_CTRL [6].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E20	VLAN_PORT_IVC_CTRL [7].RESERVED[31:7]	25
0xBB009E20	VLAN_PORT_IVC_CTRL [7].VID_RANGE_SET_IDX[6:5]	2
0xBB009E20	VLAN_PORT_IVC_CTRL [7].IVC_EN[4:4]	1
0xBB009E20	VLAN_PORT_IVC_CTRL [7].IP_VLAN_EN[3:3]	1
0xBB009E20	VLAN_PORT_IVC_CTRL [7].MAC_VLAN_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009E20	VLAN_PORT_IVC_CTRL [7].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E20	VLAN_PORT_IVC_CTRL [7].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E24	VLAN_PORT_IVC_CTRL [8].RESERVED[31:7]	25
0xBB009E24	VLAN_PORT_IVC_CTRL [8].VID_RANGE_SET_IDX[6:5]	2
0xBB009E24	VLAN_PORT_IVC_CTRL [8].IVC_EN[4:4]	1
0xBB009E24	VLAN_PORT_IVC_CTRL [8].IP_VLAN_EN[3:3]	1
0xBB009E24	VLAN_PORT_IVC_CTRL [8].MAC_VLAN_EN[2:2]	1
0xBB009E24	VLAN_PORT_IVC_CTRL [8].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E24	VLAN_PORT_IVC_CTRL [8].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E28	VLAN_PORT_IVC_CTRL [9].RESERVED[31:7]	25
0xBB009E28	VLAN_PORT_IVC_CTRL [9].VID_RANGE_SET_IDX[6:5]	2
0xBB009E28	VLAN_PORT_IVC_CTRL [9].IVC_EN[4:4]	1
0xBB009E28	VLAN_PORT_IVC_CTRL [9].IP_VLAN_EN[3:3]	1
0xBB009E28	VLAN_PORT_IVC_CTRL [9].MAC_VLAN_EN[2:2]	1
0xBB009E28	VLAN_PORT_IVC_CTRL [9].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E28	VLAN_PORT_IVC_CTRL [9].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].RESERVED[31:7]	25
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].VID_RANGE_SET_IDX[6:5]	2
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].IVC_EN[4:4]	1
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].IP_VLAN_EN[3:3]	1
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].MAC_VLAN_EN[2:2]	1
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E2C	VLAN_PORT_IVC_CTRL [10].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E30	VLAN_PORT_IVC_CTRL [11].RESERVED[31:7]	25
0xBB009E30	VLAN_PORT_IVC_CTRL [11].VID_RANGE_SET_IDX[6:5]	2
0xBB009E30	VLAN_PORT_IVC_CTRL [11].IVC_EN[4:4]	1
0xBB009E30	VLAN_PORT_IVC_CTRL [11].IP_VLAN_EN[3:3]	1
0xBB009E30	VLAN_PORT_IVC_CTRL [11].MAC_VLAN_EN[2:2]	1
0xBB009E30	VLAN_PORT_IVC_CTRL [11].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E30	VLAN_PORT_IVC_CTRL [11].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E34	VLAN_PORT_IVC_CTRL [12].RESERVED[31:7]	25
0xBB009E34	VLAN_PORT_IVC_CTRL [12].VID_RANGE_SET_IDX[6:5]	2
0xBB009E34	VLAN_PORT_IVC_CTRL [12].IVC_EN[4:4]	1
0xBB009E34	VLAN_PORT_IVC_CTRL [12].IP_VLAN_EN[3:3]	1
0xBB009E34	VLAN_PORT_IVC_CTRL [12].MAC_VLAN_EN[2:2]	1
0xBB009E34	VLAN_PORT_IVC_CTRL [12].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E34	VLAN_PORT_IVC_CTRL [12].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E38	VLAN_PORT_IVC_CTRL [13].RESERVED[31:7]	25
0xBB009E38	VLAN_PORT_IVC_CTRL [13].VID_RANGE_SET_IDX[6:5]	2
0xBB009E38	VLAN_PORT_IVC_CTRL [13].IVC_EN[4:4]	1
0xBB009E38	VLAN_PORT_IVC_CTRL [13].IP_VLAN_EN[3:3]	1
0xBB009E38	VLAN_PORT_IVC_CTRL [13].MAC_VLAN_EN[2:2]	1
0xBB009E38	VLAN_PORT_IVC_CTRL [13].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E38	VLAN_PORT_IVC_CTRL [13].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].RESERVED[31:7]	25
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].VID_RANGE_SET_IDX[6:5]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].IVC_EN[4:4]	1
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].IP_VLAN_EN[3:3]	1
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].MAC_VLAN_EN[2:2]	1
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E3C	VLAN_PORT_IVC_CTRL [14].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E40	VLAN_PORT_IVC_CTRL [15].RESERVED[31:7]	25
0xBB009E40	VLAN_PORT_IVC_CTRL [15].VID_RANGE_SET_IDX[6:5]	2
0xBB009E40	VLAN_PORT_IVC_CTRL [15].IVC_EN[4:4]	1
0xBB009E40	VLAN_PORT_IVC_CTRL [15].IP_VLAN_EN[3:3]	1
0xBB009E40	VLAN_PORT_IVC_CTRL [15].MAC_VLAN_EN[2:2]	1
0xBB009E40	VLAN_PORT_IVC_CTRL [15].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E40	VLAN_PORT_IVC_CTRL [15].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E44	VLAN_PORT_IVC_CTRL [16].RESERVED[31:7]	25
0xBB009E44	VLAN_PORT_IVC_CTRL [16].VID_RANGE_SET_IDX[6:5]	2
0xBB009E44	VLAN_PORT_IVC_CTRL [16].IVC_EN[4:4]	1
0xBB009E44	VLAN_PORT_IVC_CTRL [16].IP_VLAN_EN[3:3]	1
0xBB009E44	VLAN_PORT_IVC_CTRL [16].MAC_VLAN_EN[2:2]	1
0xBB009E44	VLAN_PORT_IVC_CTRL [16].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E44	VLAN_PORT_IVC_CTRL [16].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E48	VLAN_PORT_IVC_CTRL [17].RESERVED[31:7]	25
0xBB009E48	VLAN_PORT_IVC_CTRL [17].VID_RANGE_SET_IDX[6:5]	2
0xBB009E48	VLAN_PORT_IVC_CTRL [17].IVC_EN[4:4]	1
0xBB009E48	VLAN_PORT_IVC_CTRL [17].IP_VLAN_EN[3:3]	1
0xBB009E48	VLAN_PORT_IVC_CTRL [17].MAC_VLAN_EN[2:2]	1
0xBB009E48	VLAN_PORT_IVC_CTRL [17].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E48	VLAN_PORT_IVC_CTRL [17].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].RESERVED[31:7]	25
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].VID_RANGE_SET_IDX[6:5]	2
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].IVC_EN[4:4]	1
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].IP_VLAN_EN[3:3]	1
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].MAC_VLAN_EN[2:2]	1
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E4C	VLAN_PORT_IVC_CTRL [18].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E50	VLAN_PORT_IVC_CTRL [19].RESERVED[31:7]	25
0xBB009E50	VLAN_PORT_IVC_CTRL [19].VID_RANGE_SET_IDX[6:5]	2
0xBB009E50	VLAN_PORT_IVC_CTRL [19].IVC_EN[4:4]	1
0xBB009E50	VLAN_PORT_IVC_CTRL [19].IP_VLAN_EN[3:3]	1
0xBB009E50	VLAN_PORT_IVC_CTRL [19].MAC_VLAN_EN[2:2]	1
0xBB009E50	VLAN_PORT_IVC_CTRL [19].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E50	VLAN_PORT_IVC_CTRL [19].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E54	VLAN_PORT_IVC_CTRL [20].RESERVED[31:7]	25
0xBB009E54	VLAN_PORT_IVC_CTRL [20].VID_RANGE_SET_IDX[6:5]	2
0xBB009E54	VLAN_PORT_IVC_CTRL [20].IVC_EN[4:4]	1
0xBB009E54	VLAN_PORT_IVC_CTRL [20].IP_VLAN_EN[3:3]	1
0xBB009E54	VLAN_PORT_IVC_CTRL [20].MAC_VLAN_EN[2:2]	1
0xBB009E54	VLAN_PORT_IVC_CTRL [20].OTAG_LU_MIS_DROP[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009E54	VLAN_PORT_IVC_CTRL [20].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E58	VLAN_PORT_IVC_CTRL [21].RESERVED[31:7]	25
0xBB009E58	VLAN_PORT_IVC_CTRL [21].VID_RANGE_SET_IDX[6:5]	2
0xBB009E58	VLAN_PORT_IVC_CTRL [21].IVC_EN[4:4]	1
0xBB009E58	VLAN_PORT_IVC_CTRL [21].IP_VLAN_EN[3:3]	1
0xBB009E58	VLAN_PORT_IVC_CTRL [21].MAC_VLAN_EN[2:2]	1
0xBB009E58	VLAN_PORT_IVC_CTRL [21].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E58	VLAN_PORT_IVC_CTRL [21].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].RESERVED[31:7]	25
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].VID_RANGE_SET_IDX[6:5]	2
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].IVC_EN[4:4]	1
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].IP_VLAN_EN[3:3]	1
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].MAC_VLAN_EN[2:2]	1
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E5C	VLAN_PORT_IVC_CTRL [22].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E60	VLAN_PORT_IVC_CTRL [23].RESERVED[31:7]	25
0xBB009E60	VLAN_PORT_IVC_CTRL [23].VID_RANGE_SET_IDX[6:5]	2
0xBB009E60	VLAN_PORT_IVC_CTRL [23].IVC_EN[4:4]	1
0xBB009E60	VLAN_PORT_IVC_CTRL [23].IP_VLAN_EN[3:3]	1
0xBB009E60	VLAN_PORT_IVC_CTRL [23].MAC_VLAN_EN[2:2]	1
0xBB009E60	VLAN_PORT_IVC_CTRL [23].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E60	VLAN_PORT_IVC_CTRL [23].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E64	VLAN_PORT_IVC_CTRL [24].RESERVED[31:7]	25
0xBB009E64	VLAN_PORT_IVC_CTRL [24].VID_RANGE_SET_IDX[6:5]	2
0xBB009E64	VLAN_PORT_IVC_CTRL [24].IVC_EN[4:4]	1
0xBB009E64	VLAN_PORT_IVC_CTRL [24].IP_VLAN_EN[3:3]	1
0xBB009E64	VLAN_PORT_IVC_CTRL [24].MAC_VLAN_EN[2:2]	1
0xBB009E64	VLAN_PORT_IVC_CTRL [24].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E64	VLAN_PORT_IVC_CTRL [24].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E68	VLAN_PORT_IVC_CTRL [25].RESERVED[31:7]	25
0xBB009E68	VLAN_PORT_IVC_CTRL [25].VID_RANGE_SET_IDX[6:5]	2
0xBB009E68	VLAN_PORT_IVC_CTRL [25].IVC_EN[4:4]	1
0xBB009E68	VLAN_PORT_IVC_CTRL [25].IP_VLAN_EN[3:3]	1
0xBB009E68	VLAN_PORT_IVC_CTRL [25].MAC_VLAN_EN[2:2]	1
0xBB009E68	VLAN_PORT_IVC_CTRL [25].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E68	VLAN_PORT_IVC_CTRL [25].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].RESERVED[31:7]	25
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].VID_RANGE_SET_IDX[6:5]	2
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].IVC_EN[4:4]	1
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].IP_VLAN_EN[3:3]	1
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].MAC_VLAN_EN[2:2]	1
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E6C	VLAN_PORT_IVC_CTRL [26].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E70	VLAN_PORT_IVC_CTRL [27].RESERVED[31:7]	25
0xBB009E70	VLAN_PORT_IVC_CTRL [27].VID_RANGE_SET_IDX[6:5]	2
0xBB009E70	VLAN_PORT_IVC_CTRL [27].IVC_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009E70	VLAN_PORT_IVC_CTRL [27].IP_VLAN_EN[3:3]	1
0xBB009E70	VLAN_PORT_IVC_CTRL [27].MAC_VLAN_EN[2:2]	1
0xBB009E70	VLAN_PORT_IVC_CTRL [27].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E70	VLAN_PORT_IVC_CTRL [27].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E74	VLAN_PORT_IVC_CTRL [28].RESERVED[31:7]	25
0xBB009E74	VLAN_PORT_IVC_CTRL [28].VID_RANGE_SET_IDX[6:5]	2
0xBB009E74	VLAN_PORT_IVC_CTRL [28].IVC_EN[4:4]	1
0xBB009E74	VLAN_PORT_IVC_CTRL [28].IP_VLAN_EN[3:3]	1
0xBB009E74	VLAN_PORT_IVC_CTRL [28].MAC_VLAN_EN[2:2]	1
0xBB009E74	VLAN_PORT_IVC_CTRL [28].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E74	VLAN_PORT_IVC_CTRL [28].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E78	VLAN_PORT_IVC_CTRL [29].RESERVED[31:7]	25
0xBB009E78	VLAN_PORT_IVC_CTRL [29].VID_RANGE_SET_IDX[6:5]	2
0xBB009E78	VLAN_PORT_IVC_CTRL [29].IVC_EN[4:4]	1
0xBB009E78	VLAN_PORT_IVC_CTRL [29].IP_VLAN_EN[3:3]	1
0xBB009E78	VLAN_PORT_IVC_CTRL [29].MAC_VLAN_EN[2:2]	1
0xBB009E78	VLAN_PORT_IVC_CTRL [29].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E78	VLAN_PORT_IVC_CTRL [29].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].RESERVED[31:7]	25
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].VID_RANGE_SET_IDX[6:5]	2
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].IVC_EN[4:4]	1
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].IP_VLAN_EN[3:3]	1
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].MAC_VLAN_EN[2:2]	1
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E7C	VLAN_PORT_IVC_CTRL [30].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E80	VLAN_PORT_IVC_CTRL [31].RESERVED[31:7]	25
0xBB009E80	VLAN_PORT_IVC_CTRL [31].VID_RANGE_SET_IDX[6:5]	2
0xBB009E80	VLAN_PORT_IVC_CTRL [31].IVC_EN[4:4]	1
0xBB009E80	VLAN_PORT_IVC_CTRL [31].IP_VLAN_EN[3:3]	1
0xBB009E80	VLAN_PORT_IVC_CTRL [31].MAC_VLAN_EN[2:2]	1
0xBB009E80	VLAN_PORT_IVC_CTRL [31].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E80	VLAN_PORT_IVC_CTRL [31].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E84	VLAN_PORT_IVC_CTRL [32].RESERVED[31:7]	25
0xBB009E84	VLAN_PORT_IVC_CTRL [32].VID_RANGE_SET_IDX[6:5]	2
0xBB009E84	VLAN_PORT_IVC_CTRL [32].IVC_EN[4:4]	1
0xBB009E84	VLAN_PORT_IVC_CTRL [32].IP_VLAN_EN[3:3]	1
0xBB009E84	VLAN_PORT_IVC_CTRL [32].MAC_VLAN_EN[2:2]	1
0xBB009E84	VLAN_PORT_IVC_CTRL [32].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E84	VLAN_PORT_IVC_CTRL [32].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E88	VLAN_PORT_IVC_CTRL [33].RESERVED[31:7]	25
0xBB009E88	VLAN_PORT_IVC_CTRL [33].VID_RANGE_SET_IDX[6:5]	2
0xBB009E88	VLAN_PORT_IVC_CTRL [33].IVC_EN[4:4]	1
0xBB009E88	VLAN_PORT_IVC_CTRL [33].IP_VLAN_EN[3:3]	1
0xBB009E88	VLAN_PORT_IVC_CTRL [33].MAC_VLAN_EN[2:2]	1
0xBB009E88	VLAN_PORT_IVC_CTRL [33].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E88	VLAN_PORT_IVC_CTRL [33].ITAG_LU_MIS_DROP[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].RESERVED[31:7]	25
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].VID_RANGE_SET_IDX[6:5]	2
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].IVC_EN[4:4]	1
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].IP_VLAN_EN[3:3]	1
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].MAC_VLAN_EN[2:2]	1
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E8C	VLAN_PORT_IVC_CTRL [34].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E90	VLAN_PORT_IVC_CTRL [35].RESERVED[31:7]	25
0xBB009E90	VLAN_PORT_IVC_CTRL [35].VID_RANGE_SET_IDX[6:5]	2
0xBB009E90	VLAN_PORT_IVC_CTRL [35].IVC_EN[4:4]	1
0xBB009E90	VLAN_PORT_IVC_CTRL [35].IP_VLAN_EN[3:3]	1
0xBB009E90	VLAN_PORT_IVC_CTRL [35].MAC_VLAN_EN[2:2]	1
0xBB009E90	VLAN_PORT_IVC_CTRL [35].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E90	VLAN_PORT_IVC_CTRL [35].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E94	VLAN_PORT_IVC_CTRL [36].RESERVED[31:7]	25
0xBB009E94	VLAN_PORT_IVC_CTRL [36].VID_RANGE_SET_IDX[6:5]	2
0xBB009E94	VLAN_PORT_IVC_CTRL [36].IVC_EN[4:4]	1
0xBB009E94	VLAN_PORT_IVC_CTRL [36].IP_VLAN_EN[3:3]	1
0xBB009E94	VLAN_PORT_IVC_CTRL [36].MAC_VLAN_EN[2:2]	1
0xBB009E94	VLAN_PORT_IVC_CTRL [36].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E94	VLAN_PORT_IVC_CTRL [36].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E98	VLAN_PORT_IVC_CTRL [37].RESERVED[31:7]	25
0xBB009E98	VLAN_PORT_IVC_CTRL [37].VID_RANGE_SET_IDX[6:5]	2
0xBB009E98	VLAN_PORT_IVC_CTRL [37].IVC_EN[4:4]	1
0xBB009E98	VLAN_PORT_IVC_CTRL [37].IP_VLAN_EN[3:3]	1
0xBB009E98	VLAN_PORT_IVC_CTRL [37].MAC_VLAN_EN[2:2]	1
0xBB009E98	VLAN_PORT_IVC_CTRL [37].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E98	VLAN_PORT_IVC_CTRL [37].ITAG_LU_MIS_DROP[0:0]	1
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].RESERVED[31:7]	25
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].VID_RANGE_SET_IDX[6:5]	2
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].IVC_EN[4:4]	1
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].IP_VLAN_EN[3:3]	1
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].MAC_VLAN_EN[2:2]	1
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].OTAG_LU_MIS_DROP[1:1]	1
0xBB009E9C	VLAN_PORT_IVC_CTRL [38].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].RESERVED[31:7]	25
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].VID_RANGE_SET_IDX[6:5]	2
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].IVC_EN[4:4]	1
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].IP_VLAN_EN[3:3]	1
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].MAC_VLAN_EN[2:2]	1
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EA0	VLAN_PORT_IVC_CTRL [39].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].RESERVED[31:7]	25
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].VID_RANGE_SET_IDX[6:5]	2
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].IVC_EN[4:4]	1
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].IP_VLAN_EN[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].MAC_VLAN_EN[2:2]	1
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EA4	VLAN_PORT_IVC_CTRL [40].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].RESERVED[31:7]	25
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].VID_RANGE_SET_IDX[6:5]	2
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].IVC_EN[4:4]	1
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].IP_VLAN_EN[3:3]	1
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].MAC_VLAN_EN[2:2]	1
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EA8	VLAN_PORT_IVC_CTRL [41].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].RESERVED[31:7]	25
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].VID_RANGE_SET_IDX[6:5]	2
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].IVC_EN[4:4]	1
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].IP_VLAN_EN[3:3]	1
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].MAC_VLAN_EN[2:2]	1
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EAC	VLAN_PORT_IVC_CTRL [42].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].RESERVED[31:7]	25
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].VID_RANGE_SET_IDX[6:5]	2
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].IVC_EN[4:4]	1
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].IP_VLAN_EN[3:3]	1
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].MAC_VLAN_EN[2:2]	1
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EB0	VLAN_PORT_IVC_CTRL [43].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].RESERVED[31:7]	25
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].VID_RANGE_SET_IDX[6:5]	2
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].IVC_EN[4:4]	1
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].IP_VLAN_EN[3:3]	1
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].MAC_VLAN_EN[2:2]	1
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].CTAG_LU_MIS_DROP[1:1]	1
0xBB009EB4	VLAN_PORT_IVC_CTRL [44].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].RESERVED[31:7]	25
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].VID_RANGE_SET_IDX[6:5]	2
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].IVC_EN[4:4]	1
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].IP_VLAN_EN[3:3]	1
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].MAC_VLAN_EN[2:2]	1
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EB8	VLAN_PORT_IVC_CTRL [45].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].RESERVED[31:7]	25
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].VID_RANGE_SET_IDX[6:5]	2
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].IVC_EN[4:4]	1
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].IP_VLAN_EN[3:3]	1
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].MAC_VLAN_EN[2:2]	1
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EBC	VLAN_PORT_IVC_CTRL [46].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].RESERVED[31:7]	25

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].VID_RANGE_SET_IDX[6:5]	2
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].IVC_EN[4:4]	1
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].IP_VLAN_EN[3:3]	1
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].MAC_VLAN_EN[2:2]	1
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EC0	VLAN_PORT_IVC_CTRL [47].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].RESERVED[31:7]	25
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].VID_RANGE_SET_IDX[6:5]	2
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].IVC_EN[4:4]	1
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].IP_VLAN_EN[3:3]	1
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].MAC_VLAN_EN[2:2]	1
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EC4	VLAN_PORT_IVC_CTRL [48].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].RESERVED[31:7]	25
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].VID_RANGE_SET_IDX[6:5]	2
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].IVC_EN[4:4]	1
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].IP_VLAN_EN[3:3]	1
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].MAC_VLAN_EN[2:2]	1
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EC8	VLAN_PORT_IVC_CTRL [49].ITAG_LU_MIS_DROP[0:0]	1
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].RESERVED[31:7]	25
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].VID_RANGE_SET_IDX[6:5]	2
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].IVC_EN[4:4]	1
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].IP_VLAN_EN[3:3]	1
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].MAC_VLAN_EN[2:2]	1
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].OTAG_LU_MIS_DROP[1:1]	1
0xBB009ECC	VLAN_PORT_IVC_CTRL [50].ITAG_LU_MIS_DROP[0:0]	1
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].RESERVED[31:7]	25
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].VID_RANGE_SET_IDX[6:5]	2
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].IVC_EN[4:4]	1
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].IP_VLAN_EN[3:3]	1
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].MAC_VLAN_EN[2:2]	1
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].OTAG_LU_MIS_DROP[1:1]	1
0xBB009ED0	VLAN_PORT_IVC_CTRL [51].ITAG_LU_MIS_DROP[0:0]	1
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].RESERVED[31:7]	25
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].VID_RANGE_SET_IDX[6:5]	2
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].IVC_EN[4:4]	1
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].IP_VLAN_EN[3:3]	1
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].MAC_VLAN_EN[2:2]	1
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].OTAG_LU_MIS_DROP[1:1]	1
0xBB009ED4	VLAN_PORT_IVC_CTRL [52].ITAG_LU_MIS_DROP[0:0]	1
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].RESERVED[31:7]	25
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].VID_RANGE_SET_IDX[6:5]	2
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].IVC_EN[4:4]	1
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].IP_VLAN_EN[3:3]	1
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].MAC_VLAN_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].OTAG_LU_MIS_DROP[1:1]	1
0xBB009ED8	VLAN_PORT_IVC_CTRL [53].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].RESERVED[31:7]	25
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].VID_RANGE_SET_IDX[6:5]	2
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].IVC_EN[4:4]	1
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].IP_VLAN_EN[3:3]	1
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].MAC_VLAN_EN[2:2]	1
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EDC	VLAN_PORT_IVC_CTRL [54].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].RESERVED[31:7]	25
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].VID_RANGE_SET_IDX[6:5]	2
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].IVC_EN[4:4]	1
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].IP_VLAN_EN[3:3]	1
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].MAC_VLAN_EN[2:2]	1
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EE0	VLAN_PORT_IVC_CTRL [55].ITAG_LU_MIS_DROP[0:0]	1
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].RESERVED[31:7]	25
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].VID_RANGE_SET_IDX[6:5]	2
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].IVC_EN[4:4]	1
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].IP_VLAN_EN[3:3]	1
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].MAC_VLAN_EN[2:2]	1
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].OTAG_LU_MIS_DROP[1:1]	1
0xBB009EE4	VLAN_PORT_IVC_CTRL [56].ITAG_LU_MIS_DROP[0:0]	1
0xBB0096D0	VLAN_PORT_L2TBL_CVNT_CTRL [0].RESERVED[31:4]	28
0xBB0096D0	VLAN_PORT_L2TBL_CVNT_CTRL [0].PRI_CVNT_EN[3:3]	1
0xBB0096D0	VLAN_PORT_L2TBL_CVNT_CTRL [0].PRI_TAG_ACT[2:2]	1
0xBB0096D0	VLAN_PORT_L2TBL_CVNT_CTRL [0].VID_SELECT[1:1]	1
0xBB0096D0	VLAN_PORT_L2TBL_CVNT_CTRL [0].VID_CVNT_EN[0:0]	1
0xBB0096D4	VLAN_PORT_L2TBL_CVNT_CTRL [1].RESERVED[31:4]	28
0xBB0096D4	VLAN_PORT_L2TBL_CVNT_CTRL [1].PRI_CVNT_EN[3:3]	1
0xBB0096D4	VLAN_PORT_L2TBL_CVNT_CTRL [1].PRI_TAG_ACT[2:2]	1
0xBB0096D4	VLAN_PORT_L2TBL_CVNT_CTRL [1].VID_SELECT[1:1]	1
0xBB0096D4	VLAN_PORT_L2TBL_CVNT_CTRL [1].VID_CVNT_EN[0:0]	1
0xBB0096D8	VLAN_PORT_L2TBL_CVNT_CTRL [2].RESERVED[31:4]	28
0xBB0096D8	VLAN_PORT_L2TBL_CVNT_CTRL [2].PRI_CVNT_EN[3:3]	1
0xBB0096D8	VLAN_PORT_L2TBL_CVNT_CTRL [2].PRI_TAG_ACT[2:2]	1
0xBB0096D8	VLAN_PORT_L2TBL_CVNT_CTRL [2].VID_SELECT[1:1]	1
0xBB0096D8	VLAN_PORT_L2TBL_CVNT_CTRL [2].VID_CVNT_EN[0:0]	1
0xBB0096DC	VLAN_PORT_L2TBL_CVNT_CTRL [3].RESERVED[31:4]	28
0xBB0096DC	VLAN_PORT_L2TBL_CVNT_CTRL [3].PRI_CVNT_EN[3:3]	1
0xBB0096DC	VLAN_PORT_L2TBL_CVNT_CTRL [3].PRI_TAG_ACT[2:2]	1
0xBB0096DC	VLAN_PORT_L2TBL_CVNT_CTRL [3].VID_SELECT[1:1]	1
0xBB0096DC	VLAN_PORT_L2TBL_CVNT_CTRL [3].VID_CVNT_EN[0:0]	1
0xBB0096E0	VLAN_PORT_L2TBL_CVNT_CTRL [4].RESERVED[31:4]	28
0xBB0096E0	VLAN_PORT_L2TBL_CVNT_CTRL [4].PRI_CVNT_EN[3:3]	1
0xBB0096E0	VLAN_PORT_L2TBL_CVNT_CTRL [4].PRI_TAG_ACT[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0096E0	VLAN_PORT_L2TBL_CVNT_CTRL [4].VID_SELECT[1:1]	1
0xBB0096E0	VLAN_PORT_L2TBL_CVNT_CTRL [4].VID_CVNT_EN[0:0]	1
0xBB0096E4	VLAN_PORT_L2TBL_CVNT_CTRL [5].RESERVED[31:4]	28
0xBB0096E4	VLAN_PORT_L2TBL_CVNT_CTRL [5].PRI_CVNT_EN[3:3]	1
0xBB0096E4	VLAN_PORT_L2TBL_CVNT_CTRL [5].PRI_TAG_ACT[2:2]	1
0xBB0096E4	VLAN_PORT_L2TBL_CVNT_CTRL [5].VID_SELECT[1:1]	1
0xBB0096E4	VLAN_PORT_L2TBL_CVNT_CTRL [5].VID_CVNT_EN[0:0]	1
0xBB0096E8	VLAN_PORT_L2TBL_CVNT_CTRL [6].RESERVED[31:4]	28
0xBB0096E8	VLAN_PORT_L2TBL_CVNT_CTRL [6].PRI_CVNT_EN[3:3]	1
0xBB0096E8	VLAN_PORT_L2TBL_CVNT_CTRL [6].PRI_TAG_ACT[2:2]	1
0xBB0096E8	VLAN_PORT_L2TBL_CVNT_CTRL [6].VID_SELECT[1:1]	1
0xBB0096E8	VLAN_PORT_L2TBL_CVNT_CTRL [6].VID_CVNT_EN[0:0]	1
0xBB0096EC	VLAN_PORT_L2TBL_CVNT_CTRL [7].RESERVED[31:4]	28
0xBB0096EC	VLAN_PORT_L2TBL_CVNT_CTRL [7].PRI_CVNT_EN[3:3]	1
0xBB0096EC	VLAN_PORT_L2TBL_CVNT_CTRL [7].PRI_TAG_ACT[2:2]	1
0xBB0096EC	VLAN_PORT_L2TBL_CVNT_CTRL [7].VID_SELECT[1:1]	1
0xBB0096EC	VLAN_PORT_L2TBL_CVNT_CTRL [7].VID_CVNT_EN[0:0]	1
0xBB0096F0	VLAN_PORT_L2TBL_CVNT_CTRL [8].RESERVED[31:4]	28
0xBB0096F0	VLAN_PORT_L2TBL_CVNT_CTRL [8].PRI_CVNT_EN[3:3]	1
0xBB0096F0	VLAN_PORT_L2TBL_CVNT_CTRL [8].PRI_TAG_ACT[2:2]	1
0xBB0096F0	VLAN_PORT_L2TBL_CVNT_CTRL [8].VID_SELECT[1:1]	1
0xBB0096F0	VLAN_PORT_L2TBL_CVNT_CTRL [8].VID_CVNT_EN[0:0]	1
0xBB0096F4	VLAN_PORT_L2TBL_CVNT_CTRL [9].RESERVED[31:4]	28
0xBB0096F4	VLAN_PORT_L2TBL_CVNT_CTRL [9].PRI_CVNT_EN[3:3]	1
0xBB0096F4	VLAN_PORT_L2TBL_CVNT_CTRL [9].PRI_TAG_ACT[2:2]	1
0xBB0096F4	VLAN_PORT_L2TBL_CVNT_CTRL [9].VID_SELECT[1:1]	1
0xBB0096F4	VLAN_PORT_L2TBL_CVNT_CTRL [9].VID_CVNT_EN[0:0]	1
0xBB0096F8	VLAN_PORT_L2TBL_CVNT_CTRL [10].RESERVED[31:4]	28
0xBB0096F8	VLAN_PORT_L2TBL_CVNT_CTRL [10].PRI_CVNT_EN[3:3]	1
0xBB0096F8	VLAN_PORT_L2TBL_CVNT_CTRL [10].PRI_TAG_ACT[2:2]	1
0xBB0096F8	VLAN_PORT_L2TBL_CVNT_CTRL [10].VID_SELECT[1:1]	1
0xBB0096F8	VLAN_PORT_L2TBL_CVNT_CTRL [10].VID_CVNT_EN[0:0]	1
0xBB0096FC	VLAN_PORT_L2TBL_CVNT_CTRL [11].RESERVED[31:4]	28
0xBB0096FC	VLAN_PORT_L2TBL_CVNT_CTRL [11].PRI_CVNT_EN[3:3]	1
0xBB0096FC	VLAN_PORT_L2TBL_CVNT_CTRL [11].PRI_TAG_ACT[2:2]	1
0xBB0096FC	VLAN_PORT_L2TBL_CVNT_CTRL [11].VID_SELECT[1:1]	1
0xBB0096FC	VLAN_PORT_L2TBL_CVNT_CTRL [11].VID_CVNT_EN[0:0]	1
0xBB009700	VLAN_PORT_L2TBL_CVNT_CTRL [12].RESERVED[31:4]	28
0xBB009700	VLAN_PORT_L2TBL_CVNT_CTRL [12].PRI_CVNT_EN[3:3]	1
0xBB009700	VLAN_PORT_L2TBL_CVNT_CTRL [12].PRI_TAG_ACT[2:2]	1
0xBB009700	VLAN_PORT_L2TBL_CVNT_CTRL [12].VID_SELECT[1:1]	1
0xBB009700	VLAN_PORT_L2TBL_CVNT_CTRL [12].VID_CVNT_EN[0:0]	1
0xBB009704	VLAN_PORT_L2TBL_CVNT_CTRL [13].RESERVED[31:4]	28
0xBB009704	VLAN_PORT_L2TBL_CVNT_CTRL [13].PRI_CVNT_EN[3:3]	1
0xBB009704	VLAN_PORT_L2TBL_CVNT_CTRL [13].PRI_TAG_ACT[2:2]	1
0xBB009704	VLAN_PORT_L2TBL_CVNT_CTRL [13].VID_SELECT[1:1]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009704	VLAN_PORT_L2TBL_CVNT_CTRL [13].VID_CVNT_EN[0:0]	1
0xBB009708	VLAN_PORT_L2TBL_CVNT_CTRL [14].RESERVED[31:4]	28
0xBB009708	VLAN_PORT_L2TBL_CVNT_CTRL [14].PRI_CVNT_EN[3:3]	1
0xBB009708	VLAN_PORT_L2TBL_CVNT_CTRL [14].PRI_TAG_ACT[2:2]	1
0xBB009708	VLAN_PORT_L2TBL_CVNT_CTRL [14].VID_SELECT[1:1]	1
0xBB009708	VLAN_PORT_L2TBL_CVNT_CTRL [14].VID_CVNT_EN[0:0]	1
0xBB00970C	VLAN_PORT_L2TBL_CVNT_CTRL [15].RESERVED[31:4]	28
0xBB00970C	VLAN_PORT_L2TBL_CVNT_CTRL [15].PRI_CVNT_EN[3:3]	1
0xBB00970C	VLAN_PORT_L2TBL_CVNT_CTRL [15].PRI_TAG_ACT[2:2]	1
0xBB00970C	VLAN_PORT_L2TBL_CVNT_CTRL [15].VID_SELECT[1:1]	1
0xBB00970C	VLAN_PORT_L2TBL_CVNT_CTRL [15].VID_CVNT_EN[0:0]	1
0xBB009710	VLAN_PORT_L2TBL_CVNT_CTRL [16].RESERVED[31:4]	28
0xBB009710	VLAN_PORT_L2TBL_CVNT_CTRL [16].PRI_CVNT_EN[3:3]	1
0xBB009710	VLAN_PORT_L2TBL_CVNT_CTRL [16].PRI_TAG_ACT[2:2]	1
0xBB009710	VLAN_PORT_L2TBL_CVNT_CTRL [16].VID_SELECT[1:1]	1
0xBB009710	VLAN_PORT_L2TBL_CVNT_CTRL [16].VID_CVNT_EN[0:0]	1
0xBB009714	VLAN_PORT_L2TBL_CVNT_CTRL [17].RESERVED[31:4]	28
0xBB009714	VLAN_PORT_L2TBL_CVNT_CTRL [17].PRI_CVNT_EN[3:3]	1
0xBB009714	VLAN_PORT_L2TBL_CVNT_CTRL [17].PRI_TAG_ACT[2:2]	1
0xBB009714	VLAN_PORT_L2TBL_CVNT_CTRL [17].VID_SELECT[1:1]	1
0xBB009714	VLAN_PORT_L2TBL_CVNT_CTRL [17].VID_CVNT_EN[0:0]	1
0xBB009718	VLAN_PORT_L2TBL_CVNT_CTRL [18].RESERVED[31:4]	28
0xBB009718	VLAN_PORT_L2TBL_CVNT_CTRL [18].PRI_CVNT_EN[3:3]	1
0xBB009718	VLAN_PORT_L2TBL_CVNT_CTRL [18].PRI_TAG_ACT[2:2]	1
0xBB009718	VLAN_PORT_L2TBL_CVNT_CTRL [18].VID_SELECT[1:1]	1
0xBB009718	VLAN_PORT_L2TBL_CVNT_CTRL [18].VID_CVNT_EN[0:0]	1
0xBB00971C	VLAN_PORT_L2TBL_CVNT_CTRL [19].RESERVED[31:4]	28
0xBB00971C	VLAN_PORT_L2TBL_CVNT_CTRL [19].PRI_CVNT_EN[3:3]	1
0xBB00971C	VLAN_PORT_L2TBL_CVNT_CTRL [19].PRI_TAG_ACT[2:2]	1
0xBB00971C	VLAN_PORT_L2TBL_CVNT_CTRL [19].VID_SELECT[1:1]	1
0xBB00971C	VLAN_PORT_L2TBL_CVNT_CTRL [19].VID_CVNT_EN[0:0]	1
0xBB009720	VLAN_PORT_L2TBL_CVNT_CTRL [20].RESERVED[31:4]	28
0xBB009720	VLAN_PORT_L2TBL_CVNT_CTRL [20].PRI_CVNT_EN[3:3]	1
0xBB009720	VLAN_PORT_L2TBL_CVNT_CTRL [20].PRI_TAG_ACT[2:2]	1
0xBB009720	VLAN_PORT_L2TBL_CVNT_CTRL [20].VID_SELECT[1:1]	1
0xBB009720	VLAN_PORT_L2TBL_CVNT_CTRL [20].VID_CVNT_EN[0:0]	1
0xBB009724	VLAN_PORT_L2TBL_CVNT_CTRL [21].RESERVED[31:4]	28
0xBB009724	VLAN_PORT_L2TBL_CVNT_CTRL [21].PRI_CVNT_EN[3:3]	1
0xBB009724	VLAN_PORT_L2TBL_CVNT_CTRL [21].PRI_TAG_ACT[2:2]	1
0xBB009724	VLAN_PORT_L2TBL_CVNT_CTRL [21].VID_SELECT[1:1]	1
0xBB009724	VLAN_PORT_L2TBL_CVNT_CTRL [21].VID_CVNT_EN[0:0]	1
0xBB009728	VLAN_PORT_L2TBL_CVNT_CTRL [22].RESERVED[31:4]	28
0xBB009728	VLAN_PORT_L2TBL_CVNT_CTRL [22].PRI_CVNT_EN[3:3]	1
0xBB009728	VLAN_PORT_L2TBL_CVNT_CTRL [22].PRI_TAG_ACT[2:2]	1
0xBB009728	VLAN_PORT_L2TBL_CVNT_CTRL [22].VID_SELECT[1:1]	1
0xBB009728	VLAN_PORT_L2TBL_CVNT_CTRL [22].VID_CVNT_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00972C	VLAN_PORT_L2TBL_CVNT_CTRL [23].RESERVED[31:4]	28
0xBB00972C	VLAN_PORT_L2TBL_CVNT_CTRL [23].PRI_CVNT_EN[3:3]	1
0xBB00972C	VLAN_PORT_L2TBL_CVNT_CTRL [23].PRI_TAG_ACT[2:2]	1
0xBB00972C	VLAN_PORT_L2TBL_CVNT_CTRL [23].VID_SELECT[1:1]	1
0xBB00972C	VLAN_PORT_L2TBL_CVNT_CTRL [23].VID_CVNT_EN[0:0]	1
0xBB009730	VLAN_PORT_L2TBL_CVNT_CTRL [24].RESERVED[31:4]	28
0xBB009730	VLAN_PORT_L2TBL_CVNT_CTRL [24].PRI_CVNT_EN[3:3]	1
0xBB009730	VLAN_PORT_L2TBL_CVNT_CTRL [24].PRI_TAG_ACT[2:2]	1
0xBB009730	VLAN_PORT_L2TBL_CVNT_CTRL [24].VID_SELECT[1:1]	1
0xBB009730	VLAN_PORT_L2TBL_CVNT_CTRL [24].VID_CVNT_EN[0:0]	1
0xBB009734	VLAN_PORT_L2TBL_CVNT_CTRL [25].RESERVED[31:4]	28
0xBB009734	VLAN_PORT_L2TBL_CVNT_CTRL [25].PRI_CVNT_EN[3:3]	1
0xBB009734	VLAN_PORT_L2TBL_CVNT_CTRL [25].PRI_TAG_ACT[2:2]	1
0xBB009734	VLAN_PORT_L2TBL_CVNT_CTRL [25].VID_SELECT[1:1]	1
0xBB009734	VLAN_PORT_L2TBL_CVNT_CTRL [25].VID_CVNT_EN[0:0]	1
0xBB009738	VLAN_PORT_L2TBL_CVNT_CTRL [26].RESERVED[31:4]	28
0xBB009738	VLAN_PORT_L2TBL_CVNT_CTRL [26].PRI_CVNT_EN[3:3]	1
0xBB009738	VLAN_PORT_L2TBL_CVNT_CTRL [26].PRI_TAG_ACT[2:2]	1
0xBB009738	VLAN_PORT_L2TBL_CVNT_CTRL [26].VID_SELECT[1:1]	1
0xBB009738	VLAN_PORT_L2TBL_CVNT_CTRL [26].VID_CVNT_EN[0:0]	1
0xBB00973C	VLAN_PORT_L2TBL_CVNT_CTRL [27].RESERVED[31:4]	28
0xBB00973C	VLAN_PORT_L2TBL_CVNT_CTRL [27].PRI_CVNT_EN[3:3]	1
0xBB00973C	VLAN_PORT_L2TBL_CVNT_CTRL [27].PRI_TAG_ACT[2:2]	1
0xBB00973C	VLAN_PORT_L2TBL_CVNT_CTRL [27].VID_SELECT[1:1]	1
0xBB00973C	VLAN_PORT_L2TBL_CVNT_CTRL [27].VID_CVNT_EN[0:0]	1
0xBB009740	VLAN_PORT_L2TBL_CVNT_CTRL [28].RESERVED[31:4]	28
0xBB009740	VLAN_PORT_L2TBL_CVNT_CTRL [28].PRI_CVNT_EN[3:3]	1
0xBB009740	VLAN_PORT_L2TBL_CVNT_CTRL [28].PRI_TAG_ACT[2:2]	1
0xBB009740	VLAN_PORT_L2TBL_CVNT_CTRL [28].VID_SELECT[1:1]	1
0xBB009740	VLAN_PORT_L2TBL_CVNT_CTRL [28].VID_CVNT_EN[0:0]	1
0xBB009744	VLAN_PORT_L2TBL_CVNT_CTRL [29].RESERVED[31:4]	28
0xBB009744	VLAN_PORT_L2TBL_CVNT_CTRL [29].PRI_CVNT_EN[3:3]	1
0xBB009744	VLAN_PORT_L2TBL_CVNT_CTRL [29].PRI_TAG_ACT[2:2]	1
0xBB009744	VLAN_PORT_L2TBL_CVNT_CTRL [29].VID_SELECT[1:1]	1
0xBB009744	VLAN_PORT_L2TBL_CVNT_CTRL [29].VID_CVNT_EN[0:0]	1
0xBB009748	VLAN_PORT_L2TBL_CVNT_CTRL [30].RESERVED[31:4]	28
0xBB009748	VLAN_PORT_L2TBL_CVNT_CTRL [30].PRI_CVNT_EN[3:3]	1
0xBB009748	VLAN_PORT_L2TBL_CVNT_CTRL [30].PRI_TAG_ACT[2:2]	1
0xBB009748	VLAN_PORT_L2TBL_CVNT_CTRL [30].VID_SELECT[1:1]	1
0xBB009748	VLAN_PORT_L2TBL_CVNT_CTRL [30].VID_CVNT_EN[0:0]	1
0xBB00974C	VLAN_PORT_L2TBL_CVNT_CTRL [31].RESERVED[31:4]	28
0xBB00974C	VLAN_PORT_L2TBL_CVNT_CTRL [31].PRI_CVNT_EN[3:3]	1
0xBB00974C	VLAN_PORT_L2TBL_CVNT_CTRL [31].PRI_TAG_ACT[2:2]	1
0xBB00974C	VLAN_PORT_L2TBL_CVNT_CTRL [31].VID_SELECT[1:1]	1
0xBB00974C	VLAN_PORT_L2TBL_CVNT_CTRL [31].VID_CVNT_EN[0:0]	1
0xBB009750	VLAN_PORT_L2TBL_CVNT_CTRL [32].RESERVED[31:4]	28

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009750	VLAN_PORT_L2TBL_CVNT_CTRL [32].PRI_CVNT_EN[3:3]	1
0xBB009750	VLAN_PORT_L2TBL_CVNT_CTRL [32].PRI_TAG_ACT[2:2]	1
0xBB009750	VLAN_PORT_L2TBL_CVNT_CTRL [32].VID_SELECT[1:1]	1
0xBB009750	VLAN_PORT_L2TBL_CVNT_CTRL [32].VID_CVNT_EN[0:0]	1
0xBB009754	VLAN_PORT_L2TBL_CVNT_CTRL [33].RESERVED[31:4]	28
0xBB009754	VLAN_PORT_L2TBL_CVNT_CTRL [33].PRI_CVNT_EN[3:3]	1
0xBB009754	VLAN_PORT_L2TBL_CVNT_CTRL [33].PRI_TAG_ACT[2:2]	1
0xBB009754	VLAN_PORT_L2TBL_CVNT_CTRL [33].VID_SELECT[1:1]	1
0xBB009754	VLAN_PORT_L2TBL_CVNT_CTRL [33].VID_CVNT_EN[0:0]	1
0xBB009758	VLAN_PORT_L2TBL_CVNT_CTRL [34].RESERVED[31:4]	28
0xBB009758	VLAN_PORT_L2TBL_CVNT_CTRL [34].PRI_CVNT_EN[3:3]	1
0xBB009758	VLAN_PORT_L2TBL_CVNT_CTRL [34].PRI_TAG_ACT[2:2]	1
0xBB009758	VLAN_PORT_L2TBL_CVNT_CTRL [34].VID_SELECT[1:1]	1
0xBB009758	VLAN_PORT_L2TBL_CVNT_CTRL [34].VID_CVNT_EN[0:0]	1
0xBB00975C	VLAN_PORT_L2TBL_CVNT_CTRL [35].RESERVED[31:4]	28
0xBB00975C	VLAN_PORT_L2TBL_CVNT_CTRL [35].PRI_CVNT_EN[3:3]	1
0xBB00975C	VLAN_PORT_L2TBL_CVNT_CTRL [35].PRI_TAG_ACT[2:2]	1
0xBB00975C	VLAN_PORT_L2TBL_CVNT_CTRL [35].VID_SELECT[1:1]	1
0xBB00975C	VLAN_PORT_L2TBL_CVNT_CTRL [35].VID_CVNT_EN[0:0]	1
0xBB009760	VLAN_PORT_L2TBL_CVNT_CTRL [36].RESERVED[31:4]	28
0xBB009760	VLAN_PORT_L2TBL_CVNT_CTRL [36].PRI_CVNT_EN[3:3]	1
0xBB009760	VLAN_PORT_L2TBL_CVNT_CTRL [36].PRI_TAG_ACT[2:2]	1
0xBB009760	VLAN_PORT_L2TBL_CVNT_CTRL [36].VID_SELECT[1:1]	1
0xBB009760	VLAN_PORT_L2TBL_CVNT_CTRL [36].VID_CVNT_EN[0:0]	1
0xBB009764	VLAN_PORT_L2TBL_CVNT_CTRL [37].RESERVED[31:4]	28
0xBB009764	VLAN_PORT_L2TBL_CVNT_CTRL [37].PRI_CVNT_EN[3:3]	1
0xBB009764	VLAN_PORT_L2TBL_CVNT_CTRL [37].PRI_TAG_ACT[2:2]	1
0xBB009764	VLAN_PORT_L2TBL_CVNT_CTRL [37].VID_SELECT[1:1]	1
0xBB009764	VLAN_PORT_L2TBL_CVNT_CTRL [37].VID_CVNT_EN[0:0]	1
0xBB009768	VLAN_PORT_L2TBL_CVNT_CTRL [38].RESERVED[31:4]	28
0xBB009768	VLAN_PORT_L2TBL_CVNT_CTRL [38].PRI_CVNT_EN[3:3]	1
0xBB009768	VLAN_PORT_L2TBL_CVNT_CTRL [38].PRI_TAG_ACT[2:2]	1
0xBB009768	VLAN_PORT_L2TBL_CVNT_CTRL [38].VID_SELECT[1:1]	1
0xBB009768	VLAN_PORT_L2TBL_CVNT_CTRL [38].VID_CVNT_EN[0:0]	1
0xBB00976C	VLAN_PORT_L2TBL_CVNT_CTRL [39].RESERVED[31:4]	28
0xBB00976C	VLAN_PORT_L2TBL_CVNT_CTRL [39].PRI_CVNT_EN[3:3]	1
0xBB00976C	VLAN_PORT_L2TBL_CVNT_CTRL [39].PRI_TAG_ACT[2:2]	1
0xBB00976C	VLAN_PORT_L2TBL_CVNT_CTRL [39].VID_SELECT[1:1]	1
0xBB00976C	VLAN_PORT_L2TBL_CVNT_CTRL [39].VID_CVNT_EN[0:0]	1
0xBB009770	VLAN_PORT_L2TBL_CVNT_CTRL [40].RESERVED[31:4]	28
0xBB009770	VLAN_PORT_L2TBL_CVNT_CTRL [40].PRI_CVNT_EN[3:3]	1
0xBB009770	VLAN_PORT_L2TBL_CVNT_CTRL [40].PRI_TAG_ACT[2:2]	1
0xBB009770	VLAN_PORT_L2TBL_CVNT_CTRL [40].VID_SELECT[1:1]	1
0xBB009770	VLAN_PORT_L2TBL_CVNT_CTRL [40].VID_CVNT_EN[0:0]	1
0xBB009774	VLAN_PORT_L2TBL_CVNT_CTRL [41].RESERVED[31:4]	28
0xBB009774	VLAN_PORT_L2TBL_CVNT_CTRL [41].PRI_CVNT_EN[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009774	VLAN_PORT_L2TBL_CVNT_CTRL [41].PRI_TAG_ACT[2:2]	1
0xBB009774	VLAN_PORT_L2TBL_CVNT_CTRL [41].VID_SELECT[1:1]	1
0xBB009774	VLAN_PORT_L2TBL_CVNT_CTRL [41].VID_CVNT_EN[0:0]	1
0xBB009778	VLAN_PORT_L2TBL_CVNT_CTRL [42].RESERVED[31:4]	28
0xBB009778	VLAN_PORT_L2TBL_CVNT_CTRL [42].PRI_CVNT_EN[3:3]	1
0xBB009778	VLAN_PORT_L2TBL_CVNT_CTRL [42].PRI_TAG_ACT[2:2]	1
0xBB009778	VLAN_PORT_L2TBL_CVNT_CTRL [42].VID_SELECT[1:1]	1
0xBB009778	VLAN_PORT_L2TBL_CVNT_CTRL [42].VID_CVNT_EN[0:0]	1
0xBB00977C	VLAN_PORT_L2TBL_CVNT_CTRL [43].RESERVED[31:4]	28
0xBB00977C	VLAN_PORT_L2TBL_CVNT_CTRL [43].PRI_CVNT_EN[3:3]	1
0xBB00977C	VLAN_PORT_L2TBL_CVNT_CTRL [43].PRI_TAG_ACT[2:2]	1
0xBB00977C	VLAN_PORT_L2TBL_CVNT_CTRL [43].VID_SELECT[1:1]	1
0xBB00977C	VLAN_PORT_L2TBL_CVNT_CTRL [43].VID_CVNT_EN[0:0]	1
0xBB009780	VLAN_PORT_L2TBL_CVNT_CTRL [44].RESERVED[31:4]	28
0xBB009780	VLAN_PORT_L2TBL_CVNT_CTRL [44].PRI_CVNT_EN[3:3]	1
0xBB009780	VLAN_PORT_L2TBL_CVNT_CTRL [44].PRI_TAG_ACT[2:2]	1
0xBB009780	VLAN_PORT_L2TBL_CVNT_CTRL [44].VID_SELECT[1:1]	1
0xBB009780	VLAN_PORT_L2TBL_CVNT_CTRL [44].VID_CVNT_EN[0:0]	1
0xBB009784	VLAN_PORT_L2TBL_CVNT_CTRL [45].RESERVED[31:4]	28
0xBB009784	VLAN_PORT_L2TBL_CVNT_CTRL [45].PRI_CVNT_EN[3:3]	1
0xBB009784	VLAN_PORT_L2TBL_CVNT_CTRL [45].PRI_TAG_ACT[2:2]	1
0xBB009784	VLAN_PORT_L2TBL_CVNT_CTRL [45].VID_SELECT[1:1]	1
0xBB009784	VLAN_PORT_L2TBL_CVNT_CTRL [45].VID_CVNT_EN[0:0]	1
0xBB009788	VLAN_PORT_L2TBL_CVNT_CTRL [46].RESERVED[31:4]	28
0xBB009788	VLAN_PORT_L2TBL_CVNT_CTRL [46].PRI_CVNT_EN[3:3]	1
0xBB009788	VLAN_PORT_L2TBL_CVNT_CTRL [46].PRI_TAG_ACT[2:2]	1
0xBB009788	VLAN_PORT_L2TBL_CVNT_CTRL [46].VID_SELECT[1:1]	1
0xBB009788	VLAN_PORT_L2TBL_CVNT_CTRL [46].VID_CVNT_EN[0:0]	1
0xBB00978C	VLAN_PORT_L2TBL_CVNT_CTRL [47].RESERVED[31:4]	28
0xBB00978C	VLAN_PORT_L2TBL_CVNT_CTRL [47].PRI_CVNT_EN[3:3]	1
0xBB00978C	VLAN_PORT_L2TBL_CVNT_CTRL [47].PRI_TAG_ACT[2:2]	1
0xBB00978C	VLAN_PORT_L2TBL_CVNT_CTRL [47].VID_SELECT[1:1]	1
0xBB00978C	VLAN_PORT_L2TBL_CVNT_CTRL [47].VID_CVNT_EN[0:0]	1
0xBB009790	VLAN_PORT_L2TBL_CVNT_CTRL [48].RESERVED[31:4]	28
0xBB009790	VLAN_PORT_L2TBL_CVNT_CTRL [48].PRI_CVNT_EN[3:3]	1
0xBB009790	VLAN_PORT_L2TBL_CVNT_CTRL [48].PRI_TAG_ACT[2:2]	1
0xBB009790	VLAN_PORT_L2TBL_CVNT_CTRL [48].VID_SELECT[1:1]	1
0xBB009790	VLAN_PORT_L2TBL_CVNT_CTRL [48].VID_CVNT_EN[0:0]	1
0xBB009794	VLAN_PORT_L2TBL_CVNT_CTRL [49].RESERVED[31:4]	28
0xBB009794	VLAN_PORT_L2TBL_CVNT_CTRL [49].PRI_CVNT_EN[3:3]	1
0xBB009794	VLAN_PORT_L2TBL_CVNT_CTRL [49].PRI_TAG_ACT[2:2]	1
0xBB009794	VLAN_PORT_L2TBL_CVNT_CTRL [49].VID_SELECT[1:1]	1
0xBB009794	VLAN_PORT_L2TBL_CVNT_CTRL [49].VID_CVNT_EN[0:0]	1
0xBB009798	VLAN_PORT_L2TBL_CVNT_CTRL [50].RESERVED[31:4]	28
0xBB009798	VLAN_PORT_L2TBL_CVNT_CTRL [50].PRI_CVNT_EN[3:3]	1
0xBB009798	VLAN_PORT_L2TBL_CVNT_CTRL [50].PRI_TAG_ACT[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009798	VLAN_PORT_L2TBL_CVNT_CTRL [50].VID_SELECT[1:1]	1
0xBB009798	VLAN_PORT_L2TBL_CVNT_CTRL [50].VID_CVNT_EN[0:0]	1
0xBB00979C	VLAN_PORT_L2TBL_CVNT_CTRL [51].RESERVED[31:4]	28
0xBB00979C	VLAN_PORT_L2TBL_CVNT_CTRL [51].PRI_CVNT_EN[3:3]	1
0xBB00979C	VLAN_PORT_L2TBL_CVNT_CTRL [51].PRI_TAG_ACT[2:2]	1
0xBB00979C	VLAN_PORT_L2TBL_CVNT_CTRL [51].VID_SELECT[1:1]	1
0xBB00979C	VLAN_PORT_L2TBL_CVNT_CTRL [51].VID_CVNT_EN[0:0]	1
0xBB0097A0	VLAN_PORT_L2TBL_CVNT_CTRL [52].RESERVED[31:4]	28
0xBB0097A0	VLAN_PORT_L2TBL_CVNT_CTRL [52].PRI_CVNT_EN[3:3]	1
0xBB0097A0	VLAN_PORT_L2TBL_CVNT_CTRL [52].PRI_TAG_ACT[2:2]	1
0xBB0097A0	VLAN_PORT_L2TBL_CVNT_CTRL [52].VID_SELECT[1:1]	1
0xBB0097A0	VLAN_PORT_L2TBL_CVNT_CTRL [52].VID_CVNT_EN[0:0]	1
0xBB0097A4	VLAN_PORT_L2TBL_CVNT_CTRL [53].RESERVED[31:4]	28
0xBB0097A4	VLAN_PORT_L2TBL_CVNT_CTRL [53].PRI_CVNT_EN[3:3]	1
0xBB0097A4	VLAN_PORT_L2TBL_CVNT_CTRL [53].PRI_TAG_ACT[2:2]	1
0xBB0097A4	VLAN_PORT_L2TBL_CVNT_CTRL [53].VID_SELECT[1:1]	1
0xBB0097A4	VLAN_PORT_L2TBL_CVNT_CTRL [53].VID_CVNT_EN[0:0]	1
0xBB0097A8	VLAN_PORT_L2TBL_CVNT_CTRL [54].RESERVED[31:4]	28
0xBB0097A8	VLAN_PORT_L2TBL_CVNT_CTRL [54].PRI_CVNT_EN[3:3]	1
0xBB0097A8	VLAN_PORT_L2TBL_CVNT_CTRL [54].PRI_TAG_ACT[2:2]	1
0xBB0097A8	VLAN_PORT_L2TBL_CVNT_CTRL [54].VID_SELECT[1:1]	1
0xBB0097A8	VLAN_PORT_L2TBL_CVNT_CTRL [54].VID_CVNT_EN[0:0]	1
0xBB0097AC	VLAN_PORT_L2TBL_CVNT_CTRL [55].RESERVED[31:4]	28
0xBB0097AC	VLAN_PORT_L2TBL_CVNT_CTRL [55].PRI_CVNT_EN[3:3]	1
0xBB0097AC	VLAN_PORT_L2TBL_CVNT_CTRL [55].PRI_TAG_ACT[2:2]	1
0xBB0097AC	VLAN_PORT_L2TBL_CVNT_CTRL [55].VID_SELECT[1:1]	1
0xBB0097AC	VLAN_PORT_L2TBL_CVNT_CTRL [55].VID_CVNT_EN[0:0]	1
0xBB0097B0	VLAN_PORT_L2TBL_CVNT_CTRL [56].RESERVED[31:4]	28
0xBB0097B0	VLAN_PORT_L2TBL_CVNT_CTRL [56].PRI_CVNT_EN[3:3]	1
0xBB0097B0	VLAN_PORT_L2TBL_CVNT_CTRL [56].PRI_TAG_ACT[2:2]	1
0xBB0097B0	VLAN_PORT_L2TBL_CVNT_CTRL [56].VID_SELECT[1:1]	1
0xBB0097B0	VLAN_PORT_L2TBL_CVNT_CTRL [56].VID_CVNT_EN[0:0]	1
0xBB00600C	VLAN_PORT_OTAG_TPID_CMP_MSK [0].RESERVED[31:4]	28
0xBB00600C	VLAN_PORT_OTAG_TPID_CMP_MSK [0].OTPID_CMP_MSK[3:0]	4
0xBB00608C	VLAN_PORT_OTAG_TPID_CMP_MSK [1].RESERVED[31:4]	28
0xBB00608C	VLAN_PORT_OTAG_TPID_CMP_MSK [1].OTPID_CMP_MSK[3:0]	4
0xBB00610C	VLAN_PORT_OTAG_TPID_CMP_MSK [2].RESERVED[31:4]	28
0xBB00610C	VLAN_PORT_OTAG_TPID_CMP_MSK [2].OTPID_CMP_MSK[3:0]	4
0xBB00618C	VLAN_PORT_OTAG_TPID_CMP_MSK [3].RESERVED[31:4]	28
0xBB00618C	VLAN_PORT_OTAG_TPID_CMP_MSK [3].OTPID_CMP_MSK[3:0]	4
0xBB00620C	VLAN_PORT_OTAG_TPID_CMP_MSK [4].RESERVED[31:4]	28
0xBB00620C	VLAN_PORT_OTAG_TPID_CMP_MSK [4].OTPID_CMP_MSK[3:0]	4
0xBB00628C	VLAN_PORT_OTAG_TPID_CMP_MSK [5].RESERVED[31:4]	28
0xBB00628C	VLAN_PORT_OTAG_TPID_CMP_MSK [5].OTPID_CMP_MSK[3:0]	4
0xBB00630C	VLAN_PORT_OTAG_TPID_CMP_MSK [6].RESERVED[31:4]	28
0xBB00630C	VLAN_PORT_OTAG_TPID_CMP_MSK [6].OTPID_CMP_MSK[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00638C	VLAN_PORT_OTAG_TPID_CMP_MSK [7].RESERVED[31:4]	28
0xBB00638C	VLAN_PORT_OTAG_TPID_CMP_MSK [7].OTPID_CMP_MSK[3:0]	4
0xBB00640C	VLAN_PORT_OTAG_TPID_CMP_MSK [8].RESERVED[31:4]	28
0xBB00640C	VLAN_PORT_OTAG_TPID_CMP_MSK [8].OTPID_CMP_MSK[3:0]	4
0xBB00648C	VLAN_PORT_OTAG_TPID_CMP_MSK [9].RESERVED[31:4]	28
0xBB00648C	VLAN_PORT_OTAG_TPID_CMP_MSK [9].OTPID_CMP_MSK[3:0]	4
0xBB00650C	VLAN_PORT_OTAG_TPID_CMP_MSK [10].RESERVED[31:4]	28
0xBB00650C	VLAN_PORT_OTAG_TPID_CMP_MSK [10].OTPID_CMP_MSK[3:0]	4
0xBB00658C	VLAN_PORT_OTAG_TPID_CMP_MSK [11].RESERVED[31:4]	28
0xBB00658C	VLAN_PORT_OTAG_TPID_CMP_MSK [11].OTPID_CMP_MSK[3:0]	4
0xBB00660C	VLAN_PORT_OTAG_TPID_CMP_MSK [12].RESERVED[31:4]	28
0xBB00660C	VLAN_PORT_OTAG_TPID_CMP_MSK [12].OTPID_CMP_MSK[3:0]	4
0xBB00668C	VLAN_PORT_OTAG_TPID_CMP_MSK [13].RESERVED[31:4]	28
0xBB00668C	VLAN_PORT_OTAG_TPID_CMP_MSK [13].OTPID_CMP_MSK[3:0]	4
0xBB00670C	VLAN_PORT_OTAG_TPID_CMP_MSK [14].RESERVED[31:4]	28
0xBB00670C	VLAN_PORT_OTAG_TPID_CMP_MSK [14].OTPID_CMP_MSK[3:0]	4
0xBB00678C	VLAN_PORT_OTAG_TPID_CMP_MSK [15].RESERVED[31:4]	28
0xBB00678C	VLAN_PORT_OTAG_TPID_CMP_MSK [15].OTPID_CMP_MSK[3:0]	4
0xBB00680C	VLAN_PORT_OTAG_TPID_CMP_MSK [16].RESERVED[31:4]	28
0xBB00680C	VLAN_PORT_OTAG_TPID_CMP_MSK [16].OTPID_CMP_MSK[3:0]	4
0xBB00688C	VLAN_PORT_OTAG_TPID_CMP_MSK [17].RESERVED[31:4]	28
0xBB00688C	VLAN_PORT_OTAG_TPID_CMP_MSK [17].OTPID_CMP_MSK[3:0]	4
0xBB00690C	VLAN_PORT_OTAG_TPID_CMP_MSK [18].RESERVED[31:4]	28
0xBB00690C	VLAN_PORT_OTAG_TPID_CMP_MSK [18].OTPID_CMP_MSK[3:0]	4
0xBB00698C	VLAN_PORT_OTAG_TPID_CMP_MSK [19].RESERVED[31:4]	28
0xBB00698C	VLAN_PORT_OTAG_TPID_CMP_MSK [19].OTPID_CMP_MSK[3:0]	4
0xBB006A0C	VLAN_PORT_OTAG_TPID_CMP_MSK [20].RESERVED[31:4]	28
0xBB006A0C	VLAN_PORT_OTAG_TPID_CMP_MSK [20].OTPID_CMP_MSK[3:0]	4
0xBB006A8C	VLAN_PORT_OTAG_TPID_CMP_MSK [21].RESERVED[31:4]	28
0xBB006A8C	VLAN_PORT_OTAG_TPID_CMP_MSK [21].OTPID_CMP_MSK[3:0]	4
0xBB006B0C	VLAN_PORT_OTAG_TPID_CMP_MSK [22].RESERVED[31:4]	28
0xBB006B0C	VLAN_PORT_OTAG_TPID_CMP_MSK [22].OTPID_CMP_MSK[3:0]	4
0xBB006B8C	VLAN_PORT_OTAG_TPID_CMP_MSK [23].RESERVED[31:4]	28
0xBB006B8C	VLAN_PORT_OTAG_TPID_CMP_MSK [23].OTPID_CMP_MSK[3:0]	4
0xBB006C0C	VLAN_PORT_OTAG_TPID_CMP_MSK [24].RESERVED[31:4]	28
0xBB006C0C	VLAN_PORT_OTAG_TPID_CMP_MSK [24].OTPID_CMP_MSK[3:0]	4
0xBB006C8C	VLAN_PORT_OTAG_TPID_CMP_MSK [25].RESERVED[31:4]	28
0xBB006C8C	VLAN_PORT_OTAG_TPID_CMP_MSK [25].OTPID_CMP_MSK[3:0]	4
0xBB006D0C	VLAN_PORT_OTAG_TPID_CMP_MSK [26].RESERVED[31:4]	28
0xBB006D0C	VLAN_PORT_OTAG_TPID_CMP_MSK [26].OTPID_CMP_MSK[3:0]	4
0xBB006D8C	VLAN_PORT_OTAG_TPID_CMP_MSK [27].RESERVED[31:4]	28
0xBB006D8C	VLAN_PORT_OTAG_TPID_CMP_MSK [27].OTPID_CMP_MSK[3:0]	4
0xBB006E0C	VLAN_PORT_OTAG_TPID_CMP_MSK [28].RESERVED[31:4]	28
0xBB006E0C	VLAN_PORT_OTAG_TPID_CMP_MSK [28].OTPID_CMP_MSK[3:0]	4
0xBB006E8C	VLAN_PORT_OTAG_TPID_CMP_MSK [29].RESERVED[31:4]	28
0xBB006E8C	VLAN_PORT_OTAG_TPID_CMP_MSK [29].OTPID_CMP_MSK[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB006F0C	VLAN_PORT_OTAG_TPID_CMP_MSK [30].RESERVED[31:4]	28
0xBB006F0C	VLAN_PORT_OTAG_TPID_CMP_MSK [30].OTPID_CMP_MSK[3:0]	4
0xBB006F8C	VLAN_PORT_OTAG_TPID_CMP_MSK [31].RESERVED[31:4]	28
0xBB006F8C	VLAN_PORT_OTAG_TPID_CMP_MSK [31].OTPID_CMP_MSK[3:0]	4
0xBB00700C	VLAN_PORT_OTAG_TPID_CMP_MSK [32].RESERVED[31:4]	28
0xBB00700C	VLAN_PORT_OTAG_TPID_CMP_MSK [32].OTPID_CMP_MSK[3:0]	4
0xBB00708C	VLAN_PORT_OTAG_TPID_CMP_MSK [33].RESERVED[31:4]	28
0xBB00708C	VLAN_PORT_OTAG_TPID_CMP_MSK [33].OTPID_CMP_MSK[3:0]	4
0xBB00710C	VLAN_PORT_OTAG_TPID_CMP_MSK [34].RESERVED[31:4]	28
0xBB00710C	VLAN_PORT_OTAG_TPID_CMP_MSK [34].OTPID_CMP_MSK[3:0]	4
0xBB00718C	VLAN_PORT_OTAG_TPID_CMP_MSK [35].RESERVED[31:4]	28
0xBB00718C	VLAN_PORT_OTAG_TPID_CMP_MSK [35].OTPID_CMP_MSK[3:0]	4
0xBB00720C	VLAN_PORT_OTAG_TPID_CMP_MSK [36].RESERVED[31:4]	28
0xBB00720C	VLAN_PORT_OTAG_TPID_CMP_MSK [36].OTPID_CMP_MSK[3:0]	4
0xBB00728C	VLAN_PORT_OTAG_TPID_CMP_MSK [37].RESERVED[31:4]	28
0xBB00728C	VLAN_PORT_OTAG_TPID_CMP_MSK [37].OTPID_CMP_MSK[3:0]	4
0xBB00730C	VLAN_PORT_OTAG_TPID_CMP_MSK [38].RESERVED[31:4]	28
0xBB00730C	VLAN_PORT_OTAG_TPID_CMP_MSK [38].OTPID_CMP_MSK[3:0]	4
0xBB00738C	VLAN_PORT_OTAG_TPID_CMP_MSK [39].RESERVED[31:4]	28
0xBB00738C	VLAN_PORT_OTAG_TPID_CMP_MSK [39].OTPID_CMP_MSK[3:0]	4
0xBB00740C	VLAN_PORT_OTAG_TPID_CMP_MSK [40].RESERVED[31:4]	28
0xBB00740C	VLAN_PORT_OTAG_TPID_CMP_MSK [40].OTPID_CMP_MSK[3:0]	4
0xBB00748C	VLAN_PORT_OTAG_TPID_CMP_MSK [41].RESERVED[31:4]	28
0xBB00748C	VLAN_PORT_OTAG_TPID_CMP_MSK [41].OTPID_CMP_MSK[3:0]	4
0xBB00750C	VLAN_PORT_OTAG_TPID_CMP_MSK [42].RESERVED[31:4]	28
0xBB00750C	VLAN_PORT_OTAG_TPID_CMP_MSK [42].OTPID_CMP_MSK[3:0]	4
0xBB00758C	VLAN_PORT_OTAG_TPID_CMP_MSK [43].RESERVED[31:4]	28
0xBB00758C	VLAN_PORT_OTAG_TPID_CMP_MSK [43].OTPID_CMP_MSK[3:0]	4
0xBB00760C	VLAN_PORT_OTAG_TPID_CMP_MSK [44].RESERVED[31:4]	28
0xBB00760C	VLAN_PORT_OTAG_TPID_CMP_MSK [44].OTPID_CMP_MSK[3:0]	4
0xBB00768C	VLAN_PORT_OTAG_TPID_CMP_MSK [45].RESERVED[31:4]	28
0xBB00768C	VLAN_PORT_OTAG_TPID_CMP_MSK [45].OTPID_CMP_MSK[3:0]	4
0xBB00770C	VLAN_PORT_OTAG_TPID_CMP_MSK [46].RESERVED[31:4]	28
0xBB00770C	VLAN_PORT_OTAG_TPID_CMP_MSK [46].OTPID_CMP_MSK[3:0]	4
0xBB00778C	VLAN_PORT_OTAG_TPID_CMP_MSK [47].RESERVED[31:4]	28
0xBB00778C	VLAN_PORT_OTAG_TPID_CMP_MSK [47].OTPID_CMP_MSK[3:0]	4
0xBB00780C	VLAN_PORT_OTAG_TPID_CMP_MSK [48].RESERVED[31:4]	28
0xBB00780C	VLAN_PORT_OTAG_TPID_CMP_MSK [48].OTPID_CMP_MSK[3:0]	4
0xBB00788C	VLAN_PORT_OTAG_TPID_CMP_MSK [49].RESERVED[31:4]	28
0xBB00788C	VLAN_PORT_OTAG_TPID_CMP_MSK [49].OTPID_CMP_MSK[3:0]	4
0xBB00790C	VLAN_PORT_OTAG_TPID_CMP_MSK [50].RESERVED[31:4]	28
0xBB00790C	VLAN_PORT_OTAG_TPID_CMP_MSK [50].OTPID_CMP_MSK[3:0]	4
0xBB00798C	VLAN_PORT_OTAG_TPID_CMP_MSK [51].RESERVED[31:4]	28
0xBB00798C	VLAN_PORT_OTAG_TPID_CMP_MSK [51].OTPID_CMP_MSK[3:0]	4
0xBB007A0C	VLAN_PORT_OTAG_TPID_CMP_MSK [52].RESERVED[31:4]	28
0xBB007A0C	VLAN_PORT_OTAG_TPID_CMP_MSK [52].OTPID_CMP_MSK[3:0]	4

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB007A8C	VLAN_PORT_OTAG_TPID_CMP_MSK [53].RESERVED[31:4]	28
0xBB007A8C	VLAN_PORT_OTAG_TPID_CMP_MSK [53].OTPID_CMP_MSK[3:0]	4
0xBB007B0C	VLAN_PORT_OTAG_TPID_CMP_MSK [54].RESERVED[31:4]	28
0xBB007B0C	VLAN_PORT_OTAG_TPID_CMP_MSK [54].OTPID_CMP_MSK[3:0]	4
0xBB007B8C	VLAN_PORT_OTAG_TPID_CMP_MSK [55].RESERVED[31:4]	28
0xBB007B8C	VLAN_PORT_OTAG_TPID_CMP_MSK [55].OTPID_CMP_MSK[3:0]	4
0xBB007C0C	VLAN_PORT_OTAG_TPID_CMP_MSK [56].RESERVED[31:4]	28
0xBB007C0C	VLAN_PORT_OTAG_TPID_CMP_MSK [56].OTPID_CMP_MSK[3:0]	4
0xBB00C020	VLAN_PORT_PPB_VLAN_SET [0][0].RESERVED[31:18]	14
0xBB00C020	VLAN_PORT_PPB_VLAN_SET [0][0].VLAN_TYPE[17:17]	1
0xBB00C020	VLAN_PORT_PPB_VLAN_SET [0][0].PRI_AS[16:16]	1
0xBB00C020	VLAN_PORT_PPB_VLAN_SET [0][0].PRI[15:13]	3
0xBB00C020	VLAN_PORT_PPB_VLAN_SET [0][0].VID_AS[12:12]	1
0xBB00C020	VLAN_PORT_PPB_VLAN_SET [0][0].VID[11:0]	12
0xBB00C024	VLAN_PORT_PPB_VLAN_SET [0][1].RESERVED[31:18]	14
0xBB00C024	VLAN_PORT_PPB_VLAN_SET [0][1].VLAN_TYPE[17:17]	1
0xBB00C024	VLAN_PORT_PPB_VLAN_SET [0][1].PRI_AS[16:16]	1
0xBB00C024	VLAN_PORT_PPB_VLAN_SET [0][1].PRI[15:13]	3
0xBB00C024	VLAN_PORT_PPB_VLAN_SET [0][1].VID_AS[12:12]	1
0xBB00C024	VLAN_PORT_PPB_VLAN_SET [0][1].VID[11:0]	12
0xBB00C028	VLAN_PORT_PPB_VLAN_SET [0][2].RESERVED[31:18]	14
0xBB00C028	VLAN_PORT_PPB_VLAN_SET [0][2].VLAN_TYPE[17:17]	1
0xBB00C028	VLAN_PORT_PPB_VLAN_SET [0][2].PRI_AS[16:16]	1
0xBB00C028	VLAN_PORT_PPB_VLAN_SET [0][2].PRI[15:13]	3
0xBB00C028	VLAN_PORT_PPB_VLAN_SET [0][2].VID_AS[12:12]	1
0xBB00C028	VLAN_PORT_PPB_VLAN_SET [0][2].VID[11:0]	12
0xBB00C02C	VLAN_PORT_PPB_VLAN_SET [0][3].RESERVED[31:18]	14
0xBB00C02C	VLAN_PORT_PPB_VLAN_SET [0][3].VLAN_TYPE[17:17]	1
0xBB00C02C	VLAN_PORT_PPB_VLAN_SET [0][3].PRI_AS[16:16]	1
0xBB00C02C	VLAN_PORT_PPB_VLAN_SET [0][3].PRI[15:13]	3
0xBB00C02C	VLAN_PORT_PPB_VLAN_SET [0][3].VID_AS[12:12]	1
0xBB00C02C	VLAN_PORT_PPB_VLAN_SET [0][3].VID[11:0]	12
0xBB00C030	VLAN_PORT_PPB_VLAN_SET [0][4].RESERVED[31:18]	14
0xBB00C030	VLAN_PORT_PPB_VLAN_SET [0][4].VLAN_TYPE[17:17]	1
0xBB00C030	VLAN_PORT_PPB_VLAN_SET [0][4].PRI_AS[16:16]	1
0xBB00C030	VLAN_PORT_PPB_VLAN_SET [0][4].PRI[15:13]	3
0xBB00C030	VLAN_PORT_PPB_VLAN_SET [0][4].VID_AS[12:12]	1
0xBB00C030	VLAN_PORT_PPB_VLAN_SET [0][4].VID[11:0]	12
0xBB00C034	VLAN_PORT_PPB_VLAN_SET [0][5].RESERVED[31:18]	14
0xBB00C034	VLAN_PORT_PPB_VLAN_SET [0][5].VLAN_TYPE[17:17]	1
0xBB00C034	VLAN_PORT_PPB_VLAN_SET [0][5].PRI_AS[16:16]	1
0xBB00C034	VLAN_PORT_PPB_VLAN_SET [0][5].PRI[15:13]	3
0xBB00C034	VLAN_PORT_PPB_VLAN_SET [0][5].VID_AS[12:12]	1
0xBB00C034	VLAN_PORT_PPB_VLAN_SET [0][5].VID[11:0]	12
0xBB00C038	VLAN_PORT_PPB_VLAN_SET [0][6].RESERVED[31:18]	14
0xBB00C038	VLAN_PORT_PPB_VLAN_SET [0][6].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C038	VLAN_PORT_PPB_VLAN_SET [0][6].PRI_AS[16:16]	1
0xBB00C038	VLAN_PORT_PPB_VLAN_SET [0][6].PRI[15:13]	3
0xBB00C038	VLAN_PORT_PPB_VLAN_SET [0][6].VID_AS[12:12]	1
0xBB00C038	VLAN_PORT_PPB_VLAN_SET [0][6].VID[11:0]	12
0xBB00C03C	VLAN_PORT_PPB_VLAN_SET [0][7].RESERVED[31:18]	14
0xBB00C03C	VLAN_PORT_PPB_VLAN_SET [0][7].VLAN_TYPE[17:17]	1
0xBB00C03C	VLAN_PORT_PPB_VLAN_SET [0][7].PRI_AS[16:16]	1
0xBB00C03C	VLAN_PORT_PPB_VLAN_SET [0][7].PRI[15:13]	3
0xBB00C03C	VLAN_PORT_PPB_VLAN_SET [0][7].VID_AS[12:12]	1
0xBB00C03C	VLAN_PORT_PPB_VLAN_SET [0][7].VID[11:0]	12
0xBB00C040	VLAN_PORT_PPB_VLAN_SET [1][0].RESERVED[31:18]	14
0xBB00C040	VLAN_PORT_PPB_VLAN_SET [1][0].VLAN_TYPE[17:17]	1
0xBB00C040	VLAN_PORT_PPB_VLAN_SET [1][0].PRI_AS[16:16]	1
0xBB00C040	VLAN_PORT_PPB_VLAN_SET [1][0].PRI[15:13]	3
0xBB00C040	VLAN_PORT_PPB_VLAN_SET [1][0].VID_AS[12:12]	1
0xBB00C040	VLAN_PORT_PPB_VLAN_SET [1][0].VID[11:0]	12
0xBB00C044	VLAN_PORT_PPB_VLAN_SET [1][1].RESERVED[31:18]	14
0xBB00C044	VLAN_PORT_PPB_VLAN_SET [1][1].VLAN_TYPE[17:17]	1
0xBB00C044	VLAN_PORT_PPB_VLAN_SET [1][1].PRI_AS[16:16]	1
0xBB00C044	VLAN_PORT_PPB_VLAN_SET [1][1].PRI[15:13]	3
0xBB00C044	VLAN_PORT_PPB_VLAN_SET [1][1].VID_AS[12:12]	1
0xBB00C044	VLAN_PORT_PPB_VLAN_SET [1][1].VID[11:0]	12
0xBB00C048	VLAN_PORT_PPB_VLAN_SET [1][2].RESERVED[31:18]	14
0xBB00C048	VLAN_PORT_PPB_VLAN_SET [1][2].VLAN_TYPE[17:17]	1
0xBB00C048	VLAN_PORT_PPB_VLAN_SET [1][2].PRI_AS[16:16]	1
0xBB00C048	VLAN_PORT_PPB_VLAN_SET [1][2].PRI[15:13]	3
0xBB00C048	VLAN_PORT_PPB_VLAN_SET [1][2].VID_AS[12:12]	1
0xBB00C048	VLAN_PORT_PPB_VLAN_SET [1][2].VID[11:0]	12
0xBB00C04C	VLAN_PORT_PPB_VLAN_SET [1][3].RESERVED[31:18]	14
0xBB00C04C	VLAN_PORT_PPB_VLAN_SET [1][3].VLAN_TYPE[17:17]	1
0xBB00C04C	VLAN_PORT_PPB_VLAN_SET [1][3].PRI_AS[16:16]	1
0xBB00C04C	VLAN_PORT_PPB_VLAN_SET [1][3].PRI[15:13]	3
0xBB00C04C	VLAN_PORT_PPB_VLAN_SET [1][3].VID_AS[12:12]	1
0xBB00C04C	VLAN_PORT_PPB_VLAN_SET [1][3].VID[11:0]	12
0xBB00C050	VLAN_PORT_PPB_VLAN_SET [1][4].RESERVED[31:18]	14
0xBB00C050	VLAN_PORT_PPB_VLAN_SET [1][4].VLAN_TYPE[17:17]	1
0xBB00C050	VLAN_PORT_PPB_VLAN_SET [1][4].PRI_AS[16:16]	1
0xBB00C050	VLAN_PORT_PPB_VLAN_SET [1][4].PRI[15:13]	3
0xBB00C050	VLAN_PORT_PPB_VLAN_SET [1][4].VID_AS[12:12]	1
0xBB00C050	VLAN_PORT_PPB_VLAN_SET [1][4].VID[11:0]	12
0xBB00C054	VLAN_PORT_PPB_VLAN_SET [1][5].RESERVED[31:18]	14
0xBB00C054	VLAN_PORT_PPB_VLAN_SET [1][5].VLAN_TYPE[17:17]	1
0xBB00C054	VLAN_PORT_PPB_VLAN_SET [1][5].PRI_AS[16:16]	1
0xBB00C054	VLAN_PORT_PPB_VLAN_SET [1][5].PRI[15:13]	3
0xBB00C054	VLAN_PORT_PPB_VLAN_SET [1][5].VID_AS[12:12]	1
0xBB00C054	VLAN_PORT_PPB_VLAN_SET [1][5].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C058	VLAN_PORT_PPB_VLAN_SET [1][6].RESERVED[31:18]	14
0xBB00C058	VLAN_PORT_PPB_VLAN_SET [1][6].VLAN_TYPE[17:17]	1
0xBB00C058	VLAN_PORT_PPB_VLAN_SET [1][6].PRI_AS[16:16]	1
0xBB00C058	VLAN_PORT_PPB_VLAN_SET [1][6].PRI[15:13]	3
0xBB00C058	VLAN_PORT_PPB_VLAN_SET [1][6].VID_AS[12:12]	1
0xBB00C058	VLAN_PORT_PPB_VLAN_SET [1][6].VID[11:0]	12
0xBB00C05C	VLAN_PORT_PPB_VLAN_SET [1][7].RESERVED[31:18]	14
0xBB00C05C	VLAN_PORT_PPB_VLAN_SET [1][7].VLAN_TYPE[17:17]	1
0xBB00C05C	VLAN_PORT_PPB_VLAN_SET [1][7].PRI_AS[16:16]	1
0xBB00C05C	VLAN_PORT_PPB_VLAN_SET [1][7].PRI[15:13]	3
0xBB00C05C	VLAN_PORT_PPB_VLAN_SET [1][7].VID_AS[12:12]	1
0xBB00C05C	VLAN_PORT_PPB_VLAN_SET [1][7].VID[11:0]	12
0xBB00C060	VLAN_PORT_PPB_VLAN_SET [2][0].RESERVED[31:18]	14
0xBB00C060	VLAN_PORT_PPB_VLAN_SET [2][0].VLAN_TYPE[17:17]	1
0xBB00C060	VLAN_PORT_PPB_VLAN_SET [2][0].PRI_AS[16:16]	1
0xBB00C060	VLAN_PORT_PPB_VLAN_SET [2][0].PRI[15:13]	3
0xBB00C060	VLAN_PORT_PPB_VLAN_SET [2][0].VID_AS[12:12]	1
0xBB00C060	VLAN_PORT_PPB_VLAN_SET [2][0].VID[11:0]	12
0xBB00C064	VLAN_PORT_PPB_VLAN_SET [2][1].RESERVED[31:18]	14
0xBB00C064	VLAN_PORT_PPB_VLAN_SET [2][1].VLAN_TYPE[17:17]	1
0xBB00C064	VLAN_PORT_PPB_VLAN_SET [2][1].PRI_AS[16:16]	1
0xBB00C064	VLAN_PORT_PPB_VLAN_SET [2][1].PRI[15:13]	3
0xBB00C064	VLAN_PORT_PPB_VLAN_SET [2][1].VID_AS[12:12]	1
0xBB00C064	VLAN_PORT_PPB_VLAN_SET [2][1].VID[11:0]	12
0xBB00C068	VLAN_PORT_PPB_VLAN_SET [2][2].RESERVED[31:18]	14
0xBB00C068	VLAN_PORT_PPB_VLAN_SET [2][2].VLAN_TYPE[17:17]	1
0xBB00C068	VLAN_PORT_PPB_VLAN_SET [2][2].PRI_AS[16:16]	1
0xBB00C068	VLAN_PORT_PPB_VLAN_SET [2][2].PRI[15:13]	3
0xBB00C068	VLAN_PORT_PPB_VLAN_SET [2][2].VID_AS[12:12]	1
0xBB00C068	VLAN_PORT_PPB_VLAN_SET [2][2].VID[11:0]	12
0xBB00C06C	VLAN_PORT_PPB_VLAN_SET [2][3].RESERVED[31:18]	14
0xBB00C06C	VLAN_PORT_PPB_VLAN_SET [2][3].VLAN_TYPE[17:17]	1
0xBB00C06C	VLAN_PORT_PPB_VLAN_SET [2][3].PRI_AS[16:16]	1
0xBB00C06C	VLAN_PORT_PPB_VLAN_SET [2][3].PRI[15:13]	3
0xBB00C06C	VLAN_PORT_PPB_VLAN_SET [2][3].VID_AS[12:12]	1
0xBB00C06C	VLAN_PORT_PPB_VLAN_SET [2][3].VID[11:0]	12
0xBB00C070	VLAN_PORT_PPB_VLAN_SET [2][4].RESERVED[31:18]	14
0xBB00C070	VLAN_PORT_PPB_VLAN_SET [2][4].VLAN_TYPE[17:17]	1
0xBB00C070	VLAN_PORT_PPB_VLAN_SET [2][4].PRI_AS[16:16]	1
0xBB00C070	VLAN_PORT_PPB_VLAN_SET [2][4].PRI[15:13]	3
0xBB00C070	VLAN_PORT_PPB_VLAN_SET [2][4].VID_AS[12:12]	1
0xBB00C070	VLAN_PORT_PPB_VLAN_SET [2][4].VID[11:0]	12
0xBB00C074	VLAN_PORT_PPB_VLAN_SET [2][5].RESERVED[31:18]	14
0xBB00C074	VLAN_PORT_PPB_VLAN_SET [2][5].VLAN_TYPE[17:17]	1
0xBB00C074	VLAN_PORT_PPB_VLAN_SET [2][5].PRI_AS[16:16]	1
0xBB00C074	VLAN_PORT_PPB_VLAN_SET [2][5].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C074	VLAN_PORT_PPB_VLAN_SET [2][5].VID_AS[12:12]	1
0xBB00C074	VLAN_PORT_PPB_VLAN_SET [2][5].VID[11:0]	12
0xBB00C078	VLAN_PORT_PPB_VLAN_SET [2][6].RESERVED[31:18]	14
0xBB00C078	VLAN_PORT_PPB_VLAN_SET [2][6].VLAN_TYPE[17:17]	1
0xBB00C078	VLAN_PORT_PPB_VLAN_SET [2][6].PRI_AS[16:16]	1
0xBB00C078	VLAN_PORT_PPB_VLAN_SET [2][6].PRI[15:13]	3
0xBB00C078	VLAN_PORT_PPB_VLAN_SET [2][6].VID_AS[12:12]	1
0xBB00C078	VLAN_PORT_PPB_VLAN_SET [2][6].VID[11:0]	12
0xBB00C07C	VLAN_PORT_PPB_VLAN_SET [2][7].RESERVED[31:18]	14
0xBB00C07C	VLAN_PORT_PPB_VLAN_SET [2][7].VLAN_TYPE[17:17]	1
0xBB00C07C	VLAN_PORT_PPB_VLAN_SET [2][7].PRI_AS[16:16]	1
0xBB00C07C	VLAN_PORT_PPB_VLAN_SET [2][7].PRI[15:13]	3
0xBB00C07C	VLAN_PORT_PPB_VLAN_SET [2][7].VID_AS[12:12]	1
0xBB00C07C	VLAN_PORT_PPB_VLAN_SET [2][7].VID[11:0]	12
0xBB00C080	VLAN_PORT_PPB_VLAN_SET [3][0].RESERVED[31:18]	14
0xBB00C080	VLAN_PORT_PPB_VLAN_SET [3][0].VLAN_TYPE[17:17]	1
0xBB00C080	VLAN_PORT_PPB_VLAN_SET [3][0].PRI_AS[16:16]	1
0xBB00C080	VLAN_PORT_PPB_VLAN_SET [3][0].PRI[15:13]	3
0xBB00C080	VLAN_PORT_PPB_VLAN_SET [3][0].VID_AS[12:12]	1
0xBB00C080	VLAN_PORT_PPB_VLAN_SET [3][0].VID[11:0]	12
0xBB00C084	VLAN_PORT_PPB_VLAN_SET [3][1].RESERVED[31:18]	14
0xBB00C084	VLAN_PORT_PPB_VLAN_SET [3][1].VLAN_TYPE[17:17]	1
0xBB00C084	VLAN_PORT_PPB_VLAN_SET [3][1].PRI_AS[16:16]	1
0xBB00C084	VLAN_PORT_PPB_VLAN_SET [3][1].PRI[15:13]	3
0xBB00C084	VLAN_PORT_PPB_VLAN_SET [3][1].VID_AS[12:12]	1
0xBB00C084	VLAN_PORT_PPB_VLAN_SET [3][1].VID[11:0]	12
0xBB00C088	VLAN_PORT_PPB_VLAN_SET [3][2].RESERVED[31:18]	14
0xBB00C088	VLAN_PORT_PPB_VLAN_SET [3][2].VLAN_TYPE[17:17]	1
0xBB00C088	VLAN_PORT_PPB_VLAN_SET [3][2].PRI_AS[16:16]	1
0xBB00C088	VLAN_PORT_PPB_VLAN_SET [3][2].PRI[15:13]	3
0xBB00C088	VLAN_PORT_PPB_VLAN_SET [3][2].VID_AS[12:12]	1
0xBB00C088	VLAN_PORT_PPB_VLAN_SET [3][2].VID[11:0]	12
0xBB00C08C	VLAN_PORT_PPB_VLAN_SET [3][3].RESERVED[31:18]	14
0xBB00C08C	VLAN_PORT_PPB_VLAN_SET [3][3].VLAN_TYPE[17:17]	1
0xBB00C08C	VLAN_PORT_PPB_VLAN_SET [3][3].PRI_AS[16:16]	1
0xBB00C08C	VLAN_PORT_PPB_VLAN_SET [3][3].PRI[15:13]	3
0xBB00C08C	VLAN_PORT_PPB_VLAN_SET [3][3].VID_AS[12:12]	1
0xBB00C08C	VLAN_PORT_PPB_VLAN_SET [3][3].VID[11:0]	12
0xBB00C090	VLAN_PORT_PPB_VLAN_SET [3][4].RESERVED[31:18]	14
0xBB00C090	VLAN_PORT_PPB_VLAN_SET [3][4].VLAN_TYPE[17:17]	1
0xBB00C090	VLAN_PORT_PPB_VLAN_SET [3][4].PRI_AS[16:16]	1
0xBB00C090	VLAN_PORT_PPB_VLAN_SET [3][4].PRI[15:13]	3
0xBB00C090	VLAN_PORT_PPB_VLAN_SET [3][4].VID_AS[12:12]	1
0xBB00C090	VLAN_PORT_PPB_VLAN_SET [3][4].VID[11:0]	12
0xBB00C094	VLAN_PORT_PPB_VLAN_SET [3][5].RESERVED[31:18]	14
0xBB00C094	VLAN_PORT_PPB_VLAN_SET [3][5].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C094	VLAN_PORT_PPB_VLAN_SET [3][5].PRI_AS[16:16]	1
0xBB00C094	VLAN_PORT_PPB_VLAN_SET [3][5].PRI[15:13]	3
0xBB00C094	VLAN_PORT_PPB_VLAN_SET [3][5].VID_AS[12:12]	1
0xBB00C094	VLAN_PORT_PPB_VLAN_SET [3][5].VID[11:0]	12
0xBB00C098	VLAN_PORT_PPB_VLAN_SET [3][6].RESERVED[31:18]	14
0xBB00C098	VLAN_PORT_PPB_VLAN_SET [3][6].VLAN_TYPE[17:17]	1
0xBB00C098	VLAN_PORT_PPB_VLAN_SET [3][6].PRI_AS[16:16]	1
0xBB00C098	VLAN_PORT_PPB_VLAN_SET [3][6].PRI[15:13]	3
0xBB00C098	VLAN_PORT_PPB_VLAN_SET [3][6].VID_AS[12:12]	1
0xBB00C098	VLAN_PORT_PPB_VLAN_SET [3][6].VID[11:0]	12
0xBB00C09C	VLAN_PORT_PPB_VLAN_SET [3][7].RESERVED[31:18]	14
0xBB00C09C	VLAN_PORT_PPB_VLAN_SET [3][7].VLAN_TYPE[17:17]	1
0xBB00C09C	VLAN_PORT_PPB_VLAN_SET [3][7].PRI_AS[16:16]	1
0xBB00C09C	VLAN_PORT_PPB_VLAN_SET [3][7].PRI[15:13]	3
0xBB00C09C	VLAN_PORT_PPB_VLAN_SET [3][7].VID_AS[12:12]	1
0xBB00C09C	VLAN_PORT_PPB_VLAN_SET [3][7].VID[11:0]	12
0xBB00C0A0	VLAN_PORT_PPB_VLAN_SET [4][0].RESERVED[31:18]	14
0xBB00C0A0	VLAN_PORT_PPB_VLAN_SET [4][0].VLAN_TYPE[17:17]	1
0xBB00C0A0	VLAN_PORT_PPB_VLAN_SET [4][0].PRI_AS[16:16]	1
0xBB00C0A0	VLAN_PORT_PPB_VLAN_SET [4][0].PRI[15:13]	3
0xBB00C0A0	VLAN_PORT_PPB_VLAN_SET [4][0].VID_AS[12:12]	1
0xBB00C0A0	VLAN_PORT_PPB_VLAN_SET [4][0].VID[11:0]	12
0xBB00C0A4	VLAN_PORT_PPB_VLAN_SET [4][1].RESERVED[31:18]	14
0xBB00C0A4	VLAN_PORT_PPB_VLAN_SET [4][1].VLAN_TYPE[17:17]	1
0xBB00C0A4	VLAN_PORT_PPB_VLAN_SET [4][1].PRI_AS[16:16]	1
0xBB00C0A4	VLAN_PORT_PPB_VLAN_SET [4][1].PRI[15:13]	3
0xBB00C0A4	VLAN_PORT_PPB_VLAN_SET [4][1].VID_AS[12:12]	1
0xBB00C0A4	VLAN_PORT_PPB_VLAN_SET [4][1].VID[11:0]	12
0xBB00C0A8	VLAN_PORT_PPB_VLAN_SET [4][2].RESERVED[31:18]	14
0xBB00C0A8	VLAN_PORT_PPB_VLAN_SET [4][2].VLAN_TYPE[17:17]	1
0xBB00C0A8	VLAN_PORT_PPB_VLAN_SET [4][2].PRI_AS[16:16]	1
0xBB00C0A8	VLAN_PORT_PPB_VLAN_SET [4][2].PRI[15:13]	3
0xBB00C0A8	VLAN_PORT_PPB_VLAN_SET [4][2].VID_AS[12:12]	1
0xBB00C0A8	VLAN_PORT_PPB_VLAN_SET [4][2].VID[11:0]	12
0xBB00C0AC	VLAN_PORT_PPB_VLAN_SET [4][3].RESERVED[31:18]	14
0xBB00C0AC	VLAN_PORT_PPB_VLAN_SET [4][3].VLAN_TYPE[17:17]	1
0xBB00C0AC	VLAN_PORT_PPB_VLAN_SET [4][3].PRI_AS[16:16]	1
0xBB00C0AC	VLAN_PORT_PPB_VLAN_SET [4][3].PRI[15:13]	3
0xBB00C0AC	VLAN_PORT_PPB_VLAN_SET [4][3].VID_AS[12:12]	1
0xBB00C0AC	VLAN_PORT_PPB_VLAN_SET [4][3].VID[11:0]	12
0xBB00C0B0	VLAN_PORT_PPB_VLAN_SET [4][4].RESERVED[31:18]	14
0xBB00C0B0	VLAN_PORT_PPB_VLAN_SET [4][4].VLAN_TYPE[17:17]	1
0xBB00C0B0	VLAN_PORT_PPB_VLAN_SET [4][4].PRI_AS[16:16]	1
0xBB00C0B0	VLAN_PORT_PPB_VLAN_SET [4][4].PRI[15:13]	3
0xBB00C0B0	VLAN_PORT_PPB_VLAN_SET [4][4].VID_AS[12:12]	1
0xBB00C0B0	VLAN_PORT_PPB_VLAN_SET [4][4].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C0B4	VLAN_PORT_PPB_VLAN_SET [4][5].RESERVED[31:18]	14
0xBB00C0B4	VLAN_PORT_PPB_VLAN_SET [4][5].VLAN_TYPE[17:17]	1
0xBB00C0B4	VLAN_PORT_PPB_VLAN_SET [4][5].PRI_AS[16:16]	1
0xBB00C0B4	VLAN_PORT_PPB_VLAN_SET [4][5].PRI[15:13]	3
0xBB00C0B4	VLAN_PORT_PPB_VLAN_SET [4][5].VID_AS[12:12]	1
0xBB00C0B4	VLAN_PORT_PPB_VLAN_SET [4][5].VID[11:0]	12
0xBB00C0B8	VLAN_PORT_PPB_VLAN_SET [4][6].RESERVED[31:18]	14
0xBB00C0B8	VLAN_PORT_PPB_VLAN_SET [4][6].VLAN_TYPE[17:17]	1
0xBB00C0B8	VLAN_PORT_PPB_VLAN_SET [4][6].PRI_AS[16:16]	1
0xBB00C0B8	VLAN_PORT_PPB_VLAN_SET [4][6].PRI[15:13]	3
0xBB00C0B8	VLAN_PORT_PPB_VLAN_SET [4][6].VID_AS[12:12]	1
0xBB00C0B8	VLAN_PORT_PPB_VLAN_SET [4][6].VID[11:0]	12
0xBB00C0BC	VLAN_PORT_PPB_VLAN_SET [4][7].RESERVED[31:18]	14
0xBB00C0BC	VLAN_PORT_PPB_VLAN_SET [4][7].VLAN_TYPE[17:17]	1
0xBB00C0BC	VLAN_PORT_PPB_VLAN_SET [4][7].PRI_AS[16:16]	1
0xBB00C0BC	VLAN_PORT_PPB_VLAN_SET [4][7].PRI[15:13]	3
0xBB00C0BC	VLAN_PORT_PPB_VLAN_SET [4][7].VID_AS[12:12]	1
0xBB00C0BC	VLAN_PORT_PPB_VLAN_SET [4][7].VID[11:0]	12
0xBB00C0C0	VLAN_PORT_PPB_VLAN_SET [5][0].RESERVED[31:18]	14
0xBB00C0C0	VLAN_PORT_PPB_VLAN_SET [5][0].VLAN_TYPE[17:17]	1
0xBB00C0C0	VLAN_PORT_PPB_VLAN_SET [5][0].PRI_AS[16:16]	1
0xBB00C0C0	VLAN_PORT_PPB_VLAN_SET [5][0].PRI[15:13]	3
0xBB00C0C0	VLAN_PORT_PPB_VLAN_SET [5][0].VID_AS[12:12]	1
0xBB00C0C0	VLAN_PORT_PPB_VLAN_SET [5][0].VID[11:0]	12
0xBB00C0C4	VLAN_PORT_PPB_VLAN_SET [5][1].RESERVED[31:18]	14
0xBB00C0C4	VLAN_PORT_PPB_VLAN_SET [5][1].VLAN_TYPE[17:17]	1
0xBB00C0C4	VLAN_PORT_PPB_VLAN_SET [5][1].PRI_AS[16:16]	1
0xBB00C0C4	VLAN_PORT_PPB_VLAN_SET [5][1].PRI[15:13]	3
0xBB00C0C4	VLAN_PORT_PPB_VLAN_SET [5][1].VID_AS[12:12]	1
0xBB00C0C4	VLAN_PORT_PPB_VLAN_SET [5][1].VID[11:0]	12
0xBB00C0C8	VLAN_PORT_PPB_VLAN_SET [5][2].RESERVED[31:18]	14
0xBB00C0C8	VLAN_PORT_PPB_VLAN_SET [5][2].VLAN_TYPE[17:17]	1
0xBB00C0C8	VLAN_PORT_PPB_VLAN_SET [5][2].PRI_AS[16:16]	1
0xBB00C0C8	VLAN_PORT_PPB_VLAN_SET [5][2].PRI[15:13]	3
0xBB00C0C8	VLAN_PORT_PPB_VLAN_SET [5][2].VID_AS[12:12]	1
0xBB00C0C8	VLAN_PORT_PPB_VLAN_SET [5][2].VID[11:0]	12
0xBB00C0CC	VLAN_PORT_PPB_VLAN_SET [5][3].RESERVED[31:18]	14
0xBB00C0CC	VLAN_PORT_PPB_VLAN_SET [5][3].VLAN_TYPE[17:17]	1
0xBB00C0CC	VLAN_PORT_PPB_VLAN_SET [5][3].PRI_AS[16:16]	1
0xBB00C0CC	VLAN_PORT_PPB_VLAN_SET [5][3].PRI[15:13]	3
0xBB00C0CC	VLAN_PORT_PPB_VLAN_SET [5][3].VID_AS[12:12]	1
0xBB00C0CC	VLAN_PORT_PPB_VLAN_SET [5][3].VID[11:0]	12
0xBB00C0D0	VLAN_PORT_PPB_VLAN_SET [5][4].RESERVED[31:18]	14
0xBB00C0D0	VLAN_PORT_PPB_VLAN_SET [5][4].VLAN_TYPE[17:17]	1
0xBB00C0D0	VLAN_PORT_PPB_VLAN_SET [5][4].PRI_AS[16:16]	1
0xBB00C0D0	VLAN_PORT_PPB_VLAN_SET [5][4].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C0D0	VLAN_PORT_PPB_VLAN_SET [5][4].VID_AS[12:12]	1
0xBB00C0D0	VLAN_PORT_PPB_VLAN_SET [5][4].VID[11:0]	12
0xBB00C0D4	VLAN_PORT_PPB_VLAN_SET [5][5].RESERVED[31:18]	14
0xBB00C0D4	VLAN_PORT_PPB_VLAN_SET [5][5].VLAN_TYPE[17:17]	1
0xBB00C0D4	VLAN_PORT_PPB_VLAN_SET [5][5].PRI_AS[16:16]	1
0xBB00C0D4	VLAN_PORT_PPB_VLAN_SET [5][5].PRI[15:13]	3
0xBB00C0D4	VLAN_PORT_PPB_VLAN_SET [5][5].VID_AS[12:12]	1
0xBB00C0D4	VLAN_PORT_PPB_VLAN_SET [5][5].VID[11:0]	12
0xBB00C0D8	VLAN_PORT_PPB_VLAN_SET [5][6].RESERVED[31:18]	14
0xBB00C0D8	VLAN_PORT_PPB_VLAN_SET [5][6].VLAN_TYPE[17:17]	1
0xBB00C0D8	VLAN_PORT_PPB_VLAN_SET [5][6].PRI_AS[16:16]	1
0xBB00C0D8	VLAN_PORT_PPB_VLAN_SET [5][6].PRI[15:13]	3
0xBB00C0D8	VLAN_PORT_PPB_VLAN_SET [5][6].VID_AS[12:12]	1
0xBB00C0D8	VLAN_PORT_PPB_VLAN_SET [5][6].VID[11:0]	12
0xBB00C0DC	VLAN_PORT_PPB_VLAN_SET [5][7].RESERVED[31:18]	14
0xBB00C0DC	VLAN_PORT_PPB_VLAN_SET [5][7].VLAN_TYPE[17:17]	1
0xBB00C0DC	VLAN_PORT_PPB_VLAN_SET [5][7].PRI_AS[16:16]	1
0xBB00C0DC	VLAN_PORT_PPB_VLAN_SET [5][7].PRI[15:13]	3
0xBB00C0DC	VLAN_PORT_PPB_VLAN_SET [5][7].VID_AS[12:12]	1
0xBB00C0DC	VLAN_PORT_PPB_VLAN_SET [5][7].VID[11:0]	12
0xBB00C0E0	VLAN_PORT_PPB_VLAN_SET [6][0].RESERVED[31:18]	14
0xBB00C0E0	VLAN_PORT_PPB_VLAN_SET [6][0].VLAN_TYPE[17:17]	1
0xBB00C0E0	VLAN_PORT_PPB_VLAN_SET [6][0].PRI_AS[16:16]	1
0xBB00C0E0	VLAN_PORT_PPB_VLAN_SET [6][0].PRI[15:13]	3
0xBB00C0E0	VLAN_PORT_PPB_VLAN_SET [6][0].VID_AS[12:12]	1
0xBB00C0E0	VLAN_PORT_PPB_VLAN_SET [6][0].VID[11:0]	12
0xBB00C0E4	VLAN_PORT_PPB_VLAN_SET [6][1].RESERVED[31:18]	14
0xBB00C0E4	VLAN_PORT_PPB_VLAN_SET [6][1].VLAN_TYPE[17:17]	1
0xBB00C0E4	VLAN_PORT_PPB_VLAN_SET [6][1].PRI_AS[16:16]	1
0xBB00C0E4	VLAN_PORT_PPB_VLAN_SET [6][1].PRI[15:13]	3
0xBB00C0E4	VLAN_PORT_PPB_VLAN_SET [6][1].VID_AS[12:12]	1
0xBB00C0E4	VLAN_PORT_PPB_VLAN_SET [6][1].VID[11:0]	12
0xBB00C0E8	VLAN_PORT_PPB_VLAN_SET [6][2].RESERVED[31:18]	14
0xBB00C0E8	VLAN_PORT_PPB_VLAN_SET [6][2].VLAN_TYPE[17:17]	1
0xBB00C0E8	VLAN_PORT_PPB_VLAN_SET [6][2].PRI_AS[16:16]	1
0xBB00C0E8	VLAN_PORT_PPB_VLAN_SET [6][2].PRI[15:13]	3
0xBB00C0E8	VLAN_PORT_PPB_VLAN_SET [6][2].VID_AS[12:12]	1
0xBB00C0E8	VLAN_PORT_PPB_VLAN_SET [6][2].VID[11:0]	12
0xBB00C0EC	VLAN_PORT_PPB_VLAN_SET [6][3].RESERVED[31:18]	14
0xBB00C0EC	VLAN_PORT_PPB_VLAN_SET [6][3].VLAN_TYPE[17:17]	1
0xBB00C0EC	VLAN_PORT_PPB_VLAN_SET [6][3].PRI_AS[16:16]	1
0xBB00C0EC	VLAN_PORT_PPB_VLAN_SET [6][3].PRI[15:13]	3
0xBB00C0EC	VLAN_PORT_PPB_VLAN_SET [6][3].VID_AS[12:12]	1
0xBB00C0EC	VLAN_PORT_PPB_VLAN_SET [6][3].VID[11:0]	12
0xBB00C0F0	VLAN_PORT_PPB_VLAN_SET [6][4].RESERVED[31:18]	14
0xBB00C0F0	VLAN_PORT_PPB_VLAN_SET [6][4].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C0F0	VLAN_PORT_PPB_VLAN_SET [6][4].PRI_AS[16:16]	1
0xBB00C0F0	VLAN_PORT_PPB_VLAN_SET [6][4].PRI[15:13]	3
0xBB00C0F0	VLAN_PORT_PPB_VLAN_SET [6][4].VID_AS[12:12]	1
0xBB00C0F0	VLAN_PORT_PPB_VLAN_SET [6][4].VID[11:0]	12
0xBB00C0F4	VLAN_PORT_PPB_VLAN_SET [6][5].RESERVED[31:18]	14
0xBB00C0F4	VLAN_PORT_PPB_VLAN_SET [6][5].VLAN_TYPE[17:17]	1
0xBB00C0F4	VLAN_PORT_PPB_VLAN_SET [6][5].PRI_AS[16:16]	1
0xBB00C0F4	VLAN_PORT_PPB_VLAN_SET [6][5].PRI[15:13]	3
0xBB00C0F4	VLAN_PORT_PPB_VLAN_SET [6][5].VID_AS[12:12]	1
0xBB00C0F4	VLAN_PORT_PPB_VLAN_SET [6][5].VID[11:0]	12
0xBB00C0F8	VLAN_PORT_PPB_VLAN_SET [6][6].RESERVED[31:18]	14
0xBB00C0F8	VLAN_PORT_PPB_VLAN_SET [6][6].VLAN_TYPE[17:17]	1
0xBB00C0F8	VLAN_PORT_PPB_VLAN_SET [6][6].PRI_AS[16:16]	1
0xBB00C0F8	VLAN_PORT_PPB_VLAN_SET [6][6].PRI[15:13]	3
0xBB00C0F8	VLAN_PORT_PPB_VLAN_SET [6][6].VID_AS[12:12]	1
0xBB00C0F8	VLAN_PORT_PPB_VLAN_SET [6][6].VID[11:0]	12
0xBB00C0FC	VLAN_PORT_PPB_VLAN_SET [6][7].RESERVED[31:18]	14
0xBB00C0FC	VLAN_PORT_PPB_VLAN_SET [6][7].VLAN_TYPE[17:17]	1
0xBB00C0FC	VLAN_PORT_PPB_VLAN_SET [6][7].PRI_AS[16:16]	1
0xBB00C0FC	VLAN_PORT_PPB_VLAN_SET [6][7].PRI[15:13]	3
0xBB00C0FC	VLAN_PORT_PPB_VLAN_SET [6][7].VID_AS[12:12]	1
0xBB00C0FC	VLAN_PORT_PPB_VLAN_SET [6][7].VID[11:0]	12
0xBB00C100	VLAN_PORT_PPB_VLAN_SET [7][0].RESERVED[31:18]	14
0xBB00C100	VLAN_PORT_PPB_VLAN_SET [7][0].VLAN_TYPE[17:17]	1
0xBB00C100	VLAN_PORT_PPB_VLAN_SET [7][0].PRI_AS[16:16]	1
0xBB00C100	VLAN_PORT_PPB_VLAN_SET [7][0].PRI[15:13]	3
0xBB00C100	VLAN_PORT_PPB_VLAN_SET [7][0].VID_AS[12:12]	1
0xBB00C100	VLAN_PORT_PPB_VLAN_SET [7][0].VID[11:0]	12
0xBB00C104	VLAN_PORT_PPB_VLAN_SET [7][1].RESERVED[31:18]	14
0xBB00C104	VLAN_PORT_PPB_VLAN_SET [7][1].VLAN_TYPE[17:17]	1
0xBB00C104	VLAN_PORT_PPB_VLAN_SET [7][1].PRI_AS[16:16]	1
0xBB00C104	VLAN_PORT_PPB_VLAN_SET [7][1].PRI[15:13]	3
0xBB00C104	VLAN_PORT_PPB_VLAN_SET [7][1].VID_AS[12:12]	1
0xBB00C104	VLAN_PORT_PPB_VLAN_SET [7][1].VID[11:0]	12
0xBB00C108	VLAN_PORT_PPB_VLAN_SET [7][2].RESERVED[31:18]	14
0xBB00C108	VLAN_PORT_PPB_VLAN_SET [7][2].VLAN_TYPE[17:17]	1
0xBB00C108	VLAN_PORT_PPB_VLAN_SET [7][2].PRI_AS[16:16]	1
0xBB00C108	VLAN_PORT_PPB_VLAN_SET [7][2].PRI[15:13]	3
0xBB00C108	VLAN_PORT_PPB_VLAN_SET [7][2].VID_AS[12:12]	1
0xBB00C108	VLAN_PORT_PPB_VLAN_SET [7][2].VID[11:0]	12
0xBB00C10C	VLAN_PORT_PPB_VLAN_SET [7][3].RESERVED[31:18]	14
0xBB00C10C	VLAN_PORT_PPB_VLAN_SET [7][3].VLAN_TYPE[17:17]	1
0xBB00C10C	VLAN_PORT_PPB_VLAN_SET [7][3].PRI_AS[16:16]	1
0xBB00C10C	VLAN_PORT_PPB_VLAN_SET [7][3].PRI[15:13]	3
0xBB00C10C	VLAN_PORT_PPB_VLAN_SET [7][3].VID_AS[12:12]	1
0xBB00C10C	VLAN_PORT_PPB_VLAN_SET [7][3].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C110	VLAN_PORT_PPB_VLAN_SET [7][4].RESERVED[31:18]	14
0xBB00C110	VLAN_PORT_PPB_VLAN_SET [7][4].VLAN_TYPE[17:17]	1
0xBB00C110	VLAN_PORT_PPB_VLAN_SET [7][4].PRI_AS[16:16]	1
0xBB00C110	VLAN_PORT_PPB_VLAN_SET [7][4].PRI[15:13]	3
0xBB00C110	VLAN_PORT_PPB_VLAN_SET [7][4].VID_AS[12:12]	1
0xBB00C110	VLAN_PORT_PPB_VLAN_SET [7][4].VID[11:0]	12
0xBB00C114	VLAN_PORT_PPB_VLAN_SET [7][5].RESERVED[31:18]	14
0xBB00C114	VLAN_PORT_PPB_VLAN_SET [7][5].VLAN_TYPE[17:17]	1
0xBB00C114	VLAN_PORT_PPB_VLAN_SET [7][5].PRI_AS[16:16]	1
0xBB00C114	VLAN_PORT_PPB_VLAN_SET [7][5].PRI[15:13]	3
0xBB00C114	VLAN_PORT_PPB_VLAN_SET [7][5].VID_AS[12:12]	1
0xBB00C114	VLAN_PORT_PPB_VLAN_SET [7][5].VID[11:0]	12
0xBB00C118	VLAN_PORT_PPB_VLAN_SET [7][6].RESERVED[31:18]	14
0xBB00C118	VLAN_PORT_PPB_VLAN_SET [7][6].VLAN_TYPE[17:17]	1
0xBB00C118	VLAN_PORT_PPB_VLAN_SET [7][6].PRI_AS[16:16]	1
0xBB00C118	VLAN_PORT_PPB_VLAN_SET [7][6].PRI[15:13]	3
0xBB00C118	VLAN_PORT_PPB_VLAN_SET [7][6].VID_AS[12:12]	1
0xBB00C118	VLAN_PORT_PPB_VLAN_SET [7][6].VID[11:0]	12
0xBB00C11C	VLAN_PORT_PPB_VLAN_SET [7][7].RESERVED[31:18]	14
0xBB00C11C	VLAN_PORT_PPB_VLAN_SET [7][7].VLAN_TYPE[17:17]	1
0xBB00C11C	VLAN_PORT_PPB_VLAN_SET [7][7].PRI_AS[16:16]	1
0xBB00C11C	VLAN_PORT_PPB_VLAN_SET [7][7].PRI[15:13]	3
0xBB00C11C	VLAN_PORT_PPB_VLAN_SET [7][7].VID_AS[12:12]	1
0xBB00C11C	VLAN_PORT_PPB_VLAN_SET [7][7].VID[11:0]	12
0xBB00C120	VLAN_PORT_PPB_VLAN_SET [8][0].RESERVED[31:18]	14
0xBB00C120	VLAN_PORT_PPB_VLAN_SET [8][0].VLAN_TYPE[17:17]	1
0xBB00C120	VLAN_PORT_PPB_VLAN_SET [8][0].PRI_AS[16:16]	1
0xBB00C120	VLAN_PORT_PPB_VLAN_SET [8][0].PRI[15:13]	3
0xBB00C120	VLAN_PORT_PPB_VLAN_SET [8][0].VID_AS[12:12]	1
0xBB00C120	VLAN_PORT_PPB_VLAN_SET [8][0].VID[11:0]	12
0xBB00C124	VLAN_PORT_PPB_VLAN_SET [8][1].RESERVED[31:18]	14
0xBB00C124	VLAN_PORT_PPB_VLAN_SET [8][1].VLAN_TYPE[17:17]	1
0xBB00C124	VLAN_PORT_PPB_VLAN_SET [8][1].PRI_AS[16:16]	1
0xBB00C124	VLAN_PORT_PPB_VLAN_SET [8][1].PRI[15:13]	3
0xBB00C124	VLAN_PORT_PPB_VLAN_SET [8][1].VID_AS[12:12]	1
0xBB00C124	VLAN_PORT_PPB_VLAN_SET [8][1].VID[11:0]	12
0xBB00C128	VLAN_PORT_PPB_VLAN_SET [8][2].RESERVED[31:18]	14
0xBB00C128	VLAN_PORT_PPB_VLAN_SET [8][2].VLAN_TYPE[17:17]	1
0xBB00C128	VLAN_PORT_PPB_VLAN_SET [8][2].PRI_AS[16:16]	1
0xBB00C128	VLAN_PORT_PPB_VLAN_SET [8][2].PRI[15:13]	3
0xBB00C128	VLAN_PORT_PPB_VLAN_SET [8][2].VID_AS[12:12]	1
0xBB00C128	VLAN_PORT_PPB_VLAN_SET [8][2].VID[11:0]	12
0xBB00C12C	VLAN_PORT_PPB_VLAN_SET [8][3].RESERVED[31:18]	14
0xBB00C12C	VLAN_PORT_PPB_VLAN_SET [8][3].VLAN_TYPE[17:17]	1
0xBB00C12C	VLAN_PORT_PPB_VLAN_SET [8][3].PRI_AS[16:16]	1
0xBB00C12C	VLAN_PORT_PPB_VLAN_SET [8][3].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C12C	VLAN_PORT_PPB_VLAN_SET [8][3].VID_AS[12:12]	1
0xBB00C12C	VLAN_PORT_PPB_VLAN_SET [8][3].VID[11:0]	12
0xBB00C130	VLAN_PORT_PPB_VLAN_SET [8][4].RESERVED[31:18]	14
0xBB00C130	VLAN_PORT_PPB_VLAN_SET [8][4].VLAN_TYPE[17:17]	1
0xBB00C130	VLAN_PORT_PPB_VLAN_SET [8][4].PRI_AS[16:16]	1
0xBB00C130	VLAN_PORT_PPB_VLAN_SET [8][4].PRI[15:13]	3
0xBB00C130	VLAN_PORT_PPB_VLAN_SET [8][4].VID_AS[12:12]	1
0xBB00C130	VLAN_PORT_PPB_VLAN_SET [8][4].VID[11:0]	12
0xBB00C134	VLAN_PORT_PPB_VLAN_SET [8][5].RESERVED[31:18]	14
0xBB00C134	VLAN_PORT_PPB_VLAN_SET [8][5].VLAN_TYPE[17:17]	1
0xBB00C134	VLAN_PORT_PPB_VLAN_SET [8][5].PRI_AS[16:16]	1
0xBB00C134	VLAN_PORT_PPB_VLAN_SET [8][5].PRI[15:13]	3
0xBB00C134	VLAN_PORT_PPB_VLAN_SET [8][5].VID_AS[12:12]	1
0xBB00C134	VLAN_PORT_PPB_VLAN_SET [8][5].VID[11:0]	12
0xBB00C138	VLAN_PORT_PPB_VLAN_SET [8][6].RESERVED[31:18]	14
0xBB00C138	VLAN_PORT_PPB_VLAN_SET [8][6].VLAN_TYPE[17:17]	1
0xBB00C138	VLAN_PORT_PPB_VLAN_SET [8][6].PRI_AS[16:16]	1
0xBB00C138	VLAN_PORT_PPB_VLAN_SET [8][6].PRI[15:13]	3
0xBB00C138	VLAN_PORT_PPB_VLAN_SET [8][6].VID_AS[12:12]	1
0xBB00C138	VLAN_PORT_PPB_VLAN_SET [8][6].VID[11:0]	12
0xBB00C13C	VLAN_PORT_PPB_VLAN_SET [8][7].RESERVED[31:18]	14
0xBB00C13C	VLAN_PORT_PPB_VLAN_SET [8][7].VLAN_TYPE[17:17]	1
0xBB00C13C	VLAN_PORT_PPB_VLAN_SET [8][7].PRI_AS[16:16]	1
0xBB00C13C	VLAN_PORT_PPB_VLAN_SET [8][7].PRI[15:13]	3
0xBB00C13C	VLAN_PORT_PPB_VLAN_SET [8][7].VID_AS[12:12]	1
0xBB00C13C	VLAN_PORT_PPB_VLAN_SET [8][7].VID[11:0]	12
0xBB00C140	VLAN_PORT_PPB_VLAN_SET [9][0].RESERVED[31:18]	14
0xBB00C140	VLAN_PORT_PPB_VLAN_SET [9][0].VLAN_TYPE[17:17]	1
0xBB00C140	VLAN_PORT_PPB_VLAN_SET [9][0].PRI_AS[16:16]	1
0xBB00C140	VLAN_PORT_PPB_VLAN_SET [9][0].PRI[15:13]	3
0xBB00C140	VLAN_PORT_PPB_VLAN_SET [9][0].VID_AS[12:12]	1
0xBB00C140	VLAN_PORT_PPB_VLAN_SET [9][0].VID[11:0]	12
0xBB00C144	VLAN_PORT_PPB_VLAN_SET [9][1].RESERVED[31:18]	14
0xBB00C144	VLAN_PORT_PPB_VLAN_SET [9][1].VLAN_TYPE[17:17]	1
0xBB00C144	VLAN_PORT_PPB_VLAN_SET [9][1].PRI_AS[16:16]	1
0xBB00C144	VLAN_PORT_PPB_VLAN_SET [9][1].PRI[15:13]	3
0xBB00C144	VLAN_PORT_PPB_VLAN_SET [9][1].VID_AS[12:12]	1
0xBB00C144	VLAN_PORT_PPB_VLAN_SET [9][1].VID[11:0]	12
0xBB00C148	VLAN_PORT_PPB_VLAN_SET [9][2].RESERVED[31:18]	14
0xBB00C148	VLAN_PORT_PPB_VLAN_SET [9][2].VLAN_TYPE[17:17]	1
0xBB00C148	VLAN_PORT_PPB_VLAN_SET [9][2].PRI_AS[16:16]	1
0xBB00C148	VLAN_PORT_PPB_VLAN_SET [9][2].PRI[15:13]	3
0xBB00C148	VLAN_PORT_PPB_VLAN_SET [9][2].VID_AS[12:12]	1
0xBB00C148	VLAN_PORT_PPB_VLAN_SET [9][2].VID[11:0]	12
0xBB00C14C	VLAN_PORT_PPB_VLAN_SET [9][3].RESERVED[31:18]	14
0xBB00C14C	VLAN_PORT_PPB_VLAN_SET [9][3].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C14C	VLAN_PORT_PPB_VLAN_SET [9][3].PRI_AS[16:16]	1
0xBB00C14C	VLAN_PORT_PPB_VLAN_SET [9][3].PRI[15:13]	3
0xBB00C14C	VLAN_PORT_PPB_VLAN_SET [9][3].VID_AS[12:12]	1
0xBB00C14C	VLAN_PORT_PPB_VLAN_SET [9][3].VID[11:0]	12
0xBB00C150	VLAN_PORT_PPB_VLAN_SET [9][4].RESERVED[31:18]	14
0xBB00C150	VLAN_PORT_PPB_VLAN_SET [9][4].VLAN_TYPE[17:17]	1
0xBB00C150	VLAN_PORT_PPB_VLAN_SET [9][4].PRI_AS[16:16]	1
0xBB00C150	VLAN_PORT_PPB_VLAN_SET [9][4].PRI[15:13]	3
0xBB00C150	VLAN_PORT_PPB_VLAN_SET [9][4].VID_AS[12:12]	1
0xBB00C150	VLAN_PORT_PPB_VLAN_SET [9][4].VID[11:0]	12
0xBB00C154	VLAN_PORT_PPB_VLAN_SET [9][5].RESERVED[31:18]	14
0xBB00C154	VLAN_PORT_PPB_VLAN_SET [9][5].VLAN_TYPE[17:17]	1
0xBB00C154	VLAN_PORT_PPB_VLAN_SET [9][5].PRI_AS[16:16]	1
0xBB00C154	VLAN_PORT_PPB_VLAN_SET [9][5].PRI[15:13]	3
0xBB00C154	VLAN_PORT_PPB_VLAN_SET [9][5].VID_AS[12:12]	1
0xBB00C154	VLAN_PORT_PPB_VLAN_SET [9][5].VID[11:0]	12
0xBB00C158	VLAN_PORT_PPB_VLAN_SET [9][6].RESERVED[31:18]	14
0xBB00C158	VLAN_PORT_PPB_VLAN_SET [9][6].VLAN_TYPE[17:17]	1
0xBB00C158	VLAN_PORT_PPB_VLAN_SET [9][6].PRI_AS[16:16]	1
0xBB00C158	VLAN_PORT_PPB_VLAN_SET [9][6].PRI[15:13]	3
0xBB00C158	VLAN_PORT_PPB_VLAN_SET [9][6].VID_AS[12:12]	1
0xBB00C158	VLAN_PORT_PPB_VLAN_SET [9][6].VID[11:0]	12
0xBB00C15C	VLAN_PORT_PPB_VLAN_SET [9][7].RESERVED[31:18]	14
0xBB00C15C	VLAN_PORT_PPB_VLAN_SET [9][7].VLAN_TYPE[17:17]	1
0xBB00C15C	VLAN_PORT_PPB_VLAN_SET [9][7].PRI_AS[16:16]	1
0xBB00C15C	VLAN_PORT_PPB_VLAN_SET [9][7].PRI[15:13]	3
0xBB00C15C	VLAN_PORT_PPB_VLAN_SET [9][7].VID_AS[12:12]	1
0xBB00C15C	VLAN_PORT_PPB_VLAN_SET [9][7].VID[11:0]	12
0xBB00C160	VLAN_PORT_PPB_VLAN_SET [10][0].RESERVED[31:18]	14
0xBB00C160	VLAN_PORT_PPB_VLAN_SET [10][0].VLAN_TYPE[17:17]	1
0xBB00C160	VLAN_PORT_PPB_VLAN_SET [10][0].PRI_AS[16:16]	1
0xBB00C160	VLAN_PORT_PPB_VLAN_SET [10][0].PRI[15:13]	3
0xBB00C160	VLAN_PORT_PPB_VLAN_SET [10][0].VID_AS[12:12]	1
0xBB00C160	VLAN_PORT_PPB_VLAN_SET [10][0].VID[11:0]	12
0xBB00C164	VLAN_PORT_PPB_VLAN_SET [10][1].RESERVED[31:18]	14
0xBB00C164	VLAN_PORT_PPB_VLAN_SET [10][1].VLAN_TYPE[17:17]	1
0xBB00C164	VLAN_PORT_PPB_VLAN_SET [10][1].PRI_AS[16:16]	1
0xBB00C164	VLAN_PORT_PPB_VLAN_SET [10][1].PRI[15:13]	3
0xBB00C164	VLAN_PORT_PPB_VLAN_SET [10][1].VID_AS[12:12]	1
0xBB00C164	VLAN_PORT_PPB_VLAN_SET [10][1].VID[11:0]	12
0xBB00C168	VLAN_PORT_PPB_VLAN_SET [10][2].RESERVED[31:18]	14
0xBB00C168	VLAN_PORT_PPB_VLAN_SET [10][2].VLAN_TYPE[17:17]	1
0xBB00C168	VLAN_PORT_PPB_VLAN_SET [10][2].PRI_AS[16:16]	1
0xBB00C168	VLAN_PORT_PPB_VLAN_SET [10][2].PRI[15:13]	3
0xBB00C168	VLAN_PORT_PPB_VLAN_SET [10][2].VID_AS[12:12]	1
0xBB00C168	VLAN_PORT_PPB_VLAN_SET [10][2].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C16C	VLAN_PORT_PPB_VLAN_SET [10][3].RESERVED[31:18]	14
0xBB00C16C	VLAN_PORT_PPB_VLAN_SET [10][3].VLAN_TYPE[17:17]	1
0xBB00C16C	VLAN_PORT_PPB_VLAN_SET [10][3].PRI_AS[16:16]	1
0xBB00C16C	VLAN_PORT_PPB_VLAN_SET [10][3].PRI[15:13]	3
0xBB00C16C	VLAN_PORT_PPB_VLAN_SET [10][3].VID_AS[12:12]	1
0xBB00C16C	VLAN_PORT_PPB_VLAN_SET [10][3].VID[11:0]	12
0xBB00C170	VLAN_PORT_PPB_VLAN_SET [10][4].RESERVED[31:18]	14
0xBB00C170	VLAN_PORT_PPB_VLAN_SET [10][4].VLAN_TYPE[17:17]	1
0xBB00C170	VLAN_PORT_PPB_VLAN_SET [10][4].PRI_AS[16:16]	1
0xBB00C170	VLAN_PORT_PPB_VLAN_SET [10][4].PRI[15:13]	3
0xBB00C170	VLAN_PORT_PPB_VLAN_SET [10][4].VID_AS[12:12]	1
0xBB00C170	VLAN_PORT_PPB_VLAN_SET [10][4].VID[11:0]	12
0xBB00C174	VLAN_PORT_PPB_VLAN_SET [10][5].RESERVED[31:18]	14
0xBB00C174	VLAN_PORT_PPB_VLAN_SET [10][5].VLAN_TYPE[17:17]	1
0xBB00C174	VLAN_PORT_PPB_VLAN_SET [10][5].PRI_AS[16:16]	1
0xBB00C174	VLAN_PORT_PPB_VLAN_SET [10][5].PRI[15:13]	3
0xBB00C174	VLAN_PORT_PPB_VLAN_SET [10][5].VID_AS[12:12]	1
0xBB00C174	VLAN_PORT_PPB_VLAN_SET [10][5].VID[11:0]	12
0xBB00C178	VLAN_PORT_PPB_VLAN_SET [10][6].RESERVED[31:18]	14
0xBB00C178	VLAN_PORT_PPB_VLAN_SET [10][6].VLAN_TYPE[17:17]	1
0xBB00C178	VLAN_PORT_PPB_VLAN_SET [10][6].PRI_AS[16:16]	1
0xBB00C178	VLAN_PORT_PPB_VLAN_SET [10][6].PRI[15:13]	3
0xBB00C178	VLAN_PORT_PPB_VLAN_SET [10][6].VID_AS[12:12]	1
0xBB00C178	VLAN_PORT_PPB_VLAN_SET [10][6].VID[11:0]	12
0xBB00C17C	VLAN_PORT_PPB_VLAN_SET [10][7].RESERVED[31:18]	14
0xBB00C17C	VLAN_PORT_PPB_VLAN_SET [10][7].VLAN_TYPE[17:17]	1
0xBB00C17C	VLAN_PORT_PPB_VLAN_SET [10][7].PRI_AS[16:16]	1
0xBB00C17C	VLAN_PORT_PPB_VLAN_SET [10][7].PRI[15:13]	3
0xBB00C17C	VLAN_PORT_PPB_VLAN_SET [10][7].VID_AS[12:12]	1
0xBB00C17C	VLAN_PORT_PPB_VLAN_SET [10][7].VID[11:0]	12
0xBB00C180	VLAN_PORT_PPB_VLAN_SET [11][0].RESERVED[31:18]	14
0xBB00C180	VLAN_PORT_PPB_VLAN_SET [11][0].VLAN_TYPE[17:17]	1
0xBB00C180	VLAN_PORT_PPB_VLAN_SET [11][0].PRI_AS[16:16]	1
0xBB00C180	VLAN_PORT_PPB_VLAN_SET [11][0].PRI[15:13]	3
0xBB00C180	VLAN_PORT_PPB_VLAN_SET [11][0].VID_AS[12:12]	1
0xBB00C180	VLAN_PORT_PPB_VLAN_SET [11][0].VID[11:0]	12
0xBB00C184	VLAN_PORT_PPB_VLAN_SET [11][1].RESERVED[31:18]	14
0xBB00C184	VLAN_PORT_PPB_VLAN_SET [11][1].VLAN_TYPE[17:17]	1
0xBB00C184	VLAN_PORT_PPB_VLAN_SET [11][1].PRI_AS[16:16]	1
0xBB00C184	VLAN_PORT_PPB_VLAN_SET [11][1].PRI[15:13]	3
0xBB00C184	VLAN_PORT_PPB_VLAN_SET [11][1].VID_AS[12:12]	1
0xBB00C184	VLAN_PORT_PPB_VLAN_SET [11][1].VID[11:0]	12
0xBB00C188	VLAN_PORT_PPB_VLAN_SET [11][2].RESERVED[31:18]	14
0xBB00C188	VLAN_PORT_PPB_VLAN_SET [11][2].VLAN_TYPE[17:17]	1
0xBB00C188	VLAN_PORT_PPB_VLAN_SET [11][2].PRI_AS[16:16]	1
0xBB00C188	VLAN_PORT_PPB_VLAN_SET [11][2].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C188	VLAN_PORT_PPB_VLAN_SET [11][2].VID_AS[12:12]	1
0xBB00C188	VLAN_PORT_PPB_VLAN_SET [11][2].VID[11:0]	12
0xBB00C18C	VLAN_PORT_PPB_VLAN_SET [11][3].RESERVED[31:18]	14
0xBB00C18C	VLAN_PORT_PPB_VLAN_SET [11][3].VLAN_TYPE[17:17]	1
0xBB00C18C	VLAN_PORT_PPB_VLAN_SET [11][3].PRI_AS[16:16]	1
0xBB00C18C	VLAN_PORT_PPB_VLAN_SET [11][3].PRI[15:13]	3
0xBB00C18C	VLAN_PORT_PPB_VLAN_SET [11][3].VID_AS[12:12]	1
0xBB00C18C	VLAN_PORT_PPB_VLAN_SET [11][3].VID[11:0]	12
0xBB00C190	VLAN_PORT_PPB_VLAN_SET [11][4].RESERVED[31:18]	14
0xBB00C190	VLAN_PORT_PPB_VLAN_SET [11][4].VLAN_TYPE[17:17]	1
0xBB00C190	VLAN_PORT_PPB_VLAN_SET [11][4].PRI_AS[16:16]	1
0xBB00C190	VLAN_PORT_PPB_VLAN_SET [11][4].PRI[15:13]	3
0xBB00C190	VLAN_PORT_PPB_VLAN_SET [11][4].VID_AS[12:12]	1
0xBB00C190	VLAN_PORT_PPB_VLAN_SET [11][4].VID[11:0]	12
0xBB00C194	VLAN_PORT_PPB_VLAN_SET [11][5].RESERVED[31:18]	14
0xBB00C194	VLAN_PORT_PPB_VLAN_SET [11][5].VLAN_TYPE[17:17]	1
0xBB00C194	VLAN_PORT_PPB_VLAN_SET [11][5].PRI_AS[16:16]	1
0xBB00C194	VLAN_PORT_PPB_VLAN_SET [11][5].PRI[15:13]	3
0xBB00C194	VLAN_PORT_PPB_VLAN_SET [11][5].VID_AS[12:12]	1
0xBB00C194	VLAN_PORT_PPB_VLAN_SET [11][5].VID[11:0]	12
0xBB00C198	VLAN_PORT_PPB_VLAN_SET [11][6].RESERVED[31:18]	14
0xBB00C198	VLAN_PORT_PPB_VLAN_SET [11][6].VLAN_TYPE[17:17]	1
0xBB00C198	VLAN_PORT_PPB_VLAN_SET [11][6].PRI_AS[16:16]	1
0xBB00C198	VLAN_PORT_PPB_VLAN_SET [11][6].PRI[15:13]	3
0xBB00C198	VLAN_PORT_PPB_VLAN_SET [11][6].VID_AS[12:12]	1
0xBB00C198	VLAN_PORT_PPB_VLAN_SET [11][6].VID[11:0]	12
0xBB00C19C	VLAN_PORT_PPB_VLAN_SET [11][7].RESERVED[31:18]	14
0xBB00C19C	VLAN_PORT_PPB_VLAN_SET [11][7].VLAN_TYPE[17:17]	1
0xBB00C19C	VLAN_PORT_PPB_VLAN_SET [11][7].PRI_AS[16:16]	1
0xBB00C19C	VLAN_PORT_PPB_VLAN_SET [11][7].PRI[15:13]	3
0xBB00C19C	VLAN_PORT_PPB_VLAN_SET [11][7].VID_AS[12:12]	1
0xBB00C19C	VLAN_PORT_PPB_VLAN_SET [11][7].VID[11:0]	12
0xBB00C1A0	VLAN_PORT_PPB_VLAN_SET [12][0].RESERVED[31:18]	14
0xBB00C1A0	VLAN_PORT_PPB_VLAN_SET [12][0].VLAN_TYPE[17:17]	1
0xBB00C1A0	VLAN_PORT_PPB_VLAN_SET [12][0].PRI_AS[16:16]	1
0xBB00C1A0	VLAN_PORT_PPB_VLAN_SET [12][0].PRI[15:13]	3
0xBB00C1A0	VLAN_PORT_PPB_VLAN_SET [12][0].VID_AS[12:12]	1
0xBB00C1A0	VLAN_PORT_PPB_VLAN_SET [12][0].VID[11:0]	12
0xBB00C1A4	VLAN_PORT_PPB_VLAN_SET [12][1].RESERVED[31:18]	14
0xBB00C1A4	VLAN_PORT_PPB_VLAN_SET [12][1].VLAN_TYPE[17:17]	1
0xBB00C1A4	VLAN_PORT_PPB_VLAN_SET [12][1].PRI_AS[16:16]	1
0xBB00C1A4	VLAN_PORT_PPB_VLAN_SET [12][1].PRI[15:13]	3
0xBB00C1A4	VLAN_PORT_PPB_VLAN_SET [12][1].VID_AS[12:12]	1
0xBB00C1A4	VLAN_PORT_PPB_VLAN_SET [12][1].VID[11:0]	12
0xBB00C1A8	VLAN_PORT_PPB_VLAN_SET [12][2].RESERVED[31:18]	14
0xBB00C1A8	VLAN_PORT_PPB_VLAN_SET [12][2].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C1A8	VLAN_PORT_PPB_VLAN_SET [12][2].PRI_AS[16:16]	1
0xBB00C1A8	VLAN_PORT_PPB_VLAN_SET [12][2].PRI[15:13]	3
0xBB00C1A8	VLAN_PORT_PPB_VLAN_SET [12][2].VID_AS[12:12]	1
0xBB00C1A8	VLAN_PORT_PPB_VLAN_SET [12][2].VID[11:0]	12
0xBB00C1AC	VLAN_PORT_PPB_VLAN_SET [12][3].RESERVED[31:18]	14
0xBB00C1AC	VLAN_PORT_PPB_VLAN_SET [12][3].VLAN_TYPE[17:17]	1
0xBB00C1AC	VLAN_PORT_PPB_VLAN_SET [12][3].PRI_AS[16:16]	1
0xBB00C1AC	VLAN_PORT_PPB_VLAN_SET [12][3].PRI[15:13]	3
0xBB00C1AC	VLAN_PORT_PPB_VLAN_SET [12][3].VID_AS[12:12]	1
0xBB00C1AC	VLAN_PORT_PPB_VLAN_SET [12][3].VID[11:0]	12
0xBB00C1B0	VLAN_PORT_PPB_VLAN_SET [12][4].RESERVED[31:18]	14
0xBB00C1B0	VLAN_PORT_PPB_VLAN_SET [12][4].VLAN_TYPE[17:17]	1
0xBB00C1B0	VLAN_PORT_PPB_VLAN_SET [12][4].PRI_AS[16:16]	1
0xBB00C1B0	VLAN_PORT_PPB_VLAN_SET [12][4].PRI[15:13]	3
0xBB00C1B0	VLAN_PORT_PPB_VLAN_SET [12][4].VID_AS[12:12]	1
0xBB00C1B0	VLAN_PORT_PPB_VLAN_SET [12][4].VID[11:0]	12
0xBB00C1B4	VLAN_PORT_PPB_VLAN_SET [12][5].RESERVED[31:18]	14
0xBB00C1B4	VLAN_PORT_PPB_VLAN_SET [12][5].VLAN_TYPE[17:17]	1
0xBB00C1B4	VLAN_PORT_PPB_VLAN_SET [12][5].PRI_AS[16:16]	1
0xBB00C1B4	VLAN_PORT_PPB_VLAN_SET [12][5].PRI[15:13]	3
0xBB00C1B4	VLAN_PORT_PPB_VLAN_SET [12][5].VID_AS[12:12]	1
0xBB00C1B4	VLAN_PORT_PPB_VLAN_SET [12][5].VID[11:0]	12
0xBB00C1B8	VLAN_PORT_PPB_VLAN_SET [12][6].RESERVED[31:18]	14
0xBB00C1B8	VLAN_PORT_PPB_VLAN_SET [12][6].VLAN_TYPE[17:17]	1
0xBB00C1B8	VLAN_PORT_PPB_VLAN_SET [12][6].PRI_AS[16:16]	1
0xBB00C1B8	VLAN_PORT_PPB_VLAN_SET [12][6].PRI[15:13]	3
0xBB00C1B8	VLAN_PORT_PPB_VLAN_SET [12][6].VID_AS[12:12]	1
0xBB00C1B8	VLAN_PORT_PPB_VLAN_SET [12][6].VID[11:0]	12
0xBB00C1BC	VLAN_PORT_PPB_VLAN_SET [12][7].RESERVED[31:18]	14
0xBB00C1BC	VLAN_PORT_PPB_VLAN_SET [12][7].VLAN_TYPE[17:17]	1
0xBB00C1BC	VLAN_PORT_PPB_VLAN_SET [12][7].PRI_AS[16:16]	1
0xBB00C1BC	VLAN_PORT_PPB_VLAN_SET [12][7].PRI[15:13]	3
0xBB00C1BC	VLAN_PORT_PPB_VLAN_SET [12][7].VID_AS[12:12]	1
0xBB00C1BC	VLAN_PORT_PPB_VLAN_SET [12][7].VID[11:0]	12
0xBB00C1C0	VLAN_PORT_PPB_VLAN_SET [13][0].RESERVED[31:18]	14
0xBB00C1C0	VLAN_PORT_PPB_VLAN_SET [13][0].VLAN_TYPE[17:17]	1
0xBB00C1C0	VLAN_PORT_PPB_VLAN_SET [13][0].PRI_AS[16:16]	1
0xBB00C1C0	VLAN_PORT_PPB_VLAN_SET [13][0].PRI[15:13]	3
0xBB00C1C0	VLAN_PORT_PPB_VLAN_SET [13][0].VID_AS[12:12]	1
0xBB00C1C0	VLAN_PORT_PPB_VLAN_SET [13][0].VID[11:0]	12
0xBB00C1C4	VLAN_PORT_PPB_VLAN_SET [13][1].RESERVED[31:18]	14
0xBB00C1C4	VLAN_PORT_PPB_VLAN_SET [13][1].VLAN_TYPE[17:17]	1
0xBB00C1C4	VLAN_PORT_PPB_VLAN_SET [13][1].PRI_AS[16:16]	1
0xBB00C1C4	VLAN_PORT_PPB_VLAN_SET [13][1].PRI[15:13]	3
0xBB00C1C4	VLAN_PORT_PPB_VLAN_SET [13][1].VID_AS[12:12]	1
0xBB00C1C4	VLAN_PORT_PPB_VLAN_SET [13][1].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C1C8	VLAN_PORT_PPB_VLAN_SET [13][2].RESERVED[31:18]	14
0xBB00C1C8	VLAN_PORT_PPB_VLAN_SET [13][2].VLAN_TYPE[17:17]	1
0xBB00C1C8	VLAN_PORT_PPB_VLAN_SET [13][2].PRI_AS[16:16]	1
0xBB00C1C8	VLAN_PORT_PPB_VLAN_SET [13][2].PRI[15:13]	3
0xBB00C1C8	VLAN_PORT_PPB_VLAN_SET [13][2].VID_AS[12:12]	1
0xBB00C1C8	VLAN_PORT_PPB_VLAN_SET [13][2].VID[11:0]	12
0xBB00C1CC	VLAN_PORT_PPB_VLAN_SET [13][3].RESERVED[31:18]	14
0xBB00C1CC	VLAN_PORT_PPB_VLAN_SET [13][3].VLAN_TYPE[17:17]	1
0xBB00C1CC	VLAN_PORT_PPB_VLAN_SET [13][3].PRI_AS[16:16]	1
0xBB00C1CC	VLAN_PORT_PPB_VLAN_SET [13][3].PRI[15:13]	3
0xBB00C1CC	VLAN_PORT_PPB_VLAN_SET [13][3].VID_AS[12:12]	1
0xBB00C1CC	VLAN_PORT_PPB_VLAN_SET [13][3].VID[11:0]	12
0xBB00C1D0	VLAN_PORT_PPB_VLAN_SET [13][4].RESERVED[31:18]	14
0xBB00C1D0	VLAN_PORT_PPB_VLAN_SET [13][4].VLAN_TYPE[17:17]	1
0xBB00C1D0	VLAN_PORT_PPB_VLAN_SET [13][4].PRI_AS[16:16]	1
0xBB00C1D0	VLAN_PORT_PPB_VLAN_SET [13][4].PRI[15:13]	3
0xBB00C1D0	VLAN_PORT_PPB_VLAN_SET [13][4].VID_AS[12:12]	1
0xBB00C1D0	VLAN_PORT_PPB_VLAN_SET [13][4].VID[11:0]	12
0xBB00C1D4	VLAN_PORT_PPB_VLAN_SET [13][5].RESERVED[31:18]	14
0xBB00C1D4	VLAN_PORT_PPB_VLAN_SET [13][5].VLAN_TYPE[17:17]	1
0xBB00C1D4	VLAN_PORT_PPB_VLAN_SET [13][5].PRI_AS[16:16]	1
0xBB00C1D4	VLAN_PORT_PPB_VLAN_SET [13][5].PRI[15:13]	3
0xBB00C1D4	VLAN_PORT_PPB_VLAN_SET [13][5].VID_AS[12:12]	1
0xBB00C1D4	VLAN_PORT_PPB_VLAN_SET [13][5].VID[11:0]	12
0xBB00C1D8	VLAN_PORT_PPB_VLAN_SET [13][6].RESERVED[31:18]	14
0xBB00C1D8	VLAN_PORT_PPB_VLAN_SET [13][6].VLAN_TYPE[17:17]	1
0xBB00C1D8	VLAN_PORT_PPB_VLAN_SET [13][6].PRI_AS[16:16]	1
0xBB00C1D8	VLAN_PORT_PPB_VLAN_SET [13][6].PRI[15:13]	3
0xBB00C1D8	VLAN_PORT_PPB_VLAN_SET [13][6].VID_AS[12:12]	1
0xBB00C1D8	VLAN_PORT_PPB_VLAN_SET [13][6].VID[11:0]	12
0xBB00C1DC	VLAN_PORT_PPB_VLAN_SET [13][7].RESERVED[31:18]	14
0xBB00C1DC	VLAN_PORT_PPB_VLAN_SET [13][7].VLAN_TYPE[17:17]	1
0xBB00C1DC	VLAN_PORT_PPB_VLAN_SET [13][7].PRI_AS[16:16]	1
0xBB00C1DC	VLAN_PORT_PPB_VLAN_SET [13][7].PRI[15:13]	3
0xBB00C1DC	VLAN_PORT_PPB_VLAN_SET [13][7].VID_AS[12:12]	1
0xBB00C1DC	VLAN_PORT_PPB_VLAN_SET [13][7].VID[11:0]	12
0xBB00C1E0	VLAN_PORT_PPB_VLAN_SET [14][0].RESERVED[31:18]	14
0xBB00C1E0	VLAN_PORT_PPB_VLAN_SET [14][0].VLAN_TYPE[17:17]	1
0xBB00C1E0	VLAN_PORT_PPB_VLAN_SET [14][0].PRI_AS[16:16]	1
0xBB00C1E0	VLAN_PORT_PPB_VLAN_SET [14][0].PRI[15:13]	3
0xBB00C1E0	VLAN_PORT_PPB_VLAN_SET [14][0].VID_AS[12:12]	1
0xBB00C1E0	VLAN_PORT_PPB_VLAN_SET [14][0].VID[11:0]	12
0xBB00C1E4	VLAN_PORT_PPB_VLAN_SET [14][1].RESERVED[31:18]	14
0xBB00C1E4	VLAN_PORT_PPB_VLAN_SET [14][1].VLAN_TYPE[17:17]	1
0xBB00C1E4	VLAN_PORT_PPB_VLAN_SET [14][1].PRI_AS[16:16]	1
0xBB00C1E4	VLAN_PORT_PPB_VLAN_SET [14][1].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C1E4	VLAN_PORT_PPB_VLAN_SET [14][1].VID_AS[12:12]	1
0xBB00C1E4	VLAN_PORT_PPB_VLAN_SET [14][1].VID[11:0]	12
0xBB00C1E8	VLAN_PORT_PPB_VLAN_SET [14][2].RESERVED[31:18]	14
0xBB00C1E8	VLAN_PORT_PPB_VLAN_SET [14][2].VLAN_TYPE[17:17]	1
0xBB00C1E8	VLAN_PORT_PPB_VLAN_SET [14][2].PRI_AS[16:16]	1
0xBB00C1E8	VLAN_PORT_PPB_VLAN_SET [14][2].PRI[15:13]	3
0xBB00C1E8	VLAN_PORT_PPB_VLAN_SET [14][2].VID_AS[12:12]	1
0xBB00C1E8	VLAN_PORT_PPB_VLAN_SET [14][2].VID[11:0]	12
0xBB00C1EC	VLAN_PORT_PPB_VLAN_SET [14][3].RESERVED[31:18]	14
0xBB00C1EC	VLAN_PORT_PPB_VLAN_SET [14][3].VLAN_TYPE[17:17]	1
0xBB00C1EC	VLAN_PORT_PPB_VLAN_SET [14][3].PRI_AS[16:16]	1
0xBB00C1EC	VLAN_PORT_PPB_VLAN_SET [14][3].PRI[15:13]	3
0xBB00C1EC	VLAN_PORT_PPB_VLAN_SET [14][3].VID_AS[12:12]	1
0xBB00C1EC	VLAN_PORT_PPB_VLAN_SET [14][3].VID[11:0]	12
0xBB00C1F0	VLAN_PORT_PPB_VLAN_SET [14][4].RESERVED[31:18]	14
0xBB00C1F0	VLAN_PORT_PPB_VLAN_SET [14][4].VLAN_TYPE[17:17]	1
0xBB00C1F0	VLAN_PORT_PPB_VLAN_SET [14][4].PRI_AS[16:16]	1
0xBB00C1F0	VLAN_PORT_PPB_VLAN_SET [14][4].PRI[15:13]	3
0xBB00C1F0	VLAN_PORT_PPB_VLAN_SET [14][4].VID_AS[12:12]	1
0xBB00C1F0	VLAN_PORT_PPB_VLAN_SET [14][4].VID[11:0]	12
0xBB00C1F4	VLAN_PORT_PPB_VLAN_SET [14][5].RESERVED[31:18]	14
0xBB00C1F4	VLAN_PORT_PPB_VLAN_SET [14][5].VLAN_TYPE[17:17]	1
0xBB00C1F4	VLAN_PORT_PPB_VLAN_SET [14][5].PRI_AS[16:16]	1
0xBB00C1F4	VLAN_PORT_PPB_VLAN_SET [14][5].PRI[15:13]	3
0xBB00C1F4	VLAN_PORT_PPB_VLAN_SET [14][5].VID_AS[12:12]	1
0xBB00C1F4	VLAN_PORT_PPB_VLAN_SET [14][5].VID[11:0]	12
0xBB00C1F8	VLAN_PORT_PPB_VLAN_SET [14][6].RESERVED[31:18]	14
0xBB00C1F8	VLAN_PORT_PPB_VLAN_SET [14][6].VLAN_TYPE[17:17]	1
0xBB00C1F8	VLAN_PORT_PPB_VLAN_SET [14][6].PRI_AS[16:16]	1
0xBB00C1F8	VLAN_PORT_PPB_VLAN_SET [14][6].PRI[15:13]	3
0xBB00C1F8	VLAN_PORT_PPB_VLAN_SET [14][6].VID_AS[12:12]	1
0xBB00C1F8	VLAN_PORT_PPB_VLAN_SET [14][6].VID[11:0]	12
0xBB00C1FC	VLAN_PORT_PPB_VLAN_SET [14][7].RESERVED[31:18]	14
0xBB00C1FC	VLAN_PORT_PPB_VLAN_SET [14][7].VLAN_TYPE[17:17]	1
0xBB00C1FC	VLAN_PORT_PPB_VLAN_SET [14][7].PRI_AS[16:16]	1
0xBB00C1FC	VLAN_PORT_PPB_VLAN_SET [14][7].PRI[15:13]	3
0xBB00C1FC	VLAN_PORT_PPB_VLAN_SET [14][7].VID_AS[12:12]	1
0xBB00C1FC	VLAN_PORT_PPB_VLAN_SET [14][7].VID[11:0]	12
0xBB00C200	VLAN_PORT_PPB_VLAN_SET [15][0].RESERVED[31:18]	14
0xBB00C200	VLAN_PORT_PPB_VLAN_SET [15][0].VLAN_TYPE[17:17]	1
0xBB00C200	VLAN_PORT_PPB_VLAN_SET [15][0].PRI_AS[16:16]	1
0xBB00C200	VLAN_PORT_PPB_VLAN_SET [15][0].PRI[15:13]	3
0xBB00C200	VLAN_PORT_PPB_VLAN_SET [15][0].VID_AS[12:12]	1
0xBB00C200	VLAN_PORT_PPB_VLAN_SET [15][0].VID[11:0]	12
0xBB00C204	VLAN_PORT_PPB_VLAN_SET [15][1].RESERVED[31:18]	14
0xBB00C204	VLAN_PORT_PPB_VLAN_SET [15][1].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C204	VLAN_PORT_PPB_VLAN_SET [15][1].PRI_AS[16:16]	1
0xBB00C204	VLAN_PORT_PPB_VLAN_SET [15][1].PRI[15:13]	3
0xBB00C204	VLAN_PORT_PPB_VLAN_SET [15][1].VID_AS[12:12]	1
0xBB00C204	VLAN_PORT_PPB_VLAN_SET [15][1].VID[11:0]	12
0xBB00C208	VLAN_PORT_PPB_VLAN_SET [15][2].RESERVED[31:18]	14
0xBB00C208	VLAN_PORT_PPB_VLAN_SET [15][2].VLAN_TYPE[17:17]	1
0xBB00C208	VLAN_PORT_PPB_VLAN_SET [15][2].PRI_AS[16:16]	1
0xBB00C208	VLAN_PORT_PPB_VLAN_SET [15][2].PRI[15:13]	3
0xBB00C208	VLAN_PORT_PPB_VLAN_SET [15][2].VID_AS[12:12]	1
0xBB00C208	VLAN_PORT_PPB_VLAN_SET [15][2].VID[11:0]	12
0xBB00C20C	VLAN_PORT_PPB_VLAN_SET [15][3].RESERVED[31:18]	14
0xBB00C20C	VLAN_PORT_PPB_VLAN_SET [15][3].VLAN_TYPE[17:17]	1
0xBB00C20C	VLAN_PORT_PPB_VLAN_SET [15][3].PRI_AS[16:16]	1
0xBB00C20C	VLAN_PORT_PPB_VLAN_SET [15][3].PRI[15:13]	3
0xBB00C20C	VLAN_PORT_PPB_VLAN_SET [15][3].VID_AS[12:12]	1
0xBB00C20C	VLAN_PORT_PPB_VLAN_SET [15][3].VID[11:0]	12
0xBB00C210	VLAN_PORT_PPB_VLAN_SET [15][4].RESERVED[31:18]	14
0xBB00C210	VLAN_PORT_PPB_VLAN_SET [15][4].VLAN_TYPE[17:17]	1
0xBB00C210	VLAN_PORT_PPB_VLAN_SET [15][4].PRI_AS[16:16]	1
0xBB00C210	VLAN_PORT_PPB_VLAN_SET [15][4].PRI[15:13]	3
0xBB00C210	VLAN_PORT_PPB_VLAN_SET [15][4].VID_AS[12:12]	1
0xBB00C210	VLAN_PORT_PPB_VLAN_SET [15][4].VID[11:0]	12
0xBB00C214	VLAN_PORT_PPB_VLAN_SET [15][5].RESERVED[31:18]	14
0xBB00C214	VLAN_PORT_PPB_VLAN_SET [15][5].VLAN_TYPE[17:17]	1
0xBB00C214	VLAN_PORT_PPB_VLAN_SET [15][5].PRI_AS[16:16]	1
0xBB00C214	VLAN_PORT_PPB_VLAN_SET [15][5].PRI[15:13]	3
0xBB00C214	VLAN_PORT_PPB_VLAN_SET [15][5].VID_AS[12:12]	1
0xBB00C214	VLAN_PORT_PPB_VLAN_SET [15][5].VID[11:0]	12
0xBB00C218	VLAN_PORT_PPB_VLAN_SET [15][6].RESERVED[31:18]	14
0xBB00C218	VLAN_PORT_PPB_VLAN_SET [15][6].VLAN_TYPE[17:17]	1
0xBB00C218	VLAN_PORT_PPB_VLAN_SET [15][6].PRI_AS[16:16]	1
0xBB00C218	VLAN_PORT_PPB_VLAN_SET [15][6].PRI[15:13]	3
0xBB00C218	VLAN_PORT_PPB_VLAN_SET [15][6].VID_AS[12:12]	1
0xBB00C218	VLAN_PORT_PPB_VLAN_SET [15][6].VID[11:0]	12
0xBB00C21C	VLAN_PORT_PPB_VLAN_SET [15][7].RESERVED[31:18]	14
0xBB00C21C	VLAN_PORT_PPB_VLAN_SET [15][7].VLAN_TYPE[17:17]	1
0xBB00C21C	VLAN_PORT_PPB_VLAN_SET [15][7].PRI_AS[16:16]	1
0xBB00C21C	VLAN_PORT_PPB_VLAN_SET [15][7].PRI[15:13]	3
0xBB00C21C	VLAN_PORT_PPB_VLAN_SET [15][7].VID_AS[12:12]	1
0xBB00C21C	VLAN_PORT_PPB_VLAN_SET [15][7].VID[11:0]	12
0xBB00C220	VLAN_PORT_PPB_VLAN_SET [16][0].RESERVED[31:18]	14
0xBB00C220	VLAN_PORT_PPB_VLAN_SET [16][0].VLAN_TYPE[17:17]	1
0xBB00C220	VLAN_PORT_PPB_VLAN_SET [16][0].PRI_AS[16:16]	1
0xBB00C220	VLAN_PORT_PPB_VLAN_SET [16][0].PRI[15:13]	3
0xBB00C220	VLAN_PORT_PPB_VLAN_SET [16][0].VID_AS[12:12]	1
0xBB00C220	VLAN_PORT_PPB_VLAN_SET [16][0].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C224	VLAN_PORT_PPB_VLAN_SET [16][1].RESERVED[31:18]	14
0xBB00C224	VLAN_PORT_PPB_VLAN_SET [16][1].VLAN_TYPE[17:17]	1
0xBB00C224	VLAN_PORT_PPB_VLAN_SET [16][1].PRI_AS[16:16]	1
0xBB00C224	VLAN_PORT_PPB_VLAN_SET [16][1].PRI[15:13]	3
0xBB00C224	VLAN_PORT_PPB_VLAN_SET [16][1].VID_AS[12:12]	1
0xBB00C224	VLAN_PORT_PPB_VLAN_SET [16][1].VID[11:0]	12
0xBB00C228	VLAN_PORT_PPB_VLAN_SET [16][2].RESERVED[31:18]	14
0xBB00C228	VLAN_PORT_PPB_VLAN_SET [16][2].VLAN_TYPE[17:17]	1
0xBB00C228	VLAN_PORT_PPB_VLAN_SET [16][2].PRI_AS[16:16]	1
0xBB00C228	VLAN_PORT_PPB_VLAN_SET [16][2].PRI[15:13]	3
0xBB00C228	VLAN_PORT_PPB_VLAN_SET [16][2].VID_AS[12:12]	1
0xBB00C228	VLAN_PORT_PPB_VLAN_SET [16][2].VID[11:0]	12
0xBB00C22C	VLAN_PORT_PPB_VLAN_SET [16][3].RESERVED[31:18]	14
0xBB00C22C	VLAN_PORT_PPB_VLAN_SET [16][3].VLAN_TYPE[17:17]	1
0xBB00C22C	VLAN_PORT_PPB_VLAN_SET [16][3].PRI_AS[16:16]	1
0xBB00C22C	VLAN_PORT_PPB_VLAN_SET [16][3].PRI[15:13]	3
0xBB00C22C	VLAN_PORT_PPB_VLAN_SET [16][3].VID_AS[12:12]	1
0xBB00C22C	VLAN_PORT_PPB_VLAN_SET [16][3].VID[11:0]	12
0xBB00C230	VLAN_PORT_PPB_VLAN_SET [16][4].RESERVED[31:18]	14
0xBB00C230	VLAN_PORT_PPB_VLAN_SET [16][4].VLAN_TYPE[17:17]	1
0xBB00C230	VLAN_PORT_PPB_VLAN_SET [16][4].PRI_AS[16:16]	1
0xBB00C230	VLAN_PORT_PPB_VLAN_SET [16][4].PRI[15:13]	3
0xBB00C230	VLAN_PORT_PPB_VLAN_SET [16][4].VID_AS[12:12]	1
0xBB00C230	VLAN_PORT_PPB_VLAN_SET [16][4].VID[11:0]	12
0xBB00C234	VLAN_PORT_PPB_VLAN_SET [16][5].RESERVED[31:18]	14
0xBB00C234	VLAN_PORT_PPB_VLAN_SET [16][5].VLAN_TYPE[17:17]	1
0xBB00C234	VLAN_PORT_PPB_VLAN_SET [16][5].PRI_AS[16:16]	1
0xBB00C234	VLAN_PORT_PPB_VLAN_SET [16][5].PRI[15:13]	3
0xBB00C234	VLAN_PORT_PPB_VLAN_SET [16][5].VID_AS[12:12]	1
0xBB00C234	VLAN_PORT_PPB_VLAN_SET [16][5].VID[11:0]	12
0xBB00C238	VLAN_PORT_PPB_VLAN_SET [16][6].RESERVED[31:18]	14
0xBB00C238	VLAN_PORT_PPB_VLAN_SET [16][6].VLAN_TYPE[17:17]	1
0xBB00C238	VLAN_PORT_PPB_VLAN_SET [16][6].PRI_AS[16:16]	1
0xBB00C238	VLAN_PORT_PPB_VLAN_SET [16][6].PRI[15:13]	3
0xBB00C238	VLAN_PORT_PPB_VLAN_SET [16][6].VID_AS[12:12]	1
0xBB00C238	VLAN_PORT_PPB_VLAN_SET [16][6].VID[11:0]	12
0xBB00C23C	VLAN_PORT_PPB_VLAN_SET [16][7].RESERVED[31:18]	14
0xBB00C23C	VLAN_PORT_PPB_VLAN_SET [16][7].VLAN_TYPE[17:17]	1
0xBB00C23C	VLAN_PORT_PPB_VLAN_SET [16][7].PRI_AS[16:16]	1
0xBB00C23C	VLAN_PORT_PPB_VLAN_SET [16][7].PRI[15:13]	3
0xBB00C23C	VLAN_PORT_PPB_VLAN_SET [16][7].VID_AS[12:12]	1
0xBB00C23C	VLAN_PORT_PPB_VLAN_SET [16][7].VID[11:0]	12
0xBB00C240	VLAN_PORT_PPB_VLAN_SET [17][0].RESERVED[31:18]	14
0xBB00C240	VLAN_PORT_PPB_VLAN_SET [17][0].VLAN_TYPE[17:17]	1
0xBB00C240	VLAN_PORT_PPB_VLAN_SET [17][0].PRI_AS[16:16]	1
0xBB00C240	VLAN_PORT_PPB_VLAN_SET [17][0].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C240	VLAN_PORT_PPB_VLAN_SET [17][0].VID_AS[12:12]	1
0xBB00C240	VLAN_PORT_PPB_VLAN_SET [17][0].VID[11:0]	12
0xBB00C244	VLAN_PORT_PPB_VLAN_SET [17][1].RESERVED[31:18]	14
0xBB00C244	VLAN_PORT_PPB_VLAN_SET [17][1].VLAN_TYPE[17:17]	1
0xBB00C244	VLAN_PORT_PPB_VLAN_SET [17][1].PRI_AS[16:16]	1
0xBB00C244	VLAN_PORT_PPB_VLAN_SET [17][1].PRI[15:13]	3
0xBB00C244	VLAN_PORT_PPB_VLAN_SET [17][1].VID_AS[12:12]	1
0xBB00C244	VLAN_PORT_PPB_VLAN_SET [17][1].VID[11:0]	12
0xBB00C248	VLAN_PORT_PPB_VLAN_SET [17][2].RESERVED[31:18]	14
0xBB00C248	VLAN_PORT_PPB_VLAN_SET [17][2].VLAN_TYPE[17:17]	1
0xBB00C248	VLAN_PORT_PPB_VLAN_SET [17][2].PRI_AS[16:16]	1
0xBB00C248	VLAN_PORT_PPB_VLAN_SET [17][2].PRI[15:13]	3
0xBB00C248	VLAN_PORT_PPB_VLAN_SET [17][2].VID_AS[12:12]	1
0xBB00C248	VLAN_PORT_PPB_VLAN_SET [17][2].VID[11:0]	12
0xBB00C24C	VLAN_PORT_PPB_VLAN_SET [17][3].RESERVED[31:18]	14
0xBB00C24C	VLAN_PORT_PPB_VLAN_SET [17][3].VLAN_TYPE[17:17]	1
0xBB00C24C	VLAN_PORT_PPB_VLAN_SET [17][3].PRI_AS[16:16]	1
0xBB00C24C	VLAN_PORT_PPB_VLAN_SET [17][3].PRI[15:13]	3
0xBB00C24C	VLAN_PORT_PPB_VLAN_SET [17][3].VID_AS[12:12]	1
0xBB00C24C	VLAN_PORT_PPB_VLAN_SET [17][3].VID[11:0]	12
0xBB00C250	VLAN_PORT_PPB_VLAN_SET [17][4].RESERVED[31:18]	14
0xBB00C250	VLAN_PORT_PPB_VLAN_SET [17][4].VLAN_TYPE[17:17]	1
0xBB00C250	VLAN_PORT_PPB_VLAN_SET [17][4].PRI_AS[16:16]	1
0xBB00C250	VLAN_PORT_PPB_VLAN_SET [17][4].PRI[15:13]	3
0xBB00C250	VLAN_PORT_PPB_VLAN_SET [17][4].VID_AS[12:12]	1
0xBB00C250	VLAN_PORT_PPB_VLAN_SET [17][4].VID[11:0]	12
0xBB00C254	VLAN_PORT_PPB_VLAN_SET [17][5].RESERVED[31:18]	14
0xBB00C254	VLAN_PORT_PPB_VLAN_SET [17][5].VLAN_TYPE[17:17]	1
0xBB00C254	VLAN_PORT_PPB_VLAN_SET [17][5].PRI_AS[16:16]	1
0xBB00C254	VLAN_PORT_PPB_VLAN_SET [17][5].PRI[15:13]	3
0xBB00C254	VLAN_PORT_PPB_VLAN_SET [17][5].VID_AS[12:12]	1
0xBB00C254	VLAN_PORT_PPB_VLAN_SET [17][5].VID[11:0]	12
0xBB00C258	VLAN_PORT_PPB_VLAN_SET [17][6].RESERVED[31:18]	14
0xBB00C258	VLAN_PORT_PPB_VLAN_SET [17][6].VLAN_TYPE[17:17]	1
0xBB00C258	VLAN_PORT_PPB_VLAN_SET [17][6].PRI_AS[16:16]	1
0xBB00C258	VLAN_PORT_PPB_VLAN_SET [17][6].PRI[15:13]	3
0xBB00C258	VLAN_PORT_PPB_VLAN_SET [17][6].VID_AS[12:12]	1
0xBB00C258	VLAN_PORT_PPB_VLAN_SET [17][6].VID[11:0]	12
0xBB00C25C	VLAN_PORT_PPB_VLAN_SET [17][7].RESERVED[31:18]	14
0xBB00C25C	VLAN_PORT_PPB_VLAN_SET [17][7].VLAN_TYPE[17:17]	1
0xBB00C25C	VLAN_PORT_PPB_VLAN_SET [17][7].PRI_AS[16:16]	1
0xBB00C25C	VLAN_PORT_PPB_VLAN_SET [17][7].PRI[15:13]	3
0xBB00C25C	VLAN_PORT_PPB_VLAN_SET [17][7].VID_AS[12:12]	1
0xBB00C25C	VLAN_PORT_PPB_VLAN_SET [17][7].VID[11:0]	12
0xBB00C260	VLAN_PORT_PPB_VLAN_SET [18][0].RESERVED[31:18]	14
0xBB00C260	VLAN_PORT_PPB_VLAN_SET [18][0].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C260	VLAN_PORT_PPB_VLAN_SET [18][0].PRI_AS[16:16]	1
0xBB00C260	VLAN_PORT_PPB_VLAN_SET [18][0].PRI[15:13]	3
0xBB00C260	VLAN_PORT_PPB_VLAN_SET [18][0].VID_AS[12:12]	1
0xBB00C260	VLAN_PORT_PPB_VLAN_SET [18][0].VID[11:0]	12
0xBB00C264	VLAN_PORT_PPB_VLAN_SET [18][1].RESERVED[31:18]	14
0xBB00C264	VLAN_PORT_PPB_VLAN_SET [18][1].VLAN_TYPE[17:17]	1
0xBB00C264	VLAN_PORT_PPB_VLAN_SET [18][1].PRI_AS[16:16]	1
0xBB00C264	VLAN_PORT_PPB_VLAN_SET [18][1].PRI[15:13]	3
0xBB00C264	VLAN_PORT_PPB_VLAN_SET [18][1].VID_AS[12:12]	1
0xBB00C264	VLAN_PORT_PPB_VLAN_SET [18][1].VID[11:0]	12
0xBB00C268	VLAN_PORT_PPB_VLAN_SET [18][2].RESERVED[31:18]	14
0xBB00C268	VLAN_PORT_PPB_VLAN_SET [18][2].VLAN_TYPE[17:17]	1
0xBB00C268	VLAN_PORT_PPB_VLAN_SET [18][2].PRI_AS[16:16]	1
0xBB00C268	VLAN_PORT_PPB_VLAN_SET [18][2].PRI[15:13]	3
0xBB00C268	VLAN_PORT_PPB_VLAN_SET [18][2].VID_AS[12:12]	1
0xBB00C268	VLAN_PORT_PPB_VLAN_SET [18][2].VID[11:0]	12
0xBB00C26C	VLAN_PORT_PPB_VLAN_SET [18][3].RESERVED[31:18]	14
0xBB00C26C	VLAN_PORT_PPB_VLAN_SET [18][3].VLAN_TYPE[17:17]	1
0xBB00C26C	VLAN_PORT_PPB_VLAN_SET [18][3].PRI_AS[16:16]	1
0xBB00C26C	VLAN_PORT_PPB_VLAN_SET [18][3].PRI[15:13]	3
0xBB00C26C	VLAN_PORT_PPB_VLAN_SET [18][3].VID_AS[12:12]	1
0xBB00C26C	VLAN_PORT_PPB_VLAN_SET [18][3].VID[11:0]	12
0xBB00C270	VLAN_PORT_PPB_VLAN_SET [18][4].RESERVED[31:18]	14
0xBB00C270	VLAN_PORT_PPB_VLAN_SET [18][4].VLAN_TYPE[17:17]	1
0xBB00C270	VLAN_PORT_PPB_VLAN_SET [18][4].PRI_AS[16:16]	1
0xBB00C270	VLAN_PORT_PPB_VLAN_SET [18][4].PRI[15:13]	3
0xBB00C270	VLAN_PORT_PPB_VLAN_SET [18][4].VID_AS[12:12]	1
0xBB00C270	VLAN_PORT_PPB_VLAN_SET [18][4].VID[11:0]	12
0xBB00C274	VLAN_PORT_PPB_VLAN_SET [18][5] RESERVED[31:18]	14
0xBB00C274	VLAN_PORT_PPB_VLAN_SET [18][5].VLAN_TYPE[17:17]	1
0xBB00C274	VLAN_PORT_PPB_VLAN_SET [18][5].PRI_AS[16:16]	1
0xBB00C274	VLAN_PORT_PPB_VLAN_SET [18][5].PRI[15:13]	3
0xBB00C274	VLAN_PORT_PPB_VLAN_SET [18][5].VID_AS[12:12]	1
0xBB00C274	VLAN_PORT_PPB_VLAN_SET [18][5].VID[11:0]	12
0xBB00C278	VLAN_PORT_PPB_VLAN_SET [18][6].RESERVED[31:18]	14
0xBB00C278	VLAN_PORT_PPB_VLAN_SET [18][6].VLAN_TYPE[17:17]	1
0xBB00C278	VLAN_PORT_PPB_VLAN_SET [18][6].PRI_AS[16:16]	1
0xBB00C278	VLAN_PORT_PPB_VLAN_SET [18][6].PRI[15:13]	3
0xBB00C278	VLAN_PORT_PPB_VLAN_SET [18][6].VID_AS[12:12]	1
0xBB00C278	VLAN_PORT_PPB_VLAN_SET [18][6].VID[11:0]	12
0xBB00C27C	VLAN_PORT_PPB_VLAN_SET [18][7].RESERVED[31:18]	14
0xBB00C27C	VLAN_PORT_PPB_VLAN_SET [18][7].VLAN_TYPE[17:17]	1
0xBB00C27C	VLAN_PORT_PPB_VLAN_SET [18][7].PRI_AS[16:16]	1
0xBB00C27C	VLAN_PORT_PPB_VLAN_SET [18][7].PRI[15:13]	3
0xBB00C27C	VLAN_PORT_PPB_VLAN_SET [18][7].VID_AS[12:12]	1
0xBB00C27C	VLAN_PORT_PPB_VLAN_SET [18][7].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C280	VLAN_PORT_PPB_VLAN_SET [19][0].RESERVED[31:18]	14
0xBB00C280	VLAN_PORT_PPB_VLAN_SET [19][0].VLAN_TYPE[17:17]	1
0xBB00C280	VLAN_PORT_PPB_VLAN_SET [19][0].PRI_AS[16:16]	1
0xBB00C280	VLAN_PORT_PPB_VLAN_SET [19][0].PRI[15:13]	3
0xBB00C280	VLAN_PORT_PPB_VLAN_SET [19][0].VID_AS[12:12]	1
0xBB00C280	VLAN_PORT_PPB_VLAN_SET [19][0].VID[11:0]	12
0xBB00C284	VLAN_PORT_PPB_VLAN_SET [19][1].RESERVED[31:18]	14
0xBB00C284	VLAN_PORT_PPB_VLAN_SET [19][1].VLAN_TYPE[17:17]	1
0xBB00C284	VLAN_PORT_PPB_VLAN_SET [19][1].PRI_AS[16:16]	1
0xBB00C284	VLAN_PORT_PPB_VLAN_SET [19][1].PRI[15:13]	3
0xBB00C284	VLAN_PORT_PPB_VLAN_SET [19][1].VID_AS[12:12]	1
0xBB00C284	VLAN_PORT_PPB_VLAN_SET [19][1].VID[11:0]	12
0xBB00C288	VLAN_PORT_PPB_VLAN_SET [19][2].RESERVED[31:18]	14
0xBB00C288	VLAN_PORT_PPB_VLAN_SET [19][2].VLAN_TYPE[17:17]	1
0xBB00C288	VLAN_PORT_PPB_VLAN_SET [19][2].PRI_AS[16:16]	1
0xBB00C288	VLAN_PORT_PPB_VLAN_SET [19][2].PRI[15:13]	3
0xBB00C288	VLAN_PORT_PPB_VLAN_SET [19][2].VID_AS[12:12]	1
0xBB00C288	VLAN_PORT_PPB_VLAN_SET [19][2].VID[11:0]	12
0xBB00C28C	VLAN_PORT_PPB_VLAN_SET [19][3].RESERVED[31:18]	14
0xBB00C28C	VLAN_PORT_PPB_VLAN_SET [19][3].VLAN_TYPE[17:17]	1
0xBB00C28C	VLAN_PORT_PPB_VLAN_SET [19][3].PRI_AS[16:16]	1
0xBB00C28C	VLAN_PORT_PPB_VLAN_SET [19][3].PRI[15:13]	3
0xBB00C28C	VLAN_PORT_PPB_VLAN_SET [19][3].VID_AS[12:12]	1
0xBB00C28C	VLAN_PORT_PPB_VLAN_SET [19][3].VID[11:0]	12
0xBB00C290	VLAN_PORT_PPB_VLAN_SET [19][4].RESERVED[31:18]	14
0xBB00C290	VLAN_PORT_PPB_VLAN_SET [19][4].VLAN_TYPE[17:17]	1
0xBB00C290	VLAN_PORT_PPB_VLAN_SET [19][4].PRI_AS[16:16]	1
0xBB00C290	VLAN_PORT_PPB_VLAN_SET [19][4].PRI[15:13]	3
0xBB00C290	VLAN_PORT_PPB_VLAN_SET [19][4].VID_AS[12:12]	1
0xBB00C290	VLAN_PORT_PPB_VLAN_SET [19][4].VID[11:0]	12
0xBB00C294	VLAN_PORT_PPB_VLAN_SET [19][5].RESERVED[31:18]	14
0xBB00C294	VLAN_PORT_PPB_VLAN_SET [19][5].VLAN_TYPE[17:17]	1
0xBB00C294	VLAN_PORT_PPB_VLAN_SET [19][5].PRI_AS[16:16]	1
0xBB00C294	VLAN_PORT_PPB_VLAN_SET [19][5].PRI[15:13]	3
0xBB00C294	VLAN_PORT_PPB_VLAN_SET [19][5].VID_AS[12:12]	1
0xBB00C294	VLAN_PORT_PPB_VLAN_SET [19][5].VID[11:0]	12
0xBB00C298	VLAN_PORT_PPB_VLAN_SET [19][6].RESERVED[31:18]	14
0xBB00C298	VLAN_PORT_PPB_VLAN_SET [19][6].VLAN_TYPE[17:17]	1
0xBB00C298	VLAN_PORT_PPB_VLAN_SET [19][6].PRI_AS[16:16]	1
0xBB00C298	VLAN_PORT_PPB_VLAN_SET [19][6].PRI[15:13]	3
0xBB00C298	VLAN_PORT_PPB_VLAN_SET [19][6].VID_AS[12:12]	1
0xBB00C298	VLAN_PORT_PPB_VLAN_SET [19][6].VID[11:0]	12
0xBB00C29C	VLAN_PORT_PPB_VLAN_SET [19][7].RESERVED[31:18]	14
0xBB00C29C	VLAN_PORT_PPB_VLAN_SET [19][7].VLAN_TYPE[17:17]	1
0xBB00C29C	VLAN_PORT_PPB_VLAN_SET [19][7].PRI_AS[16:16]	1
0xBB00C29C	VLAN_PORT_PPB_VLAN_SET [19][7].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C29C	VLAN_PORT_PPB_VLAN_SET [19][7].VID_AS[12:12]	1
0xBB00C29C	VLAN_PORT_PPB_VLAN_SET [19][7].VID[11:0]	12
0xBB00C2A0	VLAN_PORT_PPB_VLAN_SET [20][0].RESERVED[31:18]	14
0xBB00C2A0	VLAN_PORT_PPB_VLAN_SET [20][0].VLAN_TYPE[17:17]	1
0xBB00C2A0	VLAN_PORT_PPB_VLAN_SET [20][0].PRI_AS[16:16]	1
0xBB00C2A0	VLAN_PORT_PPB_VLAN_SET [20][0].PRI[15:13]	3
0xBB00C2A0	VLAN_PORT_PPB_VLAN_SET [20][0].VID_AS[12:12]	1
0xBB00C2A0	VLAN_PORT_PPB_VLAN_SET [20][0].VID[11:0]	12
0xBB00C2A4	VLAN_PORT_PPB_VLAN_SET [20][1].RESERVED[31:18]	14
0xBB00C2A4	VLAN_PORT_PPB_VLAN_SET [20][1].VLAN_TYPE[17:17]	1
0xBB00C2A4	VLAN_PORT_PPB_VLAN_SET [20][1].PRI_AS[16:16]	1
0xBB00C2A4	VLAN_PORT_PPB_VLAN_SET [20][1].PRI[15:13]	3
0xBB00C2A4	VLAN_PORT_PPB_VLAN_SET [20][1].VID_AS[12:12]	1
0xBB00C2A4	VLAN_PORT_PPB_VLAN_SET [20][1].VID[11:0]	12
0xBB00C2A8	VLAN_PORT_PPB_VLAN_SET [20][2].RESERVED[31:18]	14
0xBB00C2A8	VLAN_PORT_PPB_VLAN_SET [20][2].VLAN_TYPE[17:17]	1
0xBB00C2A8	VLAN_PORT_PPB_VLAN_SET [20][2].PRI_AS[16:16]	1
0xBB00C2A8	VLAN_PORT_PPB_VLAN_SET [20][2].PRI[15:13]	3
0xBB00C2A8	VLAN_PORT_PPB_VLAN_SET [20][2].VID_AS[12:12]	1
0xBB00C2A8	VLAN_PORT_PPB_VLAN_SET [20][2].VID[11:0]	12
0xBB00C2AC	VLAN_PORT_PPB_VLAN_SET [20][3].RESERVED[31:18]	14
0xBB00C2AC	VLAN_PORT_PPB_VLAN_SET [20][3].VLAN_TYPE[17:17]	1
0xBB00C2AC	VLAN_PORT_PPB_VLAN_SET [20][3].PRI_AS[16:16]	1
0xBB00C2AC	VLAN_PORT_PPB_VLAN_SET [20][3].PRI[15:13]	3
0xBB00C2AC	VLAN_PORT_PPB_VLAN_SET [20][3].VID_AS[12:12]	1
0xBB00C2AC	VLAN_PORT_PPB_VLAN_SET [20][3].VID[11:0]	12
0xBB00C2B0	VLAN_PORT_PPB_VLAN_SET [20][4].RESERVED[31:18]	14
0xBB00C2B0	VLAN_PORT_PPB_VLAN_SET [20][4].VLAN_TYPE[17:17]	1
0xBB00C2B0	VLAN_PORT_PPB_VLAN_SET [20][4].PRI_AS[16:16]	1
0xBB00C2B0	VLAN_PORT_PPB_VLAN_SET [20][4].PRI[15:13]	3
0xBB00C2B0	VLAN_PORT_PPB_VLAN_SET [20][4].VID_AS[12:12]	1
0xBB00C2B0	VLAN_PORT_PPB_VLAN_SET [20][4].VID[11:0]	12
0xBB00C2B4	VLAN_PORT_PPB_VLAN_SET [20][5].RESERVED[31:18]	14
0xBB00C2B4	VLAN_PORT_PPB_VLAN_SET [20][5].VLAN_TYPE[17:17]	1
0xBB00C2B4	VLAN_PORT_PPB_VLAN_SET [20][5].PRI_AS[16:16]	1
0xBB00C2B4	VLAN_PORT_PPB_VLAN_SET [20][5].PRI[15:13]	3
0xBB00C2B4	VLAN_PORT_PPB_VLAN_SET [20][5].VID_AS[12:12]	1
0xBB00C2B4	VLAN_PORT_PPB_VLAN_SET [20][5].VID[11:0]	12
0xBB00C2B8	VLAN_PORT_PPB_VLAN_SET [20][6].RESERVED[31:18]	14
0xBB00C2B8	VLAN_PORT_PPB_VLAN_SET [20][6].VLAN_TYPE[17:17]	1
0xBB00C2B8	VLAN_PORT_PPB_VLAN_SET [20][6].PRI_AS[16:16]	1
0xBB00C2B8	VLAN_PORT_PPB_VLAN_SET [20][6].PRI[15:13]	3
0xBB00C2B8	VLAN_PORT_PPB_VLAN_SET [20][6].VID_AS[12:12]	1
0xBB00C2B8	VLAN_PORT_PPB_VLAN_SET [20][6].VID[11:0]	12
0xBB00C2BC	VLAN_PORT_PPB_VLAN_SET [20][7].RESERVED[31:18]	14
0xBB00C2BC	VLAN_PORT_PPB_VLAN_SET [20][7].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C2BC	VLAN_PORT_PPB_VLAN_SET [20][7].PRI_AS[16:16]	1
0xBB00C2BC	VLAN_PORT_PPB_VLAN_SET [20][7].PRI[15:13]	3
0xBB00C2BC	VLAN_PORT_PPB_VLAN_SET [20][7].VID_AS[12:12]	1
0xBB00C2BC	VLAN_PORT_PPB_VLAN_SET [20][7].VID[11:0]	12
0xBB00C2C0	VLAN_PORT_PPB_VLAN_SET [21][0].RESERVED[31:18]	14
0xBB00C2C0	VLAN_PORT_PPB_VLAN_SET [21][0].VLAN_TYPE[17:17]	1
0xBB00C2C0	VLAN_PORT_PPB_VLAN_SET [21][0].PRI_AS[16:16]	1
0xBB00C2C0	VLAN_PORT_PPB_VLAN_SET [21][0].PRI[15:13]	3
0xBB00C2C0	VLAN_PORT_PPB_VLAN_SET [21][0].VID_AS[12:12]	1
0xBB00C2C0	VLAN_PORT_PPB_VLAN_SET [21][0].VID[11:0]	12
0xBB00C2C4	VLAN_PORT_PPB_VLAN_SET [21][1].RESERVED[31:18]	14
0xBB00C2C4	VLAN_PORT_PPB_VLAN_SET [21][1].VLAN_TYPE[17:17]	1
0xBB00C2C4	VLAN_PORT_PPB_VLAN_SET [21][1].PRI_AS[16:16]	1
0xBB00C2C4	VLAN_PORT_PPB_VLAN_SET [21][1].PRI[15:13]	3
0xBB00C2C4	VLAN_PORT_PPB_VLAN_SET [21][1].VID_AS[12:12]	1
0xBB00C2C4	VLAN_PORT_PPB_VLAN_SET [21][1].VID[11:0]	12
0xBB00C2C8	VLAN_PORT_PPB_VLAN_SET [21][2].RESERVED[31:18]	14
0xBB00C2C8	VLAN_PORT_PPB_VLAN_SET [21][2].VLAN_TYPE[17:17]	1
0xBB00C2C8	VLAN_PORT_PPB_VLAN_SET [21][2].PRI_AS[16:16]	1
0xBB00C2C8	VLAN_PORT_PPB_VLAN_SET [21][2].PRI[15:13]	3
0xBB00C2C8	VLAN_PORT_PPB_VLAN_SET [21][2].VID_AS[12:12]	1
0xBB00C2C8	VLAN_PORT_PPB_VLAN_SET [21][2].VID[11:0]	12
0xBB00C2CC	VLAN_PORT_PPB_VLAN_SET [21][3].RESERVED[31:18]	14
0xBB00C2CC	VLAN_PORT_PPB_VLAN_SET [21][3].VLAN_TYPE[17:17]	1
0xBB00C2CC	VLAN_PORT_PPB_VLAN_SET [21][3].PRI_AS[16:16]	1
0xBB00C2CC	VLAN_PORT_PPB_VLAN_SET [21][3].PRI[15:13]	3
0xBB00C2CC	VLAN_PORT_PPB_VLAN_SET [21][3].VID_AS[12:12]	1
0xBB00C2CC	VLAN_PORT_PPB_VLAN_SET [21][3].VID[11:0]	12
0xBB00C2D0	VLAN_PORT_PPB_VLAN_SET [21][4].RESERVED[31:18]	14
0xBB00C2D0	VLAN_PORT_PPB_VLAN_SET [21][4].VLAN_TYPE[17:17]	1
0xBB00C2D0	VLAN_PORT_PPB_VLAN_SET [21][4].PRI_AS[16:16]	1
0xBB00C2D0	VLAN_PORT_PPB_VLAN_SET [21][4].PRI[15:13]	3
0xBB00C2D0	VLAN_PORT_PPB_VLAN_SET [21][4].VID_AS[12:12]	1
0xBB00C2D0	VLAN_PORT_PPB_VLAN_SET [21][4].VID[11:0]	12
0xBB00C2D4	VLAN_PORT_PPB_VLAN_SET [21][5].RESERVED[31:18]	14
0xBB00C2D4	VLAN_PORT_PPB_VLAN_SET [21][5].VLAN_TYPE[17:17]	1
0xBB00C2D4	VLAN_PORT_PPB_VLAN_SET [21][5].PRI_AS[16:16]	1
0xBB00C2D4	VLAN_PORT_PPB_VLAN_SET [21][5].PRI[15:13]	3
0xBB00C2D4	VLAN_PORT_PPB_VLAN_SET [21][5].VID_AS[12:12]	1
0xBB00C2D4	VLAN_PORT_PPB_VLAN_SET [21][5].VID[11:0]	12
0xBB00C2D8	VLAN_PORT_PPB_VLAN_SET [21][6].RESERVED[31:18]	14
0xBB00C2D8	VLAN_PORT_PPB_VLAN_SET [21][6].VLAN_TYPE[17:17]	1
0xBB00C2D8	VLAN_PORT_PPB_VLAN_SET [21][6].PRI_AS[16:16]	1
0xBB00C2D8	VLAN_PORT_PPB_VLAN_SET [21][6].PRI[15:13]	3
0xBB00C2D8	VLAN_PORT_PPB_VLAN_SET [21][6].VID_AS[12:12]	1
0xBB00C2D8	VLAN_PORT_PPB_VLAN_SET [21][6].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C2DC	VLAN_PORT_PPB_VLAN_SET [21][7].RESERVED[31:18]	14
0xBB00C2DC	VLAN_PORT_PPB_VLAN_SET [21][7].VLAN_TYPE[17:17]	1
0xBB00C2DC	VLAN_PORT_PPB_VLAN_SET [21][7].PRI_AS[16:16]	1
0xBB00C2DC	VLAN_PORT_PPB_VLAN_SET [21][7].PRI[15:13]	3
0xBB00C2DC	VLAN_PORT_PPB_VLAN_SET [21][7].VID_AS[12:12]	1
0xBB00C2DC	VLAN_PORT_PPB_VLAN_SET [21][7].VID[11:0]	12
0xBB00C2E0	VLAN_PORT_PPB_VLAN_SET [22][0].RESERVED[31:18]	14
0xBB00C2E0	VLAN_PORT_PPB_VLAN_SET [22][0].VLAN_TYPE[17:17]	1
0xBB00C2E0	VLAN_PORT_PPB_VLAN_SET [22][0].PRI_AS[16:16]	1
0xBB00C2E0	VLAN_PORT_PPB_VLAN_SET [22][0].PRI[15:13]	3
0xBB00C2E0	VLAN_PORT_PPB_VLAN_SET [22][0].VID_AS[12:12]	1
0xBB00C2E0	VLAN_PORT_PPB_VLAN_SET [22][0].VID[11:0]	12
0xBB00C2E4	VLAN_PORT_PPB_VLAN_SET [22][1].RESERVED[31:18]	14
0xBB00C2E4	VLAN_PORT_PPB_VLAN_SET [22][1].VLAN_TYPE[17:17]	1
0xBB00C2E4	VLAN_PORT_PPB_VLAN_SET [22][1].PRI_AS[16:16]	1
0xBB00C2E4	VLAN_PORT_PPB_VLAN_SET [22][1].PRI[15:13]	3
0xBB00C2E4	VLAN_PORT_PPB_VLAN_SET [22][1].VID_AS[12:12]	1
0xBB00C2E4	VLAN_PORT_PPB_VLAN_SET [22][1].VID[11:0]	12
0xBB00C2E8	VLAN_PORT_PPB_VLAN_SET [22][2].RESERVED[31:18]	14
0xBB00C2E8	VLAN_PORT_PPB_VLAN_SET [22][2].VLAN_TYPE[17:17]	1
0xBB00C2E8	VLAN_PORT_PPB_VLAN_SET [22][2].PRI_AS[16:16]	1
0xBB00C2E8	VLAN_PORT_PPB_VLAN_SET [22][2].PRI[15:13]	3
0xBB00C2E8	VLAN_PORT_PPB_VLAN_SET [22][2].VID_AS[12:12]	1
0xBB00C2E8	VLAN_PORT_PPB_VLAN_SET [22][2].VID[11:0]	12
0xBB00C2EC	VLAN_PORT_PPB_VLAN_SET [22][3].RESERVED[31:18]	14
0xBB00C2EC	VLAN_PORT_PPB_VLAN_SET [22][3].VLAN_TYPE[17:17]	1
0xBB00C2EC	VLAN_PORT_PPB_VLAN_SET [22][3].PRI_AS[16:16]	1
0xBB00C2EC	VLAN_PORT_PPB_VLAN_SET [22][3].PRI[15:13]	3
0xBB00C2EC	VLAN_PORT_PPB_VLAN_SET [22][3].VID_AS[12:12]	1
0xBB00C2EC	VLAN_PORT_PPB_VLAN_SET [22][3].VID[11:0]	12
0xBB00C2F0	VLAN_PORT_PPB_VLAN_SET [22][4].RESERVED[31:18]	14
0xBB00C2F0	VLAN_PORT_PPB_VLAN_SET [22][4].VLAN_TYPE[17:17]	1
0xBB00C2F0	VLAN_PORT_PPB_VLAN_SET [22][4].PRI_AS[16:16]	1
0xBB00C2F0	VLAN_PORT_PPB_VLAN_SET [22][4].PRI[15:13]	3
0xBB00C2F0	VLAN_PORT_PPB_VLAN_SET [22][4].VID_AS[12:12]	1
0xBB00C2F0	VLAN_PORT_PPB_VLAN_SET [22][4].VID[11:0]	12
0xBB00C2F4	VLAN_PORT_PPB_VLAN_SET [22][5].RESERVED[31:18]	14
0xBB00C2F4	VLAN_PORT_PPB_VLAN_SET [22][5].VLAN_TYPE[17:17]	1
0xBB00C2F4	VLAN_PORT_PPB_VLAN_SET [22][5].PRI_AS[16:16]	1
0xBB00C2F4	VLAN_PORT_PPB_VLAN_SET [22][5].PRI[15:13]	3
0xBB00C2F4	VLAN_PORT_PPB_VLAN_SET [22][5].VID_AS[12:12]	1
0xBB00C2F4	VLAN_PORT_PPB_VLAN_SET [22][5].VID[11:0]	12
0xBB00C2F8	VLAN_PORT_PPB_VLAN_SET [22][6].RESERVED[31:18]	14
0xBB00C2F8	VLAN_PORT_PPB_VLAN_SET [22][6].VLAN_TYPE[17:17]	1
0xBB00C2F8	VLAN_PORT_PPB_VLAN_SET [22][6].PRI_AS[16:16]	1
0xBB00C2F8	VLAN_PORT_PPB_VLAN_SET [22][6].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C2F8	VLAN_PORT_PPB_VLAN_SET [22][6].VID_AS[12:12]	1
0xBB00C2F8	VLAN_PORT_PPB_VLAN_SET [22][6].VID[11:0]	12
0xBB00C2FC	VLAN_PORT_PPB_VLAN_SET [22][7].RESERVED[31:18]	14
0xBB00C2FC	VLAN_PORT_PPB_VLAN_SET [22][7].VLAN_TYPE[17:17]	1
0xBB00C2FC	VLAN_PORT_PPB_VLAN_SET [22][7].PRI_AS[16:16]	1
0xBB00C2FC	VLAN_PORT_PPB_VLAN_SET [22][7].PRI[15:13]	3
0xBB00C2FC	VLAN_PORT_PPB_VLAN_SET [22][7].VID_AS[12:12]	1
0xBB00C2FC	VLAN_PORT_PPB_VLAN_SET [22][7].VID[11:0]	12
0xBB00C300	VLAN_PORT_PPB_VLAN_SET [23][0].RESERVED[31:18]	14
0xBB00C300	VLAN_PORT_PPB_VLAN_SET [23][0].VLAN_TYPE[17:17]	1
0xBB00C300	VLAN_PORT_PPB_VLAN_SET [23][0].PRI_AS[16:16]	1
0xBB00C300	VLAN_PORT_PPB_VLAN_SET [23][0].PRI[15:13]	3
0xBB00C300	VLAN_PORT_PPB_VLAN_SET [23][0].VID_AS[12:12]	1
0xBB00C300	VLAN_PORT_PPB_VLAN_SET [23][0].VID[11:0]	12
0xBB00C304	VLAN_PORT_PPB_VLAN_SET [23][1].RESERVED[31:18]	14
0xBB00C304	VLAN_PORT_PPB_VLAN_SET [23][1].VLAN_TYPE[17:17]	1
0xBB00C304	VLAN_PORT_PPB_VLAN_SET [23][1].PRI_AS[16:16]	1
0xBB00C304	VLAN_PORT_PPB_VLAN_SET [23][1].PRI[15:13]	3
0xBB00C304	VLAN_PORT_PPB_VLAN_SET [23][1].VID_AS[12:12]	1
0xBB00C304	VLAN_PORT_PPB_VLAN_SET [23][1].VID[11:0]	12
0xBB00C308	VLAN_PORT_PPB_VLAN_SET [23][2].RESERVED[31:18]	14
0xBB00C308	VLAN_PORT_PPB_VLAN_SET [23][2].VLAN_TYPE[17:17]	1
0xBB00C308	VLAN_PORT_PPB_VLAN_SET [23][2].PRI_AS[16:16]	1
0xBB00C308	VLAN_PORT_PPB_VLAN_SET [23][2].PRI[15:13]	3
0xBB00C308	VLAN_PORT_PPB_VLAN_SET [23][2].VID_AS[12:12]	1
0xBB00C308	VLAN_PORT_PPB_VLAN_SET [23][2].VID[11:0]	12
0xBB00C30C	VLAN_PORT_PPB_VLAN_SET [23][3].RESERVED[31:18]	14
0xBB00C30C	VLAN_PORT_PPB_VLAN_SET [23][3].VLAN_TYPE[17:17]	1
0xBB00C30C	VLAN_PORT_PPB_VLAN_SET [23][3].PRI_AS[16:16]	1
0xBB00C30C	VLAN_PORT_PPB_VLAN_SET [23][3].PRI[15:13]	3
0xBB00C30C	VLAN_PORT_PPB_VLAN_SET [23][3].VID_AS[12:12]	1
0xBB00C30C	VLAN_PORT_PPB_VLAN_SET [23][3].VID[11:0]	12
0xBB00C310	VLAN_PORT_PPB_VLAN_SET [23][4].RESERVED[31:18]	14
0xBB00C310	VLAN_PORT_PPB_VLAN_SET [23][4].VLAN_TYPE[17:17]	1
0xBB00C310	VLAN_PORT_PPB_VLAN_SET [23][4].PRI_AS[16:16]	1
0xBB00C310	VLAN_PORT_PPB_VLAN_SET [23][4].PRI[15:13]	3
0xBB00C310	VLAN_PORT_PPB_VLAN_SET [23][4].VID_AS[12:12]	1
0xBB00C310	VLAN_PORT_PPB_VLAN_SET [23][4].VID[11:0]	12
0xBB00C314	VLAN_PORT_PPB_VLAN_SET [23][5].RESERVED[31:18]	14
0xBB00C314	VLAN_PORT_PPB_VLAN_SET [23][5].VLAN_TYPE[17:17]	1
0xBB00C314	VLAN_PORT_PPB_VLAN_SET [23][5].PRI_AS[16:16]	1
0xBB00C314	VLAN_PORT_PPB_VLAN_SET [23][5].PRI[15:13]	3
0xBB00C314	VLAN_PORT_PPB_VLAN_SET [23][5].VID_AS[12:12]	1
0xBB00C314	VLAN_PORT_PPB_VLAN_SET [23][5].VID[11:0]	12
0xBB00C318	VLAN_PORT_PPB_VLAN_SET [23][6].RESERVED[31:18]	14
0xBB00C318	VLAN_PORT_PPB_VLAN_SET [23][6].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C318	VLAN_PORT_PPB_VLAN_SET [23][6].PRI_AS[16:16]	1
0xBB00C318	VLAN_PORT_PPB_VLAN_SET [23][6].PRI[15:13]	3
0xBB00C318	VLAN_PORT_PPB_VLAN_SET [23][6].VID_AS[12:12]	1
0xBB00C318	VLAN_PORT_PPB_VLAN_SET [23][6].VID[11:0]	12
0xBB00C31C	VLAN_PORT_PPB_VLAN_SET [23][7].RESERVED[31:18]	14
0xBB00C31C	VLAN_PORT_PPB_VLAN_SET [23][7].VLAN_TYPE[17:17]	1
0xBB00C31C	VLAN_PORT_PPB_VLAN_SET [23][7].PRI_AS[16:16]	1
0xBB00C31C	VLAN_PORT_PPB_VLAN_SET [23][7].PRI[15:13]	3
0xBB00C31C	VLAN_PORT_PPB_VLAN_SET [23][7].VID_AS[12:12]	1
0xBB00C31C	VLAN_PORT_PPB_VLAN_SET [23][7].VID[11:0]	12
0xBB00C320	VLAN_PORT_PPB_VLAN_SET [24][0].RESERVED[31:18]	14
0xBB00C320	VLAN_PORT_PPB_VLAN_SET [24][0].VLAN_TYPE[17:17]	1
0xBB00C320	VLAN_PORT_PPB_VLAN_SET [24][0].PRI_AS[16:16]	1
0xBB00C320	VLAN_PORT_PPB_VLAN_SET [24][0].PRI[15:13]	3
0xBB00C320	VLAN_PORT_PPB_VLAN_SET [24][0].VID_AS[12:12]	1
0xBB00C320	VLAN_PORT_PPB_VLAN_SET [24][0].VID[11:0]	12
0xBB00C324	VLAN_PORT_PPB_VLAN_SET [24][1].RESERVED[31:18]	14
0xBB00C324	VLAN_PORT_PPB_VLAN_SET [24][1].VLAN_TYPE[17:17]	1
0xBB00C324	VLAN_PORT_PPB_VLAN_SET [24][1].PRI_AS[16:16]	1
0xBB00C324	VLAN_PORT_PPB_VLAN_SET [24][1].PRI[15:13]	3
0xBB00C324	VLAN_PORT_PPB_VLAN_SET [24][1].VID_AS[12:12]	1
0xBB00C324	VLAN_PORT_PPB_VLAN_SET [24][1].VID[11:0]	12
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].RESERVED[31:18]	14
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].VLAN_TYPE[17:17]	1
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].PRI_AS[16:16]	1
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].PRI[15:13]	3
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].VID_AS[12:12]	1
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].VID[11:0]	12
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].RESERVED[31:18]	14
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].VLAN_TYPE[17:17]	1
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].PRI_AS[16:16]	1
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].PRI[15:13]	3
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].VID_AS[12:12]	1
0xBB00C328	VLAN_PORT_PPB_VLAN_SET [24][2].VID[11:0]	12
0xBB00C32C	VLAN_PORT_PPB_VLAN_SET [24][3].RESERVED[31:18]	14
0xBB00C32C	VLAN_PORT_PPB_VLAN_SET [24][3].VLAN_TYPE[17:17]	1
0xBB00C32C	VLAN_PORT_PPB_VLAN_SET [24][3].PRI_AS[16:16]	1
0xBB00C32C	VLAN_PORT_PPB_VLAN_SET [24][3].PRI[15:13]	3
0xBB00C32C	VLAN_PORT_PPB_VLAN_SET [24][3].VID_AS[12:12]	1
0xBB00C32C	VLAN_PORT_PPB_VLAN_SET [24][3].VID[11:0]	12
0xBB00C330	VLAN_PORT_PPB_VLAN_SET [24][4].RESERVED[31:18]	14
0xBB00C330	VLAN_PORT_PPB_VLAN_SET [24][4].VLAN_TYPE[17:17]	1
0xBB00C330	VLAN_PORT_PPB_VLAN_SET [24][4].PRI_AS[16:16]	1
0xBB00C330	VLAN_PORT_PPB_VLAN_SET [24][4].PRI[15:13]	3
0xBB00C330	VLAN_PORT_PPB_VLAN_SET [24][4].VID_AS[12:12]	1
0xBB00C330	VLAN_PORT_PPB_VLAN_SET [24][4].VID[11:0]	12
0xBB00C334	VLAN_PORT_PPB_VLAN_SET [24][5].RESERVED[31:18]	14
0xBB00C334	VLAN_PORT_PPB_VLAN_SET [24][5].VLAN_TYPE[17:17]	1
0xBB00C334	VLAN_PORT_PPB_VLAN_SET [24][5].PRI_AS[16:16]	1
0xBB00C334	VLAN_PORT_PPB_VLAN_SET [24][5].PRI[15:13]	3
0xBB00C334	VLAN_PORT_PPB_VLAN_SET [24][5].VID_AS[12:12]	1
0xBB00C334	VLAN_PORT_PPB_VLAN_SET [24][5].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C338	VLAN_PORT_PPB_VLAN_SET [24][6].RESERVED[31:18]	14
0xBB00C338	VLAN_PORT_PPB_VLAN_SET [24][6].VLAN_TYPE[17:17]	1
0xBB00C338	VLAN_PORT_PPB_VLAN_SET [24][6].PRI_AS[16:16]	1
0xBB00C338	VLAN_PORT_PPB_VLAN_SET [24][6].PRI[15:13]	3
0xBB00C338	VLAN_PORT_PPB_VLAN_SET [24][6].VID_AS[12:12]	1
0xBB00C338	VLAN_PORT_PPB_VLAN_SET [24][6].VID[11:0]	12
0xBB00C33C	VLAN_PORT_PPB_VLAN_SET [24][7].RESERVED[31:18]	14
0xBB00C33C	VLAN_PORT_PPB_VLAN_SET [24][7].VLAN_TYPE[17:17]	1
0xBB00C33C	VLAN_PORT_PPB_VLAN_SET [24][7].PRI_AS[16:16]	1
0xBB00C33C	VLAN_PORT_PPB_VLAN_SET [24][7].PRI[15:13]	3
0xBB00C33C	VLAN_PORT_PPB_VLAN_SET [24][7].VID_AS[12:12]	1
0xBB00C33C	VLAN_PORT_PPB_VLAN_SET [24][7].VID[11:0]	12
0xBB00C340	VLAN_PORT_PPB_VLAN_SET [25][0].RESERVED[31:18]	14
0xBB00C340	VLAN_PORT_PPB_VLAN_SET [25][0].VLAN_TYPE[17:17]	1
0xBB00C340	VLAN_PORT_PPB_VLAN_SET [25][0].PRI_AS[16:16]	1
0xBB00C340	VLAN_PORT_PPB_VLAN_SET [25][0].PRI[15:13]	3
0xBB00C340	VLAN_PORT_PPB_VLAN_SET [25][0].VID_AS[12:12]	1
0xBB00C340	VLAN_PORT_PPB_VLAN_SET [25][0].VID[11:0]	12
0xBB00C344	VLAN_PORT_PPB_VLAN_SET [25][1].RESERVED[31:18]	14
0xBB00C344	VLAN_PORT_PPB_VLAN_SET [25][1].VLAN_TYPE[17:17]	1
0xBB00C344	VLAN_PORT_PPB_VLAN_SET [25][1].PRI_AS[16:16]	1
0xBB00C344	VLAN_PORT_PPB_VLAN_SET [25][1].PRI[15:13]	3
0xBB00C344	VLAN_PORT_PPB_VLAN_SET [25][1].VID_AS[12:12]	1
0xBB00C344	VLAN_PORT_PPB_VLAN_SET [25][1].VID[11:0]	12
0xBB00C348	VLAN_PORT_PPB_VLAN_SET [25][2].RESERVED[31:18]	14
0xBB00C348	VLAN_PORT_PPB_VLAN_SET [25][2].VLAN_TYPE[17:17]	1
0xBB00C348	VLAN_PORT_PPB_VLAN_SET [25][2].PRI_AS[16:16]	1
0xBB00C348	VLAN_PORT_PPB_VLAN_SET [25][2].PRI[15:13]	3
0xBB00C348	VLAN_PORT_PPB_VLAN_SET [25][2].VID_AS[12:12]	1
0xBB00C348	VLAN_PORT_PPB_VLAN_SET [25][2].VID[11:0]	12
0xBB00C34C	VLAN_PORT_PPB_VLAN_SET [25][3].RESERVED[31:18]	14
0xBB00C34C	VLAN_PORT_PPB_VLAN_SET [25][3].VLAN_TYPE[17:17]	1
0xBB00C34C	VLAN_PORT_PPB_VLAN_SET [25][3].PRI_AS[16:16]	1
0xBB00C34C	VLAN_PORT_PPB_VLAN_SET [25][3].PRI[15:13]	3
0xBB00C34C	VLAN_PORT_PPB_VLAN_SET [25][3].VID_AS[12:12]	1
0xBB00C34C	VLAN_PORT_PPB_VLAN_SET [25][3].VID[11:0]	12
0xBB00C350	VLAN_PORT_PPB_VLAN_SET [25][4].RESERVED[31:18]	14
0xBB00C350	VLAN_PORT_PPB_VLAN_SET [25][4].VLAN_TYPE[17:17]	1
0xBB00C350	VLAN_PORT_PPB_VLAN_SET [25][4].PRI_AS[16:16]	1
0xBB00C350	VLAN_PORT_PPB_VLAN_SET [25][4].PRI[15:13]	3
0xBB00C350	VLAN_PORT_PPB_VLAN_SET [25][4].VID_AS[12:12]	1
0xBB00C350	VLAN_PORT_PPB_VLAN_SET [25][4].VID[11:0]	12
0xBB00C354	VLAN_PORT_PPB_VLAN_SET [25][5].RESERVED[31:18]	14
0xBB00C354	VLAN_PORT_PPB_VLAN_SET [25][5].VLAN_TYPE[17:17]	1
0xBB00C354	VLAN_PORT_PPB_VLAN_SET [25][5].PRI_AS[16:16]	1
0xBB00C354	VLAN_PORT_PPB_VLAN_SET [25][5].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C354	VLAN_PORT_PPB_VLAN_SET [25][5].VID_AS[12:12]	1
0xBB00C354	VLAN_PORT_PPB_VLAN_SET [25][5].VID[11:0]	12
0xBB00C358	VLAN_PORT_PPB_VLAN_SET [25][6].RESERVED[31:18]	14
0xBB00C358	VLAN_PORT_PPB_VLAN_SET [25][6].VLAN_TYPE[17:17]	1
0xBB00C358	VLAN_PORT_PPB_VLAN_SET [25][6].PRI_AS[16:16]	1
0xBB00C358	VLAN_PORT_PPB_VLAN_SET [25][6].PRI[15:13]	3
0xBB00C358	VLAN_PORT_PPB_VLAN_SET [25][6].VID_AS[12:12]	1
0xBB00C358	VLAN_PORT_PPB_VLAN_SET [25][6].VID[11:0]	12
0xBB00C35C	VLAN_PORT_PPB_VLAN_SET [25][7].RESERVED[31:18]	14
0xBB00C35C	VLAN_PORT_PPB_VLAN_SET [25][7].VLAN_TYPE[17:17]	1
0xBB00C35C	VLAN_PORT_PPB_VLAN_SET [25][7].PRI_AS[16:16]	1
0xBB00C35C	VLAN_PORT_PPB_VLAN_SET [25][7].PRI[15:13]	3
0xBB00C35C	VLAN_PORT_PPB_VLAN_SET [25][7].VID_AS[12:12]	1
0xBB00C35C	VLAN_PORT_PPB_VLAN_SET [25][7].VID[11:0]	12
0xBB00C360	VLAN_PORT_PPB_VLAN_SET [26][0].RESERVED[31:18]	14
0xBB00C360	VLAN_PORT_PPB_VLAN_SET [26][0].VLAN_TYPE[17:17]	1
0xBB00C360	VLAN_PORT_PPB_VLAN_SET [26][0].PRI_AS[16:16]	1
0xBB00C360	VLAN_PORT_PPB_VLAN_SET [26][0].PRI[15:13]	3
0xBB00C360	VLAN_PORT_PPB_VLAN_SET [26][0].VID_AS[12:12]	1
0xBB00C360	VLAN_PORT_PPB_VLAN_SET [26][0].VID[11:0]	12
0xBB00C364	VLAN_PORT_PPB_VLAN_SET [26][1].RESERVED[31:18]	14
0xBB00C364	VLAN_PORT_PPB_VLAN_SET [26][1].VLAN_TYPE[17:17]	1
0xBB00C364	VLAN_PORT_PPB_VLAN_SET [26][1].PRI_AS[16:16]	1
0xBB00C364	VLAN_PORT_PPB_VLAN_SET [26][1].PRI[15:13]	3
0xBB00C364	VLAN_PORT_PPB_VLAN_SET [26][1].VID_AS[12:12]	1
0xBB00C364	VLAN_PORT_PPB_VLAN_SET [26][1].VID[11:0]	12
0xBB00C368	VLAN_PORT_PPB_VLAN_SET [26][2].RESERVED[31:18]	14
0xBB00C368	VLAN_PORT_PPB_VLAN_SET [26][2].VLAN_TYPE[17:17]	1
0xBB00C368	VLAN_PORT_PPB_VLAN_SET [26][2].PRI_AS[16:16]	1
0xBB00C368	VLAN_PORT_PPB_VLAN_SET [26][2].PRI[15:13]	3
0xBB00C368	VLAN_PORT_PPB_VLAN_SET [26][2].VID_AS[12:12]	1
0xBB00C368	VLAN_PORT_PPB_VLAN_SET [26][2].VID[11:0]	12
0xBB00C36C	VLAN_PORT_PPB_VLAN_SET [26][3].RESERVED[31:18]	14
0xBB00C36C	VLAN_PORT_PPB_VLAN_SET [26][3].VLAN_TYPE[17:17]	1
0xBB00C36C	VLAN_PORT_PPB_VLAN_SET [26][3].PRI_AS[16:16]	1
0xBB00C36C	VLAN_PORT_PPB_VLAN_SET [26][3].PRI[15:13]	3
0xBB00C36C	VLAN_PORT_PPB_VLAN_SET [26][3].VID_AS[12:12]	1
0xBB00C36C	VLAN_PORT_PPB_VLAN_SET [26][3].VID[11:0]	12
0xBB00C370	VLAN_PORT_PPB_VLAN_SET [26][4].RESERVED[31:18]	14
0xBB00C370	VLAN_PORT_PPB_VLAN_SET [26][4].VLAN_TYPE[17:17]	1
0xBB00C370	VLAN_PORT_PPB_VLAN_SET [26][4].PRI_AS[16:16]	1
0xBB00C370	VLAN_PORT_PPB_VLAN_SET [26][4].PRI[15:13]	3
0xBB00C370	VLAN_PORT_PPB_VLAN_SET [26][4].VID_AS[12:12]	1
0xBB00C370	VLAN_PORT_PPB_VLAN_SET [26][4].VID[11:0]	12
0xBB00C374	VLAN_PORT_PPB_VLAN_SET [26][5].RESERVED[31:18]	14
0xBB00C374	VLAN_PORT_PPB_VLAN_SET [26][5].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C374	VLAN_PORT_PPB_VLAN_SET [26][5].PRI_AS[16:16]	1
0xBB00C374	VLAN_PORT_PPB_VLAN_SET [26][5].PRI[15:13]	3
0xBB00C374	VLAN_PORT_PPB_VLAN_SET [26][5].VID_AS[12:12]	1
0xBB00C374	VLAN_PORT_PPB_VLAN_SET [26][5].VID[11:0]	12
0xBB00C378	VLAN_PORT_PPB_VLAN_SET [26][6].RESERVED[31:18]	14
0xBB00C378	VLAN_PORT_PPB_VLAN_SET [26][6].VLAN_TYPE[17:17]	1
0xBB00C378	VLAN_PORT_PPB_VLAN_SET [26][6].PRI_AS[16:16]	1
0xBB00C378	VLAN_PORT_PPB_VLAN_SET [26][6].PRI[15:13]	3
0xBB00C378	VLAN_PORT_PPB_VLAN_SET [26][6].VID_AS[12:12]	1
0xBB00C378	VLAN_PORT_PPB_VLAN_SET [26][6].VID[11:0]	12
0xBB00C37C	VLAN_PORT_PPB_VLAN_SET [26][7].RESERVED[31:18]	14
0xBB00C37C	VLAN_PORT_PPB_VLAN_SET [26][7].VLAN_TYPE[17:17]	1
0xBB00C37C	VLAN_PORT_PPB_VLAN_SET [26][7].PRI_AS[16:16]	1
0xBB00C37C	VLAN_PORT_PPB_VLAN_SET [26][7].PRI[15:13]	3
0xBB00C37C	VLAN_PORT_PPB_VLAN_SET [26][7].VID_AS[12:12]	1
0xBB00C37C	VLAN_PORT_PPB_VLAN_SET [26][7].VID[11:0]	12
0xBB00C380	VLAN_PORT_PPB_VLAN_SET [27][0].RESERVED[31:18]	14
0xBB00C380	VLAN_PORT_PPB_VLAN_SET [27][0].VLAN_TYPE[17:17]	1
0xBB00C380	VLAN_PORT_PPB_VLAN_SET [27][0].PRI_AS[16:16]	1
0xBB00C380	VLAN_PORT_PPB_VLAN_SET [27][0].PRI[15:13]	3
0xBB00C380	VLAN_PORT_PPB_VLAN_SET [27][0].VID_AS[12:12]	1
0xBB00C380	VLAN_PORT_PPB_VLAN_SET [27][0].VID[11:0]	12
0xBB00C384	VLAN_PORT_PPB_VLAN_SET [27][1].RESERVED[31:18]	14
0xBB00C384	VLAN_PORT_PPB_VLAN_SET [27][1].VLAN_TYPE[17:17]	1
0xBB00C384	VLAN_PORT_PPB_VLAN_SET [27][1].PRI_AS[16:16]	1
0xBB00C384	VLAN_PORT_PPB_VLAN_SET [27][1].PRI[15:13]	3
0xBB00C384	VLAN_PORT_PPB_VLAN_SET [27][1].VID_AS[12:12]	1
0xBB00C384	VLAN_PORT_PPB_VLAN_SET [27][1].VID[11:0]	12
0xBB00C388	VLAN_PORT_PPB_VLAN_SET [27][2] RESERVED[31:18]	14
0xBB00C388	VLAN_PORT_PPB_VLAN_SET [27][2].VLAN_TYPE[17:17]	1
0xBB00C388	VLAN_PORT_PPB_VLAN_SET [27][2].PRI_AS[16:16]	1
0xBB00C388	VLAN_PORT_PPB_VLAN_SET [27][2].PRI[15:13]	3
0xBB00C388	VLAN_PORT_PPB_VLAN_SET [27][2].VID_AS[12:12]	1
0xBB00C388	VLAN_PORT_PPB_VLAN_SET [27][2].VID[11:0]	12
0xBB00C38C	VLAN_PORT_PPB_VLAN_SET [27][3].RESERVED[31:18]	14
0xBB00C38C	VLAN_PORT_PPB_VLAN_SET [27][3].VLAN_TYPE[17:17]	1
0xBB00C38C	VLAN_PORT_PPB_VLAN_SET [27][3].PRI_AS[16:16]	1
0xBB00C38C	VLAN_PORT_PPB_VLAN_SET [27][3].PRI[15:13]	3
0xBB00C38C	VLAN_PORT_PPB_VLAN_SET [27][3].VID_AS[12:12]	1
0xBB00C38C	VLAN_PORT_PPB_VLAN_SET [27][3].VID[11:0]	12
0xBB00C390	VLAN_PORT_PPB_VLAN_SET [27][4].RESERVED[31:18]	14
0xBB00C390	VLAN_PORT_PPB_VLAN_SET [27][4].VLAN_TYPE[17:17]	1
0xBB00C390	VLAN_PORT_PPB_VLAN_SET [27][4].PRI_AS[16:16]	1
0xBB00C390	VLAN_PORT_PPB_VLAN_SET [27][4].PRI[15:13]	3
0xBB00C390	VLAN_PORT_PPB_VLAN_SET [27][4].VID_AS[12:12]	1
0xBB00C390	VLAN_PORT_PPB_VLAN_SET [27][4].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C394	VLAN_PORT_PPB_VLAN_SET [27][5].RESERVED[31:18]	14
0xBB00C394	VLAN_PORT_PPB_VLAN_SET [27][5].VLAN_TYPE[17:17]	1
0xBB00C394	VLAN_PORT_PPB_VLAN_SET [27][5].PRI_AS[16:16]	1
0xBB00C394	VLAN_PORT_PPB_VLAN_SET [27][5].PRI[15:13]	3
0xBB00C394	VLAN_PORT_PPB_VLAN_SET [27][5].VID_AS[12:12]	1
0xBB00C394	VLAN_PORT_PPB_VLAN_SET [27][5].VID[11:0]	12
0xBB00C398	VLAN_PORT_PPB_VLAN_SET [27][6].RESERVED[31:18]	14
0xBB00C398	VLAN_PORT_PPB_VLAN_SET [27][6].VLAN_TYPE[17:17]	1
0xBB00C398	VLAN_PORT_PPB_VLAN_SET [27][6].PRI_AS[16:16]	1
0xBB00C398	VLAN_PORT_PPB_VLAN_SET [27][6].PRI[15:13]	3
0xBB00C398	VLAN_PORT_PPB_VLAN_SET [27][6].VID_AS[12:12]	1
0xBB00C398	VLAN_PORT_PPB_VLAN_SET [27][6].VID[11:0]	12
0xBB00C39C	VLAN_PORT_PPB_VLAN_SET [27][7].RESERVED[31:18]	14
0xBB00C39C	VLAN_PORT_PPB_VLAN_SET [27][7].VLAN_TYPE[17:17]	1
0xBB00C39C	VLAN_PORT_PPB_VLAN_SET [27][7].PRI_AS[16:16]	1
0xBB00C39C	VLAN_PORT_PPB_VLAN_SET [27][7].PRI[15:13]	3
0xBB00C39C	VLAN_PORT_PPB_VLAN_SET [27][7].VID_AS[12:12]	1
0xBB00C39C	VLAN_PORT_PPB_VLAN_SET [27][7].VID[11:0]	12
0xBB00C3A0	VLAN_PORT_PPB_VLAN_SET [28][0].RESERVED[31:18]	14
0xBB00C3A0	VLAN_PORT_PPB_VLAN_SET [28][0].VLAN_TYPE[17:17]	1
0xBB00C3A0	VLAN_PORT_PPB_VLAN_SET [28][0].PRI_AS[16:16]	1
0xBB00C3A0	VLAN_PORT_PPB_VLAN_SET [28][0].PRI[15:13]	3
0xBB00C3A0	VLAN_PORT_PPB_VLAN_SET [28][0].VID_AS[12:12]	1
0xBB00C3A0	VLAN_PORT_PPB_VLAN_SET [28][0].VID[11:0]	12
0xBB00C3A4	VLAN_PORT_PPB_VLAN_SET [28][1].RESERVED[31:18]	14
0xBB00C3A4	VLAN_PORT_PPB_VLAN_SET [28][1].VLAN_TYPE[17:17]	1
0xBB00C3A4	VLAN_PORT_PPB_VLAN_SET [28][1].PRI_AS[16:16]	1
0xBB00C3A4	VLAN_PORT_PPB_VLAN_SET [28][1].PRI[15:13]	3
0xBB00C3A4	VLAN_PORT_PPB_VLAN_SET [28][1].VID_AS[12:12]	1
0xBB00C3A4	VLAN_PORT_PPB_VLAN_SET [28][1].VID[11:0]	12
0xBB00C3A8	VLAN_PORT_PPB_VLAN_SET [28][2].RESERVED[31:18]	14
0xBB00C3A8	VLAN_PORT_PPB_VLAN_SET [28][2].VLAN_TYPE[17:17]	1
0xBB00C3A8	VLAN_PORT_PPB_VLAN_SET [28][2].PRI_AS[16:16]	1
0xBB00C3A8	VLAN_PORT_PPB_VLAN_SET [28][2].PRI[15:13]	3
0xBB00C3A8	VLAN_PORT_PPB_VLAN_SET [28][2].VID_AS[12:12]	1
0xBB00C3A8	VLAN_PORT_PPB_VLAN_SET [28][2].VID[11:0]	12
0xBB00C3AC	VLAN_PORT_PPB_VLAN_SET [28][3].RESERVED[31:18]	14
0xBB00C3AC	VLAN_PORT_PPB_VLAN_SET [28][3].VLAN_TYPE[17:17]	1
0xBB00C3AC	VLAN_PORT_PPB_VLAN_SET [28][3].PRI_AS[16:16]	1
0xBB00C3AC	VLAN_PORT_PPB_VLAN_SET [28][3].PRI[15:13]	3
0xBB00C3AC	VLAN_PORT_PPB_VLAN_SET [28][3].VID_AS[12:12]	1
0xBB00C3AC	VLAN_PORT_PPB_VLAN_SET [28][3].VID[11:0]	12
0xBB00C3B0	VLAN_PORT_PPB_VLAN_SET [28][4].RESERVED[31:18]	14
0xBB00C3B0	VLAN_PORT_PPB_VLAN_SET [28][4].VLAN_TYPE[17:17]	1
0xBB00C3B0	VLAN_PORT_PPB_VLAN_SET [28][4].PRI_AS[16:16]	1
0xBB00C3B0	VLAN_PORT_PPB_VLAN_SET [28][4].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C3B0	VLAN_PORT_PPB_VLAN_SET [28][4].VID_AS[12:12]	1
0xBB00C3B0	VLAN_PORT_PPB_VLAN_SET [28][4].VID[11:0]	12
0xBB00C3B4	VLAN_PORT_PPB_VLAN_SET [28][5].RESERVED[31:18]	14
0xBB00C3B4	VLAN_PORT_PPB_VLAN_SET [28][5].VLAN_TYPE[17:17]	1
0xBB00C3B4	VLAN_PORT_PPB_VLAN_SET [28][5].PRI_AS[16:16]	1
0xBB00C3B4	VLAN_PORT_PPB_VLAN_SET [28][5].PRI[15:13]	3
0xBB00C3B4	VLAN_PORT_PPB_VLAN_SET [28][5].VID_AS[12:12]	1
0xBB00C3B4	VLAN_PORT_PPB_VLAN_SET [28][5].VID[11:0]	12
0xBB00C3B8	VLAN_PORT_PPB_VLAN_SET [28][6].RESERVED[31:18]	14
0xBB00C3B8	VLAN_PORT_PPB_VLAN_SET [28][6].VLAN_TYPE[17:17]	1
0xBB00C3B8	VLAN_PORT_PPB_VLAN_SET [28][6].PRI_AS[16:16]	1
0xBB00C3B8	VLAN_PORT_PPB_VLAN_SET [28][6].PRI[15:13]	3
0xBB00C3B8	VLAN_PORT_PPB_VLAN_SET [28][6].VID_AS[12:12]	1
0xBB00C3B8	VLAN_PORT_PPB_VLAN_SET [28][6].VID[11:0]	12
0xBB00C3BC	VLAN_PORT_PPB_VLAN_SET [28][7].RESERVED[31:18]	14
0xBB00C3BC	VLAN_PORT_PPB_VLAN_SET [28][7].VLAN_TYPE[17:17]	1
0xBB00C3BC	VLAN_PORT_PPB_VLAN_SET [28][7].PRI_AS[16:16]	1
0xBB00C3BC	VLAN_PORT_PPB_VLAN_SET [28][7].PRI[15:13]	3
0xBB00C3BC	VLAN_PORT_PPB_VLAN_SET [28][7].VID_AS[12:12]	1
0xBB00C3BC	VLAN_PORT_PPB_VLAN_SET [28][7].VID[11:0]	12
0xBB00C3C0	VLAN_PORT_PPB_VLAN_SET [29][0].RESERVED[31:18]	14
0xBB00C3C0	VLAN_PORT_PPB_VLAN_SET [29][0].VLAN_TYPE[17:17]	1
0xBB00C3C0	VLAN_PORT_PPB_VLAN_SET [29][0].PRI_AS[16:16]	1
0xBB00C3C0	VLAN_PORT_PPB_VLAN_SET [29][0].PRI[15:13]	3
0xBB00C3C0	VLAN_PORT_PPB_VLAN_SET [29][0].VID_AS[12:12]	1
0xBB00C3C0	VLAN_PORT_PPB_VLAN_SET [29][0].VID[11:0]	12
0xBB00C3C4	VLAN_PORT_PPB_VLAN_SET [29][1].RESERVED[31:18]	14
0xBB00C3C4	VLAN_PORT_PPB_VLAN_SET [29][1].VLAN_TYPE[17:17]	1
0xBB00C3C4	VLAN_PORT_PPB_VLAN_SET [29][1].PRI_AS[16:16]	1
0xBB00C3C4	VLAN_PORT_PPB_VLAN_SET [29][1].PRI[15:13]	3
0xBB00C3C4	VLAN_PORT_PPB_VLAN_SET [29][1].VID_AS[12:12]	1
0xBB00C3C4	VLAN_PORT_PPB_VLAN_SET [29][1].VID[11:0]	12
0xBB00C3C8	VLAN_PORT_PPB_VLAN_SET [29][2].RESERVED[31:18]	14
0xBB00C3C8	VLAN_PORT_PPB_VLAN_SET [29][2].VLAN_TYPE[17:17]	1
0xBB00C3C8	VLAN_PORT_PPB_VLAN_SET [29][2].PRI_AS[16:16]	1
0xBB00C3C8	VLAN_PORT_PPB_VLAN_SET [29][2].PRI[15:13]	3
0xBB00C3C8	VLAN_PORT_PPB_VLAN_SET [29][2].VID_AS[12:12]	1
0xBB00C3C8	VLAN_PORT_PPB_VLAN_SET [29][2].VID[11:0]	12
0xBB00C3CC	VLAN_PORT_PPB_VLAN_SET [29][3].RESERVED[31:18]	14
0xBB00C3CC	VLAN_PORT_PPB_VLAN_SET [29][3].VLAN_TYPE[17:17]	1
0xBB00C3CC	VLAN_PORT_PPB_VLAN_SET [29][3].PRI_AS[16:16]	1
0xBB00C3CC	VLAN_PORT_PPB_VLAN_SET [29][3].PRI[15:13]	3
0xBB00C3CC	VLAN_PORT_PPB_VLAN_SET [29][3].VID_AS[12:12]	1
0xBB00C3CC	VLAN_PORT_PPB_VLAN_SET [29][3].VID[11:0]	12
0xBB00C3D0	VLAN_PORT_PPB_VLAN_SET [29][4].RESERVED[31:18]	14
0xBB00C3D0	VLAN_PORT_PPB_VLAN_SET [29][4].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C3D0	VLAN_PORT_PPB_VLAN_SET [29][4].PRI_AS[16:16]	1
0xBB00C3D0	VLAN_PORT_PPB_VLAN_SET [29][4].PRI[15:13]	3
0xBB00C3D0	VLAN_PORT_PPB_VLAN_SET [29][4].VID_AS[12:12]	1
0xBB00C3D0	VLAN_PORT_PPB_VLAN_SET [29][4].VID[11:0]	12
0xBB00C3D4	VLAN_PORT_PPB_VLAN_SET [29][5].RESERVED[31:18]	14
0xBB00C3D4	VLAN_PORT_PPB_VLAN_SET [29][5].VLAN_TYPE[17:17]	1
0xBB00C3D4	VLAN_PORT_PPB_VLAN_SET [29][5].PRI_AS[16:16]	1
0xBB00C3D4	VLAN_PORT_PPB_VLAN_SET [29][5].PRI[15:13]	3
0xBB00C3D4	VLAN_PORT_PPB_VLAN_SET [29][5].VID_AS[12:12]	1
0xBB00C3D4	VLAN_PORT_PPB_VLAN_SET [29][5].VID[11:0]	12
0xBB00C3D8	VLAN_PORT_PPB_VLAN_SET [29][6].RESERVED[31:18]	14
0xBB00C3D8	VLAN_PORT_PPB_VLAN_SET [29][6].VLAN_TYPE[17:17]	1
0xBB00C3D8	VLAN_PORT_PPB_VLAN_SET [29][6].PRI_AS[16:16]	1
0xBB00C3D8	VLAN_PORT_PPB_VLAN_SET [29][6].PRI[15:13]	3
0xBB00C3D8	VLAN_PORT_PPB_VLAN_SET [29][6].VID_AS[12:12]	1
0xBB00C3D8	VLAN_PORT_PPB_VLAN_SET [29][6].VID[11:0]	12
0xBB00C3DC	VLAN_PORT_PPB_VLAN_SET [29][7].RESERVED[31:18]	14
0xBB00C3DC	VLAN_PORT_PPB_VLAN_SET [29][7].VLAN_TYPE[17:17]	1
0xBB00C3DC	VLAN_PORT_PPB_VLAN_SET [29][7].PRI_AS[16:16]	1
0xBB00C3DC	VLAN_PORT_PPB_VLAN_SET [29][7].PRI[15:13]	3
0xBB00C3DC	VLAN_PORT_PPB_VLAN_SET [29][7].VID_AS[12:12]	1
0xBB00C3DC	VLAN_PORT_PPB_VLAN_SET [29][7].VID[11:0]	12
0xBB00C3E0	VLAN_PORT_PPB_VLAN_SET [30][0].RESERVED[31:18]	14
0xBB00C3E0	VLAN_PORT_PPB_VLAN_SET [30][0].VLAN_TYPE[17:17]	1
0xBB00C3E0	VLAN_PORT_PPB_VLAN_SET [30][0].PRI_AS[16:16]	1
0xBB00C3E0	VLAN_PORT_PPB_VLAN_SET [30][0].PRI[15:13]	3
0xBB00C3E0	VLAN_PORT_PPB_VLAN_SET [30][0].VID_AS[12:12]	1
0xBB00C3E0	VLAN_PORT_PPB_VLAN_SET [30][0].VID[11:0]	12
0xBB00C3E4	VLAN_PORT_PPB_VLAN_SET [30][1].RESERVED[31:18]	14
0xBB00C3E4	VLAN_PORT_PPB_VLAN_SET [30][1].VLAN_TYPE[17:17]	1
0xBB00C3E4	VLAN_PORT_PPB_VLAN_SET [30][1].PRI_AS[16:16]	1
0xBB00C3E4	VLAN_PORT_PPB_VLAN_SET [30][1].PRI[15:13]	3
0xBB00C3E4	VLAN_PORT_PPB_VLAN_SET [30][1].VID_AS[12:12]	1
0xBB00C3E4	VLAN_PORT_PPB_VLAN_SET [30][1].VID[11:0]	12
0xBB00C3E8	VLAN_PORT_PPB_VLAN_SET [30][2].RESERVED[31:18]	14
0xBB00C3E8	VLAN_PORT_PPB_VLAN_SET [30][2].VLAN_TYPE[17:17]	1
0xBB00C3E8	VLAN_PORT_PPB_VLAN_SET [30][2].PRI_AS[16:16]	1
0xBB00C3E8	VLAN_PORT_PPB_VLAN_SET [30][2].PRI[15:13]	3
0xBB00C3E8	VLAN_PORT_PPB_VLAN_SET [30][2].VID_AS[12:12]	1
0xBB00C3E8	VLAN_PORT_PPB_VLAN_SET [30][2].VID[11:0]	12
0xBB00C3EC	VLAN_PORT_PPB_VLAN_SET [30][3].RESERVED[31:18]	14
0xBB00C3EC	VLAN_PORT_PPB_VLAN_SET [30][3].VLAN_TYPE[17:17]	1
0xBB00C3EC	VLAN_PORT_PPB_VLAN_SET [30][3].PRI_AS[16:16]	1
0xBB00C3EC	VLAN_PORT_PPB_VLAN_SET [30][3].PRI[15:13]	3
0xBB00C3EC	VLAN_PORT_PPB_VLAN_SET [30][3].VID_AS[12:12]	1
0xBB00C3EC	VLAN_PORT_PPB_VLAN_SET [30][3].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C3F0	VLAN_PORT_PPB_VLAN_SET [30][4].RESERVED[31:18]	14
0xBB00C3F0	VLAN_PORT_PPB_VLAN_SET [30][4].VLAN_TYPE[17:17]	1
0xBB00C3F0	VLAN_PORT_PPB_VLAN_SET [30][4].PRI_AS[16:16]	1
0xBB00C3F0	VLAN_PORT_PPB_VLAN_SET [30][4].PRI[15:13]	3
0xBB00C3F0	VLAN_PORT_PPB_VLAN_SET [30][4].VID_AS[12:12]	1
0xBB00C3F0	VLAN_PORT_PPB_VLAN_SET [30][4].VID[11:0]	12
0xBB00C3F4	VLAN_PORT_PPB_VLAN_SET [30][5].RESERVED[31:18]	14
0xBB00C3F4	VLAN_PORT_PPB_VLAN_SET [30][5].VLAN_TYPE[17:17]	1
0xBB00C3F4	VLAN_PORT_PPB_VLAN_SET [30][5].PRI_AS[16:16]	1
0xBB00C3F4	VLAN_PORT_PPB_VLAN_SET [30][5].PRI[15:13]	3
0xBB00C3F4	VLAN_PORT_PPB_VLAN_SET [30][5].VID_AS[12:12]	1
0xBB00C3F4	VLAN_PORT_PPB_VLAN_SET [30][5].VID[11:0]	12
0xBB00C3F8	VLAN_PORT_PPB_VLAN_SET [30][6].RESERVED[31:18]	14
0xBB00C3F8	VLAN_PORT_PPB_VLAN_SET [30][6].VLAN_TYPE[17:17]	1
0xBB00C3F8	VLAN_PORT_PPB_VLAN_SET [30][6].PRI_AS[16:16]	1
0xBB00C3F8	VLAN_PORT_PPB_VLAN_SET [30][6].PRI[15:13]	3
0xBB00C3F8	VLAN_PORT_PPB_VLAN_SET [30][6].VID_AS[12:12]	1
0xBB00C3F8	VLAN_PORT_PPB_VLAN_SET [30][6].VID[11:0]	12
0xBB00C3FC	VLAN_PORT_PPB_VLAN_SET [30][7].RESERVED[31:18]	14
0xBB00C3FC	VLAN_PORT_PPB_VLAN_SET [30][7].VLAN_TYPE[17:17]	1
0xBB00C3FC	VLAN_PORT_PPB_VLAN_SET [30][7].PRI_AS[16:16]	1
0xBB00C3FC	VLAN_PORT_PPB_VLAN_SET [30][7].PRI[15:13]	3
0xBB00C3FC	VLAN_PORT_PPB_VLAN_SET [30][7].VID_AS[12:12]	1
0xBB00C3FC	VLAN_PORT_PPB_VLAN_SET [30][7].VID[11:0]	12
0xBB00C400	VLAN_PORT_PPB_VLAN_SET [31][0].RESERVED[31:18]	14
0xBB00C400	VLAN_PORT_PPB_VLAN_SET [31][0].VLAN_TYPE[17:17]	1
0xBB00C400	VLAN_PORT_PPB_VLAN_SET [31][0].PRI_AS[16:16]	1
0xBB00C400	VLAN_PORT_PPB_VLAN_SET [31][0].PRI[15:13]	3
0xBB00C400	VLAN_PORT_PPB_VLAN_SET [31][0].VID_AS[12:12]	1
0xBB00C400	VLAN_PORT_PPB_VLAN_SET [31][0].VID[11:0]	12
0xBB00C404	VLAN_PORT_PPB_VLAN_SET [31][1].RESERVED[31:18]	14
0xBB00C404	VLAN_PORT_PPB_VLAN_SET [31][1].VLAN_TYPE[17:17]	1
0xBB00C404	VLAN_PORT_PPB_VLAN_SET [31][1].PRI_AS[16:16]	1
0xBB00C404	VLAN_PORT_PPB_VLAN_SET [31][1].PRI[15:13]	3
0xBB00C404	VLAN_PORT_PPB_VLAN_SET [31][1].VID_AS[12:12]	1
0xBB00C404	VLAN_PORT_PPB_VLAN_SET [31][1].VID[11:0]	12
0xBB00C408	VLAN_PORT_PPB_VLAN_SET [31][2].RESERVED[31:18]	14
0xBB00C408	VLAN_PORT_PPB_VLAN_SET [31][2].VLAN_TYPE[17:17]	1
0xBB00C408	VLAN_PORT_PPB_VLAN_SET [31][2].PRI_AS[16:16]	1
0xBB00C408	VLAN_PORT_PPB_VLAN_SET [31][2].PRI[15:13]	3
0xBB00C408	VLAN_PORT_PPB_VLAN_SET [31][2].VID_AS[12:12]	1
0xBB00C408	VLAN_PORT_PPB_VLAN_SET [31][2].VID[11:0]	12
0xBB00C40C	VLAN_PORT_PPB_VLAN_SET [31][3].RESERVED[31:18]	14
0xBB00C40C	VLAN_PORT_PPB_VLAN_SET [31][3].VLAN_TYPE[17:17]	1
0xBB00C40C	VLAN_PORT_PPB_VLAN_SET [31][3].PRI_AS[16:16]	1
0xBB00C40C	VLAN_PORT_PPB_VLAN_SET [31][3].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C40C	VLAN_PORT_PPB_VLAN_SET [31][3].VID_AS[12:12]	1
0xBB00C40C	VLAN_PORT_PPB_VLAN_SET [31][3].VID[11:0]	12
0xBB00C410	VLAN_PORT_PPB_VLAN_SET [31][4].RESERVED[31:18]	14
0xBB00C410	VLAN_PORT_PPB_VLAN_SET [31][4].VLAN_TYPE[17:17]	1
0xBB00C410	VLAN_PORT_PPB_VLAN_SET [31][4].PRI_AS[16:16]	1
0xBB00C410	VLAN_PORT_PPB_VLAN_SET [31][4].PRI[15:13]	3
0xBB00C410	VLAN_PORT_PPB_VLAN_SET [31][4].VID_AS[12:12]	1
0xBB00C410	VLAN_PORT_PPB_VLAN_SET [31][4].VID[11:0]	12
0xBB00C414	VLAN_PORT_PPB_VLAN_SET [31][5].RESERVED[31:18]	14
0xBB00C414	VLAN_PORT_PPB_VLAN_SET [31][5].VLAN_TYPE[17:17]	1
0xBB00C414	VLAN_PORT_PPB_VLAN_SET [31][5].PRI_AS[16:16]	1
0xBB00C414	VLAN_PORT_PPB_VLAN_SET [31][5].PRI[15:13]	3
0xBB00C414	VLAN_PORT_PPB_VLAN_SET [31][5].VID_AS[12:12]	1
0xBB00C414	VLAN_PORT_PPB_VLAN_SET [31][5].VID[11:0]	12
0xBB00C418	VLAN_PORT_PPB_VLAN_SET [31][6].RESERVED[31:18]	14
0xBB00C418	VLAN_PORT_PPB_VLAN_SET [31][6].VLAN_TYPE[17:17]	1
0xBB00C418	VLAN_PORT_PPB_VLAN_SET [31][6].PRI_AS[16:16]	1
0xBB00C418	VLAN_PORT_PPB_VLAN_SET [31][6].PRI[15:13]	3
0xBB00C418	VLAN_PORT_PPB_VLAN_SET [31][6].VID_AS[12:12]	1
0xBB00C418	VLAN_PORT_PPB_VLAN_SET [31][6].VID[11:0]	12
0xBB00C41C	VLAN_PORT_PPB_VLAN_SET [31][7].RESERVED[31:18]	14
0xBB00C41C	VLAN_PORT_PPB_VLAN_SET [31][7].VLAN_TYPE[17:17]	1
0xBB00C41C	VLAN_PORT_PPB_VLAN_SET [31][7].PRI_AS[16:16]	1
0xBB00C41C	VLAN_PORT_PPB_VLAN_SET [31][7].PRI[15:13]	3
0xBB00C41C	VLAN_PORT_PPB_VLAN_SET [31][7].VID_AS[12:12]	1
0xBB00C41C	VLAN_PORT_PPB_VLAN_SET [31][7].VID[11:0]	12
0xBB00C420	VLAN_PORT_PPB_VLAN_SET [32][0].RESERVED[31:18]	14
0xBB00C420	VLAN_PORT_PPB_VLAN_SET [32][0].VLAN_TYPE[17:17]	1
0xBB00C420	VLAN_PORT_PPB_VLAN_SET [32][0].PRI_AS[16:16]	1
0xBB00C420	VLAN_PORT_PPB_VLAN_SET [32][0].PRI[15:13]	3
0xBB00C420	VLAN_PORT_PPB_VLAN_SET [32][0].VID_AS[12:12]	1
0xBB00C420	VLAN_PORT_PPB_VLAN_SET [32][0].VID[11:0]	12
0xBB00C424	VLAN_PORT_PPB_VLAN_SET [32][1].RESERVED[31:18]	14
0xBB00C424	VLAN_PORT_PPB_VLAN_SET [32][1].VLAN_TYPE[17:17]	1
0xBB00C424	VLAN_PORT_PPB_VLAN_SET [32][1].PRI_AS[16:16]	1
0xBB00C424	VLAN_PORT_PPB_VLAN_SET [32][1].PRI[15:13]	3
0xBB00C424	VLAN_PORT_PPB_VLAN_SET [32][1].VID_AS[12:12]	1
0xBB00C424	VLAN_PORT_PPB_VLAN_SET [32][1].VID[11:0]	12
0xBB00C428	VLAN_PORT_PPB_VLAN_SET [32][2].RESERVED[31:18]	14
0xBB00C428	VLAN_PORT_PPB_VLAN_SET [32][2].VLAN_TYPE[17:17]	1
0xBB00C428	VLAN_PORT_PPB_VLAN_SET [32][2].PRI_AS[16:16]	1
0xBB00C428	VLAN_PORT_PPB_VLAN_SET [32][2].PRI[15:13]	3
0xBB00C428	VLAN_PORT_PPB_VLAN_SET [32][2].VID_AS[12:12]	1
0xBB00C428	VLAN_PORT_PPB_VLAN_SET [32][2].VID[11:0]	12
0xBB00C42C	VLAN_PORT_PPB_VLAN_SET [32][3].RESERVED[31:18]	14
0xBB00C42C	VLAN_PORT_PPB_VLAN_SET [32][3].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C42C	VLAN_PORT_PPB_VLAN_SET [32][3].PRI_AS[16:16]	1
0xBB00C42C	VLAN_PORT_PPB_VLAN_SET [32][3].PRI[15:13]	3
0xBB00C42C	VLAN_PORT_PPB_VLAN_SET [32][3].VID_AS[12:12]	1
0xBB00C42C	VLAN_PORT_PPB_VLAN_SET [32][3].VID[11:0]	12
0xBB00C430	VLAN_PORT_PPB_VLAN_SET [32][4].RESERVED[31:18]	14
0xBB00C430	VLAN_PORT_PPB_VLAN_SET [32][4].VLAN_TYPE[17:17]	1
0xBB00C430	VLAN_PORT_PPB_VLAN_SET [32][4].PRI_AS[16:16]	1
0xBB00C430	VLAN_PORT_PPB_VLAN_SET [32][4].PRI[15:13]	3
0xBB00C430	VLAN_PORT_PPB_VLAN_SET [32][4].VID_AS[12:12]	1
0xBB00C430	VLAN_PORT_PPB_VLAN_SET [32][4].VID[11:0]	12
0xBB00C434	VLAN_PORT_PPB_VLAN_SET [32][5].RESERVED[31:18]	14
0xBB00C434	VLAN_PORT_PPB_VLAN_SET [32][5].VLAN_TYPE[17:17]	1
0xBB00C434	VLAN_PORT_PPB_VLAN_SET [32][5].PRI_AS[16:16]	1
0xBB00C434	VLAN_PORT_PPB_VLAN_SET [32][5].PRI[15:13]	3
0xBB00C434	VLAN_PORT_PPB_VLAN_SET [32][5].VID_AS[12:12]	1
0xBB00C434	VLAN_PORT_PPB_VLAN_SET [32][5].VID[11:0]	12
0xBB00C438	VLAN_PORT_PPB_VLAN_SET [32][6].RESERVED[31:18]	14
0xBB00C438	VLAN_PORT_PPB_VLAN_SET [32][6].VLAN_TYPE[17:17]	1
0xBB00C438	VLAN_PORT_PPB_VLAN_SET [32][6].PRI_AS[16:16]	1
0xBB00C438	VLAN_PORT_PPB_VLAN_SET [32][6].PRI[15:13]	3
0xBB00C438	VLAN_PORT_PPB_VLAN_SET [32][6].VID_AS[12:12]	1
0xBB00C438	VLAN_PORT_PPB_VLAN_SET [32][6].VID[11:0]	12
0xBB00C43C	VLAN_PORT_PPB_VLAN_SET [32][7].RESERVED[31:18]	14
0xBB00C43C	VLAN_PORT_PPB_VLAN_SET [32][7].VLAN_TYPE[17:17]	1
0xBB00C43C	VLAN_PORT_PPB_VLAN_SET [32][7].PRI_AS[16:16]	1
0xBB00C43C	VLAN_PORT_PPB_VLAN_SET [32][7].PRI[15:13]	3
0xBB00C43C	VLAN_PORT_PPB_VLAN_SET [32][7].VID_AS[12:12]	1
0xBB00C43C	VLAN_PORT_PPB_VLAN_SET [32][7].VID[11:0]	12
0xBB00C440	VLAN_PORT_PPB_VLAN_SET [33][0].RESERVED[31:18]	14
0xBB00C440	VLAN_PORT_PPB_VLAN_SET [33][0].VLAN_TYPE[17:17]	1
0xBB00C440	VLAN_PORT_PPB_VLAN_SET [33][0].PRI_AS[16:16]	1
0xBB00C440	VLAN_PORT_PPB_VLAN_SET [33][0].PRI[15:13]	3
0xBB00C440	VLAN_PORT_PPB_VLAN_SET [33][0].VID_AS[12:12]	1
0xBB00C440	VLAN_PORT_PPB_VLAN_SET [33][0].VID[11:0]	12
0xBB00C444	VLAN_PORT_PPB_VLAN_SET [33][1].RESERVED[31:18]	14
0xBB00C444	VLAN_PORT_PPB_VLAN_SET [33][1].VLAN_TYPE[17:17]	1
0xBB00C444	VLAN_PORT_PPB_VLAN_SET [33][1].PRI_AS[16:16]	1
0xBB00C444	VLAN_PORT_PPB_VLAN_SET [33][1].PRI[15:13]	3
0xBB00C444	VLAN_PORT_PPB_VLAN_SET [33][1].VID_AS[12:12]	1
0xBB00C444	VLAN_PORT_PPB_VLAN_SET [33][1].VID[11:0]	12
0xBB00C448	VLAN_PORT_PPB_VLAN_SET [33][2].RESERVED[31:18]	14
0xBB00C448	VLAN_PORT_PPB_VLAN_SET [33][2].VLAN_TYPE[17:17]	1
0xBB00C448	VLAN_PORT_PPB_VLAN_SET [33][2].PRI_AS[16:16]	1
0xBB00C448	VLAN_PORT_PPB_VLAN_SET [33][2].PRI[15:13]	3
0xBB00C448	VLAN_PORT_PPB_VLAN_SET [33][2].VID_AS[12:12]	1
0xBB00C448	VLAN_PORT_PPB_VLAN_SET [33][2].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C44C	VLAN_PORT_PPB_VLAN_SET [33][3].RESERVED[31:18]	14
0xBB00C44C	VLAN_PORT_PPB_VLAN_SET [33][3].VLAN_TYPE[17:17]	1
0xBB00C44C	VLAN_PORT_PPB_VLAN_SET [33][3].PRI_AS[16:16]	1
0xBB00C44C	VLAN_PORT_PPB_VLAN_SET [33][3].PRI[15:13]	3
0xBB00C44C	VLAN_PORT_PPB_VLAN_SET [33][3].VID_AS[12:12]	1
0xBB00C44C	VLAN_PORT_PPB_VLAN_SET [33][3].VID[11:0]	12
0xBB00C450	VLAN_PORT_PPB_VLAN_SET [33][4].RESERVED[31:18]	14
0xBB00C450	VLAN_PORT_PPB_VLAN_SET [33][4].VLAN_TYPE[17:17]	1
0xBB00C450	VLAN_PORT_PPB_VLAN_SET [33][4].PRI_AS[16:16]	1
0xBB00C450	VLAN_PORT_PPB_VLAN_SET [33][4].PRI[15:13]	3
0xBB00C450	VLAN_PORT_PPB_VLAN_SET [33][4].VID_AS[12:12]	1
0xBB00C450	VLAN_PORT_PPB_VLAN_SET [33][4].VID[11:0]	12
0xBB00C454	VLAN_PORT_PPB_VLAN_SET [33][5].RESERVED[31:18]	14
0xBB00C454	VLAN_PORT_PPB_VLAN_SET [33][5].VLAN_TYPE[17:17]	1
0xBB00C454	VLAN_PORT_PPB_VLAN_SET [33][5].PRI_AS[16:16]	1
0xBB00C454	VLAN_PORT_PPB_VLAN_SET [33][5].PRI[15:13]	3
0xBB00C454	VLAN_PORT_PPB_VLAN_SET [33][5].VID_AS[12:12]	1
0xBB00C454	VLAN_PORT_PPB_VLAN_SET [33][5].VID[11:0]	12
0xBB00C458	VLAN_PORT_PPB_VLAN_SET [33][6].RESERVED[31:18]	14
0xBB00C458	VLAN_PORT_PPB_VLAN_SET [33][6].VLAN_TYPE[17:17]	1
0xBB00C458	VLAN_PORT_PPB_VLAN_SET [33][6].PRI_AS[16:16]	1
0xBB00C458	VLAN_PORT_PPB_VLAN_SET [33][6].PRI[15:13]	3
0xBB00C458	VLAN_PORT_PPB_VLAN_SET [33][6].VID_AS[12:12]	1
0xBB00C458	VLAN_PORT_PPB_VLAN_SET [33][6].VID[11:0]	12
0xBB00C45C	VLAN_PORT_PPB_VLAN_SET [33][7].RESERVED[31:18]	14
0xBB00C45C	VLAN_PORT_PPB_VLAN_SET [33][7].VLAN_TYPE[17:17]	1
0xBB00C45C	VLAN_PORT_PPB_VLAN_SET [33][7].PRI_AS[16:16]	1
0xBB00C45C	VLAN_PORT_PPB_VLAN_SET [33][7].PRI[15:13]	3
0xBB00C45C	VLAN_PORT_PPB_VLAN_SET [33][7].VID_AS[12:12]	1
0xBB00C45C	VLAN_PORT_PPB_VLAN_SET [33][7].VID[11:0]	12
0xBB00C460	VLAN_PORT_PPB_VLAN_SET [34][0].RESERVED[31:18]	14
0xBB00C460	VLAN_PORT_PPB_VLAN_SET [34][0].VLAN_TYPE[17:17]	1
0xBB00C460	VLAN_PORT_PPB_VLAN_SET [34][0].PRI_AS[16:16]	1
0xBB00C460	VLAN_PORT_PPB_VLAN_SET [34][0].PRI[15:13]	3
0xBB00C460	VLAN_PORT_PPB_VLAN_SET [34][0].VID_AS[12:12]	1
0xBB00C460	VLAN_PORT_PPB_VLAN_SET [34][0].VID[11:0]	12
0xBB00C464	VLAN_PORT_PPB_VLAN_SET [34][1].RESERVED[31:18]	14
0xBB00C464	VLAN_PORT_PPB_VLAN_SET [34][1].VLAN_TYPE[17:17]	1
0xBB00C464	VLAN_PORT_PPB_VLAN_SET [34][1].PRI_AS[16:16]	1
0xBB00C464	VLAN_PORT_PPB_VLAN_SET [34][1].PRI[15:13]	3
0xBB00C464	VLAN_PORT_PPB_VLAN_SET [34][1].VID_AS[12:12]	1
0xBB00C464	VLAN_PORT_PPB_VLAN_SET [34][1].VID[11:0]	12
0xBB00C468	VLAN_PORT_PPB_VLAN_SET [34][2].RESERVED[31:18]	14
0xBB00C468	VLAN_PORT_PPB_VLAN_SET [34][2].VLAN_TYPE[17:17]	1
0xBB00C468	VLAN_PORT_PPB_VLAN_SET [34][2].PRI_AS[16:16]	1
0xBB00C468	VLAN_PORT_PPB_VLAN_SET [34][2].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C468	VLAN_PORT_PPB_VLAN_SET [34][2].VID_AS[12:12]	1
0xBB00C468	VLAN_PORT_PPB_VLAN_SET [34][2].VID[11:0]	12
0xBB00C46C	VLAN_PORT_PPB_VLAN_SET [34][3].RESERVED[31:18]	14
0xBB00C46C	VLAN_PORT_PPB_VLAN_SET [34][3].VLAN_TYPE[17:17]	1
0xBB00C46C	VLAN_PORT_PPB_VLAN_SET [34][3].PRI_AS[16:16]	1
0xBB00C46C	VLAN_PORT_PPB_VLAN_SET [34][3].PRI[15:13]	3
0xBB00C46C	VLAN_PORT_PPB_VLAN_SET [34][3].VID_AS[12:12]	1
0xBB00C46C	VLAN_PORT_PPB_VLAN_SET [34][3].VID[11:0]	12
0xBB00C470	VLAN_PORT_PPB_VLAN_SET [34][4].RESERVED[31:18]	14
0xBB00C470	VLAN_PORT_PPB_VLAN_SET [34][4].VLAN_TYPE[17:17]	1
0xBB00C470	VLAN_PORT_PPB_VLAN_SET [34][4].PRI_AS[16:16]	1
0xBB00C470	VLAN_PORT_PPB_VLAN_SET [34][4].PRI[15:13]	3
0xBB00C470	VLAN_PORT_PPB_VLAN_SET [34][4].VID_AS[12:12]	1
0xBB00C470	VLAN_PORT_PPB_VLAN_SET [34][4].VID[11:0]	12
0xBB00C474	VLAN_PORT_PPB_VLAN_SET [34][5].RESERVED[31:18]	14
0xBB00C474	VLAN_PORT_PPB_VLAN_SET [34][5].VLAN_TYPE[17:17]	1
0xBB00C474	VLAN_PORT_PPB_VLAN_SET [34][5].PRI_AS[16:16]	1
0xBB00C474	VLAN_PORT_PPB_VLAN_SET [34][5].PRI[15:13]	3
0xBB00C474	VLAN_PORT_PPB_VLAN_SET [34][5].VID_AS[12:12]	1
0xBB00C474	VLAN_PORT_PPB_VLAN_SET [34][5].VID[11:0]	12
0xBB00C478	VLAN_PORT_PPB_VLAN_SET [34][6].RESERVED[31:18]	14
0xBB00C478	VLAN_PORT_PPB_VLAN_SET [34][6].VLAN_TYPE[17:17]	1
0xBB00C478	VLAN_PORT_PPB_VLAN_SET [34][6].PRI_AS[16:16]	1
0xBB00C478	VLAN_PORT_PPB_VLAN_SET [34][6].PRI[15:13]	3
0xBB00C478	VLAN_PORT_PPB_VLAN_SET [34][6].VID_AS[12:12]	1
0xBB00C478	VLAN_PORT_PPB_VLAN_SET [34][6].VID[11:0]	12
0xBB00C47C	VLAN_PORT_PPB_VLAN_SET [34][7].RESERVED[31:18]	14
0xBB00C47C	VLAN_PORT_PPB_VLAN_SET [34][7].VLAN_TYPE[17:17]	1
0xBB00C47C	VLAN_PORT_PPB_VLAN_SET [34][7].PRI_AS[16:16]	1
0xBB00C47C	VLAN_PORT_PPB_VLAN_SET [34][7].PRI[15:13]	3
0xBB00C47C	VLAN_PORT_PPB_VLAN_SET [34][7].VID_AS[12:12]	1
0xBB00C47C	VLAN_PORT_PPB_VLAN_SET [34][7].VID[11:0]	12
0xBB00C480	VLAN_PORT_PPB_VLAN_SET [35][0].RESERVED[31:18]	14
0xBB00C480	VLAN_PORT_PPB_VLAN_SET [35][0].VLAN_TYPE[17:17]	1
0xBB00C480	VLAN_PORT_PPB_VLAN_SET [35][0].PRI_AS[16:16]	1
0xBB00C480	VLAN_PORT_PPB_VLAN_SET [35][0].PRI[15:13]	3
0xBB00C480	VLAN_PORT_PPB_VLAN_SET [35][0].VID_AS[12:12]	1
0xBB00C480	VLAN_PORT_PPB_VLAN_SET [35][0].VID[11:0]	12
0xBB00C484	VLAN_PORT_PPB_VLAN_SET [35][1].RESERVED[31:18]	14
0xBB00C484	VLAN_PORT_PPB_VLAN_SET [35][1].VLAN_TYPE[17:17]	1
0xBB00C484	VLAN_PORT_PPB_VLAN_SET [35][1].PRI_AS[16:16]	1
0xBB00C484	VLAN_PORT_PPB_VLAN_SET [35][1].PRI[15:13]	3
0xBB00C484	VLAN_PORT_PPB_VLAN_SET [35][1].VID_AS[12:12]	1
0xBB00C484	VLAN_PORT_PPB_VLAN_SET [35][1].VID[11:0]	12
0xBB00C488	VLAN_PORT_PPB_VLAN_SET [35][2].RESERVED[31:18]	14
0xBB00C488	VLAN_PORT_PPB_VLAN_SET [35][2].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C488	VLAN_PORT_PPB_VLAN_SET [35][2].PRI_AS[16:16]	1
0xBB00C488	VLAN_PORT_PPB_VLAN_SET [35][2].PRI[15:13]	3
0xBB00C488	VLAN_PORT_PPB_VLAN_SET [35][2].VID_AS[12:12]	1
0xBB00C488	VLAN_PORT_PPB_VLAN_SET [35][2].VID[11:0]	12
0xBB00C48C	VLAN_PORT_PPB_VLAN_SET [35][3].RESERVED[31:18]	14
0xBB00C48C	VLAN_PORT_PPB_VLAN_SET [35][3].VLAN_TYPE[17:17]	1
0xBB00C48C	VLAN_PORT_PPB_VLAN_SET [35][3].PRI_AS[16:16]	1
0xBB00C48C	VLAN_PORT_PPB_VLAN_SET [35][3].PRI[15:13]	3
0xBB00C48C	VLAN_PORT_PPB_VLAN_SET [35][3].VID_AS[12:12]	1
0xBB00C48C	VLAN_PORT_PPB_VLAN_SET [35][3].VID[11:0]	12
0xBB00C490	VLAN_PORT_PPB_VLAN_SET [35][4].RESERVED[31:18]	14
0xBB00C490	VLAN_PORT_PPB_VLAN_SET [35][4].VLAN_TYPE[17:17]	1
0xBB00C490	VLAN_PORT_PPB_VLAN_SET [35][4].PRI_AS[16:16]	1
0xBB00C490	VLAN_PORT_PPB_VLAN_SET [35][4].PRI[15:13]	3
0xBB00C490	VLAN_PORT_PPB_VLAN_SET [35][4].VID_AS[12:12]	1
0xBB00C490	VLAN_PORT_PPB_VLAN_SET [35][4].VID[11:0]	12
0xBB00C494	VLAN_PORT_PPB_VLAN_SET [35][5].RESERVED[31:18]	14
0xBB00C494	VLAN_PORT_PPB_VLAN_SET [35][5].VLAN_TYPE[17:17]	1
0xBB00C494	VLAN_PORT_PPB_VLAN_SET [35][5].PRI_AS[16:16]	1
0xBB00C494	VLAN_PORT_PPB_VLAN_SET [35][5].PRI[15:13]	3
0xBB00C494	VLAN_PORT_PPB_VLAN_SET [35][5].VID_AS[12:12]	1
0xBB00C494	VLAN_PORT_PPB_VLAN_SET [35][5].VID[11:0]	12
0xBB00C498	VLAN_PORT_PPB_VLAN_SET [35][6].RESERVED[31:18]	14
0xBB00C498	VLAN_PORT_PPB_VLAN_SET [35][6].VLAN_TYPE[17:17]	1
0xBB00C498	VLAN_PORT_PPB_VLAN_SET [35][6].PRI_AS[16:16]	1
0xBB00C498	VLAN_PORT_PPB_VLAN_SET [35][6].PRI[15:13]	3
0xBB00C498	VLAN_PORT_PPB_VLAN_SET [35][6].VID_AS[12:12]	1
0xBB00C498	VLAN_PORT_PPB_VLAN_SET [35][6].VID[11:0]	12
0xBB00C49C	VLAN_PORT_PPB_VLAN_SET [35][7].RESERVED[31:18]	14
0xBB00C49C	VLAN_PORT_PPB_VLAN_SET [35][7].VLAN_TYPE[17:17]	1
0xBB00C49C	VLAN_PORT_PPB_VLAN_SET [35][7].PRI_AS[16:16]	1
0xBB00C49C	VLAN_PORT_PPB_VLAN_SET [35][7].PRI[15:13]	3
0xBB00C49C	VLAN_PORT_PPB_VLAN_SET [35][7].VID_AS[12:12]	1
0xBB00C49C	VLAN_PORT_PPB_VLAN_SET [35][7].VID[11:0]	12
0xBB00C4A0	VLAN_PORT_PPB_VLAN_SET [36][0].RESERVED[31:18]	14
0xBB00C4A0	VLAN_PORT_PPB_VLAN_SET [36][0].VLAN_TYPE[17:17]	1
0xBB00C4A0	VLAN_PORT_PPB_VLAN_SET [36][0].PRI_AS[16:16]	1
0xBB00C4A0	VLAN_PORT_PPB_VLAN_SET [36][0].PRI[15:13]	3
0xBB00C4A0	VLAN_PORT_PPB_VLAN_SET [36][0].VID_AS[12:12]	1
0xBB00C4A0	VLAN_PORT_PPB_VLAN_SET [36][0].VID[11:0]	12
0xBB00C4A4	VLAN_PORT_PPB_VLAN_SET [36][1].RESERVED[31:18]	14
0xBB00C4A4	VLAN_PORT_PPB_VLAN_SET [36][1].VLAN_TYPE[17:17]	1
0xBB00C4A4	VLAN_PORT_PPB_VLAN_SET [36][1].PRI_AS[16:16]	1
0xBB00C4A4	VLAN_PORT_PPB_VLAN_SET [36][1].PRI[15:13]	3
0xBB00C4A4	VLAN_PORT_PPB_VLAN_SET [36][1].VID_AS[12:12]	1
0xBB00C4A4	VLAN_PORT_PPB_VLAN_SET [36][1].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C4A8	VLAN_PORT_PPB_VLAN_SET [36][2].RESERVED[31:18]	14
0xBB00C4A8	VLAN_PORT_PPB_VLAN_SET [36][2].VLAN_TYPE[17:17]	1
0xBB00C4A8	VLAN_PORT_PPB_VLAN_SET [36][2].PRI_AS[16:16]	1
0xBB00C4A8	VLAN_PORT_PPB_VLAN_SET [36][2].PRI[15:13]	3
0xBB00C4A8	VLAN_PORT_PPB_VLAN_SET [36][2].VID_AS[12:12]	1
0xBB00C4A8	VLAN_PORT_PPB_VLAN_SET [36][2].VID[11:0]	12
0xBB00C4AC	VLAN_PORT_PPB_VLAN_SET [36][3].RESERVED[31:18]	14
0xBB00C4AC	VLAN_PORT_PPB_VLAN_SET [36][3].VLAN_TYPE[17:17]	1
0xBB00C4AC	VLAN_PORT_PPB_VLAN_SET [36][3].PRI_AS[16:16]	1
0xBB00C4AC	VLAN_PORT_PPB_VLAN_SET [36][3].PRI[15:13]	3
0xBB00C4AC	VLAN_PORT_PPB_VLAN_SET [36][3].VID_AS[12:12]	1
0xBB00C4AC	VLAN_PORT_PPB_VLAN_SET [36][3].VID[11:0]	12
0xBB00C4B0	VLAN_PORT_PPB_VLAN_SET [36][4].RESERVED[31:18]	14
0xBB00C4B0	VLAN_PORT_PPB_VLAN_SET [36][4].VLAN_TYPE[17:17]	1
0xBB00C4B0	VLAN_PORT_PPB_VLAN_SET [36][4].PRI_AS[16:16]	1
0xBB00C4B0	VLAN_PORT_PPB_VLAN_SET [36][4].PRI[15:13]	3
0xBB00C4B0	VLAN_PORT_PPB_VLAN_SET [36][4].VID_AS[12:12]	1
0xBB00C4B0	VLAN_PORT_PPB_VLAN_SET [36][4].VID[11:0]	12
0xBB00C4B4	VLAN_PORT_PPB_VLAN_SET [36][5].RESERVED[31:18]	14
0xBB00C4B4	VLAN_PORT_PPB_VLAN_SET [36][5].VLAN_TYPE[17:17]	1
0xBB00C4B4	VLAN_PORT_PPB_VLAN_SET [36][5].PRI_AS[16:16]	1
0xBB00C4B4	VLAN_PORT_PPB_VLAN_SET [36][5].PRI[15:13]	3
0xBB00C4B4	VLAN_PORT_PPB_VLAN_SET [36][5].VID_AS[12:12]	1
0xBB00C4B4	VLAN_PORT_PPB_VLAN_SET [36][5].VID[11:0]	12
0xBB00C4B8	VLAN_PORT_PPB_VLAN_SET [36][6].RESERVED[31:18]	14
0xBB00C4B8	VLAN_PORT_PPB_VLAN_SET [36][6].VLAN_TYPE[17:17]	1
0xBB00C4B8	VLAN_PORT_PPB_VLAN_SET [36][6].PRI_AS[16:16]	1
0xBB00C4B8	VLAN_PORT_PPB_VLAN_SET [36][6].PRI[15:13]	3
0xBB00C4B8	VLAN_PORT_PPB_VLAN_SET [36][6].VID_AS[12:12]	1
0xBB00C4B8	VLAN_PORT_PPB_VLAN_SET [36][6].VID[11:0]	12
0xBB00C4BC	VLAN_PORT_PPB_VLAN_SET [36][7].RESERVED[31:18]	14
0xBB00C4BC	VLAN_PORT_PPB_VLAN_SET [36][7].VLAN_TYPE[17:17]	1
0xBB00C4BC	VLAN_PORT_PPB_VLAN_SET [36][7].PRI_AS[16:16]	1
0xBB00C4BC	VLAN_PORT_PPB_VLAN_SET [36][7].PRI[15:13]	3
0xBB00C4BC	VLAN_PORT_PPB_VLAN_SET [36][7].VID_AS[12:12]	1
0xBB00C4BC	VLAN_PORT_PPB_VLAN_SET [36][7].VID[11:0]	12
0xBB00C4C0	VLAN_PORT_PPB_VLAN_SET [37][0].RESERVED[31:18]	14
0xBB00C4C0	VLAN_PORT_PPB_VLAN_SET [37][0].VLAN_TYPE[17:17]	1
0xBB00C4C0	VLAN_PORT_PPB_VLAN_SET [37][0].PRI_AS[16:16]	1
0xBB00C4C0	VLAN_PORT_PPB_VLAN_SET [37][0].PRI[15:13]	3
0xBB00C4C0	VLAN_PORT_PPB_VLAN_SET [37][0].VID_AS[12:12]	1
0xBB00C4C0	VLAN_PORT_PPB_VLAN_SET [37][0].VID[11:0]	12
0xBB00C4C4	VLAN_PORT_PPB_VLAN_SET [37][1].RESERVED[31:18]	14
0xBB00C4C4	VLAN_PORT_PPB_VLAN_SET [37][1].VLAN_TYPE[17:17]	1
0xBB00C4C4	VLAN_PORT_PPB_VLAN_SET [37][1].PRI_AS[16:16]	1
0xBB00C4C4	VLAN_PORT_PPB_VLAN_SET [37][1].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C4C4	VLAN_PORT_PPB_VLAN_SET [37][1].VID_AS[12:12]	1
0xBB00C4C4	VLAN_PORT_PPB_VLAN_SET [37][1].VID[11:0]	12
0xBB00C4C8	VLAN_PORT_PPB_VLAN_SET [37][2].RESERVED[31:18]	14
0xBB00C4C8	VLAN_PORT_PPB_VLAN_SET [37][2].VLAN_TYPE[17:17]	1
0xBB00C4C8	VLAN_PORT_PPB_VLAN_SET [37][2].PRI_AS[16:16]	1
0xBB00C4C8	VLAN_PORT_PPB_VLAN_SET [37][2].PRI[15:13]	3
0xBB00C4C8	VLAN_PORT_PPB_VLAN_SET [37][2].VID_AS[12:12]	1
0xBB00C4C8	VLAN_PORT_PPB_VLAN_SET [37][2].VID[11:0]	12
0xBB00C4CC	VLAN_PORT_PPB_VLAN_SET [37][3].RESERVED[31:18]	14
0xBB00C4CC	VLAN_PORT_PPB_VLAN_SET [37][3].VLAN_TYPE[17:17]	1
0xBB00C4CC	VLAN_PORT_PPB_VLAN_SET [37][3].PRI_AS[16:16]	1
0xBB00C4CC	VLAN_PORT_PPB_VLAN_SET [37][3].PRI[15:13]	3
0xBB00C4CC	VLAN_PORT_PPB_VLAN_SET [37][3].VID_AS[12:12]	1
0xBB00C4CC	VLAN_PORT_PPB_VLAN_SET [37][3].VID[11:0]	12
0xBB00C4D0	VLAN_PORT_PPB_VLAN_SET [37][4].RESERVED[31:18]	14
0xBB00C4D0	VLAN_PORT_PPB_VLAN_SET [37][4].VLAN_TYPE[17:17]	1
0xBB00C4D0	VLAN_PORT_PPB_VLAN_SET [37][4].PRI_AS[16:16]	1
0xBB00C4D0	VLAN_PORT_PPB_VLAN_SET [37][4].PRI[15:13]	3
0xBB00C4D0	VLAN_PORT_PPB_VLAN_SET [37][4].VID_AS[12:12]	1
0xBB00C4D0	VLAN_PORT_PPB_VLAN_SET [37][4].VID[11:0]	12
0xBB00C4D4	VLAN_PORT_PPB_VLAN_SET [37][5].RESERVED[31:18]	14
0xBB00C4D4	VLAN_PORT_PPB_VLAN_SET [37][5].VLAN_TYPE[17:17]	1
0xBB00C4D4	VLAN_PORT_PPB_VLAN_SET [37][5].PRI_AS[16:16]	1
0xBB00C4D4	VLAN_PORT_PPB_VLAN_SET [37][5].PRI[15:13]	3
0xBB00C4D4	VLAN_PORT_PPB_VLAN_SET [37][5].VID_AS[12:12]	1
0xBB00C4D4	VLAN_PORT_PPB_VLAN_SET [37][5].VID[11:0]	12
0xBB00C4D8	VLAN_PORT_PPB_VLAN_SET [37][6].RESERVED[31:18]	14
0xBB00C4D8	VLAN_PORT_PPB_VLAN_SET [37][6].VLAN_TYPE[17:17]	1
0xBB00C4D8	VLAN_PORT_PPB_VLAN_SET [37][6].PRI_AS[16:16]	1
0xBB00C4D8	VLAN_PORT_PPB_VLAN_SET [37][6].PRI[15:13]	3
0xBB00C4D8	VLAN_PORT_PPB_VLAN_SET [37][6].VID_AS[12:12]	1
0xBB00C4D8	VLAN_PORT_PPB_VLAN_SET [37][6].VID[11:0]	12
0xBB00C4DC	VLAN_PORT_PPB_VLAN_SET [37][7].RESERVED[31:18]	14
0xBB00C4DC	VLAN_PORT_PPB_VLAN_SET [37][7].VLAN_TYPE[17:17]	1
0xBB00C4DC	VLAN_PORT_PPB_VLAN_SET [37][7].PRI_AS[16:16]	1
0xBB00C4DC	VLAN_PORT_PPB_VLAN_SET [37][7].PRI[15:13]	3
0xBB00C4DC	VLAN_PORT_PPB_VLAN_SET [37][7].VID_AS[12:12]	1
0xBB00C4DC	VLAN_PORT_PPB_VLAN_SET [37][7].VID[11:0]	12
0xBB00C4E0	VLAN_PORT_PPB_VLAN_SET [38][0].RESERVED[31:18]	14
0xBB00C4E0	VLAN_PORT_PPB_VLAN_SET [38][0].VLAN_TYPE[17:17]	1
0xBB00C4E0	VLAN_PORT_PPB_VLAN_SET [38][0].PRI_AS[16:16]	1
0xBB00C4E0	VLAN_PORT_PPB_VLAN_SET [38][0].PRI[15:13]	3
0xBB00C4E0	VLAN_PORT_PPB_VLAN_SET [38][0].VID_AS[12:12]	1
0xBB00C4E0	VLAN_PORT_PPB_VLAN_SET [38][0].VID[11:0]	12
0xBB00C4E4	VLAN_PORT_PPB_VLAN_SET [38][1].RESERVED[31:18]	14
0xBB00C4E4	VLAN_PORT_PPB_VLAN_SET [38][1].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C4E4	VLAN_PORT_PPB_VLAN_SET [38][1].PRI_AS[16:16]	1
0xBB00C4E4	VLAN_PORT_PPB_VLAN_SET [38][1].PRI[15:13]	3
0xBB00C4E4	VLAN_PORT_PPB_VLAN_SET [38][1].VID_AS[12:12]	1
0xBB00C4E4	VLAN_PORT_PPB_VLAN_SET [38][1].VID[11:0]	12
0xBB00C4E8	VLAN_PORT_PPB_VLAN_SET [38][2].RESERVED[31:18]	14
0xBB00C4E8	VLAN_PORT_PPB_VLAN_SET [38][2].VLAN_TYPE[17:17]	1
0xBB00C4E8	VLAN_PORT_PPB_VLAN_SET [38][2].PRI_AS[16:16]	1
0xBB00C4E8	VLAN_PORT_PPB_VLAN_SET [38][2].PRI[15:13]	3
0xBB00C4E8	VLAN_PORT_PPB_VLAN_SET [38][2].VID_AS[12:12]	1
0xBB00C4E8	VLAN_PORT_PPB_VLAN_SET [38][2].VID[11:0]	12
0xBB00C4EC	VLAN_PORT_PPB_VLAN_SET [38][3].RESERVED[31:18]	14
0xBB00C4EC	VLAN_PORT_PPB_VLAN_SET [38][3].VLAN_TYPE[17:17]	1
0xBB00C4EC	VLAN_PORT_PPB_VLAN_SET [38][3].PRI_AS[16:16]	1
0xBB00C4EC	VLAN_PORT_PPB_VLAN_SET [38][3].PRI[15:13]	3
0xBB00C4EC	VLAN_PORT_PPB_VLAN_SET [38][3].VID_AS[12:12]	1
0xBB00C4EC	VLAN_PORT_PPB_VLAN_SET [38][3].VID[11:0]	12
0xBB00C4F0	VLAN_PORT_PPB_VLAN_SET [38][4].RESERVED[31:18]	14
0xBB00C4F0	VLAN_PORT_PPB_VLAN_SET [38][4].VLAN_TYPE[17:17]	1
0xBB00C4F0	VLAN_PORT_PPB_VLAN_SET [38][4].PRI_AS[16:16]	1
0xBB00C4F0	VLAN_PORT_PPB_VLAN_SET [38][4].PRI[15:13]	3
0xBB00C4F0	VLAN_PORT_PPB_VLAN_SET [38][4].VID_AS[12:12]	1
0xBB00C4F0	VLAN_PORT_PPB_VLAN_SET [38][4].VID[11:0]	12
0xBB00C4F4	VLAN_PORT_PPB_VLAN_SET [38][5].RESERVED[31:18]	14
0xBB00C4F4	VLAN_PORT_PPB_VLAN_SET [38][5].VLAN_TYPE[17:17]	1
0xBB00C4F4	VLAN_PORT_PPB_VLAN_SET [38][5].PRI_AS[16:16]	1
0xBB00C4F4	VLAN_PORT_PPB_VLAN_SET [38][5].PRI[15:13]	3
0xBB00C4F4	VLAN_PORT_PPB_VLAN_SET [38][5].VID_AS[12:12]	1
0xBB00C4F4	VLAN_PORT_PPB_VLAN_SET [38][5].VID[11:0]	12
0xBB00C4F8	VLAN_PORT_PPB_VLAN_SET [38][6].RESERVED[31:18]	14
0xBB00C4F8	VLAN_PORT_PPB_VLAN_SET [38][6].VLAN_TYPE[17:17]	1
0xBB00C4F8	VLAN_PORT_PPB_VLAN_SET [38][6].PRI_AS[16:16]	1
0xBB00C4F8	VLAN_PORT_PPB_VLAN_SET [38][6].PRI[15:13]	3
0xBB00C4F8	VLAN_PORT_PPB_VLAN_SET [38][6].VID_AS[12:12]	1
0xBB00C4F8	VLAN_PORT_PPB_VLAN_SET [38][6].VID[11:0]	12
0xBB00C4FC	VLAN_PORT_PPB_VLAN_SET [38][7].RESERVED[31:18]	14
0xBB00C4FC	VLAN_PORT_PPB_VLAN_SET [38][7].VLAN_TYPE[17:17]	1
0xBB00C4FC	VLAN_PORT_PPB_VLAN_SET [38][7].PRI_AS[16:16]	1
0xBB00C4FC	VLAN_PORT_PPB_VLAN_SET [38][7].PRI[15:13]	3
0xBB00C4FC	VLAN_PORT_PPB_VLAN_SET [38][7].VID_AS[12:12]	1
0xBB00C4FC	VLAN_PORT_PPB_VLAN_SET [38][7].VID[11:0]	12
0xBB00C500	VLAN_PORT_PPB_VLAN_SET [39][0].RESERVED[31:18]	14
0xBB00C500	VLAN_PORT_PPB_VLAN_SET [39][0].VLAN_TYPE[17:17]	1
0xBB00C500	VLAN_PORT_PPB_VLAN_SET [39][0].PRI_AS[16:16]	1
0xBB00C500	VLAN_PORT_PPB_VLAN_SET [39][0].PRI[15:13]	3
0xBB00C500	VLAN_PORT_PPB_VLAN_SET [39][0].VID_AS[12:12]	1
0xBB00C500	VLAN_PORT_PPB_VLAN_SET [39][0].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C504	VLAN_PORT_PPB_VLAN_SET [39][1].RESERVED[31:18]	14
0xBB00C504	VLAN_PORT_PPB_VLAN_SET [39][1].VLAN_TYPE[17:17]	1
0xBB00C504	VLAN_PORT_PPB_VLAN_SET [39][1].PRI_AS[16:16]	1
0xBB00C504	VLAN_PORT_PPB_VLAN_SET [39][1].PRI[15:13]	3
0xBB00C504	VLAN_PORT_PPB_VLAN_SET [39][1].VID_AS[12:12]	1
0xBB00C504	VLAN_PORT_PPB_VLAN_SET [39][1].VID[11:0]	12
0xBB00C508	VLAN_PORT_PPB_VLAN_SET [39][2].RESERVED[31:18]	14
0xBB00C508	VLAN_PORT_PPB_VLAN_SET [39][2].VLAN_TYPE[17:17]	1
0xBB00C508	VLAN_PORT_PPB_VLAN_SET [39][2].PRI_AS[16:16]	1
0xBB00C508	VLAN_PORT_PPB_VLAN_SET [39][2].PRI[15:13]	3
0xBB00C508	VLAN_PORT_PPB_VLAN_SET [39][2].VID_AS[12:12]	1
0xBB00C508	VLAN_PORT_PPB_VLAN_SET [39][2].VID[11:0]	12
0xBB00C50C	VLAN_PORT_PPB_VLAN_SET [39][3].RESERVED[31:18]	14
0xBB00C50C	VLAN_PORT_PPB_VLAN_SET [39][3].VLAN_TYPE[17:17]	1
0xBB00C50C	VLAN_PORT_PPB_VLAN_SET [39][3].PRI_AS[16:16]	1
0xBB00C50C	VLAN_PORT_PPB_VLAN_SET [39][3].PRI[15:13]	3
0xBB00C50C	VLAN_PORT_PPB_VLAN_SET [39][3].VID_AS[12:12]	1
0xBB00C50C	VLAN_PORT_PPB_VLAN_SET [39][3].VID[11:0]	12
0xBB00C510	VLAN_PORT_PPB_VLAN_SET [39][4].RESERVED[31:18]	14
0xBB00C510	VLAN_PORT_PPB_VLAN_SET [39][4].VLAN_TYPE[17:17]	1
0xBB00C510	VLAN_PORT_PPB_VLAN_SET [39][4].PRI_AS[16:16]	1
0xBB00C510	VLAN_PORT_PPB_VLAN_SET [39][4].PRI[15:13]	3
0xBB00C510	VLAN_PORT_PPB_VLAN_SET [39][4].VID_AS[12:12]	1
0xBB00C510	VLAN_PORT_PPB_VLAN_SET [39][4].VID[11:0]	12
0xBB00C514	VLAN_PORT_PPB_VLAN_SET [39][5].RESERVED[31:18]	14
0xBB00C514	VLAN_PORT_PPB_VLAN_SET [39][5].VLAN_TYPE[17:17]	1
0xBB00C514	VLAN_PORT_PPB_VLAN_SET [39][5].PRI_AS[16:16]	1
0xBB00C514	VLAN_PORT_PPB_VLAN_SET [39][5].PRI[15:13]	3
0xBB00C514	VLAN_PORT_PPB_VLAN_SET [39][5].VID_AS[12:12]	1
0xBB00C514	VLAN_PORT_PPB_VLAN_SET [39][5].VID[11:0]	12
0xBB00C518	VLAN_PORT_PPB_VLAN_SET [39][6].RESERVED[31:18]	14
0xBB00C518	VLAN_PORT_PPB_VLAN_SET [39][6].VLAN_TYPE[17:17]	1
0xBB00C518	VLAN_PORT_PPB_VLAN_SET [39][6].PRI_AS[16:16]	1
0xBB00C518	VLAN_PORT_PPB_VLAN_SET [39][6].PRI[15:13]	3
0xBB00C518	VLAN_PORT_PPB_VLAN_SET [39][6].VID_AS[12:12]	1
0xBB00C518	VLAN_PORT_PPB_VLAN_SET [39][6].VID[11:0]	12
0xBB00C51C	VLAN_PORT_PPB_VLAN_SET [39][7].RESERVED[31:18]	14
0xBB00C51C	VLAN_PORT_PPB_VLAN_SET [39][7].VLAN_TYPE[17:17]	1
0xBB00C51C	VLAN_PORT_PPB_VLAN_SET [39][7].PRI_AS[16:16]	1
0xBB00C51C	VLAN_PORT_PPB_VLAN_SET [39][7].PRI[15:13]	3
0xBB00C51C	VLAN_PORT_PPB_VLAN_SET [39][7].VID_AS[12:12]	1
0xBB00C51C	VLAN_PORT_PPB_VLAN_SET [39][7].VID[11:0]	12
0xBB00C520	VLAN_PORT_PPB_VLAN_SET [40][0].RESERVED[31:18]	14
0xBB00C520	VLAN_PORT_PPB_VLAN_SET [40][0].VLAN_TYPE[17:17]	1
0xBB00C520	VLAN_PORT_PPB_VLAN_SET [40][0].PRI_AS[16:16]	1
0xBB00C520	VLAN_PORT_PPB_VLAN_SET [40][0].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C520	VLAN_PORT_PPB_VLAN_SET [40][0].VID_AS[12:12]	1
0xBB00C520	VLAN_PORT_PPB_VLAN_SET [40][0].VID[11:0]	12
0xBB00C524	VLAN_PORT_PPB_VLAN_SET [40][1].RESERVED[31:18]	14
0xBB00C524	VLAN_PORT_PPB_VLAN_SET [40][1].VLAN_TYPE[17:17]	1
0xBB00C524	VLAN_PORT_PPB_VLAN_SET [40][1].PRI_AS[16:16]	1
0xBB00C524	VLAN_PORT_PPB_VLAN_SET [40][1].PRI[15:13]	3
0xBB00C524	VLAN_PORT_PPB_VLAN_SET [40][1].VID_AS[12:12]	1
0xBB00C524	VLAN_PORT_PPB_VLAN_SET [40][1].VID[11:0]	12
0xBB00C528	VLAN_PORT_PPB_VLAN_SET [40][2].RESERVED[31:18]	14
0xBB00C528	VLAN_PORT_PPB_VLAN_SET [40][2].VLAN_TYPE[17:17]	1
0xBB00C528	VLAN_PORT_PPB_VLAN_SET [40][2].PRI_AS[16:16]	1
0xBB00C528	VLAN_PORT_PPB_VLAN_SET [40][2].PRI[15:13]	3
0xBB00C528	VLAN_PORT_PPB_VLAN_SET [40][2].VID_AS[12:12]	1
0xBB00C528	VLAN_PORT_PPB_VLAN_SET [40][2].VID[11:0]	12
0xBB00C52C	VLAN_PORT_PPB_VLAN_SET [40][3].RESERVED[31:18]	14
0xBB00C52C	VLAN_PORT_PPB_VLAN_SET [40][3].VLAN_TYPE[17:17]	1
0xBB00C52C	VLAN_PORT_PPB_VLAN_SET [40][3].PRI_AS[16:16]	1
0xBB00C52C	VLAN_PORT_PPB_VLAN_SET [40][3].PRI[15:13]	3
0xBB00C52C	VLAN_PORT_PPB_VLAN_SET [40][3].VID_AS[12:12]	1
0xBB00C52C	VLAN_PORT_PPB_VLAN_SET [40][3].VID[11:0]	12
0xBB00C530	VLAN_PORT_PPB_VLAN_SET [40][4].RESERVED[31:18]	14
0xBB00C530	VLAN_PORT_PPB_VLAN_SET [40][4].VLAN_TYPE[17:17]	1
0xBB00C530	VLAN_PORT_PPB_VLAN_SET [40][4].PRI_AS[16:16]	1
0xBB00C530	VLAN_PORT_PPB_VLAN_SET [40][4].PRI[15:13]	3
0xBB00C530	VLAN_PORT_PPB_VLAN_SET [40][4].VID_AS[12:12]	1
0xBB00C530	VLAN_PORT_PPB_VLAN_SET [40][4].VID[11:0]	12
0xBB00C534	VLAN_PORT_PPB_VLAN_SET [40][5].RESERVED[31:18]	14
0xBB00C534	VLAN_PORT_PPB_VLAN_SET [40][5].VLAN_TYPE[17:17]	1
0xBB00C534	VLAN_PORT_PPB_VLAN_SET [40][5].PRI_AS[16:16]	1
0xBB00C534	VLAN_PORT_PPB_VLAN_SET [40][5].PRI[15:13]	3
0xBB00C534	VLAN_PORT_PPB_VLAN_SET [40][5].VID_AS[12:12]	1
0xBB00C534	VLAN_PORT_PPB_VLAN_SET [40][5].VID[11:0]	12
0xBB00C538	VLAN_PORT_PPB_VLAN_SET [40][6].RESERVED[31:18]	14
0xBB00C538	VLAN_PORT_PPB_VLAN_SET [40][6].VLAN_TYPE[17:17]	1
0xBB00C538	VLAN_PORT_PPB_VLAN_SET [40][6].PRI_AS[16:16]	1
0xBB00C538	VLAN_PORT_PPB_VLAN_SET [40][6].PRI[15:13]	3
0xBB00C538	VLAN_PORT_PPB_VLAN_SET [40][6].VID_AS[12:12]	1
0xBB00C538	VLAN_PORT_PPB_VLAN_SET [40][6].VID[11:0]	12
0xBB00C53C	VLAN_PORT_PPB_VLAN_SET [40][7].RESERVED[31:18]	14
0xBB00C53C	VLAN_PORT_PPB_VLAN_SET [40][7].VLAN_TYPE[17:17]	1
0xBB00C53C	VLAN_PORT_PPB_VLAN_SET [40][7].PRI_AS[16:16]	1
0xBB00C53C	VLAN_PORT_PPB_VLAN_SET [40][7].PRI[15:13]	3
0xBB00C53C	VLAN_PORT_PPB_VLAN_SET [40][7].VID_AS[12:12]	1
0xBB00C53C	VLAN_PORT_PPB_VLAN_SET [40][7].VID[11:0]	12
0xBB00C540	VLAN_PORT_PPB_VLAN_SET [41][0].RESERVED[31:18]	14
0xBB00C540	VLAN_PORT_PPB_VLAN_SET [41][0].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C540	VLAN_PORT_PPB_VLAN_SET [41][0].PRI_AS[16:16]	1
0xBB00C540	VLAN_PORT_PPB_VLAN_SET [41][0].PRI[15:13]	3
0xBB00C540	VLAN_PORT_PPB_VLAN_SET [41][0].VID_AS[12:12]	1
0xBB00C540	VLAN_PORT_PPB_VLAN_SET [41][0].VID[11:0]	12
0xBB00C544	VLAN_PORT_PPB_VLAN_SET [41][1].RESERVED[31:18]	14
0xBB00C544	VLAN_PORT_PPB_VLAN_SET [41][1].VLAN_TYPE[17:17]	1
0xBB00C544	VLAN_PORT_PPB_VLAN_SET [41][1].PRI_AS[16:16]	1
0xBB00C544	VLAN_PORT_PPB_VLAN_SET [41][1].PRI[15:13]	3
0xBB00C544	VLAN_PORT_PPB_VLAN_SET [41][1].VID_AS[12:12]	1
0xBB00C544	VLAN_PORT_PPB_VLAN_SET [41][1].VID[11:0]	12
0xBB00C548	VLAN_PORT_PPB_VLAN_SET [41][2].RESERVED[31:18]	14
0xBB00C548	VLAN_PORT_PPB_VLAN_SET [41][2].VLAN_TYPE[17:17]	1
0xBB00C548	VLAN_PORT_PPB_VLAN_SET [41][2].PRI_AS[16:16]	1
0xBB00C548	VLAN_PORT_PPB_VLAN_SET [41][2].PRI[15:13]	3
0xBB00C548	VLAN_PORT_PPB_VLAN_SET [41][2].VID_AS[12:12]	1
0xBB00C548	VLAN_PORT_PPB_VLAN_SET [41][2].VID[11:0]	12
0xBB00C54C	VLAN_PORT_PPB_VLAN_SET [41][3].RESERVED[31:18]	14
0xBB00C54C	VLAN_PORT_PPB_VLAN_SET [41][3].VLAN_TYPE[17:17]	1
0xBB00C54C	VLAN_PORT_PPB_VLAN_SET [41][3].PRI_AS[16:16]	1
0xBB00C54C	VLAN_PORT_PPB_VLAN_SET [41][3].PRI[15:13]	3
0xBB00C54C	VLAN_PORT_PPB_VLAN_SET [41][3].VID_AS[12:12]	1
0xBB00C54C	VLAN_PORT_PPB_VLAN_SET [41][3].VID[11:0]	12
0xBB00C550	VLAN_PORT_PPB_VLAN_SET [41][4].RESERVED[31:18]	14
0xBB00C550	VLAN_PORT_PPB_VLAN_SET [41][4].VLAN_TYPE[17:17]	1
0xBB00C550	VLAN_PORT_PPB_VLAN_SET [41][4].PRI_AS[16:16]	1
0xBB00C550	VLAN_PORT_PPB_VLAN_SET [41][4].PRI[15:13]	3
0xBB00C550	VLAN_PORT_PPB_VLAN_SET [41][4].VID_AS[12:12]	1
0xBB00C550	VLAN_PORT_PPB_VLAN_SET [41][4].VID[11:0]	12
0xBB00C554	VLAN_PORT_PPB_VLAN_SET [41][5] RESERVED[31:18]	14
0xBB00C554	VLAN_PORT_PPB_VLAN_SET [41][5].VLAN_TYPE[17:17]	1
0xBB00C554	VLAN_PORT_PPB_VLAN_SET [41][5].PRI_AS[16:16]	1
0xBB00C554	VLAN_PORT_PPB_VLAN_SET [41][5].PRI[15:13]	3
0xBB00C554	VLAN_PORT_PPB_VLAN_SET [41][5].VID_AS[12:12]	1
0xBB00C554	VLAN_PORT_PPB_VLAN_SET [41][5].VID[11:0]	12
0xBB00C558	VLAN_PORT_PPB_VLAN_SET [41][6].RESERVED[31:18]	14
0xBB00C558	VLAN_PORT_PPB_VLAN_SET [41][6].VLAN_TYPE[17:17]	1
0xBB00C558	VLAN_PORT_PPB_VLAN_SET [41][6].PRI_AS[16:16]	1
0xBB00C558	VLAN_PORT_PPB_VLAN_SET [41][6].PRI[15:13]	3
0xBB00C558	VLAN_PORT_PPB_VLAN_SET [41][6].VID_AS[12:12]	1
0xBB00C558	VLAN_PORT_PPB_VLAN_SET [41][6].VID[11:0]	12
0xBB00C55C	VLAN_PORT_PPB_VLAN_SET [41][7].RESERVED[31:18]	14
0xBB00C55C	VLAN_PORT_PPB_VLAN_SET [41][7].VLAN_TYPE[17:17]	1
0xBB00C55C	VLAN_PORT_PPB_VLAN_SET [41][7].PRI_AS[16:16]	1
0xBB00C55C	VLAN_PORT_PPB_VLAN_SET [41][7].PRI[15:13]	3
0xBB00C55C	VLAN_PORT_PPB_VLAN_SET [41][7].VID_AS[12:12]	1
0xBB00C55C	VLAN_PORT_PPB_VLAN_SET [41][7].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C560	VLAN_PORT_PPB_VLAN_SET [42][0].RESERVED[31:18]	14
0xBB00C560	VLAN_PORT_PPB_VLAN_SET [42][0].VLAN_TYPE[17:17]	1
0xBB00C560	VLAN_PORT_PPB_VLAN_SET [42][0].PRI_AS[16:16]	1
0xBB00C560	VLAN_PORT_PPB_VLAN_SET [42][0].PRI[15:13]	3
0xBB00C560	VLAN_PORT_PPB_VLAN_SET [42][0].VID_AS[12:12]	1
0xBB00C560	VLAN_PORT_PPB_VLAN_SET [42][0].VID[11:0]	12
0xBB00C564	VLAN_PORT_PPB_VLAN_SET [42][1].RESERVED[31:18]	14
0xBB00C564	VLAN_PORT_PPB_VLAN_SET [42][1].VLAN_TYPE[17:17]	1
0xBB00C564	VLAN_PORT_PPB_VLAN_SET [42][1].PRI_AS[16:16]	1
0xBB00C564	VLAN_PORT_PPB_VLAN_SET [42][1].PRI[15:13]	3
0xBB00C564	VLAN_PORT_PPB_VLAN_SET [42][1].VID_AS[12:12]	1
0xBB00C564	VLAN_PORT_PPB_VLAN_SET [42][1].VID[11:0]	12
0xBB00C568	VLAN_PORT_PPB_VLAN_SET [42][2].RESERVED[31:18]	14
0xBB00C568	VLAN_PORT_PPB_VLAN_SET [42][2].VLAN_TYPE[17:17]	1
0xBB00C568	VLAN_PORT_PPB_VLAN_SET [42][2].PRI_AS[16:16]	1
0xBB00C568	VLAN_PORT_PPB_VLAN_SET [42][2].PRI[15:13]	3
0xBB00C568	VLAN_PORT_PPB_VLAN_SET [42][2].VID_AS[12:12]	1
0xBB00C568	VLAN_PORT_PPB_VLAN_SET [42][2].VID[11:0]	12
0xBB00C56C	VLAN_PORT_PPB_VLAN_SET [42][3].RESERVED[31:18]	14
0xBB00C56C	VLAN_PORT_PPB_VLAN_SET [42][3].VLAN_TYPE[17:17]	1
0xBB00C56C	VLAN_PORT_PPB_VLAN_SET [42][3].PRI_AS[16:16]	1
0xBB00C56C	VLAN_PORT_PPB_VLAN_SET [42][3].PRI[15:13]	3
0xBB00C56C	VLAN_PORT_PPB_VLAN_SET [42][3].VID_AS[12:12]	1
0xBB00C56C	VLAN_PORT_PPB_VLAN_SET [42][3].VID[11:0]	12
0xBB00C570	VLAN_PORT_PPB_VLAN_SET [42][4].RESERVED[31:18]	14
0xBB00C570	VLAN_PORT_PPB_VLAN_SET [42][4].VLAN_TYPE[17:17]	1
0xBB00C570	VLAN_PORT_PPB_VLAN_SET [42][4].PRI_AS[16:16]	1
0xBB00C570	VLAN_PORT_PPB_VLAN_SET [42][4].PRI[15:13]	3
0xBB00C570	VLAN_PORT_PPB_VLAN_SET [42][4].VID_AS[12:12]	1
0xBB00C570	VLAN_PORT_PPB_VLAN_SET [42][4].VID[11:0]	12
0xBB00C574	VLAN_PORT_PPB_VLAN_SET [42][5].RESERVED[31:18]	14
0xBB00C574	VLAN_PORT_PPB_VLAN_SET [42][5].VLAN_TYPE[17:17]	1
0xBB00C574	VLAN_PORT_PPB_VLAN_SET [42][5].PRI_AS[16:16]	1
0xBB00C574	VLAN_PORT_PPB_VLAN_SET [42][5].PRI[15:13]	3
0xBB00C574	VLAN_PORT_PPB_VLAN_SET [42][5].VID_AS[12:12]	1
0xBB00C574	VLAN_PORT_PPB_VLAN_SET [42][5].VID[11:0]	12
0xBB00C578	VLAN_PORT_PPB_VLAN_SET [42][6].RESERVED[31:18]	14
0xBB00C578	VLAN_PORT_PPB_VLAN_SET [42][6].VLAN_TYPE[17:17]	1
0xBB00C578	VLAN_PORT_PPB_VLAN_SET [42][6].PRI_AS[16:16]	1
0xBB00C578	VLAN_PORT_PPB_VLAN_SET [42][6].PRI[15:13]	3
0xBB00C578	VLAN_PORT_PPB_VLAN_SET [42][6].VID_AS[12:12]	1
0xBB00C578	VLAN_PORT_PPB_VLAN_SET [42][6].VID[11:0]	12
0xBB00C57C	VLAN_PORT_PPB_VLAN_SET [42][7].RESERVED[31:18]	14
0xBB00C57C	VLAN_PORT_PPB_VLAN_SET [42][7].VLAN_TYPE[17:17]	1
0xBB00C57C	VLAN_PORT_PPB_VLAN_SET [42][7].PRI_AS[16:16]	1
0xBB00C57C	VLAN_PORT_PPB_VLAN_SET [42][7].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C57C	VLAN_PORT_PPB_VLAN_SET [42][7].VID_AS[12:12]	1
0xBB00C57C	VLAN_PORT_PPB_VLAN_SET [42][7].VID[11:0]	12
0xBB00C580	VLAN_PORT_PPB_VLAN_SET [43][0].RESERVED[31:18]	14
0xBB00C580	VLAN_PORT_PPB_VLAN_SET [43][0].VLAN_TYPE[17:17]	1
0xBB00C580	VLAN_PORT_PPB_VLAN_SET [43][0].PRI_AS[16:16]	1
0xBB00C580	VLAN_PORT_PPB_VLAN_SET [43][0].PRI[15:13]	3
0xBB00C580	VLAN_PORT_PPB_VLAN_SET [43][0].VID_AS[12:12]	1
0xBB00C580	VLAN_PORT_PPB_VLAN_SET [43][0].VID[11:0]	12
0xBB00C584	VLAN_PORT_PPB_VLAN_SET [43][1].RESERVED[31:18]	14
0xBB00C584	VLAN_PORT_PPB_VLAN_SET [43][1].VLAN_TYPE[17:17]	1
0xBB00C584	VLAN_PORT_PPB_VLAN_SET [43][1].PRI_AS[16:16]	1
0xBB00C584	VLAN_PORT_PPB_VLAN_SET [43][1].PRI[15:13]	3
0xBB00C584	VLAN_PORT_PPB_VLAN_SET [43][1].VID_AS[12:12]	1
0xBB00C584	VLAN_PORT_PPB_VLAN_SET [43][1].VID[11:0]	12
0xBB00C588	VLAN_PORT_PPB_VLAN_SET [43][2].RESERVED[31:18]	14
0xBB00C588	VLAN_PORT_PPB_VLAN_SET [43][2].VLAN_TYPE[17:17]	1
0xBB00C588	VLAN_PORT_PPB_VLAN_SET [43][2].PRI_AS[16:16]	1
0xBB00C588	VLAN_PORT_PPB_VLAN_SET [43][2].PRI[15:13]	3
0xBB00C588	VLAN_PORT_PPB_VLAN_SET [43][2].VID_AS[12:12]	1
0xBB00C588	VLAN_PORT_PPB_VLAN_SET [43][2].VID[11:0]	12
0xBB00C58C	VLAN_PORT_PPB_VLAN_SET [43][3].RESERVED[31:18]	14
0xBB00C58C	VLAN_PORT_PPB_VLAN_SET [43][3].VLAN_TYPE[17:17]	1
0xBB00C58C	VLAN_PORT_PPB_VLAN_SET [43][3].PRI_AS[16:16]	1
0xBB00C58C	VLAN_PORT_PPB_VLAN_SET [43][3].PRI[15:13]	3
0xBB00C58C	VLAN_PORT_PPB_VLAN_SET [43][3].VID_AS[12:12]	1
0xBB00C58C	VLAN_PORT_PPB_VLAN_SET [43][3].VID[11:0]	12
0xBB00C590	VLAN_PORT_PPB_VLAN_SET [43][4].RESERVED[31:18]	14
0xBB00C590	VLAN_PORT_PPB_VLAN_SET [43][4].VLAN_TYPE[17:17]	1
0xBB00C590	VLAN_PORT_PPB_VLAN_SET [43][4].PRI_AS[16:16]	1
0xBB00C590	VLAN_PORT_PPB_VLAN_SET [43][4].PRI[15:13]	3
0xBB00C590	VLAN_PORT_PPB_VLAN_SET [43][4].VID_AS[12:12]	1
0xBB00C590	VLAN_PORT_PPB_VLAN_SET [43][4].VID[11:0]	12
0xBB00C594	VLAN_PORT_PPB_VLAN_SET [43][5].RESERVED[31:18]	14
0xBB00C594	VLAN_PORT_PPB_VLAN_SET [43][5].VLAN_TYPE[17:17]	1
0xBB00C594	VLAN_PORT_PPB_VLAN_SET [43][5].PRI_AS[16:16]	1
0xBB00C594	VLAN_PORT_PPB_VLAN_SET [43][5].PRI[15:13]	3
0xBB00C594	VLAN_PORT_PPB_VLAN_SET [43][5].VID_AS[12:12]	1
0xBB00C594	VLAN_PORT_PPB_VLAN_SET [43][5].VID[11:0]	12
0xBB00C598	VLAN_PORT_PPB_VLAN_SET [43][6].RESERVED[31:18]	14
0xBB00C598	VLAN_PORT_PPB_VLAN_SET [43][6].VLAN_TYPE[17:17]	1
0xBB00C598	VLAN_PORT_PPB_VLAN_SET [43][6].PRI_AS[16:16]	1
0xBB00C598	VLAN_PORT_PPB_VLAN_SET [43][6].PRI[15:13]	3
0xBB00C598	VLAN_PORT_PPB_VLAN_SET [43][6].VID_AS[12:12]	1
0xBB00C598	VLAN_PORT_PPB_VLAN_SET [43][6].VID[11:0]	12
0xBB00C59C	VLAN_PORT_PPB_VLAN_SET [43][7].RESERVED[31:18]	14
0xBB00C59C	VLAN_PORT_PPB_VLAN_SET [43][7].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C59C	VLAN_PORT_PPB_VLAN_SET [43][7].PRI_AS[16:16]	1
0xBB00C59C	VLAN_PORT_PPB_VLAN_SET [43][7].PRI[15:13]	3
0xBB00C59C	VLAN_PORT_PPB_VLAN_SET [43][7].VID_AS[12:12]	1
0xBB00C59C	VLAN_PORT_PPB_VLAN_SET [43][7].VID[11:0]	12
0xBB00C5A0	VLAN_PORT_PPB_VLAN_SET [44][0].RESERVED[31:18]	14
0xBB00C5A0	VLAN_PORT_PPB_VLAN_SET [44][0].VLAN_TYPE[17:17]	1
0xBB00C5A0	VLAN_PORT_PPB_VLAN_SET [44][0].PRI_AS[16:16]	1
0xBB00C5A0	VLAN_PORT_PPB_VLAN_SET [44][0].PRI[15:13]	3
0xBB00C5A0	VLAN_PORT_PPB_VLAN_SET [44][0].VID_AS[12:12]	1
0xBB00C5A0	VLAN_PORT_PPB_VLAN_SET [44][0].VID[11:0]	12
0xBB00C5A4	VLAN_PORT_PPB_VLAN_SET [44][1].RESERVED[31:18]	14
0xBB00C5A4	VLAN_PORT_PPB_VLAN_SET [44][1].VLAN_TYPE[17:17]	1
0xBB00C5A4	VLAN_PORT_PPB_VLAN_SET [44][1].PRI_AS[16:16]	1
0xBB00C5A4	VLAN_PORT_PPB_VLAN_SET [44][1].PRI[15:13]	3
0xBB00C5A4	VLAN_PORT_PPB_VLAN_SET [44][1].VID_AS[12:12]	1
0xBB00C5A4	VLAN_PORT_PPB_VLAN_SET [44][1].VID[11:0]	12
0xBB00C5A8	VLAN_PORT_PPB_VLAN_SET [44][2].RESERVED[31:18]	14
0xBB00C5A8	VLAN_PORT_PPB_VLAN_SET [44][2].VLAN_TYPE[17:17]	1
0xBB00C5A8	VLAN_PORT_PPB_VLAN_SET [44][2].PRI_AS[16:16]	1
0xBB00C5A8	VLAN_PORT_PPB_VLAN_SET [44][2].PRI[15:13]	3
0xBB00C5A8	VLAN_PORT_PPB_VLAN_SET [44][2].VID_AS[12:12]	1
0xBB00C5A8	VLAN_PORT_PPB_VLAN_SET [44][2].VID[11:0]	12
0xBB00C5AC	VLAN_PORT_PPB_VLAN_SET [44][3].RESERVED[31:18]	14
0xBB00C5AC	VLAN_PORT_PPB_VLAN_SET [44][3].VLAN_TYPE[17:17]	1
0xBB00C5AC	VLAN_PORT_PPB_VLAN_SET [44][3].PRI_AS[16:16]	1
0xBB00C5AC	VLAN_PORT_PPB_VLAN_SET [44][3].PRI[15:13]	3
0xBB00C5AC	VLAN_PORT_PPB_VLAN_SET [44][3].VID_AS[12:12]	1
0xBB00C5AC	VLAN_PORT_PPB_VLAN_SET [44][3].VID[11:0]	12
0xBB00C5B0	VLAN_PORT_PPB_VLAN_SET [44][4].RESERVED[31:18]	14
0xBB00C5B0	VLAN_PORT_PPB_VLAN_SET [44][4].VLAN_TYPE[17:17]	1
0xBB00C5B0	VLAN_PORT_PPB_VLAN_SET [44][4].PRI_AS[16:16]	1
0xBB00C5B0	VLAN_PORT_PPB_VLAN_SET [44][4].PRI[15:13]	3
0xBB00C5B0	VLAN_PORT_PPB_VLAN_SET [44][4].VID_AS[12:12]	1
0xBB00C5B0	VLAN_PORT_PPB_VLAN_SET [44][4].VID[11:0]	12
0xBB00C5B4	VLAN_PORT_PPB_VLAN_SET [44][5].RESERVED[31:18]	14
0xBB00C5B4	VLAN_PORT_PPB_VLAN_SET [44][5].VLAN_TYPE[17:17]	1
0xBB00C5B4	VLAN_PORT_PPB_VLAN_SET [44][5].PRI_AS[16:16]	1
0xBB00C5B4	VLAN_PORT_PPB_VLAN_SET [44][5].PRI[15:13]	3
0xBB00C5B4	VLAN_PORT_PPB_VLAN_SET [44][5].VID_AS[12:12]	1
0xBB00C5B4	VLAN_PORT_PPB_VLAN_SET [44][5].VID[11:0]	12
0xBB00C5B8	VLAN_PORT_PPB_VLAN_SET [44][6].RESERVED[31:18]	14
0xBB00C5B8	VLAN_PORT_PPB_VLAN_SET [44][6].VLAN_TYPE[17:17]	1
0xBB00C5B8	VLAN_PORT_PPB_VLAN_SET [44][6].PRI_AS[16:16]	1
0xBB00C5B8	VLAN_PORT_PPB_VLAN_SET [44][6].PRI[15:13]	3
0xBB00C5B8	VLAN_PORT_PPB_VLAN_SET [44][6].VID_AS[12:12]	1
0xBB00C5B8	VLAN_PORT_PPB_VLAN_SET [44][6].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C5BC	VLAN_PORT_PPB_VLAN_SET [44][7].RESERVED[31:18]	14
0xBB00C5BC	VLAN_PORT_PPB_VLAN_SET [44][7].VLAN_TYPE[17:17]	1
0xBB00C5BC	VLAN_PORT_PPB_VLAN_SET [44][7].PRI_AS[16:16]	1
0xBB00C5BC	VLAN_PORT_PPB_VLAN_SET [44][7].PRI[15:13]	3
0xBB00C5BC	VLAN_PORT_PPB_VLAN_SET [44][7].VID_AS[12:12]	1
0xBB00C5BC	VLAN_PORT_PPB_VLAN_SET [44][7].VID[11:0]	12
0xBB00C5C0	VLAN_PORT_PPB_VLAN_SET [45][0].RESERVED[31:18]	14
0xBB00C5C0	VLAN_PORT_PPB_VLAN_SET [45][0].VLAN_TYPE[17:17]	1
0xBB00C5C0	VLAN_PORT_PPB_VLAN_SET [45][0].PRI_AS[16:16]	1
0xBB00C5C0	VLAN_PORT_PPB_VLAN_SET [45][0].PRI[15:13]	3
0xBB00C5C0	VLAN_PORT_PPB_VLAN_SET [45][0].VID_AS[12:12]	1
0xBB00C5C0	VLAN_PORT_PPB_VLAN_SET [45][0].VID[11:0]	12
0xBB00C5C4	VLAN_PORT_PPB_VLAN_SET [45][1].RESERVED[31:18]	14
0xBB00C5C4	VLAN_PORT_PPB_VLAN_SET [45][1].VLAN_TYPE[17:17]	1
0xBB00C5C4	VLAN_PORT_PPB_VLAN_SET [45][1].PRI_AS[16:16]	1
0xBB00C5C4	VLAN_PORT_PPB_VLAN_SET [45][1].PRI[15:13]	3
0xBB00C5C4	VLAN_PORT_PPB_VLAN_SET [45][1].VID_AS[12:12]	1
0xBB00C5C4	VLAN_PORT_PPB_VLAN_SET [45][1].VID[11:0]	12
0xBB00C5C8	VLAN_PORT_PPB_VLAN_SET [45][2].RESERVED[31:18]	14
0xBB00C5C8	VLAN_PORT_PPB_VLAN_SET [45][2].VLAN_TYPE[17:17]	1
0xBB00C5C8	VLAN_PORT_PPB_VLAN_SET [45][2].PRI_AS[16:16]	1
0xBB00C5C8	VLAN_PORT_PPB_VLAN_SET [45][2].PRI[15:13]	3
0xBB00C5C8	VLAN_PORT_PPB_VLAN_SET [45][2].VID_AS[12:12]	1
0xBB00C5C8	VLAN_PORT_PPB_VLAN_SET [45][2].VID[11:0]	12
0xBB00C5CC	VLAN_PORT_PPB_VLAN_SET [45][3].RESERVED[31:18]	14
0xBB00C5CC	VLAN_PORT_PPB_VLAN_SET [45][3].VLAN_TYPE[17:17]	1
0xBB00C5CC	VLAN_PORT_PPB_VLAN_SET [45][3].PRI_AS[16:16]	1
0xBB00C5CC	VLAN_PORT_PPB_VLAN_SET [45][3].PRI[15:13]	3
0xBB00C5CC	VLAN_PORT_PPB_VLAN_SET [45][3].VID_AS[12:12]	1
0xBB00C5CC	VLAN_PORT_PPB_VLAN_SET [45][3].VID[11:0]	12
0xBB00C5D0	VLAN_PORT_PPB_VLAN_SET [45][4].RESERVED[31:18]	14
0xBB00C5D0	VLAN_PORT_PPB_VLAN_SET [45][4].VLAN_TYPE[17:17]	1
0xBB00C5D0	VLAN_PORT_PPB_VLAN_SET [45][4].PRI_AS[16:16]	1
0xBB00C5D0	VLAN_PORT_PPB_VLAN_SET [45][4].PRI[15:13]	3
0xBB00C5D0	VLAN_PORT_PPB_VLAN_SET [45][4].VID_AS[12:12]	1
0xBB00C5D0	VLAN_PORT_PPB_VLAN_SET [45][4].VID[11:0]	12
0xBB00C5D4	VLAN_PORT_PPB_VLAN_SET [45][5].RESERVED[31:18]	14
0xBB00C5D4	VLAN_PORT_PPB_VLAN_SET [45][5].VLAN_TYPE[17:17]	1
0xBB00C5D4	VLAN_PORT_PPB_VLAN_SET [45][5].PRI_AS[16:16]	1
0xBB00C5D4	VLAN_PORT_PPB_VLAN_SET [45][5].PRI[15:13]	3
0xBB00C5D4	VLAN_PORT_PPB_VLAN_SET [45][5].VID_AS[12:12]	1
0xBB00C5D4	VLAN_PORT_PPB_VLAN_SET [45][5].VID[11:0]	12
0xBB00C5D8	VLAN_PORT_PPB_VLAN_SET [45][6].RESERVED[31:18]	14
0xBB00C5D8	VLAN_PORT_PPB_VLAN_SET [45][6].VLAN_TYPE[17:17]	1
0xBB00C5D8	VLAN_PORT_PPB_VLAN_SET [45][6].PRI_AS[16:16]	1
0xBB00C5D8	VLAN_PORT_PPB_VLAN_SET [45][6].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C5D8	VLAN_PORT_PPB_VLAN_SET [45][6].VID_AS[12:12]	1
0xBB00C5D8	VLAN_PORT_PPB_VLAN_SET [45][6].VID[11:0]	12
0xBB00C5DC	VLAN_PORT_PPB_VLAN_SET [45][7].RESERVED[31:18]	14
0xBB00C5DC	VLAN_PORT_PPB_VLAN_SET [45][7].VLAN_TYPE[17:17]	1
0xBB00C5DC	VLAN_PORT_PPB_VLAN_SET [45][7].PRI_AS[16:16]	1
0xBB00C5DC	VLAN_PORT_PPB_VLAN_SET [45][7].PRI[15:13]	3
0xBB00C5DC	VLAN_PORT_PPB_VLAN_SET [45][7].VID_AS[12:12]	1
0xBB00C5DC	VLAN_PORT_PPB_VLAN_SET [45][7].VID[11:0]	12
0xBB00C5E0	VLAN_PORT_PPB_VLAN_SET [46][0].RESERVED[31:18]	14
0xBB00C5E0	VLAN_PORT_PPB_VLAN_SET [46][0].VLAN_TYPE[17:17]	1
0xBB00C5E0	VLAN_PORT_PPB_VLAN_SET [46][0].PRI_AS[16:16]	1
0xBB00C5E0	VLAN_PORT_PPB_VLAN_SET [46][0].PRI[15:13]	3
0xBB00C5E0	VLAN_PORT_PPB_VLAN_SET [46][0].VID_AS[12:12]	1
0xBB00C5E0	VLAN_PORT_PPB_VLAN_SET [46][0].VID[11:0]	12
0xBB00C5E4	VLAN_PORT_PPB_VLAN_SET [46][1].RESERVED[31:18]	14
0xBB00C5E4	VLAN_PORT_PPB_VLAN_SET [46][1].VLAN_TYPE[17:17]	1
0xBB00C5E4	VLAN_PORT_PPB_VLAN_SET [46][1].PRI_AS[16:16]	1
0xBB00C5E4	VLAN_PORT_PPB_VLAN_SET [46][1].PRI[15:13]	3
0xBB00C5E4	VLAN_PORT_PPB_VLAN_SET [46][1].VID_AS[12:12]	1
0xBB00C5E4	VLAN_PORT_PPB_VLAN_SET [46][1].VID[11:0]	12
0xBB00C5E8	VLAN_PORT_PPB_VLAN_SET [46][2].RESERVED[31:18]	14
0xBB00C5E8	VLAN_PORT_PPB_VLAN_SET [46][2].VLAN_TYPE[17:17]	1
0xBB00C5E8	VLAN_PORT_PPB_VLAN_SET [46][2].PRI_AS[16:16]	1
0xBB00C5E8	VLAN_PORT_PPB_VLAN_SET [46][2].PRI[15:13]	3
0xBB00C5E8	VLAN_PORT_PPB_VLAN_SET [46][2].VID_AS[12:12]	1
0xBB00C5E8	VLAN_PORT_PPB_VLAN_SET [46][2].VID[11:0]	12
0xBB00C5EC	VLAN_PORT_PPB_VLAN_SET [46][3].RESERVED[31:18]	14
0xBB00C5EC	VLAN_PORT_PPB_VLAN_SET [46][3].VLAN_TYPE[17:17]	1
0xBB00C5EC	VLAN_PORT_PPB_VLAN_SET [46][3].PRI_AS[16:16]	1
0xBB00C5EC	VLAN_PORT_PPB_VLAN_SET [46][3].PRI[15:13]	3
0xBB00C5EC	VLAN_PORT_PPB_VLAN_SET [46][3].VID_AS[12:12]	1
0xBB00C5EC	VLAN_PORT_PPB_VLAN_SET [46][3].VID[11:0]	12
0xBB00C5F0	VLAN_PORT_PPB_VLAN_SET [46][4].RESERVED[31:18]	14
0xBB00C5F0	VLAN_PORT_PPB_VLAN_SET [46][4].VLAN_TYPE[17:17]	1
0xBB00C5F0	VLAN_PORT_PPB_VLAN_SET [46][4].PRI_AS[16:16]	1
0xBB00C5F0	VLAN_PORT_PPB_VLAN_SET [46][4].PRI[15:13]	3
0xBB00C5F0	VLAN_PORT_PPB_VLAN_SET [46][4].VID_AS[12:12]	1
0xBB00C5F0	VLAN_PORT_PPB_VLAN_SET [46][4].VID[11:0]	12
0xBB00C5F4	VLAN_PORT_PPB_VLAN_SET [46][5].RESERVED[31:18]	14
0xBB00C5F4	VLAN_PORT_PPB_VLAN_SET [46][5].VLAN_TYPE[17:17]	1
0xBB00C5F4	VLAN_PORT_PPB_VLAN_SET [46][5].PRI_AS[16:16]	1
0xBB00C5F4	VLAN_PORT_PPB_VLAN_SET [46][5].PRI[15:13]	3
0xBB00C5F4	VLAN_PORT_PPB_VLAN_SET [46][5].VID_AS[12:12]	1
0xBB00C5F4	VLAN_PORT_PPB_VLAN_SET [46][5].VID[11:0]	12
0xBB00C5F8	VLAN_PORT_PPB_VLAN_SET [46][6].RESERVED[31:18]	14
0xBB00C5F8	VLAN_PORT_PPB_VLAN_SET [46][6].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C5F8	VLAN_PORT_PPB_VLAN_SET [46][6].PRI_AS[16:16]	1
0xBB00C5F8	VLAN_PORT_PPB_VLAN_SET [46][6].PRI[15:13]	3
0xBB00C5F8	VLAN_PORT_PPB_VLAN_SET [46][6].VID_AS[12:12]	1
0xBB00C5F8	VLAN_PORT_PPB_VLAN_SET [46][6].VID[11:0]	12
0xBB00C5FC	VLAN_PORT_PPB_VLAN_SET [46][7].RESERVED[31:18]	14
0xBB00C5FC	VLAN_PORT_PPB_VLAN_SET [46][7].VLAN_TYPE[17:17]	1
0xBB00C5FC	VLAN_PORT_PPB_VLAN_SET [46][7].PRI_AS[16:16]	1
0xBB00C5FC	VLAN_PORT_PPB_VLAN_SET [46][7].PRI[15:13]	3
0xBB00C5FC	VLAN_PORT_PPB_VLAN_SET [46][7].VID_AS[12:12]	1
0xBB00C5FC	VLAN_PORT_PPB_VLAN_SET [46][7].VID[11:0]	12
0xBB00C600	VLAN_PORT_PPB_VLAN_SET [47][0].RESERVED[31:18]	14
0xBB00C600	VLAN_PORT_PPB_VLAN_SET [47][0].VLAN_TYPE[17:17]	1
0xBB00C600	VLAN_PORT_PPB_VLAN_SET [47][0].PRI_AS[16:16]	1
0xBB00C600	VLAN_PORT_PPB_VLAN_SET [47][0].PRI[15:13]	3
0xBB00C600	VLAN_PORT_PPB_VLAN_SET [47][0].VID_AS[12:12]	1
0xBB00C600	VLAN_PORT_PPB_VLAN_SET [47][0].VID[11:0]	12
0xBB00C604	VLAN_PORT_PPB_VLAN_SET [47][1].RESERVED[31:18]	14
0xBB00C604	VLAN_PORT_PPB_VLAN_SET [47][1].VLAN_TYPE[17:17]	1
0xBB00C604	VLAN_PORT_PPB_VLAN_SET [47][1].PRI_AS[16:16]	1
0xBB00C604	VLAN_PORT_PPB_VLAN_SET [47][1].PRI[15:13]	3
0xBB00C604	VLAN_PORT_PPB_VLAN_SET [47][1].VID_AS[12:12]	1
0xBB00C604	VLAN_PORT_PPB_VLAN_SET [47][1].VID[11:0]	12
0xBB00C608	VLAN_PORT_PPB_VLAN_SET [47][2].RESERVED[31:18]	14
0xBB00C608	VLAN_PORT_PPB_VLAN_SET [47][2].VLAN_TYPE[17:17]	1
0xBB00C608	VLAN_PORT_PPB_VLAN_SET [47][2].PRI_AS[16:16]	1
0xBB00C608	VLAN_PORT_PPB_VLAN_SET [47][2].PRI[15:13]	3
0xBB00C608	VLAN_PORT_PPB_VLAN_SET [47][2].VID_AS[12:12]	1
0xBB00C608	VLAN_PORT_PPB_VLAN_SET [47][2].VID[11:0]	12
0xBB00C60C	VLAN_PORT_PPB_VLAN_SET [47][3].RESERVED[31:18]	14
0xBB00C60C	VLAN_PORT_PPB_VLAN_SET [47][3].VLAN_TYPE[17:17]	1
0xBB00C60C	VLAN_PORT_PPB_VLAN_SET [47][3].PRI_AS[16:16]	1
0xBB00C60C	VLAN_PORT_PPB_VLAN_SET [47][3].PRI[15:13]	3
0xBB00C60C	VLAN_PORT_PPB_VLAN_SET [47][3].VID_AS[12:12]	1
0xBB00C60C	VLAN_PORT_PPB_VLAN_SET [47][3].VID[11:0]	12
0xBB00C610	VLAN_PORT_PPB_VLAN_SET [47][4].RESERVED[31:18]	14
0xBB00C610	VLAN_PORT_PPB_VLAN_SET [47][4].VLAN_TYPE[17:17]	1
0xBB00C610	VLAN_PORT_PPB_VLAN_SET [47][4].PRI_AS[16:16]	1
0xBB00C610	VLAN_PORT_PPB_VLAN_SET [47][4].PRI[15:13]	3
0xBB00C610	VLAN_PORT_PPB_VLAN_SET [47][4].VID_AS[12:12]	1
0xBB00C610	VLAN_PORT_PPB_VLAN_SET [47][4].VID[11:0]	12
0xBB00C614	VLAN_PORT_PPB_VLAN_SET [47][5].RESERVED[31:18]	14
0xBB00C614	VLAN_PORT_PPB_VLAN_SET [47][5].VLAN_TYPE[17:17]	1
0xBB00C614	VLAN_PORT_PPB_VLAN_SET [47][5].PRI_AS[16:16]	1
0xBB00C614	VLAN_PORT_PPB_VLAN_SET [47][5].PRI[15:13]	3
0xBB00C614	VLAN_PORT_PPB_VLAN_SET [47][5].VID_AS[12:12]	1
0xBB00C614	VLAN_PORT_PPB_VLAN_SET [47][5].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C618	VLAN_PORT_PPB_VLAN_SET [47][6].RESERVED[31:18]	14
0xBB00C618	VLAN_PORT_PPB_VLAN_SET [47][6].VLAN_TYPE[17:17]	1
0xBB00C618	VLAN_PORT_PPB_VLAN_SET [47][6].PRI_AS[16:16]	1
0xBB00C618	VLAN_PORT_PPB_VLAN_SET [47][6].PRI[15:13]	3
0xBB00C618	VLAN_PORT_PPB_VLAN_SET [47][6].VID_AS[12:12]	1
0xBB00C618	VLAN_PORT_PPB_VLAN_SET [47][6].VID[11:0]	12
0xBB00C61C	VLAN_PORT_PPB_VLAN_SET [47][7].RESERVED[31:18]	14
0xBB00C61C	VLAN_PORT_PPB_VLAN_SET [47][7].VLAN_TYPE[17:17]	1
0xBB00C61C	VLAN_PORT_PPB_VLAN_SET [47][7].PRI_AS[16:16]	1
0xBB00C61C	VLAN_PORT_PPB_VLAN_SET [47][7].PRI[15:13]	3
0xBB00C61C	VLAN_PORT_PPB_VLAN_SET [47][7].VID_AS[12:12]	1
0xBB00C61C	VLAN_PORT_PPB_VLAN_SET [47][7].VID[11:0]	12
0xBB00C620	VLAN_PORT_PPB_VLAN_SET [48][0].RESERVED[31:18]	14
0xBB00C620	VLAN_PORT_PPB_VLAN_SET [48][0].VLAN_TYPE[17:17]	1
0xBB00C620	VLAN_PORT_PPB_VLAN_SET [48][0].PRI_AS[16:16]	1
0xBB00C620	VLAN_PORT_PPB_VLAN_SET [48][0].PRI[15:13]	3
0xBB00C620	VLAN_PORT_PPB_VLAN_SET [48][0].VID_AS[12:12]	1
0xBB00C620	VLAN_PORT_PPB_VLAN_SET [48][0].VID[11:0]	12
0xBB00C624	VLAN_PORT_PPB_VLAN_SET [48][1].RESERVED[31:18]	14
0xBB00C624	VLAN_PORT_PPB_VLAN_SET [48][1].VLAN_TYPE[17:17]	1
0xBB00C624	VLAN_PORT_PPB_VLAN_SET [48][1].PRI_AS[16:16]	1
0xBB00C624	VLAN_PORT_PPB_VLAN_SET [48][1].PRI[15:13]	3
0xBB00C624	VLAN_PORT_PPB_VLAN_SET [48][1].VID_AS[12:12]	1
0xBB00C624	VLAN_PORT_PPB_VLAN_SET [48][1].VID[11:0]	12
0xBB00C628	VLAN_PORT_PPB_VLAN_SET [48][2].RESERVED[31:18]	14
0xBB00C628	VLAN_PORT_PPB_VLAN_SET [48][2].VLAN_TYPE[17:17]	1
0xBB00C628	VLAN_PORT_PPB_VLAN_SET [48][2].PRI_AS[16:16]	1
0xBB00C628	VLAN_PORT_PPB_VLAN_SET [48][2].PRI[15:13]	3
0xBB00C628	VLAN_PORT_PPB_VLAN_SET [48][2].VID_AS[12:12]	1
0xBB00C628	VLAN_PORT_PPB_VLAN_SET [48][2].VID[11:0]	12
0xBB00C62C	VLAN_PORT_PPB_VLAN_SET [48][3].RESERVED[31:18]	14
0xBB00C62C	VLAN_PORT_PPB_VLAN_SET [48][3].VLAN_TYPE[17:17]	1
0xBB00C62C	VLAN_PORT_PPB_VLAN_SET [48][3].PRI_AS[16:16]	1
0xBB00C62C	VLAN_PORT_PPB_VLAN_SET [48][3].PRI[15:13]	3
0xBB00C62C	VLAN_PORT_PPB_VLAN_SET [48][3].VID_AS[12:12]	1
0xBB00C62C	VLAN_PORT_PPB_VLAN_SET [48][3].VID[11:0]	12
0xBB00C630	VLAN_PORT_PPB_VLAN_SET [48][4].RESERVED[31:18]	14
0xBB00C630	VLAN_PORT_PPB_VLAN_SET [48][4].VLAN_TYPE[17:17]	1
0xBB00C630	VLAN_PORT_PPB_VLAN_SET [48][4].PRI_AS[16:16]	1
0xBB00C630	VLAN_PORT_PPB_VLAN_SET [48][4].PRI[15:13]	3
0xBB00C630	VLAN_PORT_PPB_VLAN_SET [48][4].VID_AS[12:12]	1
0xBB00C630	VLAN_PORT_PPB_VLAN_SET [48][4].VID[11:0]	12
0xBB00C634	VLAN_PORT_PPB_VLAN_SET [48][5].RESERVED[31:18]	14
0xBB00C634	VLAN_PORT_PPB_VLAN_SET [48][5].VLAN_TYPE[17:17]	1
0xBB00C634	VLAN_PORT_PPB_VLAN_SET [48][5].PRI_AS[16:16]	1
0xBB00C634	VLAN_PORT_PPB_VLAN_SET [48][5].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C634	VLAN_PORT_PPB_VLAN_SET [48][5].VID_AS[12:12]	1
0xBB00C634	VLAN_PORT_PPB_VLAN_SET [48][5].VID[11:0]	12
0xBB00C638	VLAN_PORT_PPB_VLAN_SET [48][6].RESERVED[31:18]	14
0xBB00C638	VLAN_PORT_PPB_VLAN_SET [48][6].VLAN_TYPE[17:17]	1
0xBB00C638	VLAN_PORT_PPB_VLAN_SET [48][6].PRI_AS[16:16]	1
0xBB00C638	VLAN_PORT_PPB_VLAN_SET [48][6].PRI[15:13]	3
0xBB00C638	VLAN_PORT_PPB_VLAN_SET [48][6].VID_AS[12:12]	1
0xBB00C638	VLAN_PORT_PPB_VLAN_SET [48][6].VID[11:0]	12
0xBB00C63C	VLAN_PORT_PPB_VLAN_SET [48][7].RESERVED[31:18]	14
0xBB00C63C	VLAN_PORT_PPB_VLAN_SET [48][7].VLAN_TYPE[17:17]	1
0xBB00C63C	VLAN_PORT_PPB_VLAN_SET [48][7].PRI_AS[16:16]	1
0xBB00C63C	VLAN_PORT_PPB_VLAN_SET [48][7].PRI[15:13]	3
0xBB00C63C	VLAN_PORT_PPB_VLAN_SET [48][7].VID_AS[12:12]	1
0xBB00C63C	VLAN_PORT_PPB_VLAN_SET [48][7].VID[11:0]	12
0xBB00C640	VLAN_PORT_PPB_VLAN_SET [49][0].RESERVED[31:18]	14
0xBB00C640	VLAN_PORT_PPB_VLAN_SET [49][0].VLAN_TYPE[17:17]	1
0xBB00C640	VLAN_PORT_PPB_VLAN_SET [49][0].PRI_AS[16:16]	1
0xBB00C640	VLAN_PORT_PPB_VLAN_SET [49][0].PRI[15:13]	3
0xBB00C640	VLAN_PORT_PPB_VLAN_SET [49][0].VID_AS[12:12]	1
0xBB00C640	VLAN_PORT_PPB_VLAN_SET [49][0].VID[11:0]	12
0xBB00C644	VLAN_PORT_PPB_VLAN_SET [49][1].RESERVED[31:18]	14
0xBB00C644	VLAN_PORT_PPB_VLAN_SET [49][1].VLAN_TYPE[17:17]	1
0xBB00C644	VLAN_PORT_PPB_VLAN_SET [49][1].PRI_AS[16:16]	1
0xBB00C644	VLAN_PORT_PPB_VLAN_SET [49][1].PRI[15:13]	3
0xBB00C644	VLAN_PORT_PPB_VLAN_SET [49][1].VID_AS[12:12]	1
0xBB00C644	VLAN_PORT_PPB_VLAN_SET [49][1].VID[11:0]	12
0xBB00C648	VLAN_PORT_PPB_VLAN_SET [49][2].RESERVED[31:18]	14
0xBB00C648	VLAN_PORT_PPB_VLAN_SET [49][2].VLAN_TYPE[17:17]	1
0xBB00C648	VLAN_PORT_PPB_VLAN_SET [49][2].PRI_AS[16:16]	1
0xBB00C648	VLAN_PORT_PPB_VLAN_SET [49][2].PRI[15:13]	3
0xBB00C648	VLAN_PORT_PPB_VLAN_SET [49][2].VID_AS[12:12]	1
0xBB00C648	VLAN_PORT_PPB_VLAN_SET [49][2].VID[11:0]	12
0xBB00C64C	VLAN_PORT_PPB_VLAN_SET [49][3].RESERVED[31:18]	14
0xBB00C64C	VLAN_PORT_PPB_VLAN_SET [49][3].VLAN_TYPE[17:17]	1
0xBB00C64C	VLAN_PORT_PPB_VLAN_SET [49][3].PRI_AS[16:16]	1
0xBB00C64C	VLAN_PORT_PPB_VLAN_SET [49][3].PRI[15:13]	3
0xBB00C64C	VLAN_PORT_PPB_VLAN_SET [49][3].VID_AS[12:12]	1
0xBB00C64C	VLAN_PORT_PPB_VLAN_SET [49][3].VID[11:0]	12
0xBB00C650	VLAN_PORT_PPB_VLAN_SET [49][4].RESERVED[31:18]	14
0xBB00C650	VLAN_PORT_PPB_VLAN_SET [49][4].VLAN_TYPE[17:17]	1
0xBB00C650	VLAN_PORT_PPB_VLAN_SET [49][4].PRI_AS[16:16]	1
0xBB00C650	VLAN_PORT_PPB_VLAN_SET [49][4].PRI[15:13]	3
0xBB00C650	VLAN_PORT_PPB_VLAN_SET [49][4].VID_AS[12:12]	1
0xBB00C650	VLAN_PORT_PPB_VLAN_SET [49][4].VID[11:0]	12
0xBB00C654	VLAN_PORT_PPB_VLAN_SET [49][5].RESERVED[31:18]	14
0xBB00C654	VLAN_PORT_PPB_VLAN_SET [49][5].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C654	VLAN_PORT_PPB_VLAN_SET [49][5].PRI_AS[16:16]	1
0xBB00C654	VLAN_PORT_PPB_VLAN_SET [49][5].PRI[15:13]	3
0xBB00C654	VLAN_PORT_PPB_VLAN_SET [49][5].VID_AS[12:12]	1
0xBB00C654	VLAN_PORT_PPB_VLAN_SET [49][5].VID[11:0]	12
0xBB00C658	VLAN_PORT_PPB_VLAN_SET [49][6].RESERVED[31:18]	14
0xBB00C658	VLAN_PORT_PPB_VLAN_SET [49][6].VLAN_TYPE[17:17]	1
0xBB00C658	VLAN_PORT_PPB_VLAN_SET [49][6].PRI_AS[16:16]	1
0xBB00C658	VLAN_PORT_PPB_VLAN_SET [49][6].PRI[15:13]	3
0xBB00C658	VLAN_PORT_PPB_VLAN_SET [49][6].VID_AS[12:12]	1
0xBB00C658	VLAN_PORT_PPB_VLAN_SET [49][6].VID[11:0]	12
0xBB00C65C	VLAN_PORT_PPB_VLAN_SET [49][7].RESERVED[31:18]	14
0xBB00C65C	VLAN_PORT_PPB_VLAN_SET [49][7].VLAN_TYPE[17:17]	1
0xBB00C65C	VLAN_PORT_PPB_VLAN_SET [49][7].PRI_AS[16:16]	1
0xBB00C65C	VLAN_PORT_PPB_VLAN_SET [49][7].PRI[15:13]	3
0xBB00C65C	VLAN_PORT_PPB_VLAN_SET [49][7].VID_AS[12:12]	1
0xBB00C65C	VLAN_PORT_PPB_VLAN_SET [49][7].VID[11:0]	12
0xBB00C660	VLAN_PORT_PPB_VLAN_SET [50][0].RESERVED[31:18]	14
0xBB00C660	VLAN_PORT_PPB_VLAN_SET [50][0].VLAN_TYPE[17:17]	1
0xBB00C660	VLAN_PORT_PPB_VLAN_SET [50][0].PRI_AS[16:16]	1
0xBB00C660	VLAN_PORT_PPB_VLAN_SET [50][0].PRI[15:13]	3
0xBB00C660	VLAN_PORT_PPB_VLAN_SET [50][0].VID_AS[12:12]	1
0xBB00C660	VLAN_PORT_PPB_VLAN_SET [50][0].VID[11:0]	12
0xBB00C664	VLAN_PORT_PPB_VLAN_SET [50][1].RESERVED[31:18]	14
0xBB00C664	VLAN_PORT_PPB_VLAN_SET [50][1].VLAN_TYPE[17:17]	1
0xBB00C664	VLAN_PORT_PPB_VLAN_SET [50][1].PRI_AS[16:16]	1
0xBB00C664	VLAN_PORT_PPB_VLAN_SET [50][1].PRI[15:13]	3
0xBB00C664	VLAN_PORT_PPB_VLAN_SET [50][1].VID_AS[12:12]	1
0xBB00C664	VLAN_PORT_PPB_VLAN_SET [50][1].VID[11:0]	12
0xBB00C668	VLAN_PORT_PPB_VLAN_SET [50][2] RESERVED[31:18]	14
0xBB00C668	VLAN_PORT_PPB_VLAN_SET [50][2].VLAN_TYPE[17:17]	1
0xBB00C668	VLAN_PORT_PPB_VLAN_SET [50][2].PRI_AS[16:16]	1
0xBB00C668	VLAN_PORT_PPB_VLAN_SET [50][2].PRI[15:13]	3
0xBB00C668	VLAN_PORT_PPB_VLAN_SET [50][2].VID_AS[12:12]	1
0xBB00C668	VLAN_PORT_PPB_VLAN_SET [50][2].VID[11:0]	12
0xBB00C66C	VLAN_PORT_PPB_VLAN_SET [50][3].RESERVED[31:18]	14
0xBB00C66C	VLAN_PORT_PPB_VLAN_SET [50][3].VLAN_TYPE[17:17]	1
0xBB00C66C	VLAN_PORT_PPB_VLAN_SET [50][3].PRI_AS[16:16]	1
0xBB00C66C	VLAN_PORT_PPB_VLAN_SET [50][3].PRI[15:13]	3
0xBB00C66C	VLAN_PORT_PPB_VLAN_SET [50][3].VID_AS[12:12]	1
0xBB00C66C	VLAN_PORT_PPB_VLAN_SET [50][3].VID[11:0]	12
0xBB00C670	VLAN_PORT_PPB_VLAN_SET [50][4].RESERVED[31:18]	14
0xBB00C670	VLAN_PORT_PPB_VLAN_SET [50][4].VLAN_TYPE[17:17]	1
0xBB00C670	VLAN_PORT_PPB_VLAN_SET [50][4].PRI_AS[16:16]	1
0xBB00C670	VLAN_PORT_PPB_VLAN_SET [50][4].PRI[15:13]	3
0xBB00C670	VLAN_PORT_PPB_VLAN_SET [50][4].VID_AS[12:12]	1
0xBB00C670	VLAN_PORT_PPB_VLAN_SET [50][4].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C674	VLAN_PORT_PPB_VLAN_SET [50][5].RESERVED[31:18]	14
0xBB00C674	VLAN_PORT_PPB_VLAN_SET [50][5].VLAN_TYPE[17:17]	1
0xBB00C674	VLAN_PORT_PPB_VLAN_SET [50][5].PRI_AS[16:16]	1
0xBB00C674	VLAN_PORT_PPB_VLAN_SET [50][5].PRI[15:13]	3
0xBB00C674	VLAN_PORT_PPB_VLAN_SET [50][5].VID_AS[12:12]	1
0xBB00C674	VLAN_PORT_PPB_VLAN_SET [50][5].VID[11:0]	12
0xBB00C678	VLAN_PORT_PPB_VLAN_SET [50][6].RESERVED[31:18]	14
0xBB00C678	VLAN_PORT_PPB_VLAN_SET [50][6].VLAN_TYPE[17:17]	1
0xBB00C678	VLAN_PORT_PPB_VLAN_SET [50][6].PRI_AS[16:16]	1
0xBB00C678	VLAN_PORT_PPB_VLAN_SET [50][6].PRI[15:13]	3
0xBB00C678	VLAN_PORT_PPB_VLAN_SET [50][6].VID_AS[12:12]	1
0xBB00C678	VLAN_PORT_PPB_VLAN_SET [50][6].VID[11:0]	12
0xBB00C67C	VLAN_PORT_PPB_VLAN_SET [50][7].RESERVED[31:18]	14
0xBB00C67C	VLAN_PORT_PPB_VLAN_SET [50][7].VLAN_TYPE[17:17]	1
0xBB00C67C	VLAN_PORT_PPB_VLAN_SET [50][7].PRI_AS[16:16]	1
0xBB00C67C	VLAN_PORT_PPB_VLAN_SET [50][7].PRI[15:13]	3
0xBB00C67C	VLAN_PORT_PPB_VLAN_SET [50][7].VID_AS[12:12]	1
0xBB00C67C	VLAN_PORT_PPB_VLAN_SET [50][7].VID[11:0]	12
0xBB00C680	VLAN_PORT_PPB_VLAN_SET [51][0].RESERVED[31:18]	14
0xBB00C680	VLAN_PORT_PPB_VLAN_SET [51][0].VLAN_TYPE[17:17]	1
0xBB00C680	VLAN_PORT_PPB_VLAN_SET [51][0].PRI_AS[16:16]	1
0xBB00C680	VLAN_PORT_PPB_VLAN_SET [51][0].PRI[15:13]	3
0xBB00C680	VLAN_PORT_PPB_VLAN_SET [51][0].VID_AS[12:12]	1
0xBB00C680	VLAN_PORT_PPB_VLAN_SET [51][0].VID[11:0]	12
0xBB00C684	VLAN_PORT_PPB_VLAN_SET [51][1].RESERVED[31:18]	14
0xBB00C684	VLAN_PORT_PPB_VLAN_SET [51][1].VLAN_TYPE[17:17]	1
0xBB00C684	VLAN_PORT_PPB_VLAN_SET [51][1].PRI_AS[16:16]	1
0xBB00C684	VLAN_PORT_PPB_VLAN_SET [51][1].PRI[15:13]	3
0xBB00C684	VLAN_PORT_PPB_VLAN_SET [51][1].VID_AS[12:12]	1
0xBB00C684	VLAN_PORT_PPB_VLAN_SET [51][1].VID[11:0]	12
0xBB00C688	VLAN_PORT_PPB_VLAN_SET [51][2].RESERVED[31:18]	14
0xBB00C688	VLAN_PORT_PPB_VLAN_SET [51][2].VLAN_TYPE[17:17]	1
0xBB00C688	VLAN_PORT_PPB_VLAN_SET [51][2].PRI_AS[16:16]	1
0xBB00C688	VLAN_PORT_PPB_VLAN_SET [51][2].PRI[15:13]	3
0xBB00C688	VLAN_PORT_PPB_VLAN_SET [51][2].VID_AS[12:12]	1
0xBB00C688	VLAN_PORT_PPB_VLAN_SET [51][2].VID[11:0]	12
0xBB00C68C	VLAN_PORT_PPB_VLAN_SET [51][3].RESERVED[31:18]	14
0xBB00C68C	VLAN_PORT_PPB_VLAN_SET [51][3].VLAN_TYPE[17:17]	1
0xBB00C68C	VLAN_PORT_PPB_VLAN_SET [51][3].PRI_AS[16:16]	1
0xBB00C68C	VLAN_PORT_PPB_VLAN_SET [51][3].PRI[15:13]	3
0xBB00C68C	VLAN_PORT_PPB_VLAN_SET [51][3].VID_AS[12:12]	1
0xBB00C68C	VLAN_PORT_PPB_VLAN_SET [51][3].VID[11:0]	12
0xBB00C690	VLAN_PORT_PPB_VLAN_SET [51][4].RESERVED[31:18]	14
0xBB00C690	VLAN_PORT_PPB_VLAN_SET [51][4].VLAN_TYPE[17:17]	1
0xBB00C690	VLAN_PORT_PPB_VLAN_SET [51][4].PRI_AS[16:16]	1
0xBB00C690	VLAN_PORT_PPB_VLAN_SET [51][4].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C690	VLAN_PORT_PPB_VLAN_SET [51][4].VID_AS[12:12]	1
0xBB00C690	VLAN_PORT_PPB_VLAN_SET [51][4].VID[11:0]	12
0xBB00C694	VLAN_PORT_PPB_VLAN_SET [51][5].RESERVED[31:18]	14
0xBB00C694	VLAN_PORT_PPB_VLAN_SET [51][5].VLAN_TYPE[17:17]	1
0xBB00C694	VLAN_PORT_PPB_VLAN_SET [51][5].PRI_AS[16:16]	1
0xBB00C694	VLAN_PORT_PPB_VLAN_SET [51][5].PRI[15:13]	3
0xBB00C694	VLAN_PORT_PPB_VLAN_SET [51][5].VID_AS[12:12]	1
0xBB00C694	VLAN_PORT_PPB_VLAN_SET [51][5].VID[11:0]	12
0xBB00C698	VLAN_PORT_PPB_VLAN_SET [51][6].RESERVED[31:18]	14
0xBB00C698	VLAN_PORT_PPB_VLAN_SET [51][6].VLAN_TYPE[17:17]	1
0xBB00C698	VLAN_PORT_PPB_VLAN_SET [51][6].PRI_AS[16:16]	1
0xBB00C698	VLAN_PORT_PPB_VLAN_SET [51][6].PRI[15:13]	3
0xBB00C698	VLAN_PORT_PPB_VLAN_SET [51][6].VID_AS[12:12]	1
0xBB00C698	VLAN_PORT_PPB_VLAN_SET [51][6].VID[11:0]	12
0xBB00C69C	VLAN_PORT_PPB_VLAN_SET [51][7].RESERVED[31:18]	14
0xBB00C69C	VLAN_PORT_PPB_VLAN_SET [51][7].VLAN_TYPE[17:17]	1
0xBB00C69C	VLAN_PORT_PPB_VLAN_SET [51][7].PRI_AS[16:16]	1
0xBB00C69C	VLAN_PORT_PPB_VLAN_SET [51][7].PRI[15:13]	3
0xBB00C69C	VLAN_PORT_PPB_VLAN_SET [51][7].VID_AS[12:12]	1
0xBB00C69C	VLAN_PORT_PPB_VLAN_SET [51][7].VID[11:0]	12
0xBB00C6A0	VLAN_PORT_PPB_VLAN_SET [52][0].RESERVED[31:18]	14
0xBB00C6A0	VLAN_PORT_PPB_VLAN_SET [52][0].VLAN_TYPE[17:17]	1
0xBB00C6A0	VLAN_PORT_PPB_VLAN_SET [52][0].PRI_AS[16:16]	1
0xBB00C6A0	VLAN_PORT_PPB_VLAN_SET [52][0].PRI[15:13]	3
0xBB00C6A0	VLAN_PORT_PPB_VLAN_SET [52][0].VID_AS[12:12]	1
0xBB00C6A0	VLAN_PORT_PPB_VLAN_SET [52][0].VID[11:0]	12
0xBB00C6A4	VLAN_PORT_PPB_VLAN_SET [52][1].RESERVED[31:18]	14
0xBB00C6A4	VLAN_PORT_PPB_VLAN_SET [52][1].VLAN_TYPE[17:17]	1
0xBB00C6A4	VLAN_PORT_PPB_VLAN_SET [52][1].PRI_AS[16:16]	1
0xBB00C6A4	VLAN_PORT_PPB_VLAN_SET [52][1].PRI[15:13]	3
0xBB00C6A4	VLAN_PORT_PPB_VLAN_SET [52][1].VID_AS[12:12]	1
0xBB00C6A4	VLAN_PORT_PPB_VLAN_SET [52][1].VID[11:0]	12
0xBB00C6A8	VLAN_PORT_PPB_VLAN_SET [52][2].RESERVED[31:18]	14
0xBB00C6A8	VLAN_PORT_PPB_VLAN_SET [52][2].VLAN_TYPE[17:17]	1
0xBB00C6A8	VLAN_PORT_PPB_VLAN_SET [52][2].PRI_AS[16:16]	1
0xBB00C6A8	VLAN_PORT_PPB_VLAN_SET [52][2].PRI[15:13]	3
0xBB00C6A8	VLAN_PORT_PPB_VLAN_SET [52][2].VID_AS[12:12]	1
0xBB00C6A8	VLAN_PORT_PPB_VLAN_SET [52][2].VID[11:0]	12
0xBB00C6AC	VLAN_PORT_PPB_VLAN_SET [52][3].RESERVED[31:18]	14
0xBB00C6AC	VLAN_PORT_PPB_VLAN_SET [52][3].VLAN_TYPE[17:17]	1
0xBB00C6AC	VLAN_PORT_PPB_VLAN_SET [52][3].PRI_AS[16:16]	1
0xBB00C6AC	VLAN_PORT_PPB_VLAN_SET [52][3].PRI[15:13]	3
0xBB00C6AC	VLAN_PORT_PPB_VLAN_SET [52][3].VID_AS[12:12]	1
0xBB00C6AC	VLAN_PORT_PPB_VLAN_SET [52][3].VID[11:0]	12
0xBB00C6B0	VLAN_PORT_PPB_VLAN_SET [52][4].RESERVED[31:18]	14
0xBB00C6B0	VLAN_PORT_PPB_VLAN_SET [52][4].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C6B0	VLAN_PORT_PPB_VLAN_SET [52][4].PRI_AS[16:16]	1
0xBB00C6B0	VLAN_PORT_PPB_VLAN_SET [52][4].PRI[15:13]	3
0xBB00C6B0	VLAN_PORT_PPB_VLAN_SET [52][4].VID_AS[12:12]	1
0xBB00C6B0	VLAN_PORT_PPB_VLAN_SET [52][4].VID[11:0]	12
0xBB00C6B4	VLAN_PORT_PPB_VLAN_SET [52][5].RESERVED[31:18]	14
0xBB00C6B4	VLAN_PORT_PPB_VLAN_SET [52][5].VLAN_TYPE[17:17]	1
0xBB00C6B4	VLAN_PORT_PPB_VLAN_SET [52][5].PRI_AS[16:16]	1
0xBB00C6B4	VLAN_PORT_PPB_VLAN_SET [52][5].PRI[15:13]	3
0xBB00C6B4	VLAN_PORT_PPB_VLAN_SET [52][5].VID_AS[12:12]	1
0xBB00C6B4	VLAN_PORT_PPB_VLAN_SET [52][5].VID[11:0]	12
0xBB00C6B8	VLAN_PORT_PPB_VLAN_SET [52][6].RESERVED[31:18]	14
0xBB00C6B8	VLAN_PORT_PPB_VLAN_SET [52][6].VLAN_TYPE[17:17]	1
0xBB00C6B8	VLAN_PORT_PPB_VLAN_SET [52][6].PRI_AS[16:16]	1
0xBB00C6B8	VLAN_PORT_PPB_VLAN_SET [52][6].PRI[15:13]	3
0xBB00C6B8	VLAN_PORT_PPB_VLAN_SET [52][6].VID_AS[12:12]	1
0xBB00C6B8	VLAN_PORT_PPB_VLAN_SET [52][6].VID[11:0]	12
0xBB00C6BC	VLAN_PORT_PPB_VLAN_SET [52][7].RESERVED[31:18]	14
0xBB00C6BC	VLAN_PORT_PPB_VLAN_SET [52][7].VLAN_TYPE[17:17]	1
0xBB00C6BC	VLAN_PORT_PPB_VLAN_SET [52][7].PRI_AS[16:16]	1
0xBB00C6BC	VLAN_PORT_PPB_VLAN_SET [52][7].PRI[15:13]	3
0xBB00C6BC	VLAN_PORT_PPB_VLAN_SET [52][7].VID_AS[12:12]	1
0xBB00C6BC	VLAN_PORT_PPB_VLAN_SET [52][7].VID[11:0]	12
0xBB00C6C0	VLAN_PORT_PPB_VLAN_SET [53][0].RESERVED[31:18]	14
0xBB00C6C0	VLAN_PORT_PPB_VLAN_SET [53][0].VLAN_TYPE[17:17]	1
0xBB00C6C0	VLAN_PORT_PPB_VLAN_SET [53][0].PRI_AS[16:16]	1
0xBB00C6C0	VLAN_PORT_PPB_VLAN_SET [53][0].PRI[15:13]	3
0xBB00C6C0	VLAN_PORT_PPB_VLAN_SET [53][0].VID_AS[12:12]	1
0xBB00C6C0	VLAN_PORT_PPB_VLAN_SET [53][0].VID[11:0]	12
0xBB00C6C4	VLAN_PORT_PPB_VLAN_SET [53][1].RESERVED[31:18]	14
0xBB00C6C4	VLAN_PORT_PPB_VLAN_SET [53][1].VLAN_TYPE[17:17]	1
0xBB00C6C4	VLAN_PORT_PPB_VLAN_SET [53][1].PRI_AS[16:16]	1
0xBB00C6C4	VLAN_PORT_PPB_VLAN_SET [53][1].PRI[15:13]	3
0xBB00C6C4	VLAN_PORT_PPB_VLAN_SET [53][1].VID_AS[12:12]	1
0xBB00C6C4	VLAN_PORT_PPB_VLAN_SET [53][1].VID[11:0]	12
0xBB00C6C8	VLAN_PORT_PPB_VLAN_SET [53][2].RESERVED[31:18]	14
0xBB00C6C8	VLAN_PORT_PPB_VLAN_SET [53][2].VLAN_TYPE[17:17]	1
0xBB00C6C8	VLAN_PORT_PPB_VLAN_SET [53][2].PRI_AS[16:16]	1
0xBB00C6C8	VLAN_PORT_PPB_VLAN_SET [53][2].PRI[15:13]	3
0xBB00C6C8	VLAN_PORT_PPB_VLAN_SET [53][2].VID_AS[12:12]	1
0xBB00C6C8	VLAN_PORT_PPB_VLAN_SET [53][2].VID[11:0]	12
0xBB00C6CC	VLAN_PORT_PPB_VLAN_SET [53][3].RESERVED[31:18]	14
0xBB00C6CC	VLAN_PORT_PPB_VLAN_SET [53][3].VLAN_TYPE[17:17]	1
0xBB00C6CC	VLAN_PORT_PPB_VLAN_SET [53][3].PRI_AS[16:16]	1
0xBB00C6CC	VLAN_PORT_PPB_VLAN_SET [53][3].PRI[15:13]	3
0xBB00C6CC	VLAN_PORT_PPB_VLAN_SET [53][3].VID_AS[12:12]	1
0xBB00C6CC	VLAN_PORT_PPB_VLAN_SET [53][3].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C6D0	VLAN_PORT_PPB_VLAN_SET [53][4].RESERVED[31:18]	14
0xBB00C6D0	VLAN_PORT_PPB_VLAN_SET [53][4].VLAN_TYPE[17:17]	1
0xBB00C6D0	VLAN_PORT_PPB_VLAN_SET [53][4].PRI_AS[16:16]	1
0xBB00C6D0	VLAN_PORT_PPB_VLAN_SET [53][4].PRI[15:13]	3
0xBB00C6D0	VLAN_PORT_PPB_VLAN_SET [53][4].VID_AS[12:12]	1
0xBB00C6D0	VLAN_PORT_PPB_VLAN_SET [53][4].VID[11:0]	12
0xBB00C6D4	VLAN_PORT_PPB_VLAN_SET [53][5].RESERVED[31:18]	14
0xBB00C6D4	VLAN_PORT_PPB_VLAN_SET [53][5].VLAN_TYPE[17:17]	1
0xBB00C6D4	VLAN_PORT_PPB_VLAN_SET [53][5].PRI_AS[16:16]	1
0xBB00C6D4	VLAN_PORT_PPB_VLAN_SET [53][5].PRI[15:13]	3
0xBB00C6D4	VLAN_PORT_PPB_VLAN_SET [53][5].VID_AS[12:12]	1
0xBB00C6D4	VLAN_PORT_PPB_VLAN_SET [53][5].VID[11:0]	12
0xBB00C6D8	VLAN_PORT_PPB_VLAN_SET [53][6].RESERVED[31:18]	14
0xBB00C6D8	VLAN_PORT_PPB_VLAN_SET [53][6].VLAN_TYPE[17:17]	1
0xBB00C6D8	VLAN_PORT_PPB_VLAN_SET [53][6].PRI_AS[16:16]	1
0xBB00C6D8	VLAN_PORT_PPB_VLAN_SET [53][6].PRI[15:13]	3
0xBB00C6D8	VLAN_PORT_PPB_VLAN_SET [53][6].VID_AS[12:12]	1
0xBB00C6D8	VLAN_PORT_PPB_VLAN_SET [53][6].VID[11:0]	12
0xBB00C6DC	VLAN_PORT_PPB_VLAN_SET [53][7].RESERVED[31:18]	14
0xBB00C6DC	VLAN_PORT_PPB_VLAN_SET [53][7].VLAN_TYPE[17:17]	1
0xBB00C6DC	VLAN_PORT_PPB_VLAN_SET [53][7].PRI_AS[16:16]	1
0xBB00C6DC	VLAN_PORT_PPB_VLAN_SET [53][7].PRI[15:13]	3
0xBB00C6DC	VLAN_PORT_PPB_VLAN_SET [53][7].VID_AS[12:12]	1
0xBB00C6DC	VLAN_PORT_PPB_VLAN_SET [53][7].VID[11:0]	12
0xBB00C6E0	VLAN_PORT_PPB_VLAN_SET [54][0].RESERVED[31:18]	14
0xBB00C6E0	VLAN_PORT_PPB_VLAN_SET [54][0].VLAN_TYPE[17:17]	1
0xBB00C6E0	VLAN_PORT_PPB_VLAN_SET [54][0].PRI_AS[16:16]	1
0xBB00C6E0	VLAN_PORT_PPB_VLAN_SET [54][0].PRI[15:13]	3
0xBB00C6E0	VLAN_PORT_PPB_VLAN_SET [54][0].VID_AS[12:12]	1
0xBB00C6E0	VLAN_PORT_PPB_VLAN_SET [54][0].VID[11:0]	12
0xBB00C6E4	VLAN_PORT_PPB_VLAN_SET [54][1].RESERVED[31:18]	14
0xBB00C6E4	VLAN_PORT_PPB_VLAN_SET [54][1].VLAN_TYPE[17:17]	1
0xBB00C6E4	VLAN_PORT_PPB_VLAN_SET [54][1].PRI_AS[16:16]	1
0xBB00C6E4	VLAN_PORT_PPB_VLAN_SET [54][1].PRI[15:13]	3
0xBB00C6E4	VLAN_PORT_PPB_VLAN_SET [54][1].VID_AS[12:12]	1
0xBB00C6E4	VLAN_PORT_PPB_VLAN_SET [54][1].VID[11:0]	12
0xBB00C6E8	VLAN_PORT_PPB_VLAN_SET [54][2].RESERVED[31:18]	14
0xBB00C6E8	VLAN_PORT_PPB_VLAN_SET [54][2].VLAN_TYPE[17:17]	1
0xBB00C6E8	VLAN_PORT_PPB_VLAN_SET [54][2].PRI_AS[16:16]	1
0xBB00C6E8	VLAN_PORT_PPB_VLAN_SET [54][2].PRI[15:13]	3
0xBB00C6E8	VLAN_PORT_PPB_VLAN_SET [54][2].VID_AS[12:12]	1
0xBB00C6E8	VLAN_PORT_PPB_VLAN_SET [54][2].VID[11:0]	12
0xBB00C6EC	VLAN_PORT_PPB_VLAN_SET [54][3].RESERVED[31:18]	14
0xBB00C6EC	VLAN_PORT_PPB_VLAN_SET [54][3].VLAN_TYPE[17:17]	1
0xBB00C6EC	VLAN_PORT_PPB_VLAN_SET [54][3].PRI_AS[16:16]	1
0xBB00C6EC	VLAN_PORT_PPB_VLAN_SET [54][3].PRI[15:13]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C6EC	VLAN_PORT_PPB_VLAN_SET [54][3].VID_AS[12:12]	1
0xBB00C6EC	VLAN_PORT_PPB_VLAN_SET [54][3].VID[11:0]	12
0xBB00C6F0	VLAN_PORT_PPB_VLAN_SET [54][4].RESERVED[31:18]	14
0xBB00C6F0	VLAN_PORT_PPB_VLAN_SET [54][4].VLAN_TYPE[17:17]	1
0xBB00C6F0	VLAN_PORT_PPB_VLAN_SET [54][4].PRI_AS[16:16]	1
0xBB00C6F0	VLAN_PORT_PPB_VLAN_SET [54][4].PRI[15:13]	3
0xBB00C6F0	VLAN_PORT_PPB_VLAN_SET [54][4].VID_AS[12:12]	1
0xBB00C6F0	VLAN_PORT_PPB_VLAN_SET [54][4].VID[11:0]	12
0xBB00C6F4	VLAN_PORT_PPB_VLAN_SET [54][5].RESERVED[31:18]	14
0xBB00C6F4	VLAN_PORT_PPB_VLAN_SET [54][5].VLAN_TYPE[17:17]	1
0xBB00C6F4	VLAN_PORT_PPB_VLAN_SET [54][5].PRI_AS[16:16]	1
0xBB00C6F4	VLAN_PORT_PPB_VLAN_SET [54][5].PRI[15:13]	3
0xBB00C6F4	VLAN_PORT_PPB_VLAN_SET [54][5].VID_AS[12:12]	1
0xBB00C6F4	VLAN_PORT_PPB_VLAN_SET [54][5].VID[11:0]	12
0xBB00C6F8	VLAN_PORT_PPB_VLAN_SET [54][6].RESERVED[31:18]	14
0xBB00C6F8	VLAN_PORT_PPB_VLAN_SET [54][6].VLAN_TYPE[17:17]	1
0xBB00C6F8	VLAN_PORT_PPB_VLAN_SET [54][6].PRI_AS[16:16]	1
0xBB00C6F8	VLAN_PORT_PPB_VLAN_SET [54][6].PRI[15:13]	3
0xBB00C6F8	VLAN_PORT_PPB_VLAN_SET [54][6].VID_AS[12:12]	1
0xBB00C6F8	VLAN_PORT_PPB_VLAN_SET [54][6].VID[11:0]	12
0xBB00C6FC	VLAN_PORT_PPB_VLAN_SET [54][7].RESERVED[31:18]	14
0xBB00C6FC	VLAN_PORT_PPB_VLAN_SET [54][7].VLAN_TYPE[17:17]	1
0xBB00C6FC	VLAN_PORT_PPB_VLAN_SET [54][7].PRI_AS[16:16]	1
0xBB00C6FC	VLAN_PORT_PPB_VLAN_SET [54][7].PRI[15:13]	3
0xBB00C6FC	VLAN_PORT_PPB_VLAN_SET [54][7].VID_AS[12:12]	1
0xBB00C6FC	VLAN_PORT_PPB_VLAN_SET [54][7].VID[11:0]	12
0xBB00C700	VLAN_PORT_PPB_VLAN_SET [55][0].RESERVED[31:18]	14
0xBB00C700	VLAN_PORT_PPB_VLAN_SET [55][0].VLAN_TYPE[17:17]	1
0xBB00C700	VLAN_PORT_PPB_VLAN_SET [55][0].PRI_AS[16:16]	1
0xBB00C700	VLAN_PORT_PPB_VLAN_SET [55][0].PRI[15:13]	3
0xBB00C700	VLAN_PORT_PPB_VLAN_SET [55][0].VID_AS[12:12]	1
0xBB00C700	VLAN_PORT_PPB_VLAN_SET [55][0].VID[11:0]	12
0xBB00C704	VLAN_PORT_PPB_VLAN_SET [55][1].RESERVED[31:18]	14
0xBB00C704	VLAN_PORT_PPB_VLAN_SET [55][1].VLAN_TYPE[17:17]	1
0xBB00C704	VLAN_PORT_PPB_VLAN_SET [55][1].PRI_AS[16:16]	1
0xBB00C704	VLAN_PORT_PPB_VLAN_SET [55][1].PRI[15:13]	3
0xBB00C704	VLAN_PORT_PPB_VLAN_SET [55][1].VID_AS[12:12]	1
0xBB00C704	VLAN_PORT_PPB_VLAN_SET [55][1].VID[11:0]	12
0xBB00C708	VLAN_PORT_PPB_VLAN_SET [55][2].RESERVED[31:18]	14
0xBB00C708	VLAN_PORT_PPB_VLAN_SET [55][2].VLAN_TYPE[17:17]	1
0xBB00C708	VLAN_PORT_PPB_VLAN_SET [55][2].PRI_AS[16:16]	1
0xBB00C708	VLAN_PORT_PPB_VLAN_SET [55][2].PRI[15:13]	3
0xBB00C708	VLAN_PORT_PPB_VLAN_SET [55][2].VID_AS[12:12]	1
0xBB00C708	VLAN_PORT_PPB_VLAN_SET [55][2].VID[11:0]	12
0xBB00C70C	VLAN_PORT_PPB_VLAN_SET [55][3].RESERVED[31:18]	14
0xBB00C70C	VLAN_PORT_PPB_VLAN_SET [55][3].VLAN_TYPE[17:17]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C70C	VLAN_PORT_PPB_VLAN_SET [55][3].PRI_AS[16:16]	1
0xBB00C70C	VLAN_PORT_PPB_VLAN_SET [55][3].PRI[15:13]	3
0xBB00C70C	VLAN_PORT_PPB_VLAN_SET [55][3].VID_AS[12:12]	1
0xBB00C70C	VLAN_PORT_PPB_VLAN_SET [55][3].VID[11:0]	12
0xBB00C710	VLAN_PORT_PPB_VLAN_SET [55][4].RESERVED[31:18]	14
0xBB00C710	VLAN_PORT_PPB_VLAN_SET [55][4].VLAN_TYPE[17:17]	1
0xBB00C710	VLAN_PORT_PPB_VLAN_SET [55][4].PRI_AS[16:16]	1
0xBB00C710	VLAN_PORT_PPB_VLAN_SET [55][4].PRI[15:13]	3
0xBB00C710	VLAN_PORT_PPB_VLAN_SET [55][4].VID_AS[12:12]	1
0xBB00C710	VLAN_PORT_PPB_VLAN_SET [55][4].VID[11:0]	12
0xBB00C714	VLAN_PORT_PPB_VLAN_SET [55][5].RESERVED[31:18]	14
0xBB00C714	VLAN_PORT_PPB_VLAN_SET [55][5].VLAN_TYPE[17:17]	1
0xBB00C714	VLAN_PORT_PPB_VLAN_SET [55][5].PRI_AS[16:16]	1
0xBB00C714	VLAN_PORT_PPB_VLAN_SET [55][5].PRI[15:13]	3
0xBB00C714	VLAN_PORT_PPB_VLAN_SET [55][5].VID_AS[12:12]	1
0xBB00C714	VLAN_PORT_PPB_VLAN_SET [55][5].VID[11:0]	12
0xBB00C718	VLAN_PORT_PPB_VLAN_SET [55][6].RESERVED[31:18]	14
0xBB00C718	VLAN_PORT_PPB_VLAN_SET [55][6].VLAN_TYPE[17:17]	1
0xBB00C718	VLAN_PORT_PPB_VLAN_SET [55][6].PRI_AS[16:16]	1
0xBB00C718	VLAN_PORT_PPB_VLAN_SET [55][6].PRI[15:13]	3
0xBB00C718	VLAN_PORT_PPB_VLAN_SET [55][6].VID_AS[12:12]	1
0xBB00C718	VLAN_PORT_PPB_VLAN_SET [55][6].VID[11:0]	12
0xBB00C71C	VLAN_PORT_PPB_VLAN_SET [55][7].RESERVED[31:18]	14
0xBB00C71C	VLAN_PORT_PPB_VLAN_SET [55][7].VLAN_TYPE[17:17]	1
0xBB00C71C	VLAN_PORT_PPB_VLAN_SET [55][7].PRI_AS[16:16]	1
0xBB00C71C	VLAN_PORT_PPB_VLAN_SET [55][7].PRI[15:13]	3
0xBB00C71C	VLAN_PORT_PPB_VLAN_SET [55][7].VID_AS[12:12]	1
0xBB00C71C	VLAN_PORT_PPB_VLAN_SET [55][7].VID[11:0]	12
0xBB00C720	VLAN_PORT_PPB_VLAN_SET [56][0] RESERVED[31:18]	14
0xBB00C720	VLAN_PORT_PPB_VLAN_SET [56][0].VLAN_TYPE[17:17]	1
0xBB00C720	VLAN_PORT_PPB_VLAN_SET [56][0].PRI_AS[16:16]	1
0xBB00C720	VLAN_PORT_PPB_VLAN_SET [56][0].PRI[15:13]	3
0xBB00C720	VLAN_PORT_PPB_VLAN_SET [56][0].VID_AS[12:12]	1
0xBB00C720	VLAN_PORT_PPB_VLAN_SET [56][0].VID[11:0]	12
0xBB00C724	VLAN_PORT_PPB_VLAN_SET [56][1].RESERVED[31:18]	14
0xBB00C724	VLAN_PORT_PPB_VLAN_SET [56][1].VLAN_TYPE[17:17]	1
0xBB00C724	VLAN_PORT_PPB_VLAN_SET [56][1].PRI_AS[16:16]	1
0xBB00C724	VLAN_PORT_PPB_VLAN_SET [56][1].PRI[15:13]	3
0xBB00C724	VLAN_PORT_PPB_VLAN_SET [56][1].VID_AS[12:12]	1
0xBB00C724	VLAN_PORT_PPB_VLAN_SET [56][1].VID[11:0]	12
0xBB00C728	VLAN_PORT_PPB_VLAN_SET [56][2].RESERVED[31:18]	14
0xBB00C728	VLAN_PORT_PPB_VLAN_SET [56][2].VLAN_TYPE[17:17]	1
0xBB00C728	VLAN_PORT_PPB_VLAN_SET [56][2].PRI_AS[16:16]	1
0xBB00C728	VLAN_PORT_PPB_VLAN_SET [56][2].PRI[15:13]	3
0xBB00C728	VLAN_PORT_PPB_VLAN_SET [56][2].VID_AS[12:12]	1
0xBB00C728	VLAN_PORT_PPB_VLAN_SET [56][2].VID[11:0]	12

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00C72C	VLAN_PORT_PPB_VLAN_SET [56][3].RESERVED[31:18]	14
0xBB00C72C	VLAN_PORT_PPB_VLAN_SET [56][3].VLAN_TYPE[17:17]	1
0xBB00C72C	VLAN_PORT_PPB_VLAN_SET [56][3].PRI_AS[16:16]	1
0xBB00C72C	VLAN_PORT_PPB_VLAN_SET [56][3].PRI[15:13]	3
0xBB00C72C	VLAN_PORT_PPB_VLAN_SET [56][3].VID_AS[12:12]	1
0xBB00C72C	VLAN_PORT_PPB_VLAN_SET [56][3].VID[11:0]	12
0xBB00C730	VLAN_PORT_PPB_VLAN_SET [56][4].RESERVED[31:18]	14
0xBB00C730	VLAN_PORT_PPB_VLAN_SET [56][4].VLAN_TYPE[17:17]	1
0xBB00C730	VLAN_PORT_PPB_VLAN_SET [56][4].PRI_AS[16:16]	1
0xBB00C730	VLAN_PORT_PPB_VLAN_SET [56][4].PRI[15:13]	3
0xBB00C730	VLAN_PORT_PPB_VLAN_SET [56][4].VID_AS[12:12]	1
0xBB00C730	VLAN_PORT_PPB_VLAN_SET [56][4].VID[11:0]	12
0xBB00C734	VLAN_PORT_PPB_VLAN_SET [56][5].RESERVED[31:18]	14
0xBB00C734	VLAN_PORT_PPB_VLAN_SET [56][5].VLAN_TYPE[17:17]	1
0xBB00C734	VLAN_PORT_PPB_VLAN_SET [56][5].PRI_AS[16:16]	1
0xBB00C734	VLAN_PORT_PPB_VLAN_SET [56][5].PRI[15:13]	3
0xBB00C734	VLAN_PORT_PPB_VLAN_SET [56][5].VID_AS[12:12]	1
0xBB00C734	VLAN_PORT_PPB_VLAN_SET [56][5].VID[11:0]	12
0xBB00C738	VLAN_PORT_PPB_VLAN_SET [56][6].RESERVED[31:18]	14
0xBB00C738	VLAN_PORT_PPB_VLAN_SET [56][6].VLAN_TYPE[17:17]	1
0xBB00C738	VLAN_PORT_PPB_VLAN_SET [56][6].PRI_AS[16:16]	1
0xBB00C738	VLAN_PORT_PPB_VLAN_SET [56][6].PRI[15:13]	3
0xBB00C738	VLAN_PORT_PPB_VLAN_SET [56][6].VID_AS[12:12]	1
0xBB00C738	VLAN_PORT_PPB_VLAN_SET [56][6].VID[11:0]	12
0xBB00C73C	VLAN_PORT_PPB_VLAN_SET [56][7].RESERVED[31:18]	14
0xBB00C73C	VLAN_PORT_PPB_VLAN_SET [56][7].VLAN_TYPE[17:17]	1
0xBB00C73C	VLAN_PORT_PPB_VLAN_SET [56][7].PRI_AS[16:16]	1
0xBB00C73C	VLAN_PORT_PPB_VLAN_SET [56][7].PRI[15:13]	3
0xBB00C73C	VLAN_PORT_PPB_VLAN_SET [56][7].VID_AS[12:12]	1
0xBB00C73C	VLAN_PORT_PPB_VLAN_SET [56][7].VID[11:0]	12
0xBB004860	VLAN_PORT_TAG_CTRL [0].RESERVED[31:14]	18
0xBB004860	VLAN_PORT_TAG_CTRL [0].EGR_OTAG_STS[13:12]	2
0xBB004860	VLAN_PORT_TAG_CTRL [0].EGR_ITAG_STS[11:10]	2
0xBB004860	VLAN_PORT_TAG_CTRL [0].EGR_OTAG_KEEP[9:9]	1
0xBB004860	VLAN_PORT_TAG_CTRL [0].EGR_ITAG_KEEP[8:8]	1
0xBB004860	VLAN_PORT_TAG_CTRL [0].IGR_OTAG_KEEP[7:7]	1
0xBB004860	VLAN_PORT_TAG_CTRL [0].IGR_ITAG_KEEP[6:6]	1
0xBB004860	VLAN_PORT_TAG_CTRL [0].OTPID_IDX[5:4]	2
0xBB004860	VLAN_PORT_TAG_CTRL [0].OTPID_KEEP[3:3]	1
0xBB004860	VLAN_PORT_TAG_CTRL [0].ITPID_IDX[2:1]	2
0xBB004860	VLAN_PORT_TAG_CTRL [0].ITPID_KEEP[0:0]	1
0xBB004864	VLAN_PORT_TAG_CTRL [1].RESERVED[31:14]	18
0xBB004864	VLAN_PORT_TAG_CTRL [1].EGR_OTAG_STS[13:12]	2
0xBB004864	VLAN_PORT_TAG_CTRL [1].EGR_ITAG_STS[11:10]	2
0xBB004864	VLAN_PORT_TAG_CTRL [1].EGR_OTAG_KEEP[9:9]	1
0xBB004864	VLAN_PORT_TAG_CTRL [1].EGR_ITAG_KEEP[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004864	VLAN_PORT_TAG_CTRL [1].IGR_OTAG_KEEP[7:7]	1
0xBB004864	VLAN_PORT_TAG_CTRL [1].IGR_ITAG_KEEP[6:6]	1
0xBB004864	VLAN_PORT_TAG_CTRL [1].OTPID_IDX[5:4]	2
0xBB004864	VLAN_PORT_TAG_CTRL [1].OTPID_KEEP[3:3]	1
0xBB004864	VLAN_PORT_TAG_CTRL [1].ITPID_IDX[2:1]	2
0xBB004864	VLAN_PORT_TAG_CTRL [1].ITPID_KEEP[0:0]	1
0xBB004868	VLAN_PORT_TAG_CTRL [2].RESERVED[31:14]	18
0xBB004868	VLAN_PORT_TAG_CTRL [2].EGR_OTAG_STS[13:12]	2
0xBB004868	VLAN_PORT_TAG_CTRL [2].EGR_ITAG_STS[11:10]	2
0xBB004868	VLAN_PORT_TAG_CTRL [2].EGR_OTAG_KEEP[9:9]	1
0xBB004868	VLAN_PORT_TAG_CTRL [2].EGR_ITAG_KEEP[8:8]	1
0xBB004868	VLAN_PORT_TAG_CTRL [2].IGR_OTAG_KEEP[7:7]	1
0xBB004868	VLAN_PORT_TAG_CTRL [2].IGR_ITAG_KEEP[6:6]	1
0xBB004868	VLAN_PORT_TAG_CTRL [2].OTPID_IDX[5:4]	2
0xBB004868	VLAN_PORT_TAG_CTRL [2].OTPID_KEEP[3:3]	1
0xBB004868	VLAN_PORT_TAG_CTRL [2].ITPID_IDX[2:1]	2
0xBB004868	VLAN_PORT_TAG_CTRL [2].ITPID_KEEP[0:0]	1
0xBB00486C	VLAN_PORT_TAG_CTRL [3].RESERVED[31:14]	18
0xBB00486C	VLAN_PORT_TAG_CTRL [3].EGR_OTAG_STS[13:12]	2
0xBB00486C	VLAN_PORT_TAG_CTRL [3].EGR_ITAG_STS[11:10]	2
0xBB00486C	VLAN_PORT_TAG_CTRL [3].EGR_OTAG_KEEP[9:9]	1
0xBB00486C	VLAN_PORT_TAG_CTRL [3].EGR_ITAG_KEEP[8:8]	1
0xBB00486C	VLAN_PORT_TAG_CTRL [3].IGR_OTAG_KEEP[7:7]	1
0xBB00486C	VLAN_PORT_TAG_CTRL [3].IGR_ITAG_KEEP[6:6]	1
0xBB00486C	VLAN_PORT_TAG_CTRL [3].OTPID_IDX[5:4]	2
0xBB00486C	VLAN_PORT_TAG_CTRL [3].OTPID_KEEP[3:3]	1
0xBB00486C	VLAN_PORT_TAG_CTRL [3].ITPID_IDX[2:1]	2
0xBB00486C	VLAN_PORT_TAG_CTRL [3].ITPID_KEEP[0:0]	1
0xBB004870	VLAN_PORT_TAG_CTRL [4].RESERVED[31:14]	18
0xBB004870	VLAN_PORT_TAG_CTRL [4].EGR_OTAG_STS[13:12]	2
0xBB004870	VLAN_PORT_TAG_CTRL [4].EGR_ITAG_STS[11:10]	2
0xBB004870	VLAN_PORT_TAG_CTRL [4].EGR_OTAG_KEEP[9:9]	1
0xBB004870	VLAN_PORT_TAG_CTRL [4].EGR_ITAG_KEEP[8:8]	1
0xBB004870	VLAN_PORT_TAG_CTRL [4].IGR_OTAG_KEEP[7:7]	1
0xBB004870	VLAN_PORT_TAG_CTRL [4].IGR_ITAG_KEEP[6:6]	1
0xBB004870	VLAN_PORT_TAG_CTRL [4].OTPID_IDX[5:4]	2
0xBB004870	VLAN_PORT_TAG_CTRL [4].OTPID_KEEP[3:3]	1
0xBB004870	VLAN_PORT_TAG_CTRL [4].ITPID_IDX[2:1]	2
0xBB004870	VLAN_PORT_TAG_CTRL [4].ITPID_KEEP[0:0]	1
0xBB004874	VLAN_PORT_TAG_CTRL [5].RESERVED[31:14]	18
0xBB004874	VLAN_PORT_TAG_CTRL [5].EGR_OTAG_STS[13:12]	2
0xBB004874	VLAN_PORT_TAG_CTRL [5].EGR_ITAG_STS[11:10]	2
0xBB004874	VLAN_PORT_TAG_CTRL [5].EGR_OTAG_KEEP[9:9]	1
0xBB004874	VLAN_PORT_TAG_CTRL [5].EGR_ITAG_KEEP[8:8]	1
0xBB004874	VLAN_PORT_TAG_CTRL [5].IGR_OTAG_KEEP[7:7]	1
0xBB004874	VLAN_PORT_TAG_CTRL [5].IGR_ITAG_KEEP[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004874	VLAN_PORT_TAG_CTRL [5].OTPID_IDX[5:4]	2
0xBB004874	VLAN_PORT_TAG_CTRL [5].OTPID_KEEP[3:3]	1
0xBB004874	VLAN_PORT_TAG_CTRL [5].ITPID_IDX[2:1]	2
0xBB004874	VLAN_PORT_TAG_CTRL [5].ITPID_KEEP[0:0]	1
0xBB004878	VLAN_PORT_TAG_CTRL [6].RESERVED[31:14]	18
0xBB004878	VLAN_PORT_TAG_CTRL [6].EGR_OTAG_STS[13:12]	2
0xBB004878	VLAN_PORT_TAG_CTRL [6].EGR_ITAG_STS[11:10]	2
0xBB004878	VLAN_PORT_TAG_CTRL [6].EGR_OTAG_KEEP[9:9]	1
0xBB004878	VLAN_PORT_TAG_CTRL [6].EGR_ITAG_KEEP[8:8]	1
0xBB004878	VLAN_PORT_TAG_CTRL [6].IGR_OTAG_KEEP[7:7]	1
0xBB004878	VLAN_PORT_TAG_CTRL [6].IGR_ITAG_KEEP[6:6]	1
0xBB004878	VLAN_PORT_TAG_CTRL [6].OTPID_IDX[5:4]	2
0xBB004878	VLAN_PORT_TAG_CTRL [6].OTPID_KEEP[3:3]	1
0xBB004878	VLAN_PORT_TAG_CTRL [6].ITPID_IDX[2:1]	2
0xBB004878	VLAN_PORT_TAG_CTRL [6].ITPID_KEEP[0:0]	1
0xBB00487C	VLAN_PORT_TAG_CTRL [7].RESERVED[31:14]	18
0xBB00487C	VLAN_PORT_TAG_CTRL [7].EGR_OTAG_STS[13:12]	2
0xBB00487C	VLAN_PORT_TAG_CTRL [7].EGR_ITAG_STS[11:10]	2
0xBB00487C	VLAN_PORT_TAG_CTRL [7].EGR_OTAG_KEEP[9:9]	1
0xBB00487C	VLAN_PORT_TAG_CTRL [7].EGR_ITAG_KEEP[8:8]	1
0xBB00487C	VLAN_PORT_TAG_CTRL [7].IGR_OTAG_KEEP[7:7]	1
0xBB00487C	VLAN_PORT_TAG_CTRL [7].IGR_ITAG_KEEP[6:6]	1
0xBB00487C	VLAN_PORT_TAG_CTRL [7].OTPID_IDX[5:4]	2
0xBB00487C	VLAN_PORT_TAG_CTRL [7].OTPID_KEEP[3:3]	1
0xBB00487C	VLAN_PORT_TAG_CTRL [7].ITPID_IDX[2:1]	2
0xBB00487C	VLAN_PORT_TAG_CTRL [7].ITPID_KEEP[0:0]	1
0xBB004880	VLAN_PORT_TAG_CTRL [8].RESERVED[31:14]	18
0xBB004880	VLAN_PORT_TAG_CTRL [8].EGR_OTAG_STS[13:12]	2
0xBB004880	VLAN_PORT_TAG_CTRL [8].EGR_ITAG_STS[11:10]	2
0xBB004880	VLAN_PORT_TAG_CTRL [8].EGR_OTAG_KEEP[9:9]	1
0xBB004880	VLAN_PORT_TAG_CTRL [8].EGR_ITAG_KEEP[8:8]	1
0xBB004880	VLAN_PORT_TAG_CTRL [8].IGR_OTAG_KEEP[7:7]	1
0xBB004880	VLAN_PORT_TAG_CTRL [8].IGR_ITAG_KEEP[6:6]	1
0xBB004880	VLAN_PORT_TAG_CTRL [8].OTPID_IDX[5:4]	2
0xBB004880	VLAN_PORT_TAG_CTRL [8].OTPID_KEEP[3:3]	1
0xBB004880	VLAN_PORT_TAG_CTRL [8].ITPID_IDX[2:1]	2
0xBB004880	VLAN_PORT_TAG_CTRL [8].ITPID_KEEP[0:0]	1
0xBB004884	VLAN_PORT_TAG_CTRL [9].RESERVED[31:14]	18
0xBB004884	VLAN_PORT_TAG_CTRL [9].EGR_OTAG_STS[13:12]	2
0xBB004884	VLAN_PORT_TAG_CTRL [9].EGR_ITAG_STS[11:10]	2
0xBB004884	VLAN_PORT_TAG_CTRL [9].EGR_OTAG_KEEP[9:9]	1
0xBB004884	VLAN_PORT_TAG_CTRL [9].EGR_ITAG_KEEP[8:8]	1
0xBB004884	VLAN_PORT_TAG_CTRL [9].IGR_OTAG_KEEP[7:7]	1
0xBB004884	VLAN_PORT_TAG_CTRL [9].IGR_ITAG_KEEP[6:6]	1
0xBB004884	VLAN_PORT_TAG_CTRL [9].OTPID_IDX[5:4]	2
0xBB004884	VLAN_PORT_TAG_CTRL [9].OTPID_KEEP[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004884	VLAN_PORT_TAG_CTRL [9].ITPID_IDX[2:1]	2
0xBB004884	VLAN_PORT_TAG_CTRL [9].ITPID_KEEP[0:0]	1
0xBB004888	VLAN_PORT_TAG_CTRL [10].RESERVED[31:14]	18
0xBB004888	VLAN_PORT_TAG_CTRL [10].EGR_OTAG_STS[13:12]	2
0xBB004888	VLAN_PORT_TAG_CTRL [10].EGR_ITAG_STS[11:10]	2
0xBB004888	VLAN_PORT_TAG_CTRL [10].EGR_OTAG_KEEP[9:9]	1
0xBB004888	VLAN_PORT_TAG_CTRL [10].EGR_ITAG_KEEP[8:8]	1
0xBB004888	VLAN_PORT_TAG_CTRL [10].IGR_OTAG_KEEP[7:7]	1
0xBB004888	VLAN_PORT_TAG_CTRL [10].IGR_ITAG_KEEP[6:6]	1
0xBB004888	VLAN_PORT_TAG_CTRL [10].OTPID_IDX[5:4]	2
0xBB004888	VLAN_PORT_TAG_CTRL [10].OTPID_KEEP[3:3]	1
0xBB004888	VLAN_PORT_TAG_CTRL [10].ITPID_IDX[2:1]	2
0xBB004888	VLAN_PORT_TAG_CTRL [10].ITPID_KEEP[0:0]	1
0xBB00488C	VLAN_PORT_TAG_CTRL [11].RESERVED[31:14]	18
0xBB00488C	VLAN_PORT_TAG_CTRL [11].EGR_OTAG_STS[13:12]	2
0xBB00488C	VLAN_PORT_TAG_CTRL [11].EGR_ITAG_STS[11:10]	2
0xBB00488C	VLAN_PORT_TAG_CTRL [11].EGR_OTAG_KEEP[9:9]	1
0xBB00488C	VLAN_PORT_TAG_CTRL [11].EGR_ITAG_KEEP[8:8]	1
0xBB00488C	VLAN_PORT_TAG_CTRL [11].IGR_OTAG_KEEP[7:7]	1
0xBB00488C	VLAN_PORT_TAG_CTRL [11].IGR_ITAG_KEEP[6:6]	1
0xBB00488C	VLAN_PORT_TAG_CTRL [11].OTPID_IDX[5:4]	2
0xBB00488C	VLAN_PORT_TAG_CTRL [11].OTPID_KEEP[3:3]	1
0xBB00488C	VLAN_PORT_TAG_CTRL [11].ITPID_IDX[2:1]	2
0xBB00488C	VLAN_PORT_TAG_CTRL [11].ITPID_KEEP[0:0]	1
0xBB004890	VLAN_PORT_TAG_CTRL [12].RESERVED[31:14]	18
0xBB004890	VLAN_PORT_TAG_CTRL [12].EGR_OTAG_STS[13:12]	2
0xBB004890	VLAN_PORT_TAG_CTRL [12].EGR_ITAG_STS[11:10]	2
0xBB004890	VLAN_PORT_TAG_CTRL [12].EGR_OTAG_KEEP[9:9]	1
0xBB004890	VLAN_PORT_TAG_CTRL [12].EGR_ITAG_KEEP[8:8]	1
0xBB004890	VLAN_PORT_TAG_CTRL [12].IGR_OTAG_KEEP[7:7]	1
0xBB004890	VLAN_PORT_TAG_CTRL [12].IGR_ITAG_KEEP[6:6]	1
0xBB004890	VLAN_PORT_TAG_CTRL [12].OTPID_IDX[5:4]	2
0xBB004890	VLAN_PORT_TAG_CTRL [12].OTPID_KEEP[3:3]	1
0xBB004890	VLAN_PORT_TAG_CTRL [12].ITPID_IDX[2:1]	2
0xBB004890	VLAN_PORT_TAG_CTRL [12].ITPID_KEEP[0:0]	1
0xBB004894	VLAN_PORT_TAG_CTRL [13].RESERVED[31:14]	18
0xBB004894	VLAN_PORT_TAG_CTRL [13].EGR_OTAG_STS[13:12]	2
0xBB004894	VLAN_PORT_TAG_CTRL [13].EGR_ITAG_STS[11:10]	2
0xBB004894	VLAN_PORT_TAG_CTRL [13].EGR_OTAG_KEEP[9:9]	1
0xBB004894	VLAN_PORT_TAG_CTRL [13].EGR_ITAG_KEEP[8:8]	1
0xBB004894	VLAN_PORT_TAG_CTRL [13].IGR_OTAG_KEEP[7:7]	1
0xBB004894	VLAN_PORT_TAG_CTRL [13].IGR_ITAG_KEEP[6:6]	1
0xBB004894	VLAN_PORT_TAG_CTRL [13].OTPID_IDX[5:4]	2
0xBB004894	VLAN_PORT_TAG_CTRL [13].OTPID_KEEP[3:3]	1
0xBB004894	VLAN_PORT_TAG_CTRL [13].ITPID_IDX[2:1]	2
0xBB004894	VLAN_PORT_TAG_CTRL [13].ITPID_KEEP[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB004898	VLAN_PORT_TAG_CTRL [14].RESERVED[31:14]	18
0xBB004898	VLAN_PORT_TAG_CTRL [14].EGR_OTAG_STS[13:12]	2
0xBB004898	VLAN_PORT_TAG_CTRL [14].EGR_ITAG_STS[11:10]	2
0xBB004898	VLAN_PORT_TAG_CTRL [14].EGR_OTAG_KEEP[9:9]	1
0xBB004898	VLAN_PORT_TAG_CTRL [14].EGR_ITAG_KEEP[8:8]	1
0xBB004898	VLAN_PORT_TAG_CTRL [14].IGR_OTAG_KEEP[7:7]	1
0xBB004898	VLAN_PORT_TAG_CTRL [14].IGR_ITAG_KEEP[6:6]	1
0xBB004898	VLAN_PORT_TAG_CTRL [14].OTPID_IDX[5:4]	2
0xBB004898	VLAN_PORT_TAG_CTRL [14].OTPID_KEEP[3:3]	1
0xBB004898	VLAN_PORT_TAG_CTRL [14].ITPID_IDX[2:1]	2
0xBB004898	VLAN_PORT_TAG_CTRL [14].ITPID_KEEP[0:0]	1
0xBB00489C	VLAN_PORT_TAG_CTRL [15].RESERVED[31:14]	18
0xBB00489C	VLAN_PORT_TAG_CTRL [15].EGR_OTAG_STS[13:12]	2
0xBB00489C	VLAN_PORT_TAG_CTRL [15].EGR_ITAG_STS[11:10]	2
0xBB00489C	VLAN_PORT_TAG_CTRL [15].EGR_OTAG_KEEP[9:9]	1
0xBB00489C	VLAN_PORT_TAG_CTRL [15].EGR_ITAG_KEEP[8:8]	1
0xBB00489C	VLAN_PORT_TAG_CTRL [15].IGR_OTAG_KEEP[7:7]	1
0xBB00489C	VLAN_PORT_TAG_CTRL [15].IGR_ITAG_KEEP[6:6]	1
0xBB00489C	VLAN_PORT_TAG_CTRL [15].OTPID_IDX[5:4]	2
0xBB00489C	VLAN_PORT_TAG_CTRL [15].OTPID_KEEP[3:3]	1
0xBB00489C	VLAN_PORT_TAG_CTRL [15].ITPID_IDX[2:1]	2
0xBB00489C	VLAN_PORT_TAG_CTRL [15].ITPID_KEEP[0:0]	1
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].RESERVED[31:14]	18
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].EGR_OTAG_STS[13:12]	2
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].EGR_ITAG_STS[11:10]	2
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].EGR_OTAG_KEEP[9:9]	1
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].EGR_ITAG_KEEP[8:8]	1
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].IGR_OTAG_KEEP[7:7]	1
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].IGR_ITAG_KEEP[6:6]	1
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].OTPID_IDX[5:4]	2
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].OTPID_KEEP[3:3]	1
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].ITPID_IDX[2:1]	2
0xBB0048A0	VLAN_PORT_TAG_CTRL [16].ITPID_KEEP[0:0]	1
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].RESERVED[31:14]	18
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].EGR_OTAG_STS[13:12]	2
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].EGR_ITAG_STS[11:10]	2
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].EGR_OTAG_KEEP[9:9]	1
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].EGR_ITAG_KEEP[8:8]	1
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].IGR_OTAG_KEEP[7:7]	1
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].IGR_ITAG_KEEP[6:6]	1
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].OTPID_IDX[5:4]	2
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].OTPID_KEEP[3:3]	1
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].ITPID_IDX[2:1]	2
0xBB0048A4	VLAN_PORT_TAG_CTRL [17].ITPID_KEEP[0:0]	1
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].RESERVED[31:14]	18
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].EGR_OTAG_STS[13:12]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].EGR_ITAG_STS[11:10]	2
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].EGR_OTAG_KEEP[9:9]	1
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].EGR_ITAG_KEEP[8:8]	1
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].IGR_OTAG_KEEP[7:7]	1
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].IGR_ITAG_KEEP[6:6]	1
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].OTPID_IDX[5:4]	2
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].OTPID_KEEP[3:3]	1
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].ITPID_IDX[2:1]	2
0xBB0048A8	VLAN_PORT_TAG_CTRL [18].ITPID_KEEP[0:0]	1
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].RESERVED[31:14]	18
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].EGR_OTAG_STS[13:12]	2
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].EGR_ITAG_STS[11:10]	2
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].EGR_OTAG_KEEP[9:9]	1
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].EGR_ITAG_KEEP[8:8]	1
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].IGR_OTAG_KEEP[7:7]	1
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].IGR_ITAG_KEEP[6:6]	1
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].OTPID_IDX[5:4]	2
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].OTPID_KEEP[3:3]	1
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].ITPID_IDX[2:1]	2
0xBB0048AC	VLAN_PORT_TAG_CTRL [19].ITPID_KEEP[0:0]	1
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].RESERVED[31:14]	18
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].EGR_OTAG_STS[13:12]	2
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].EGR_ITAG_STS[11:10]	2
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].EGR_OTAG_KEEP[9:9]	1
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].EGR_ITAG_KEEP[8:8]	1
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].IGR_OTAG_KEEP[7:7]	1
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].IGR_ITAG_KEEP[6:6]	1
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].OTPID_IDX[5:4]	2
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].OTPID_KEEP[3:3]	1
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].ITPID_IDX[2:1]	2
0xBB0048B0	VLAN_PORT_TAG_CTRL [20].ITPID_KEEP[0:0]	1
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].RESERVED[31:14]	18
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].EGR_OTAG_STS[13:12]	2
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].EGR_ITAG_STS[11:10]	2
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].EGR_OTAG_KEEP[9:9]	1
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].EGR_ITAG_KEEP[8:8]	1
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].IGR_OTAG_KEEP[7:7]	1
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].IGR_ITAG_KEEP[6:6]	1
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].OTPID_IDX[5:4]	2
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].OTPID_KEEP[3:3]	1
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].ITPID_IDX[2:1]	2
0xBB0048B4	VLAN_PORT_TAG_CTRL [21].ITPID_KEEP[0:0]	1
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].RESERVED[31:14]	18
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].EGR_OTAG_STS[13:12]	2
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].EGR_ITAG_STS[11:10]	2
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].EGR_OTAG_KEEP[9:9]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].EGR_ITAG_KEEP[8:8]	1
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].IGR_OTAG_KEEP[7:7]	1
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].IGR_ITAG_KEEP[6:6]	1
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].OTPID_IDX[5:4]	2
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].OTPID_KEEP[3:3]	1
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].ITPID_IDX[2:1]	2
0xBB0048B8	VLAN_PORT_TAG_CTRL [22].ITPID_KEEP[0:0]	1
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].RESERVED[31:14]	18
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].EGR_OTAG_STS[13:12]	2
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].EGR_ITAG_STS[11:10]	2
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].EGR_OTAG_KEEP[9:9]	1
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].EGR_ITAG_KEEP[8:8]	1
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].IGR_OTAG_KEEP[7:7]	1
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].IGR_ITAG_KEEP[6:6]	1
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].OTPID_IDX[5:4]	2
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].OTPID_KEEP[3:3]	1
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].ITPID_IDX[2:1]	2
0xBB0048BC	VLAN_PORT_TAG_CTRL [23].ITPID_KEEP[0:0]	1
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].RESERVED[31:14]	18
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].EGR_OTAG_STS[13:12]	2
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].EGR_ITAG_STS[11:10]	2
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].EGR_OTAG_KEEP[9:9]	1
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].EGR_ITAG_KEEP[8:8]	1
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].IGR_OTAG_KEEP[7:7]	1
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].IGR_ITAG_KEEP[6:6]	1
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].OTPID_IDX[5:4]	2
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].OTPID_KEEP[3:3]	1
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].ITPID_IDX[2:1]	2
0xBB0048C0	VLAN_PORT_TAG_CTRL [24].ITPID_KEEP[0:0]	1
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].RESERVED[31:14]	18
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].EGR_OTAG_STS[13:12]	2
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].EGR_ITAG_STS[11:10]	2
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].EGR_OTAG_KEEP[9:9]	1
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].EGR_ITAG_KEEP[8:8]	1
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].IGR_OTAG_KEEP[7:7]	1
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].IGR_ITAG_KEEP[6:6]	1
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].OTPID_IDX[5:4]	2
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].OTPID_KEEP[3:3]	1
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].ITPID_IDX[2:1]	2
0xBB0048C4	VLAN_PORT_TAG_CTRL [25].ITPID_KEEP[0:0]	1
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].RESERVED[31:14]	18
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].EGR_OTAG_STS[13:12]	2
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].EGR_ITAG_STS[11:10]	2
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].EGR_OTAG_KEEP[9:9]	1
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].EGR_ITAG_KEEP[8:8]	1
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].IGR_OTAG_KEEP[7:7]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].IGR_ITAG_KEEP[6:6]	1
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].OTPID_IDX[5:4]	2
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].OTPID_KEEP[3:3]	1
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].ITPID_IDX[2:1]	2
0xBB0048C8	VLAN_PORT_TAG_CTRL [26].ITPID_KEEP[0:0]	1
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].RESERVED[31:14]	18
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].EGR_OTAG_STS[13:12]	2
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].EGR_ITAG_STS[11:10]	2
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].EGR_OTAG_KEEP[9:9]	1
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].EGR_ITAG_KEEP[8:8]	1
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].IGR_OTAG_KEEP[7:7]	1
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].IGR_ITAC_KEEP[6:6]	1
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].OTPID_IDX[5:4]	2
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].OTPID_KEEP[3:3]	1
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].ITPID_IDX[2:1]	2
0xBB0048CC	VLAN_PORT_TAG_CTRL [27].ITPID_KEEP[0:0]	1
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].RESERVED[31:14]	18
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].EGR_OTAG_STS[13:12]	2
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].EGR_ITAG_STS[11:10]	2
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].EGR_OTAG_KEEP[9:9]	1
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].EGR_ITAG_KEEP[8:8]	1
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].IGR_OTAG_KEEP[7:7]	1
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].IGR_ITAG_KEEP[6:6]	1
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].OTPID_IDX[5:4]	2
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].OTPID_KEEP[3:3]	1
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].ITPID_IDX[2:1]	2
0xBB0048D0	VLAN_PORT_TAG_CTRL [28].ITPID_KEEP[0:0]	1
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].RESERVED[31:14]	18
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].EGR_OTAG_STS[13:12]	2
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].EGR_ITAG_STS[11:10]	2
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].EGR_OTAG_KEEP[9:9]	1
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].EGR_ITAG_KEEP[8:8]	1
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].IGR_OTAG_KEEP[7:7]	1
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].IGR_ITAG_KEEP[6:6]	1
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].OTPID_IDX[5:4]	2
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].OTPID_KEEP[3:3]	1
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].ITPID_IDX[2:1]	2
0xBB0048D4	VLAN_PORT_TAG_CTRL [29].ITPID_KEEP[0:0]	1
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].RESERVED[31:14]	18
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].EGR_OTAG_STS[13:12]	2
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].EGR_ITAG_STS[11:10]	2
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].EGR_OTAG_KEEP[9:9]	1
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].EGR_ITAG_KEEP[8:8]	1
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].IGR_OTAG_KEEP[7:7]	1
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].IGR_ITAG_KEEP[6:6]	1
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].OTPID_IDX[5:4]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].OTPID_KEEP[3:3]	1
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].ITPID_IDX[2:1]	2
0xBB0048D8	VLAN_PORT_TAG_CTRL [30].ITPID_KEEP[0:0]	1
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].RESERVED[31:14]	18
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].EGR_OTAG_STS[13:12]	2
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].EGR_ITAG_STS[11:10]	2
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].EGR_OTAG_KEEP[9:9]	1
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].EGR_ITAG_KEEP[8:8]	1
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].IGR_OTAG_KEEP[7:7]	1
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].IGR_ITAG_KEEP[6:6]	1
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].OTPID_IDX[5:4]	2
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].OTPID_KEEP[3:3]	1
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].ITPID_IDX[2:1]	2
0xBB0048DC	VLAN_PORT_TAG_CTRL [31].ITPID_KEEP[0:0]	1
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].RESERVED[31:14]	18
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].EGR_OTAG_STS[13:12]	2
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].EGR_ITAG_STS[11:10]	2
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].EGR_OTAG_KEEP[9:9]	1
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].EGR_ITAG_KEEP[8:8]	1
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].IGR_OTAG_KEEP[7:7]	1
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].IGR_ITAG_KEEP[6:6]	1
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].OTPID_IDX[5:4]	2
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].OTPID_KEEP[3:3]	1
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].ITPID_IDX[2:1]	2
0xBB0048E0	VLAN_PORT_TAG_CTRL [32].ITPID_KEEP[0:0]	1
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].RESERVED[31:14]	18
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].EGR_OTAG_STS[13:12]	2
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].EGR_ITAG_STS[11:10]	2
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].EGR_OTAG_KEEP[9:9]	1
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].EGR_ITAG_KEEP[8:8]	1
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].IGR_OTAG_KEEP[7:7]	1
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].IGR_ITAG_KEEP[6:6]	1
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].OTPID_IDX[5:4]	2
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].OTPID_KEEP[3:3]	1
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].ITPID_IDX[2:1]	2
0xBB0048E4	VLAN_PORT_TAG_CTRL [33].ITPID_KEEP[0:0]	1
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].RESERVED[31:14]	18
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].EGR_OTAG_STS[13:12]	2
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].EGR_ITAG_STS[11:10]	2
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].EGR_OTAG_KEEP[9:9]	1
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].EGR_ITAG_KEEP[8:8]	1
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].IGR_OTAG_KEEP[7:7]	1
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].IGR_ITAG_KEEP[6:6]	1
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].OTPID_IDX[5:4]	2
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].OTPID_KEEP[3:3]	1
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].ITPID_IDX[2:1]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0048E8	VLAN_PORT_TAG_CTRL [34].ITPID_KEEP[0:0]	1
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].RESERVED[31:14]	18
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].EGR_OTAG_STS[13:12]	2
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].EGR_ITAG_STS[11:10]	2
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].EGR_OTAG_KEEP[9:9]	1
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].EGR_ITAG_KEEP[8:8]	1
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].IGR_OTAG_KEEP[7:7]	1
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].IGR_ITAG_KEEP[6:6]	1
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].OTPID_IDX[5:4]	2
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].OTPID_KEEP[3:3]	1
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].ITPID_IDX[2:1]	2
0xBB0048EC	VLAN_PORT_TAG_CTRL [35].ITPID_KEEP[0:0]	1
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].RESERVED[31:14]	18
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].EGR_OTAG_STS[13:12]	2
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].EGR_ITAG_STS[11:10]	2
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].EGR_OTAG_KEEP[9:9]	1
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].EGR_ITAG_KEEP[8:8]	1
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].IGR_OTAG_KEEP[7:7]	1
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].IGR_ITAG_KEEP[6:6]	1
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].OTPID_IDX[5:4]	2
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].OTPID_KEEP[3:3]	1
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].ITPID_IDX[2:1]	2
0xBB0048F0	VLAN_PORT_TAG_CTRL [36].ITPID_KEEP[0:0]	1
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].RESERVED[31:14]	18
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].EGR_OTAG_STS[13:12]	2
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].EGR_ITAG_STS[11:10]	2
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].EGR_OTAG_KEEP[9:9]	1
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].EGR_ITAG_KEEP[8:8]	1
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].IGR_OTAG_KEEP[7:7]	1
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].IGR_ITAG_KEEP[6:6]	1
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].OTPID_IDX[5:4]	2
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].OTPID_KEEP[3:3]	1
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].ITPID_IDX[2:1]	2
0xBB0048F4	VLAN_PORT_TAG_CTRL [37].ITPID_KEEP[0:0]	1
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].RESERVED[31:14]	18
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].EGR_OTAG_STS[13:12]	2
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].EGR_ITAG_STS[11:10]	2
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].EGR_OTAG_KEEP[9:9]	1
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].EGR_ITAG_KEEP[8:8]	1
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].IGR_OTAG_KEEP[7:7]	1
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].IGR_ITAG_KEEP[6:6]	1
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].OTPID_IDX[5:4]	2
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].OTPID_KEEP[3:3]	1
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].ITPID_IDX[2:1]	2
0xBB0048F8	VLAN_PORT_TAG_CTRL [38].ITPID_KEEP[0:0]	1
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].RESERVED[31:14]	18

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].EGR_OTAG_STS[13:12]	2
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].EGR_ITAG_STS[11:10]	2
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].EGR_OTAG_KEEP[9:9]	1
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].EGR_ITAG_KEEP[8:8]	1
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].IGR_OTAG_KEEP[7:7]	1
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].IGR_ITAG_KEEP[6:6]	1
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].OTPID_IDX[5:4]	2
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].OTPID_KEEP[3:3]	1
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].ITPID_IDX[2:1]	2
0xBB0048FC	VLAN_PORT_TAG_CTRL [39].ITPID_KEEP[0:0]	1
0xBB004900	VLAN_PORT_TAG_CTRL [40].RESERVED[31:14]	18
0xBB004900	VLAN_PORT_TAG_CTRL [40].EGR_OTAG_STS[13:12]	2
0xBB004900	VLAN_PORT_TAG_CTRL [40].EGR_ITAG_STS[11:10]	2
0xBB004900	VLAN_PORT_TAG_CTRL [40].EGR_OTAG_KEEP[9:9]	1
0xBB004900	VLAN_PORT_TAG_CTRL [40].EGR_ITAG_KEEP[8:8]	1
0xBB004900	VLAN_PORT_TAG_CTRL [40].IGR_OTAG_KEEP[7:7]	1
0xBB004900	VLAN_PORT_TAG_CTRL [40].IGR_ITAG_KEEP[6:6]	1
0xBB004900	VLAN_PORT_TAG_CTRL [40].OTPID_IDX[5:4]	2
0xBB004900	VLAN_PORT_TAG_CTRL [40].OTPID_KEEP[3:3]	1
0xBB004900	VLAN_PORT_TAG_CTRL [40].ITPID_IDX[2:1]	2
0xBB004900	VLAN_PORT_TAG_CTRL [40].ITPID_KEEP[0:0]	1
0xBB004904	VLAN_PORT_TAG_CTRL [41].RESERVED[31:14]	18
0xBB004904	VLAN_PORT_TAG_CTRL [41].EGR_OTAG_STS[13:12]	2
0xBB004904	VLAN_PORT_TAG_CTRL [41].EGR_ITAG_STS[11:10]	2
0xBB004904	VLAN_PORT_TAG_CTRL [41].EGR_OTAG_KEEP[9:9]	1
0xBB004904	VLAN_PORT_TAG_CTRL [41].EGR_ITAG_KEEP[8:8]	1
0xBB004904	VLAN_PORT_TAG_CTRL [41].IGR_OTAG_KEEP[7:7]	1
0xBB004904	VLAN_PORT_TAG_CTRL [41].IGR_ITAG_KEEP[6:6]	1
0xBB004904	VLAN_PORT_TAG_CTRL [41].OTPID_IDX[5:4]	2
0xBB004904	VLAN_PORT_TAG_CTRL [41].OTPID_KEEP[3:3]	1
0xBB004904	VLAN_PORT_TAG_CTRL [41].ITPID_IDX[2:1]	2
0xBB004904	VLAN_PORT_TAG_CTRL [41].ITPID_KEEP[0:0]	1
0xBB004908	VLAN_PORT_TAG_CTRL [42].RESERVED[31:14]	18
0xBB004908	VLAN_PORT_TAG_CTRL [42].EGR_OTAG_STS[13:12]	2
0xBB004908	VLAN_PORT_TAG_CTRL [42].EGR_ITAG_STS[11:10]	2
0xBB004908	VLAN_PORT_TAG_CTRL [42].EGR_OTAG_KEEP[9:9]	1
0xBB004908	VLAN_PORT_TAG_CTRL [42].EGR_ITAG_KEEP[8:8]	1
0xBB004908	VLAN_PORT_TAG_CTRL [42].IGR_OTAG_KEEP[7:7]	1
0xBB004908	VLAN_PORT_TAG_CTRL [42].IGR_ITAG_KEEP[6:6]	1
0xBB004908	VLAN_PORT_TAG_CTRL [42].OTPID_IDX[5:4]	2
0xBB004908	VLAN_PORT_TAG_CTRL [42].OTPID_KEEP[3:3]	1
0xBB004908	VLAN_PORT_TAG_CTRL [42].ITPID_IDX[2:1]	2
0xBB004908	VLAN_PORT_TAG_CTRL [42].ITPID_KEEP[0:0]	1
0xBB00490C	VLAN_PORT_TAG_CTRL [43].RESERVED[31:14]	18
0xBB00490C	VLAN_PORT_TAG_CTRL [43].EGR_OTAG_STS[13:12]	2
0xBB00490C	VLAN_PORT_TAG_CTRL [43].EGR_ITAG_STS[11:10]	2

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00490C	VLAN_PORT_TAG_CTRL [43].EGR_OTAG_KEEP[9:9]	1
0xBB00490C	VLAN_PORT_TAG_CTRL [43].EGR_ITAG_KEEP[8:8]	1
0xBB00490C	VLAN_PORT_TAG_CTRL [43].IGR_OTAG_KEEP[7:7]	1
0xBB00490C	VLAN_PORT_TAG_CTRL [43].IGR_ITAG_KEEP[6:6]	1
0xBB00490C	VLAN_PORT_TAG_CTRL [43].OTPID_IDX[5:4]	2
0xBB00490C	VLAN_PORT_TAG_CTRL [43].OTPID_KEEP[3:3]	1
0xBB00490C	VLAN_PORT_TAG_CTRL [43].ITPID_IDX[2:1]	2
0xBB00490C	VLAN_PORT_TAG_CTRL [43].ITPID_KEEP[0:0]	1
0xBB004910	VLAN_PORT_TAG_CTRL [44].RESERVED[31:14]	18
0xBB004910	VLAN_PORT_TAG_CTRL [44].EGR_OTAG_STS[13:12]	2
0xBB004910	VLAN_PORT_TAG_CTRL [44].EGR_ITAG_STS[11:10]	2
0xBB004910	VLAN_PORT_TAG_CTRL [44].EGR_OTAG_KEEP[9:9]	1
0xBB004910	VLAN_PORT_TAG_CTRL [44].EGR_ITAG_KEEP[8:8]	1
0xBB004910	VLAN_PORT_TAG_CTRL [44].IGR_OTAG_KEEP[7:7]	1
0xBB004910	VLAN_PORT_TAG_CTRL [44].IGR_ITAG_KEEP[6:6]	1
0xBB004910	VLAN_PORT_TAG_CTRL [44].OTPID_IDX[5:4]	2
0xBB004910	VLAN_PORT_TAG_CTRL [44].OTPID_KEEP[3:3]	1
0xBB004910	VLAN_PORT_TAG_CTRL [44].ITPID_IDX[2:1]	2
0xBB004910	VLAN_PORT_TAG_CTRL [44].ITPID_KEEP[0:0]	1
0xBB004914	VLAN_PORT_TAG_CTRL [45].RESERVED[31:14]	18
0xBB004914	VLAN_PORT_TAG_CTRL [45].EGR_OTAG_STS[13:12]	2
0xBB004914	VLAN_PORT_TAG_CTRL [45].EGR_ITAG_STS[11:10]	2
0xBB004914	VLAN_PORT_TAG_CTRL [45].EGR_OTAG_KEEP[9:9]	1
0xBB004914	VLAN_PORT_TAG_CTRL [45].EGR_ITAG_KEEP[8:8]	1
0xBB004914	VLAN_PORT_TAG_CTRL [45].IGR_OTAG_KEEP[7:7]	1
0xBB004914	VLAN_PORT_TAG_CTRL [45].IGR_ITAG_KEEP[6:6]	1
0xBB004914	VLAN_PORT_TAG_CTRL [45].OTPID_IDX[5:4]	2
0xBB004914	VLAN_PORT_TAG_CTRL [45].OTPID_KEEP[3:3]	1
0xBB004914	VLAN_PORT_TAG_CTRL [45].ITPID_IDX[2:1]	2
0xBB004914	VLAN_PORT_TAG_CTRL [45].ITPID_KEEP[0:0]	1
0xBB004918	VLAN_PORT_TAG_CTRL [46].RESERVED[31:14]	18
0xBB004918	VLAN_PORT_TAG_CTRL [46].EGR_OTAG_STS[13:12]	2
0xBB004918	VLAN_PORT_TAG_CTRL [46].EGR_ITAG_STS[11:10]	2
0xBB004918	VLAN_PORT_TAG_CTRL [46].EGR_OTAG_KEEP[9:9]	1
0xBB004918	VLAN_PORT_TAG_CTRL [46].EGR_ITAG_KEEP[8:8]	1
0xBB004918	VLAN_PORT_TAG_CTRL [46].IGR_OTAG_KEEP[7:7]	1
0xBB004918	VLAN_PORT_TAG_CTRL [46].IGR_ITAG_KEEP[6:6]	1
0xBB004918	VLAN_PORT_TAG_CTRL [46].OTPID_IDX[5:4]	2
0xBB004918	VLAN_PORT_TAG_CTRL [46].OTPID_KEEP[3:3]	1
0xBB004918	VLAN_PORT_TAG_CTRL [46].ITPID_IDX[2:1]	2
0xBB004918	VLAN_PORT_TAG_CTRL [46].ITPID_KEEP[0:0]	1
0xBB00491C	VLAN_PORT_TAG_CTRL [47].RESERVED[31:14]	18
0xBB00491C	VLAN_PORT_TAG_CTRL [47].EGR_OTAG_STS[13:12]	2
0xBB00491C	VLAN_PORT_TAG_CTRL [47].EGR_ITAG_STS[11:10]	2
0xBB00491C	VLAN_PORT_TAG_CTRL [47].EGR_OTAG_KEEP[9:9]	1
0xBB00491C	VLAN_PORT_TAG_CTRL [47].EGR_ITAG_KEEP[8:8]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00491C	VLAN_PORT_TAG_CTRL [47].IGR_OTAG_KEEP[7:7]	1
0xBB00491C	VLAN_PORT_TAG_CTRL [47].IGR_ITAG_KEEP[6:6]	1
0xBB00491C	VLAN_PORT_TAG_CTRL [47].OTPID_IDX[5:4]	2
0xBB00491C	VLAN_PORT_TAG_CTRL [47].OTPID_KEEP[3:3]	1
0xBB00491C	VLAN_PORT_TAG_CTRL [47].ITPID_IDX[2:1]	2
0xBB00491C	VLAN_PORT_TAG_CTRL [47].ITPID_KEEP[0:0]	1
0xBB004920	VLAN_PORT_TAG_CTRL [48].RESERVED[31:14]	18
0xBB004920	VLAN_PORT_TAG_CTRL [48].EGR_OTAG_STS[13:12]	2
0xBB004920	VLAN_PORT_TAG_CTRL [48].EGR_ITAG_STS[11:10]	2
0xBB004920	VLAN_PORT_TAG_CTRL [48].EGR_OTAG_KEEP[9:9]	1
0xBB004920	VLAN_PORT_TAG_CTRL [48].EGR_ITAG_KEEP[8:8]	1
0xBB004920	VLAN_PORT_TAG_CTRL [48].IGR_OTAG_KEEP[7:7]	1
0xBB004920	VLAN_PORT_TAG_CTRL [48].IGR_ITAG_KEEP[6:6]	1
0xBB004920	VLAN_PORT_TAG_CTRL [48].OTPID_IDX[5:4]	2
0xBB004920	VLAN_PORT_TAG_CTRL [48].OTPID_KEEP[3:3]	1
0xBB004920	VLAN_PORT_TAG_CTRL [48].ITPID_IDX[2:1]	2
0xBB004920	VLAN_PORT_TAG_CTRL [48].ITPID_KEEP[0:0]	1
0xBB004924	VLAN_PORT_TAG_CTRL [49].RESERVED[31:14]	18
0xBB004924	VLAN_PORT_TAG_CTRL [49].EGR_OTAG_STS[13:12]	2
0xBB004924	VLAN_PORT_TAG_CTRL [49].EGR_ITAG_STS[11:10]	2
0xBB004924	VLAN_PORT_TAG_CTRL [49].EGR_OTAG_KEEP[9:9]	1
0xBB004924	VLAN_PORT_TAG_CTRL [49].EGR_ITAG_KEEP[8:8]	1
0xBB004924	VLAN_PORT_TAG_CTRL [49].IGR_OTAG_KEEP[7:7]	1
0xBB004924	VLAN_PORT_TAG_CTRL [49].IGR_ITAG_KEEP[6:6]	1
0xBB004924	VLAN_PORT_TAG_CTRL [49].OTPID_IDX[5:4]	2
0xBB004924	VLAN_PORT_TAG_CTRL [49].OTPID_KEEP[3:3]	1
0xBB004924	VLAN_PORT_TAG_CTRL [49].ITPID_IDX[2:1]	2
0xBB004924	VLAN_PORT_TAG_CTRL [49].ITPID_KEEP[0:0]	1
0xBB004928	VLAN_PORT_TAG_CTRL [50].RESERVED[31:14]	18
0xBB004928	VLAN_PORT_TAG_CTRL [50].EGR_OTAG_STS[13:12]	2
0xBB004928	VLAN_PORT_TAG_CTRL [50].EGR_ITAG_STS[11:10]	2
0xBB004928	VLAN_PORT_TAG_CTRL [50].EGR_OTAG_KEEP[9:9]	1
0xBB004928	VLAN_PORT_TAG_CTRL [50].EGR_ITAG_KEEP[8:8]	1
0xBB004928	VLAN_PORT_TAG_CTRL [50].IGR_OTAG_KEEP[7:7]	1
0xBB004928	VLAN_PORT_TAG_CTRL [50].IGR_ITAG_KEEP[6:6]	1
0xBB004928	VLAN_PORT_TAG_CTRL [50].OTPID_IDX[5:4]	2
0xBB004928	VLAN_PORT_TAG_CTRL [50].OTPID_KEEP[3:3]	1
0xBB004928	VLAN_PORT_TAG_CTRL [50].ITPID_IDX[2:1]	2
0xBB004928	VLAN_PORT_TAG_CTRL [50].ITPID_KEEP[0:0]	1
0xBB00492C	VLAN_PORT_TAG_CTRL [51].RESERVED[31:14]	18
0xBB00492C	VLAN_PORT_TAG_CTRL [51].EGR_OTAG_STS[13:12]	2
0xBB00492C	VLAN_PORT_TAG_CTRL [51].EGR_ITAG_STS[11:10]	2
0xBB00492C	VLAN_PORT_TAG_CTRL [51].EGR_OTAG_KEEP[9:9]	1
0xBB00492C	VLAN_PORT_TAG_CTRL [51].EGR_ITAG_KEEP[8:8]	1
0xBB00492C	VLAN_PORT_TAG_CTRL [51].IGR_OTAG_KEEP[7:7]	1
0xBB00492C	VLAN_PORT_TAG_CTRL [51].IGR_ITAG_KEEP[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00492C	VLAN_PORT_TAG_CTRL [51].OTPID_IDX[5:4]	2
0xBB00492C	VLAN_PORT_TAG_CTRL [51].OTPID_KEEP[3:3]	1
0xBB00492C	VLAN_PORT_TAG_CTRL [51].ITPID_IDX[2:1]	2
0xBB00492C	VLAN_PORT_TAG_CTRL [51].ITPID_KEEP[0:0]	1
0xBB004930	VLAN_PORT_TAG_CTRL [52].RESERVED[31:14]	18
0xBB004930	VLAN_PORT_TAG_CTRL [52].EGR_OTAG_STS[13:12]	2
0xBB004930	VLAN_PORT_TAG_CTRL [52].EGR_ITAG_STS[11:10]	2
0xBB004930	VLAN_PORT_TAG_CTRL [52].EGR_OTAG_KEEP[9:9]	1
0xBB004930	VLAN_PORT_TAG_CTRL [52].EGR_ITAG_KEEP[8:8]	1
0xBB004930	VLAN_PORT_TAG_CTRL [52].IGR_OTAG_KEEP[7:7]	1
0xBB004930	VLAN_PORT_TAG_CTRL [52].IGR_ITAG_KEEP[6:6]	1
0xBB004930	VLAN_PORT_TAG_CTRL [52].OTPID_IDX[5:4]	2
0xBB004930	VLAN_PORT_TAG_CTRL [52].OTPID_KEEP[3:3]	1
0xBB004930	VLAN_PORT_TAG_CTRL [52].ITPID_IDX[2:1]	2
0xBB004930	VLAN_PORT_TAG_CTRL [52].ITPID_KEEP[0:0]	1
0xBB004934	VLAN_PORT_TAG_CTRL [53].RESERVED[31:14]	18
0xBB004934	VLAN_PORT_TAG_CTRL [53].EGR_OTAG_STS[13:12]	2
0xBB004934	VLAN_PORT_TAG_CTRL [53].EGR_ITAG_STS[11:10]	2
0xBB004934	VLAN_PORT_TAG_CTRL [53].EGR_OTAG_KEEP[9:9]	1
0xBB004934	VLAN_PORT_TAG_CTRL [53].EGR_ITAG_KEEP[8:8]	1
0xBB004934	VLAN_PORT_TAG_CTRL [53].IGR_OTAG_KEEP[7:7]	1
0xBB004934	VLAN_PORT_TAG_CTRL [53].IGR_ITAG_KEEP[6:6]	1
0xBB004934	VLAN_PORT_TAG_CTRL [53].OTPID_IDX[5:4]	2
0xBB004934	VLAN_PORT_TAG_CTRL [53].OTPID_KEEP[3:3]	1
0xBB004934	VLAN_PORT_TAG_CTRL [53].ITPID_IDX[2:1]	2
0xBB004934	VLAN_PORT_TAG_CTRL [53].ITPID_KEEP[0:0]	1
0xBB004938	VLAN_PORT_TAG_CTRL [54].RESERVED[31:14]	18
0xBB004938	VLAN_PORT_TAG_CTRL [54].EGR_OTAG_STS[13:12]	2
0xBB004938	VLAN_PORT_TAG_CTRL [54].EGR_ITAG_STS[11:10]	2
0xBB004938	VLAN_PORT_TAG_CTRL [54].EGR_OTAG_KEEP[9:9]	1
0xBB004938	VLAN_PORT_TAG_CTRL [54].EGR_ITAG_KEEP[8:8]	1
0xBB004938	VLAN_PORT_TAG_CTRL [54].IGR_OTAG_KEEP[7:7]	1
0xBB004938	VLAN_PORT_TAG_CTRL [54].IGR_ITAG_KEEP[6:6]	1
0xBB004938	VLAN_PORT_TAG_CTRL [54].OTPID_IDX[5:4]	2
0xBB004938	VLAN_PORT_TAG_CTRL [54].OTPID_KEEP[3:3]	1
0xBB004938	VLAN_PORT_TAG_CTRL [54].ITPID_IDX[2:1]	2
0xBB004938	VLAN_PORT_TAG_CTRL [54].ITPID_KEEP[0:0]	1
0xBB00493C	VLAN_PORT_TAG_CTRL [55].RESERVED[31:14]	18
0xBB00493C	VLAN_PORT_TAG_CTRL [55].EGR_OTAG_STS[13:12]	2
0xBB00493C	VLAN_PORT_TAG_CTRL [55].EGR_ITAG_STS[11:10]	2
0xBB00493C	VLAN_PORT_TAG_CTRL [55].EGR_OTAG_KEEP[9:9]	1
0xBB00493C	VLAN_PORT_TAG_CTRL [55].EGR_ITAG_KEEP[8:8]	1
0xBB00493C	VLAN_PORT_TAG_CTRL [55].IGR_OTAG_KEEP[7:7]	1
0xBB00493C	VLAN_PORT_TAG_CTRL [55].IGR_ITAG_KEEP[6:6]	1
0xBB00493C	VLAN_PORT_TAG_CTRL [55].OTPID_IDX[5:4]	2
0xBB00493C	VLAN_PORT_TAG_CTRL [55].OTPID_KEEP[3:3]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB00493C	VLAN_PORT_TAG_CTRL [55].ITPID_IDX[2:1]	2
0xBB00493C	VLAN_PORT_TAG_CTRL [55].ITPID_KEEP[0:0]	1
0xBB004940	VLAN_PORT_TAG_CTRL [56].RESERVED[31:14]	18
0xBB004940	VLAN_PORT_TAG_CTRL [56].EGR_OTAG_STS[13:12]	2
0xBB004940	VLAN_PORT_TAG_CTRL [56].EGR_ITAG_STS[11:10]	2
0xBB004940	VLAN_PORT_TAG_CTRL [56].EGR_OTAG_KEEP[9:9]	1
0xBB004940	VLAN_PORT_TAG_CTRL [56].EGR_ITAG_KEEP[8:8]	1
0xBB004940	VLAN_PORT_TAG_CTRL [56].IGR_OTAG_KEEP[7:7]	1
0xBB004940	VLAN_PORT_TAG_CTRL [56].IGR_ITAG_KEEP[6:6]	1
0xBB004940	VLAN_PORT_TAG_CTRL [56].OTPID_IDX[5:4]	2
0xBB004940	VLAN_PORT_TAG_CTRL [56].OTPID_KEEP[3:3]	1
0xBB004940	VLAN_PORT_TAG_CTRL [56].ITPID_IDX[2:1]	2
0xBB004940	VLAN_PORT_TAG_CTRL [56].ITPID_KEEP[0:0]	1
0xBB00C000	VLAN_PPB_VLAN_SET [0].RESERVED[31:18]	14
0xBB00C000	VLAN_PPB_VLAN_SET [0].FRAME_TYPE[17:16]	2
0xBB00C000	VLAN_PPB_VLAN_SET [0].ETHER_TYPE[15:0]	16
0xBB00C004	VLAN_PPB_VLAN_SET [1].RESERVED[31:18]	14
0xBB00C004	VLAN_PPB_VLAN_SET [1].FRAME_TYPE[17:16]	2
0xBB00C004	VLAN_PPB_VLAN_SET [1].ETHER_TYPE[15:0]	16
0xBB00C008	VLAN_PPB_VLAN_SET [2].RESERVED[31:18]	14
0xBB00C008	VLAN_PPB_VLAN_SET [2].FRAME_TYPE[17:16]	2
0xBB00C008	VLAN_PPB_VLAN_SET [2].ETHER_TYPE[15:0]	16
0xBB00C00C	VLAN_PPB_VLAN_SET [3].RESERVED[31:18]	14
0xBB00C00C	VLAN_PPB_VLAN_SET [3].FRAME_TYPE[17:16]	2
0xBB00C00C	VLAN_PPB_VLAN_SET [3].ETHER_TYPE[15:0]	16
0xBB00C010	VLAN_PPB_VLAN_SET [4].RESERVED[31:18]	14
0xBB00C010	VLAN_PPB_VLAN_SET [4].FRAME_TYPE[17:16]	2
0xBB00C010	VLAN_PPB_VLAN_SET [4].ETHER_TYPE[15:0]	16
0xBB00C014	VLAN_PPB_VLAN_SET [5].RESERVED[31:18]	14
0xBB00C014	VLAN_PPB_VLAN_SET [5].FRAME_TYPE[17:16]	2
0xBB00C014	VLAN_PPB_VLAN_SET [5].ETHER_TYPE[15:0]	16
0xBB00C018	VLAN_PPB_VLAN_SET [6].RESERVED[31:18]	14
0xBB00C018	VLAN_PPB_VLAN_SET [6].FRAME_TYPE[17:16]	2
0xBB00C018	VLAN_PPB_VLAN_SET [6].ETHER_TYPE[15:0]	16
0xBB00C01C	VLAN_PPB_VLAN_SET [7].RESERVED[31:18]	14
0xBB00C01C	VLAN_PPB_VLAN_SET [7].FRAME_TYPE[17:16]	2
0xBB00C01C	VLAN_PPB_VLAN_SET [7].ETHER_TYPE[15:0]	16
0xBB009800	VLAN_PROFILE_SET [0].RESERVED[31:16]	16
0xBB009800	VLAN_PROFILE_SET [0].L2_NEW_SA_LRN[15:14]	2
0xBB009800	VLAN_PROFILE_SET [0].L2_NEW_SA_ACT[13:11]	3
0xBB009800	VLAN_PROFILE_SET [0].IPMC_BRIDGE_MODE[10:10]	1
0xBB009800	VLAN_PROFILE_SET [0].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009800	VLAN_PROFILE_SET [0].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009800	VLAN_PROFILE_SET [0].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009800	VLAN_PROFILE_SET [0].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009804	VLAN_PROFILE_SET [0].RESERVED[31:25]	7

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009804	VLAN_PROFILE_SET [0].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009808	VLAN_PROFILE_SET [0].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00980C	VLAN_PROFILE_SET [0].RESERVED[31:25]	7
0xBB00980C	VLAN_PROFILE_SET [0].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009810	VLAN_PROFILE_SET [0].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009814	VLAN_PROFILE_SET [0].RESERVED[31:25]	7
0xBB009814	VLAN_PROFILE_SET [0].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009818	VLAN_PROFILE_SET [0].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00981C	VLAN_PROFILE_SET [1].RESERVED[31:16]	16
0xBB00981C	VLAN_PROFILE_SET [1].L2_NEW_SA_LRN[15:14]	2
0xBB00981C	VLAN_PROFILE_SET [1].L2_NEW_SA_ACT[13:11]	3
0xBB00981C	VLAN_PROFILE_SET [1].IPMC_BRIDGE_MODE[10:10]	1
0xBB00981C	VLAN_PROFILE_SET [1].IP6MC_BRIDGE_MODE[9:9]	1
0xBB00981C	VLAN_PROFILE_SET [1].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB00981C	VLAN_PROFILE_SET [1].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB00981C	VLAN_PROFILE_SET [1].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009820	VLAN_PROFILE_SET [1].RESERVED[31:25]	7
0xBB009820	VLAN_PROFILE_SET [1].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009824	VLAN_PROFILE_SET [1].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009828	VLAN_PROFILE_SET [1].RESERVED[31:25]	7
0xBB009828	VLAN_PROFILE_SET [1].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00982C	VLAN_PROFILE_SET [1].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009830	VLAN_PROFILE_SET [1].RESERVED[31:25]	7
0xBB009830	VLAN_PROFILE_SET [1].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009834	VLAN_PROFILE_SET [1].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009838	VLAN_PROFILE_SET [2].RESERVED[31:16]	16
0xBB009838	VLAN_PROFILE_SET [2].L2_NEW_SA_LRN[15:14]	2
0xBB009838	VLAN_PROFILE_SET [2].L2_NEW_SA_ACT[13:11]	3
0xBB009838	VLAN_PROFILE_SET [2].IPMC_BRIDGE_MODE[10:10]	1
0xBB009838	VLAN_PROFILE_SET [2].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009838	VLAN_PROFILE_SET [2].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009838	VLAN_PROFILE_SET [2].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009838	VLAN_PROFILE_SET [2].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB00983C	VLAN_PROFILE_SET [2].RESERVED[31:25]	7
0xBB00983C	VLAN_PROFILE_SET [2].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009840	VLAN_PROFILE_SET [2].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009844	VLAN_PROFILE_SET [2].RESERVED[31:25]	7
0xBB009844	VLAN_PROFILE_SET [2].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009848	VLAN_PROFILE_SET [2].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00984C	VLAN_PROFILE_SET [2].RESERVED[31:25]	7
0xBB00984C	VLAN_PROFILE_SET [2].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009850	VLAN_PROFILE_SET [2].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009854	VLAN_PROFILE_SET [3].RESERVED[31:16]	16
0xBB009854	VLAN_PROFILE_SET [3].L2_NEW_SA_LRN[15:14]	2
0xBB009854	VLAN_PROFILE_SET [3].L2_NEW_SA_ACT[13:11]	3
0xBB009854	VLAN_PROFILE_SET [3].IPMC_BRIDGE_MODE[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009854	VLAN_PROFILE_SET [3].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009854	VLAN_PROFILE_SET [3].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009854	VLAN_PROFILE_SET [3].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009854	VLAN_PROFILE_SET [3].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009858	VLAN_PROFILE_SET [3].RESERVED[31:25]	7
0xBB009858	VLAN_PROFILE_SET [3].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00985C	VLAN_PROFILE_SET [3].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009860	VLAN_PROFILE_SET [3].RESERVED[31:25]	7
0xBB009860	VLAN_PROFILE_SET [3].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009864	VLAN_PROFILE_SET [3].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009868	VLAN_PROFILE_SET [3].RESERVED[31:25]	7
0xBB009868	VLAN_PROFILE_SET [3].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00986C	VLAN_PROFILE_SET [3].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009870	VLAN_PROFILE_SET [4].RESERVED[31:16]	16
0xBB009870	VLAN_PROFILE_SET [4].L2_NEW_SA_LRN[15:14]	2
0xBB009870	VLAN_PROFILE_SET [4].L2_NEW_SA_ACT[13:11]	3
0xBB009870	VLAN_PROFILE_SET [4].IPMC_BRIDGE_MODE[10:10]	1
0xBB009870	VLAN_PROFILE_SET [4].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009870	VLAN_PROFILE_SET [4].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009870	VLAN_PROFILE_SET [4].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009870	VLAN_PROFILE_SET [4].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009874	VLAN_PROFILE_SET [4].RESERVED[31:25]	7
0xBB009874	VLAN_PROFILE_SET [4].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009878	VLAN_PROFILE_SET [4].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00987C	VLAN_PROFILE_SET [4].RESERVED[31:25]	7
0xBB00987C	VLAN_PROFILE_SET [4].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009880	VLAN_PROFILE_SET [4].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009884	VLAN_PROFILE_SET [4].RESERVED[31:25]	7
0xBB009884	VLAN_PROFILE_SET [4].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009888	VLAN_PROFILE_SET [4].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00988C	VLAN_PROFILE_SET [5].RESERVED[31:16]	16
0xBB00988C	VLAN_PROFILE_SET [5].L2_NEW_SA_LRN[15:14]	2
0xBB00988C	VLAN_PROFILE_SET [5].L2_NEW_SA_ACT[13:11]	3
0xBB00988C	VLAN_PROFILE_SET [5].IPMC_BRIDGE_MODE[10:10]	1
0xBB00988C	VLAN_PROFILE_SET [5].IP6MC_BRIDGE_MODE[9:9]	1
0xBB00988C	VLAN_PROFILE_SET [5].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB00988C	VLAN_PROFILE_SET [5].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB00988C	VLAN_PROFILE_SET [5].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009890	VLAN_PROFILE_SET [5].RESERVED[31:25]	7
0xBB009890	VLAN_PROFILE_SET [5].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009894	VLAN_PROFILE_SET [5].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009898	VLAN_PROFILE_SET [5].RESERVED[31:25]	7
0xBB009898	VLAN_PROFILE_SET [5].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00989C	VLAN_PROFILE_SET [5].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098A0	VLAN_PROFILE_SET [5].RESERVED[31:25]	7
0xBB0098A0	VLAN_PROFILE_SET [5].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0098A4	VLAN_PROFILE_SET [5].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098A8	VLAN_PROFILE_SET [6].RESERVED[31:16]	16
0xBB0098A8	VLAN_PROFILE_SET [6].L2_NEW_SA_LRN[15:14]	2
0xBB0098A8	VLAN_PROFILE_SET [6].L2_NEW_SA_ACT[13:11]	3
0xBB0098A8	VLAN_PROFILE_SET [6].IPMC_BRIDGE_MODE[10:10]	1
0xBB0098A8	VLAN_PROFILE_SET [6].IP6MC_BRIDGE_MODE[9:9]	1
0xBB0098A8	VLAN_PROFILE_SET [6].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB0098A8	VLAN_PROFILE_SET [6].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB0098A8	VLAN_PROFILE_SET [6].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB0098AC	VLAN_PROFILE_SET [6].RESERVED[31:25]	7
0xBB0098AC	VLAN_PROFILE_SET [6].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098B0	VLAN_PROFILE_SET [6].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098B4	VLAN_PROFILE_SET [6].RESERVED[31:25]	7
0xBB0098B4	VLAN_PROFILE_SET [6].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098B8	VLAN_PROFILE_SET [6].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098BC	VLAN_PROFILE_SET [6].RESERVED[31:25]	7
0xBB0098BC	VLAN_PROFILE_SET [6].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098C0	VLAN_PROFILE_SET [6].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098C4	VLAN_PROFILE_SET [7].RESERVED[31:16]	16
0xBB0098C4	VLAN_PROFILE_SET [7].L2_NEW_SA_LRN[15:14]	2
0xBB0098C4	VLAN_PROFILE_SET [7].L2_NEW_SA_ACT[13:11]	3
0xBB0098C4	VLAN_PROFILE_SET [7].IPMC_BRIDGE_MODE[10:10]	1
0xBB0098C4	VLAN_PROFILE_SET [7].IP6MC_BRIDGE_MODE[9:9]	1
0xBB0098C4	VLAN_PROFILE_SET [7].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB0098C4	VLAN_PROFILE_SET [7].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB0098C4	VLAN_PROFILE_SET [7].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB0098C8	VLAN_PROFILE_SET [7].RESERVED[31:25]	7
0xBB0098C8	VLAN_PROFILE_SET [7].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098CC	VLAN_PROFILE_SET [7].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098D0	VLAN_PROFILE_SET [7].RESERVED[31:25]	7
0xBB0098D0	VLAN_PROFILE_SET [7].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098D4	VLAN_PROFILE_SET [7].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098D8	VLAN_PROFILE_SET [7].RESERVED[31:25]	7
0xBB0098D8	VLAN_PROFILE_SET [7].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098DC	VLAN_PROFILE_SET [7].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098E0	VLAN_PROFILE_SET [8].RESERVED[31:16]	16
0xBB0098E0	VLAN_PROFILE_SET [8].L2_NEW_SA_LRN[15:14]	2
0xBB0098E0	VLAN_PROFILE_SET [8].L2_NEW_SA_ACT[13:11]	3
0xBB0098E0	VLAN_PROFILE_SET [8].IPMC_BRIDGE_MODE[10:10]	1
0xBB0098E0	VLAN_PROFILE_SET [8].IP6MC_BRIDGE_MODE[9:9]	1
0xBB0098E0	VLAN_PROFILE_SET [8].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB0098E0	VLAN_PROFILE_SET [8].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB0098E0	VLAN_PROFILE_SET [8].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB0098E4	VLAN_PROFILE_SET [8].RESERVED[31:25]	7
0xBB0098E4	VLAN_PROFILE_SET [8].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098E8	VLAN_PROFILE_SET [8].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0098EC	VLAN_PROFILE_SET [8].RESERVED[31:25]	7
0xBB0098EC	VLAN_PROFILE_SET [8].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098F0	VLAN_PROFILE_SET [8].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098F4	VLAN_PROFILE_SET [8].RESERVED[31:25]	7
0xBB0098F4	VLAN_PROFILE_SET [8].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0098F8	VLAN_PROFILE_SET [8].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0098FC	VLAN_PROFILE_SET [9].RESERVED[31:16]	16
0xBB0098FC	VLAN_PROFILE_SET [9].L2_NEW_SA_LRN[15:14]	2
0xBB0098FC	VLAN_PROFILE_SET [9].L2_NEW_SA_ACT[13:11]	3
0xBB0098FC	VLAN_PROFILE_SET [9].IPMC_BRIDGE_MODE[10:10]	1
0xBB0098FC	VLAN_PROFILE_SET [9].IP6MC_BRIDGE_MODE[9:9]	1
0xBB0098FC	VLAN_PROFILE_SET [9].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB0098FC	VLAN_PROFILE_SET [9].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB0098FC	VLAN_PROFILE_SET [9].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009900	VLAN_PROFILE_SET [9].RESERVED[31:25]	7
0xBB009900	VLAN_PROFILE_SET [9].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009904	VLAN_PROFILE_SET [9].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009908	VLAN_PROFILE_SET [9].RESERVED[31:25]	7
0xBB009908	VLAN_PROFILE_SET [9].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00990C	VLAN_PROFILE_SET [9].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009910	VLAN_PROFILE_SET [9].RESERVED[31:25]	7
0xBB009910	VLAN_PROFILE_SET [9].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009914	VLAN_PROFILE_SET [9].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009918	VLAN_PROFILE_SET [10].RESERVED[31:16]	16
0xBB009918	VLAN_PROFILE_SET [10].L2_NEW_SA_LRN[15:14]	2
0xBB009918	VLAN_PROFILE_SET [10].L2_NEW_SA_ACT[13:11]	3
0xBB009918	VLAN_PROFILE_SET [10].IPMC_BRIDGE_MODE[10:10]	1
0xBB009918	VLAN_PROFILE_SET [10].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009918	VLAN_PROFILE_SET [10].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009918	VLAN_PROFILE_SET [10].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009918	VLAN_PROFILE_SET [10].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB00991C	VLAN_PROFILE_SET [10].RESERVED[31:25]	7
0xBB00991C	VLAN_PROFILE_SET [10].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009920	VLAN_PROFILE_SET [10].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009924	VLAN_PROFILE_SET [10].RESERVED[31:25]	7
0xBB009924	VLAN_PROFILE_SET [10].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009928	VLAN_PROFILE_SET [10].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00992C	VLAN_PROFILE_SET [10].RESERVED[31:25]	7
0xBB00992C	VLAN_PROFILE_SET [10].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009930	VLAN_PROFILE_SET [10].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009934	VLAN_PROFILE_SET [11].RESERVED[31:16]	16
0xBB009934	VLAN_PROFILE_SET [11].L2_NEW_SA_LRN[15:14]	2
0xBB009934	VLAN_PROFILE_SET [11].L2_NEW_SA_ACT[13:11]	3
0xBB009934	VLAN_PROFILE_SET [11].IPMC_BRIDGE_MODE[10:10]	1
0xBB009934	VLAN_PROFILE_SET [11].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009934	VLAN_PROFILE_SET [11].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009934	VLAN_PROFILE_SET [11].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009934	VLAN_PROFILE_SET [11].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009938	VLAN_PROFILE_SET [11].RESERVED[31:25]	7
0xBB009938	VLAN_PROFILE_SET [11].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00993C	VLAN_PROFILE_SET [11].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009940	VLAN_PROFILE_SET [11].RESERVED[31:25]	7
0xBB009940	VLAN_PROFILE_SET [11].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009944	VLAN_PROFILE_SET [11].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009948	VLAN_PROFILE_SET [11].RESERVED[31:25]	7
0xBB009948	VLAN_PROFILE_SET [11].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00994C	VLAN_PROFILE_SET [11].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009950	VLAN_PROFILE_SET [12].RESERVED[31:16]	16
0xBB009950	VLAN_PROFILE_SET [12].L2_NEW_SA_LRN[15:14]	2
0xBB009950	VLAN_PROFILE_SET [12].L2_NEW_SA_ACT[13:11]	3
0xBB009950	VLAN_PROFILE_SET [12].IPMC_BRIDGE_MODE[10:10]	1
0xBB009950	VLAN_PROFILE_SET [12].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009950	VLAN_PROFILE_SET [12].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009950	VLAN_PROFILE_SET [12].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009950	VLAN_PROFILE_SET [12].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009954	VLAN_PROFILE_SET [12].RESERVED[31:25]	7
0xBB009954	VLAN_PROFILE_SET [12].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009958	VLAN_PROFILE_SET [12].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00995C	VLAN_PROFILE_SET [12].RESERVED[31:25]	7
0xBB00995C	VLAN_PROFILE_SET [12].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009960	VLAN_PROFILE_SET [12].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009964	VLAN_PROFILE_SET [12].RESERVED[31:25]	7
0xBB009964	VLAN_PROFILE_SET [12].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009968	VLAN_PROFILE_SET [12].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00996C	VLAN_PROFILE_SET [13].RESERVED[31:16]	16
0xBB00996C	VLAN_PROFILE_SET [13].L2_NEW_SA_LRN[15:14]	2
0xBB00996C	VLAN_PROFILE_SET [13].L2_NEW_SA_ACT[13:11]	3
0xBB00996C	VLAN_PROFILE_SET [13].IPMC_BRIDGE_MODE[10:10]	1
0xBB00996C	VLAN_PROFILE_SET [13].IP6MC_BRIDGE_MODE[9:9]	1
0xBB00996C	VLAN_PROFILE_SET [13].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB00996C	VLAN_PROFILE_SET [13].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB00996C	VLAN_PROFILE_SET [13].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB009970	VLAN_PROFILE_SET [13].RESERVED[31:25]	7
0xBB009970	VLAN_PROFILE_SET [13].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009974	VLAN_PROFILE_SET [13].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009978	VLAN_PROFILE_SET [13].RESERVED[31:25]	7
0xBB009978	VLAN_PROFILE_SET [13].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB00997C	VLAN_PROFILE_SET [13].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009980	VLAN_PROFILE_SET [13].RESERVED[31:25]	7
0xBB009980	VLAN_PROFILE_SET [13].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009984	VLAN_PROFILE_SET [13].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009988	VLAN_PROFILE_SET [14].RESERVED[31:16]	16

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB009988	VLAN_PROFILE_SET [14].L2_NEW_SA_LRN[15:14]	2
0xBB009988	VLAN_PROFILE_SET [14].L2_NEW_SA_ACT[13:11]	3
0xBB009988	VLAN_PROFILE_SET [14].IPMC_BRIDGE_MODE[10:10]	1
0xBB009988	VLAN_PROFILE_SET [14].IP6MC_BRIDGE_MODE[9:9]	1
0xBB009988	VLAN_PROFILE_SET [14].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB009988	VLAN_PROFILE_SET [14].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB009988	VLAN_PROFILE_SET [14].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB00998C	VLAN_PROFILE_SET [14].RESERVED[31:25]	7
0xBB00998C	VLAN_PROFILE_SET [14].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009990	VLAN_PROFILE_SET [14].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB009994	VLAN_PROFILE_SET [14].RESERVED[31:25]	7
0xBB009994	VLAN_PROFILE_SET [14].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB009998	VLAN_PROFILE_SET [14].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB00999C	VLAN_PROFILE_SET [14].RESERVED[31:25]	7
0xBB00999C	VLAN_PROFILE_SET [14].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0099A0	VLAN_PROFILE_SET [14].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0099A4	VLAN_PROFILE_SET [15].RESERVED[31:16]	16
0xBB0099A4	VLAN_PROFILE_SET [15].L2_NEW_SA_LRN[15:14]	2
0xBB0099A4	VLAN_PROFILE_SET [15].L2_NEW_SA_ACT[13:11]	3
0xBB0099A4	VLAN_PROFILE_SET [15].IPMC_BRIDGE_MODE[10:10]	1
0xBB0099A4	VLAN_PROFILE_SET [15].IP6MC_BRIDGE_MODE[9:9]	1
0xBB0099A4	VLAN_PROFILE_SET [15].L2MC_BRIDGE_LU_MIS_ACT[8:6]	3
0xBB0099A4	VLAN_PROFILE_SET [15].IPMC_BRIDGE_LU_MIS_ACT[5:3]	3
0xBB0099A4	VLAN_PROFILE_SET [15].IP6MC_BRIDGE_LU_MIS_ACT[2:0]	3
0xBB0099A8	VLAN_PROFILE_SET [15].RESERVED[31:25]	7
0xBB0099A8	VLAN_PROFILE_SET [15].L2MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0099AC	VLAN_PROFILE_SET [15].L2MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0099B0	VLAN_PROFILE_SET [15].RESERVED[31:25]	7
0xBB0099B0	VLAN_PROFILE_SET [15].IPMC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0099B4	VLAN_PROFILE_SET [15].IPMC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0099B8	VLAN_PROFILE_SET [15].RESERVED[31:25]	7
0xBB0099B8	VLAN_PROFILE_SET [15].IP6MC_UNKN_FLD_PMSK_52_32[24:0]	25
0xBB0099BC	VLAN_PROFILE_SET [15].IP6MC_UNKN_FLD_PMSK_31_0[31:0]	32
0xBB0013FC	VLAN_TAG_TPID_CTRL [0].OTPID[31:16]	16
0xBB0013FC	VLAN_TAG_TPID_CTRL [0].ITPID[15:0]	16
0xBB001400	VLAN_TAG_TPID_CTRL [1].OTPID[31:16]	16
0xBB001400	VLAN_TAG_TPID_CTRL [1].ITPID[15:0]	16
0xBB001404	VLAN_TAG_TPID_CTRL [2].OTPID[31:16]	16
0xBB001404	VLAN_TAG_TPID_CTRL [2].ITPID[15:0]	16
0xBB001408	VLAN_TAG_TPID_CTRL [3].OTPID[31:16]	16
0xBB001408	VLAN_TAG_TPID_CTRL [3].ITPID[15:0]	16
0xBB0097E4	VLAN_TRUNK_L2TBL_CNVT_CTRL_0 [0].VID_CNVT_EN[0:0]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CNVT_CTRL_0 [1].VID_CNVT_EN[1:1]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CNVT_CTRL_0 [2].VID_CNVT_EN[2:2]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CNVT_CTRL_0 [3].VID_CNVT_EN[3:3]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CNVT_CTRL_0 [4].VID_CNVT_EN[4:4]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [5].VID_CVNT_EN[5:5]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [6].VID_CVNT_EN[6:6]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [7].VID_CVNT_EN[7:7]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [8].VID_CVNT_EN[8:8]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [9].VID_CVNT_EN[9:9]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [10].VID_CVNT_EN[10:10]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [11].VID_CVNT_EN[11:11]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [12].VID_CVNT_EN[12:12]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [13].VID_CVNT_EN[13:13]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [14].VID_CVNT_EN[14:14]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [15].VID_CVNT_EN[15:15]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [16].VID_CVNT_EN[16:16]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [17].VID_CVNT_EN[17:17]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [18].VID_CVNT_EN[18:18]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [19].VID_CVNT_EN[19:19]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [20].VID_CVNT_EN[20:20]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [21].VID_CVNT_EN[21:21]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [22].VID_CVNT_EN[22:22]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [23].VID_CVNT_EN[23:23]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [24].VID_CVNT_EN[24:24]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [25].VID_CVNT_EN[25:25]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [26].VID_CVNT_EN[26:26]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [27].VID_CVNT_EN[27:27]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [28].VID_CVNT_EN[28:28]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [29].VID_CVNT_EN[29:29]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [30].VID_CVNT_EN[30:30]	1
0xBB0097E4	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [31].VID_CVNT_EN[31:31]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [32].VID_CVNT_EN[0:0]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [33].VID_CVNT_EN[1:1]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [34].VID_CVNT_EN[2:2]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [35].VID_CVNT_EN[3:3]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [36].VID_CVNT_EN[4:4]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [37].VID_CVNT_EN[5:5]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [38].VID_CVNT_EN[6:6]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [39].VID_CVNT_EN[7:7]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [40].VID_CVNT_EN[8:8]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [41].VID_CVNT_EN[9:9]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [42].VID_CVNT_EN[10:10]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [43].VID_CVNT_EN[11:11]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [44].VID_CVNT_EN[12:12]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [45].VID_CVNT_EN[13:13]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [46].VID_CVNT_EN[14:14]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [47].VID_CVNT_EN[15:15]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [48].VID_CVNT_EN[16:16]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [49].VID_CVNT_EN[17:17]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [50].VID_CVNT_EN[18:18]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [51].VID_CVNT_EN[19:19]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [52].VID_CVNT_EN[20:20]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [53].VID_CVNT_EN[21:21]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [54].VID_CVNT_EN[22:22]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [55].VID_CVNT_EN[23:23]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [56].VID_CVNT_EN[24:24]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [57].VID_CVNT_EN[25:25]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [58].VID_CVNT_EN[26:26]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [59].VID_CVNT_EN[27:27]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [60].VID_CVNT_EN[28:28]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [61].VID_CVNT_EN[29:29]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [62].VID_CVNT_EN[30:30]	1
0xBB0097E8	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [63].VID_CVNT_EN[31:31]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [64].VID_CVNT_EN[0:0]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [65].VID_CVNT_EN[1:1]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [66].VID_CVNT_EN[2:2]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [67].VID_CVNT_EN[3:3]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [68].VID_CVNT_EN[4:4]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [69].VID_CVNT_EN[5:5]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [70].VID_CVNT_EN[6:6]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [71].VID_CVNT_EN[7:7]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [72].VID_CVNT_EN[8:8]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [73].VID_CVNT_EN[9:9]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [74].VID_CVNT_EN[10:10]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [75].VID_CVNT_EN[11:11]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [76].VID_CVNT_EN[12:12]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [77].VID_CVNT_EN[13:13]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [78].VID_CVNT_EN[14:14]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [79].VID_CVNT_EN[15:15]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [80].VID_CVNT_EN[16:16]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [81].VID_CVNT_EN[17:17]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [82].VID_CVNT_EN[18:18]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [83].VID_CVNT_EN[19:19]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [84].VID_CVNT_EN[20:20]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [85].VID_CVNT_EN[21:21]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [86].VID_CVNT_EN[22:22]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [87].VID_CVNT_EN[23:23]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [88].VID_CVNT_EN[24:24]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [89].VID_CVNT_EN[25:25]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [90].VID_CVNT_EN[26:26]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [91].VID_CVNT_EN[27:27]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [92].VID_CVNT_EN[28:28]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [93].VID_CVNT_EN[29:29]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [94].VID_CVNT_EN[30:30]	1
0xBB0097EC	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [95].VID_CVNT_EN[31:31]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [96].VID_CVNT_EN[0:0]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [97].VID_CVNT_EN[1:1]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [98].VID_CVNT_EN[2:2]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [99].VID_CVNT_EN[3:3]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [100].VID_CVNT_EN[4:4]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [101].VID_CVNT_EN[5:5]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [102].VID_CVNT_EN[6:6]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [103].VID_CVNT_EN[7:7]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [104].VID_CVNT_EN[8:8]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [105].VID_CVNT_EN[9:9]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [106].VID_CVNT_EN[10:10]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [107].VID_CVNT_EN[11:11]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [108].VID_CVNT_EN[12:12]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [109].VID_CVNT_EN[13:13]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [110].VID_CVNT_EN[14:14]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [111].VID_CVNT_EN[15:15]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [112].VID_CVNT_EN[16:16]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [113].VID_CVNT_EN[17:17]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [114].VID_CVNT_EN[18:18]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [115].VID_CVNT_EN[19:19]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [116].VID_CVNT_EN[20:20]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [117].VID_CVNT_EN[21:21]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [118].VID_CVNT_EN[22:22]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [119].VID_CVNT_EN[23:23]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [120].VID_CVNT_EN[24:24]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [121].VID_CVNT_EN[25:25]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [122].VID_CVNT_EN[26:26]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [123].VID_CVNT_EN[27:27]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [124].VID_CVNT_EN[28:28]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [125].VID_CVNT_EN[29:29]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [126].VID_CVNT_EN[30:30]	1
0xBB0097F0	VLAN_TRUNK_L2TBL_CVNT_CTRL_0 [127].VID_CVNT_EN[31:31]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [0].VID_SELECT[0:0]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [1].VID_SELECT[1:1]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [2].VID_SELECT[2:2]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [3].VID_SELECT[3:3]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [4].VID_SELECT[4:4]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [5].VID_SELECT[5:5]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [6].VID_SELECT[6:6]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [7].VID_SELECT[7:7]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [8].VID_SELECT[8:8]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [9].VID_SELECT[9:9]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [10].VID_SELECT[10:10]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [11].VID_SELECT[11:11]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [12].VID_SELECT[12:12]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [13].VID_SELECT[13:13]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [14].VID_SELECT[14:14]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [15].VID_SELECT[15:15]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [16].VID_SELECT[16:16]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [17].VID_SELECT[17:17]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [18].VID_SELECT[18:18]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [19].VID_SELECT[19:19]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [20].VID_SELECT[20:20]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [21].VID_SELECT[21:21]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [22].VID_SELECT[22:22]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [23].VID_SELECT[23:23]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [24].VID_SELECT[24:24]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [25].VID_SELECT[25:25]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [26].VID_SELECT[26:26]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [27].VID_SELECT[27:27]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [28].VID_SELECT[28:28]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [29].VID_SELECT[29:29]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [30].VID_SELECT[30:30]	1
0xBB0097D4	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [31].VID_SELECT[31:31]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [32].VID_SELECT[0:0]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [33].VID_SELECT[1:1]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [34].VID_SELECT[2:2]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [35].VID_SELECT[3:3]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [36].VID_SELECT[4:4]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [37].VID_SELECT[5:5]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [38].VID_SELECT[6:6]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [39].VID_SELECT[7:7]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [40].VID_SELECT[8:8]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [41].VID_SELECT[9:9]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [42].VID_SELECT[10:10]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [43].VID_SELECT[11:11]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [44].VID_SELECT[12:12]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [45].VID_SELECT[13:13]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [46].VID_SELECT[14:14]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [47].VID_SELECT[15:15]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [48].VID_SELECT[16:16]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [49].VID_SELECT[17:17]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [50].VID_SELECT[18:18]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [51].VID_SELECT[19:19]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [52].VID_SELECT[20:20]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [53].VID_SELECT[21:21]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [54].VID_SELECT[22:22]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [55].VID_SELECT[23:23]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [56].VID_SELECT[24:24]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [57].VID_SELECT[25:25]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [58].VID_SELECT[26:26]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [59].VID_SELECT[27:27]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [60].VID_SELECT[28:28]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [61].VID_SELECT[29:29]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [62].VID_SELECT[30:30]	1
0xBB0097D8	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [63].VID_SELECT[31:31]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [64].VID_SELECT[0:0]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [65].VID_SELECT[1:1]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [66].VID_SELECT[2:2]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [67].VID_SELECT[3:3]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [68].VID_SELECT[4:4]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [69].VID_SELECT[5:5]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [70].VID_SELECT[6:6]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [71].VID_SELECT[7:7]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [72].VID_SELECT[8:8]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [73].VID_SELECT[9:9]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [74].VID_SELECT[10:10]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [75].VID_SELECT[11:11]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [76].VID_SELECT[12:12]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [77].VID_SELECT[13:13]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [78].VID_SELECT[14:14]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [79].VID_SELECT[15:15]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [80].VID_SELECT[16:16]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [81].VID_SELECT[17:17]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [82].VID_SELECT[18:18]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [83].VID_SELECT[19:19]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [84].VID_SELECT[20:20]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [85].VID_SELECT[21:21]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [86].VID_SELECT[22:22]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [87].VID_SELECT[23:23]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [88].VID_SELECT[24:24]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [89].VID_SELECT[25:25]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [90].VID_SELECT[26:26]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [91].VID_SELECT[27:27]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [92].VID_SELECT[28:28]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [93].VID_SELECT[29:29]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [94].VID_SELECT[30:30]	1
0xBB0097DC	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [95].VID_SELECT[31:31]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [96].VID_SELECT[0:0]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [97].VID_SELECT[1:1]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [98].VID_SELECT[2:2]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [99].VID_SELECT[3:3]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [100].VID_SELECT[4:4]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [101].VID_SELECT[5:5]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [102].VID_SELECT[6:6]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [103].VID_SELECT[7:7]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [104].VID_SELECT[8:8]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [105].VID_SELECT[9:9]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [106].VID_SELECT[10:10]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [107].VID_SELECT[11:11]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [108].VID_SELECT[12:12]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [109].VID_SELECT[13:13]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [110].VID_SELECT[14:14]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [111].VID_SELECT[15:15]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [112].VID_SELECT[16:16]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [113].VID_SELECT[17:17]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [114].VID_SELECT[18:18]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [115].VID_SELECT[19:19]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [116].VID_SELECT[20:20]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [117].VID_SELECT[21:21]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [118].VID_SELECT[22:22]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [119].VID_SELECT[23:23]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [120].VID_SELECT[24:24]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [121].VID_SELECT[25:25]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [122].VID_SELECT[26:26]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [123].VID_SELECT[27:27]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [124].VID_SELECT[28:28]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [125].VID_SELECT[29:29]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [126].VID_SELECT[30:30]	1
0xBB0097E0	VLAN_TRUNK_L2TBL_CVNT_CTRL_1 [127].VID_SELECT[31:31]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [0].PRI_TAG_ACT[0:0]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [1].PRI_TAG_ACT[1:1]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [2].PRI_TAG_ACT[2:2]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [3].PRI_TAG_ACT[3:3]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [4].PRI_TAG_ACT[4:4]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [5].PRI_TAG_ACT[5:5]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [6].PRI_TAG_ACT[6:6]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [7].PRI_TAG_ACT[7:7]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [8].PRI_TAG_ACT[8:8]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [9].PRI_TAG_ACT[9:9]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [10].PRI_TAG_ACT[10:10]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [11].PRI_TAG_ACT[11:11]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [12].PRI_TAG_ACT[12:12]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [13].PRI_TAG_ACT[13:13]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [14].PRI_TAG_ACT[14:14]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [15].PRI_TAG_ACT[15:15]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [16].PRI_TAG_ACT[16:16]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [17].PRI_TAG_ACT[17:17]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [18].PRI_TAG_ACT[18:18]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [19].PRI_TAG_ACT[19:19]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [20].PRI_TAG_ACT[20:20]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [21].PRI_TAG_ACT[21:21]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [22].PRI_TAG_ACT[22:22]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [23].PRI_TAG_ACT[23:23]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [24].PRI_TAG_ACT[24:24]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [25].PRI_TAG_ACT[25:25]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [26].PRI_TAG_ACT[26:26]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [27].PRI_TAG_ACT[27:27]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [28].PRI_TAG_ACT[28:28]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [29].PRI_TAG_ACT[29:29]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [30].PRI_TAG_ACT[30:30]	1
0xBB0097C4	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [31].PRI_TAG_ACT[31:31]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [32].PRI_TAG_ACT[0:0]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [33].PRI_TAG_ACT[1:1]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [34].PRI_TAG_ACT[2:2]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [35].PRI_TAG_ACT[3:3]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [36].PRI_TAG_ACT[4:4]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [37].PRI_TAG_ACT[5:5]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [38].PRI_TAG_ACT[6:6]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [39].PRI_TAG_ACT[7:7]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [40].PRI_TAG_ACT[8:8]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [41].PRI_TAG_ACT[9:9]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [42].PRI_TAG_ACT[10:10]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [43].PRI_TAG_ACT[11:11]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [44].PRI_TAG_ACT[12:12]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [45].PRI_TAG_ACT[13:13]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [46].PRI_TAG_ACT[14:14]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [47].PRI_TAG_ACT[15:15]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [48].PRI_TAG_ACT[16:16]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [49].PRI_TAG_ACT[17:17]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [50].PRI_TAG_ACT[18:18]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [51].PRI_TAG_ACT[19:19]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [52].PRI_TAG_ACT[20:20]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [53].PRI_TAG_ACT[21:21]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [54].PRI_TAG_ACT[22:22]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [55].PRI_TAG_ACT[23:23]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [56].PRI_TAG_ACT[24:24]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [57].PRI_TAG_ACT[25:25]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [58].PRI_TAG_ACT[26:26]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [59].PRI_TAG_ACT[27:27]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [60].PRI_TAG_ACT[28:28]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [61].PRI_TAG_ACT[29:29]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [62].PRI_TAG_ACT[30:30]	1
0xBB0097C8	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [63].PRI_TAG_ACT[31:31]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [64].PRI_TAG_ACT[0:0]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [65].PRI_TAG_ACT[1:1]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [66].PRI_TAG_ACT[2:2]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [67].PRI_TAG_ACT[3:3]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [68].PRI_TAG_ACT[4:4]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [69].PRI_TAG_ACT[5:5]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [70].PRI_TAG_ACT[6:6]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [71].PRI_TAG_ACT[7:7]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [72].PRI_TAG_ACT[8:8]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [73].PRI_TAG_ACT[9:9]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [74].PRI_TAG_ACT[10:10]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [75].PRI_TAG_ACT[11:11]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [76].PRI_TAG_ACT[12:12]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [77].PRI_TAG_ACT[13:13]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [78].PRI_TAG_ACT[14:14]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [79].PRI_TAG_ACT[15:15]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [80].PRI_TAG_ACT[16:16]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [81].PRI_TAG_ACT[17:17]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [82].PRI_TAG_ACT[18:18]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [83].PRI_TAG_ACT[19:19]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [84].PRI_TAG_ACT[20:20]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [85].PRI_TAG_ACT[21:21]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [86].PRI_TAG_ACT[22:22]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [87].PRI_TAG_ACT[23:23]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [88].PRI_TAG_ACT[24:24]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [89].PRI_TAG_ACT[25:25]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [90].PRI_TAG_ACT[26:26]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [91].PRI_TAG_ACT[27:27]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [92].PRI_TAG_ACT[28:28]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [93].PRI_TAG_ACT[29:29]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [94].PRI_TAG_ACT[30:30]	1
0xBB0097CC	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [95].PRI_TAG_ACT[31:31]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [96].PRI_TAG_ACT[0:0]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [97].PRI_TAG_ACT[1:1]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [98].PRI_TAG_ACT[2:2]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [99].PRI_TAG_ACT[3:3]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [100].PRI_TAG_ACT[4:4]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [101].PRI_TAG_ACT[5:5]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [102].PRI_TAG_ACT[6:6]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [103].PRI_TAG_ACT[7:7]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [104].PRI_TAG_ACT[8:8]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [105].PRI_TAG_ACT[9:9]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [106].PRI_TAG_ACT[10:10]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [107].PRI_TAG_ACT[11:11]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [108].PRI_TAG_ACT[12:12]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [109].PRI_TAG_ACT[13:13]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [110].PRI_TAG_ACT[14:14]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [111].PRI_TAG_ACT[15:15]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [112].PRI_TAG_ACT[16:16]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [113].PRI_TAG_ACT[17:17]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [114].PRI_TAG_ACT[18:18]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [115].PRI_TAG_ACT[19:19]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [116].PRI_TAG_ACT[20:20]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [117].PRI_TAG_ACT[21:21]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [118].PRI_TAG_ACT[22:22]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [119].PRI_TAG_ACT[23:23]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [120].PRI_TAG_ACT[24:24]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [121].PRI_TAG_ACT[25:25]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [122].PRI_TAG_ACT[26:26]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [123].PRI_TAG_ACT[27:27]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [124].PRI_TAG_ACT[28:28]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [125].PRI_TAG_ACT[29:29]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [126].PRI_TAG_ACT[30:30]	1
0xBB0097D0	VLAN_TRUNK_L2TBL_CVNT_CTRL_2 [127].PRI_TAG_ACT[31:31]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [0].PRI_CVNT_EN[0:0]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [1].PRI_CVNT_EN[1:1]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [2].PRI_CVNT_EN[2:2]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [3].PRI_CVNT_EN[3:3]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [4].PRI_CVNT_EN[4:4]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [5].PRI_CVNT_EN[5:5]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [6].PRI_CVNT_EN[6:6]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [7].PRI_CVNT_EN[7:7]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [8].PRI_CVNT_EN[8:8]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [9].PRI_CVNT_EN[9:9]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [10].PRI_CVNT_EN[10:10]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [11].PRI_CVNT_EN[11:11]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [12].PRI_CVNT_EN[12:12]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [13].PRI_CVNT_EN[13:13]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [14].PRI_CVNT_EN[14:14]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [15].PRI_CVNT_EN[15:15]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [16].PRI_CVNT_EN[16:16]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [17].PRI_CVNT_EN[17:17]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [18].PRI_CVNT_EN[18:18]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [19].PRI_CVNT_EN[19:19]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [20].PRI_CVNT_EN[20:20]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [21].PRI_CVNT_EN[21:21]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [22].PRI_CVNT_EN[22:22]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [23].PRI_CVNT_EN[23:23]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [24].PRI_CVNT_EN[24:24]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [25].PRI_CVNT_EN[25:25]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [26].PRI_CVNT_EN[26:26]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [27].PRI_CVNT_EN[27:27]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [28].PRI_CVNT_EN[28:28]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [29].PRI_CVNT_EN[29:29]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [30].PRI_CVNT_EN[30:30]	1
0xBB0097B4	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [31].PRI_CVNT_EN[31:31]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [32].PRI_CVNT_EN[0:0]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [33].PRI_CVNT_EN[1:1]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [34].PRI_CVNT_EN[2:2]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [35].PRI_CVNT_EN[3:3]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [36].PRI_CVNT_EN[4:4]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [37].PRI_CVNT_EN[5:5]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [38].PRI_CVNT_EN[6:6]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [39].PRI_CVNT_EN[7:7]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [40].PRI_CVNT_EN[8:8]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [41].PRI_CVNT_EN[9:9]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [42].PRI_CVNT_EN[10:10]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [43].PRI_CVNT_EN[11:11]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [44].PRI_CVNT_EN[12:12]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [45].PRI_CVNT_EN[13:13]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [46].PRI_CVNT_EN[14:14]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [47].PRI_CVNT_EN[15:15]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [48].PRI_CVNT_EN[16:16]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [49].PRI_CVNT_EN[17:17]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [50].PRI_CVNT_EN[18:18]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [51].PRI_CVNT_EN[19:19]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [52].PRI_CVNT_EN[20:20]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [53].PRI_CVNT_EN[21:21]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [54].PRI_CVNT_EN[22:22]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [55].PRI_CVNT_EN[23:23]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [56].PRI_CVNT_EN[24:24]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [57].PRI_CVNT_EN[25:25]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [58].PRI_CVNT_EN[26:26]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [59].PRI_CVNT_EN[27:27]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [60].PRI_CVNT_EN[28:28]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [61].PRI_CVNT_EN[29:29]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [62].PRI_CVNT_EN[30:30]	1
0xBB0097B8	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [63].PRI_CVNT_EN[31:31]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [64].PRI_CVNT_EN[0:0]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [65].PRI_CVNT_EN[1:1]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [66].PRI_CVNT_EN[2:2]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [67].PRI_CVNT_EN[3:3]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [68].PRI_CVNT_EN[4:4]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [69].PRI_CVNT_EN[5:5]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [70].PRI_CVNT_EN[6:6]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [71].PRI_CVNT_EN[7:7]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [72].PRI_CVNT_EN[8:8]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [73].PRI_CVNT_EN[9:9]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [74].PRI_CVNT_EN[10:10]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [75].PRI_CVNT_EN[11:11]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [76].PRI_CVNT_EN[12:12]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [77].PRI_CVNT_EN[13:13]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [78].PRI_CVNT_EN[14:14]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [79].PRI_CVNT_EN[15:15]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [80].PRI_CVNT_EN[16:16]	1

<b>Address</b>	<b>Register</b>	<b>Len</b>
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [81].PRI_CVNT_EN[17:17]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [82].PRI_CVNT_EN[18:18]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [83].PRI_CVNT_EN[19:19]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [84].PRI_CVNT_EN[20:20]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [85].PRI_CVNT_EN[21:21]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [86].PRI_CVNT_EN[22:22]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [87].PRI_CVNT_EN[23:23]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [88].PRI_CVNT_EN[24:24]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [89].PRI_CVNT_EN[25:25]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [90].PRI_CVNT_EN[26:26]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [91].PRI_CVNT_EN[27:27]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [92].PRI_CVNT_EN[28:28]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [93].PRI_CVNT_EN[29:29]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [94].PRI_CVNT_EN[30:30]	1
0xBB0097BC	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [95].PRI_CVNT_EN[31:31]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [96].PRI_CVNT_EN[0:0]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [97].PRI_CVNT_EN[1:1]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [98].PRI_CVNT_EN[2:2]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [99].PRI_CVNT_EN[3:3]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [100].PRI_CVNT_EN[4:4]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [101].PRI_CVNT_EN[5:5]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [102].PRI_CVNT_EN[6:6]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [103].PRI_CVNT_EN[7:7]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [104].PRI_CVNT_EN[8:8]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [105].PRI_CVNT_EN[9:9]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [106].PRI_CVNT_EN[10:10]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [107].PRI_CVNT_EN[11:11]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [108].PRI_CVNT_EN[12:12]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [109].PRI_CVNT_EN[13:13]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [110].PRI_CVNT_EN[14:14]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [111].PRI_CVNT_EN[15:15]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [112].PRI_CVNT_EN[16:16]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [113].PRI_CVNT_EN[17:17]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [114].PRI_CVNT_EN[18:18]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [115].PRI_CVNT_EN[19:19]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [116].PRI_CVNT_EN[20:20]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [117].PRI_CVNT_EN[21:21]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [118].PRI_CVNT_EN[22:22]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [119].PRI_CVNT_EN[23:23]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [120].PRI_CVNT_EN[24:24]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [121].PRI_CVNT_EN[25:25]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [122].PRI_CVNT_EN[26:26]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [123].PRI_CVNT_EN[27:27]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [124].PRI_CVNT_EN[28:28]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [125].PRI_CVNT_EN[29:29]	1
0xBB0097C0	VLAN_TRUNK_L2TBL_CVNT_CTRL_3 [126].PRI_CVNT_EN[30:30]	1

Address	Register	Len
0xBB0097C0	VLAN_TRUNK_L2TBL_CNVT_CTRL_3 [127].PRI_CNVT_EN[31:31]	1
0xBB000408	WDOG_RST_STS.WDOG_RST_STS[31:0]	32

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