

Falcon 2.1



Designer : MJ
Date : February 2016
Board revision : 1.1
Board description : BBB cape with EPD Controller and PMIC

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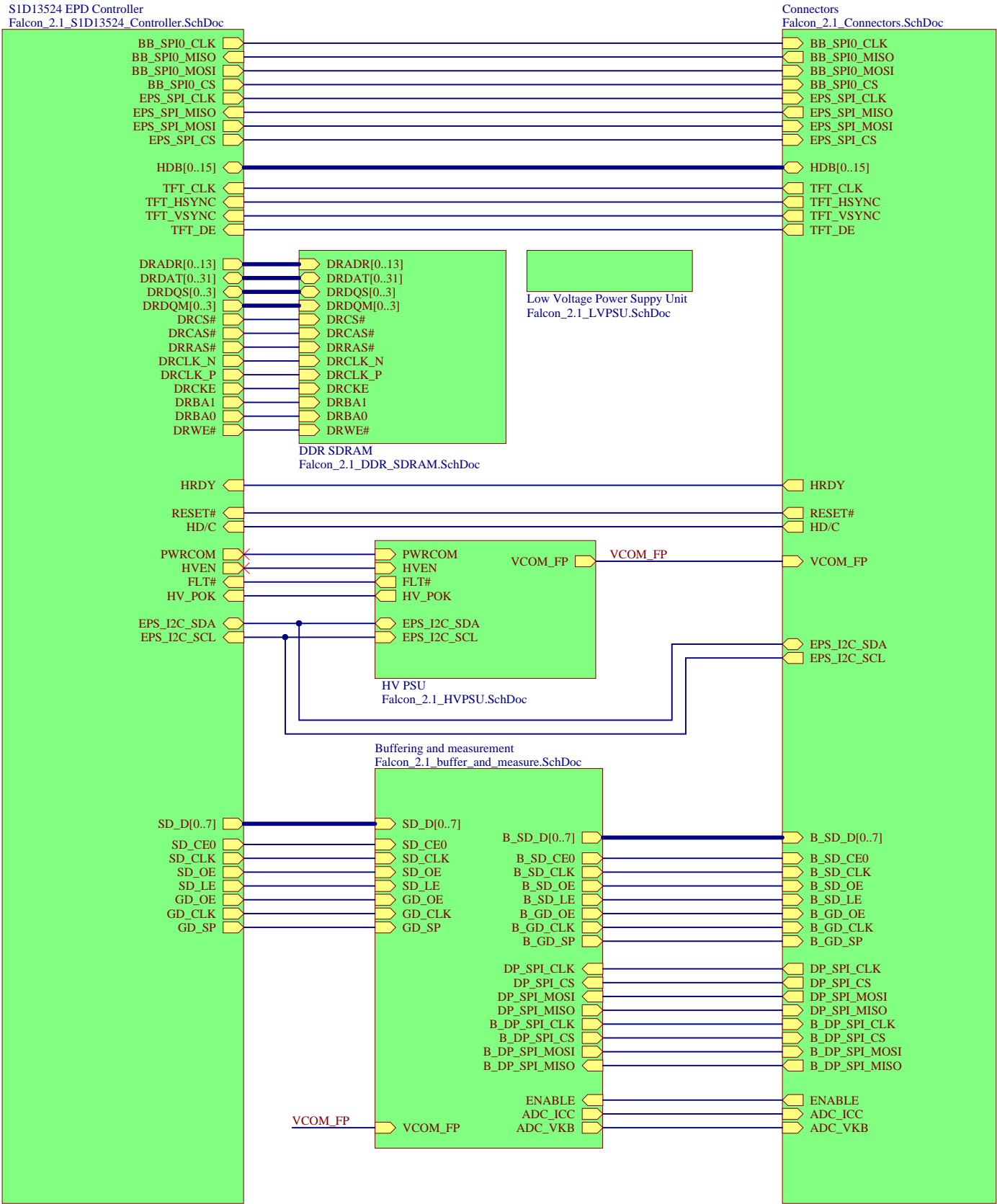
Revision History

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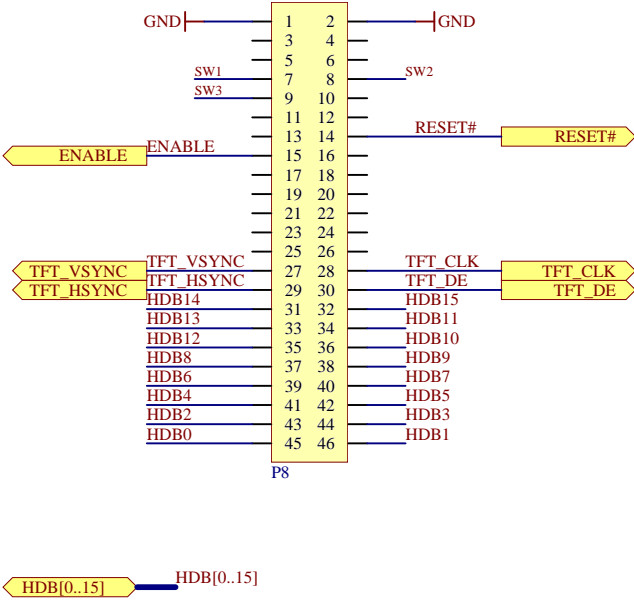
Revision number	Date	Engineer	Description
1	05/17	MJ	- parallel interface pinout fixed - UART RX/TX swapped - display load switch, SPI buffer and I2C buffer added - buttons and LEDs added - Icc and Vkb measurement opt. through 0 ohm resistor - test points for PMIC signals
1.1	02/18	MJ	- bug in COM switch supply fixed



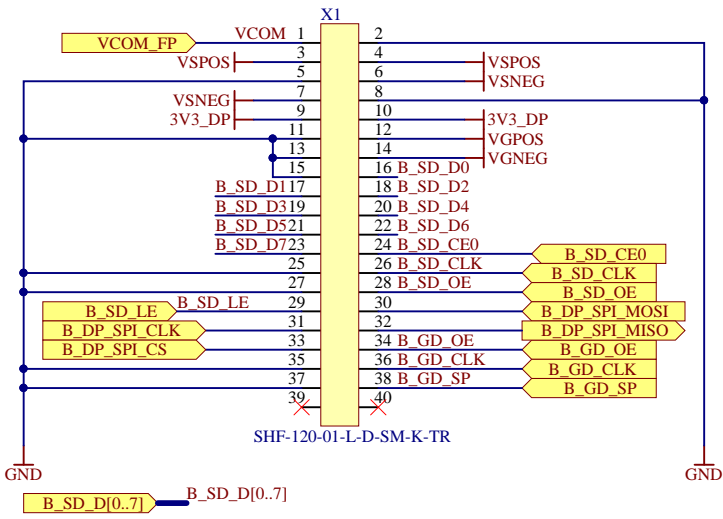
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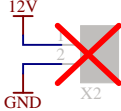
BBB Interface



10.7" 40 way interface



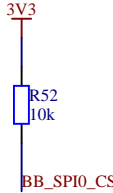
Power connector



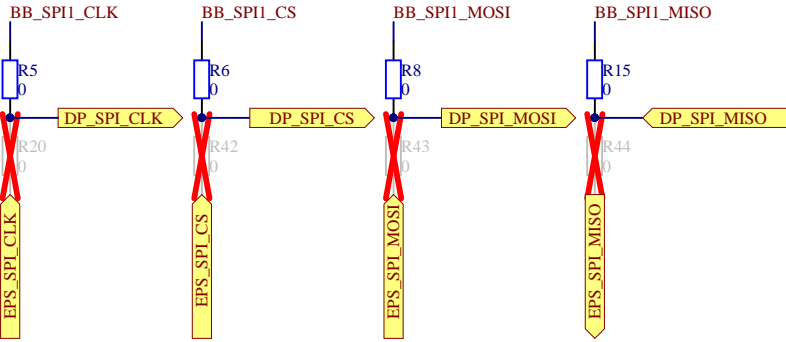
CAN connector



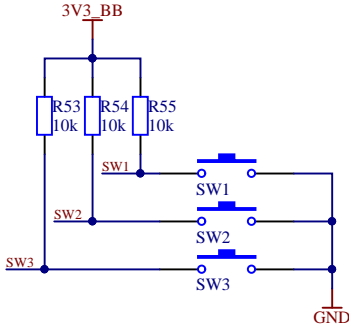
SPI0_CS pull-up



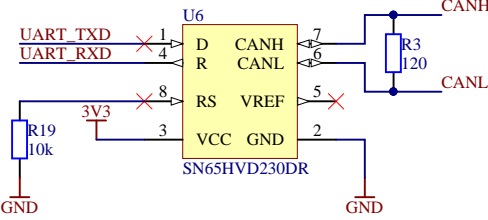
display SPI master option



user buttons



CAN Interface



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Technical Information

Plastic Logic GmbH
An der Bartlake 5
D-01109 Dresden

Title: **Connectors**

Author: MJ

Date: November 2016

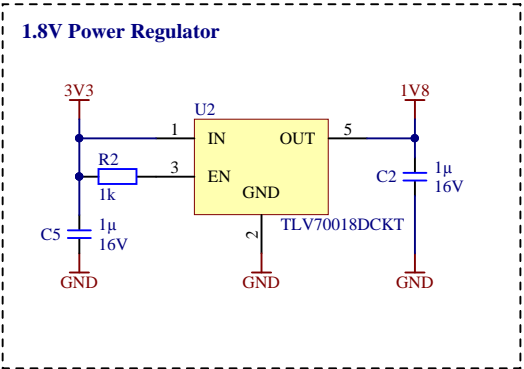
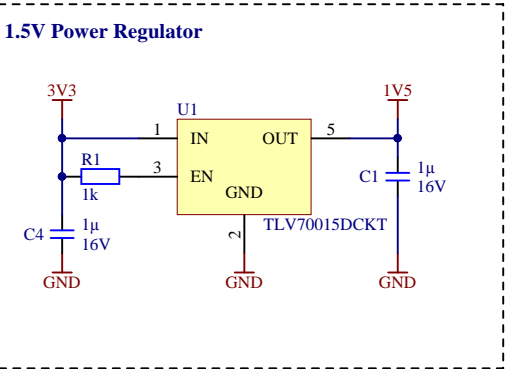
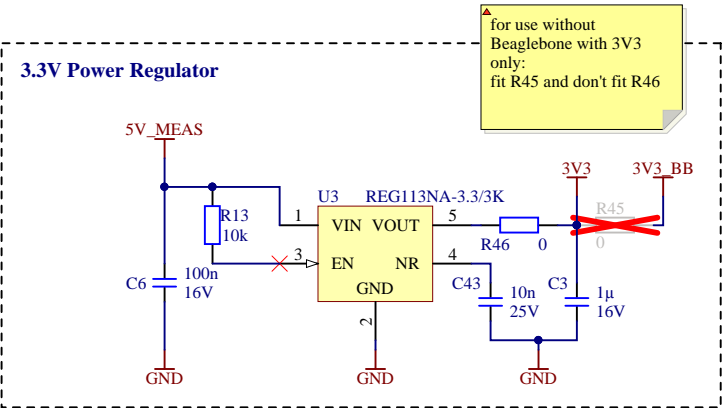
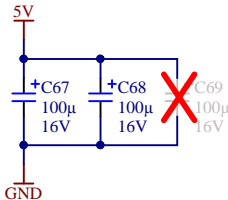
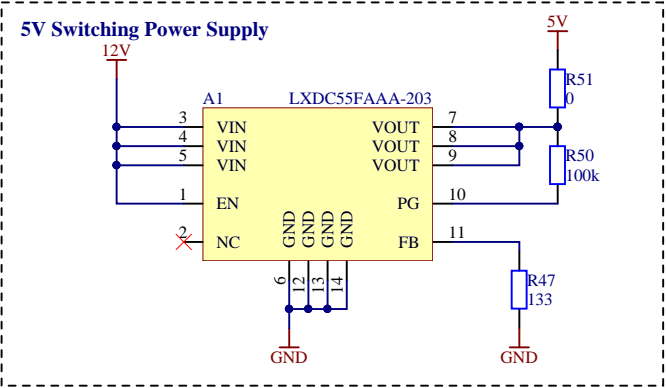
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Revision: 1.1

Sheet Size: A3

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Plastic Logic GmbH

An der Bartlake 5
D-01109 Dresden

Title: **Low Voltage Power Supply**

Revision: 1.1

Author: MJ

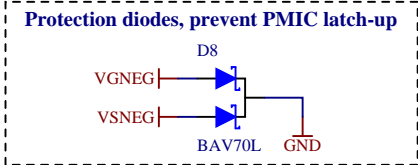
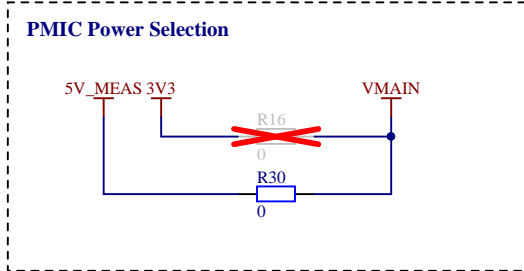
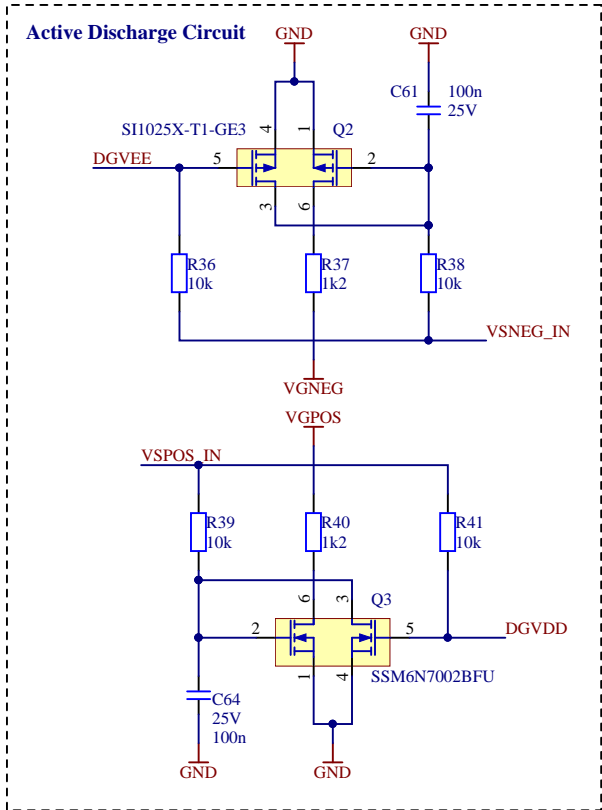
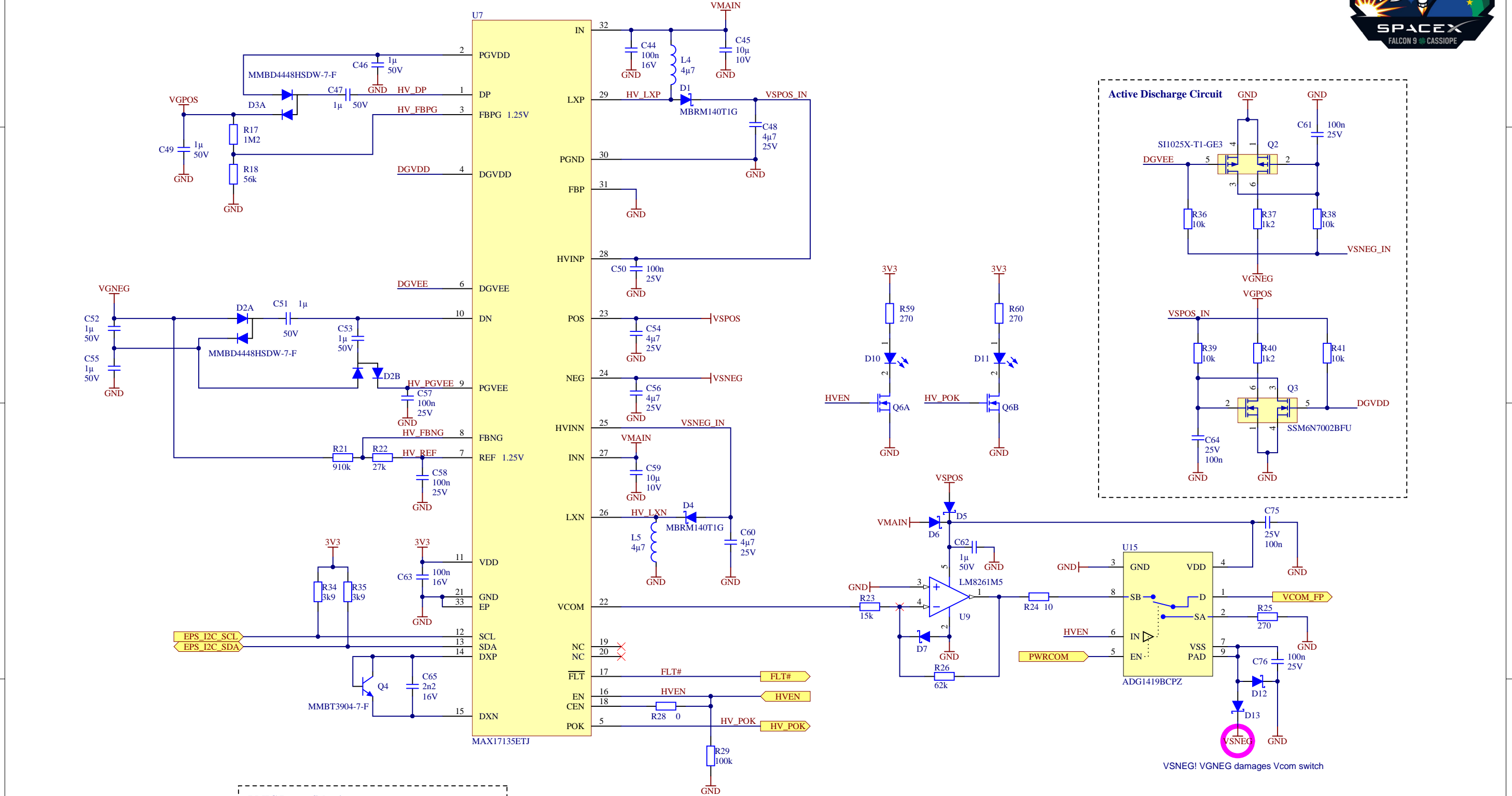
Date: November 2016

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Product Number: 700982

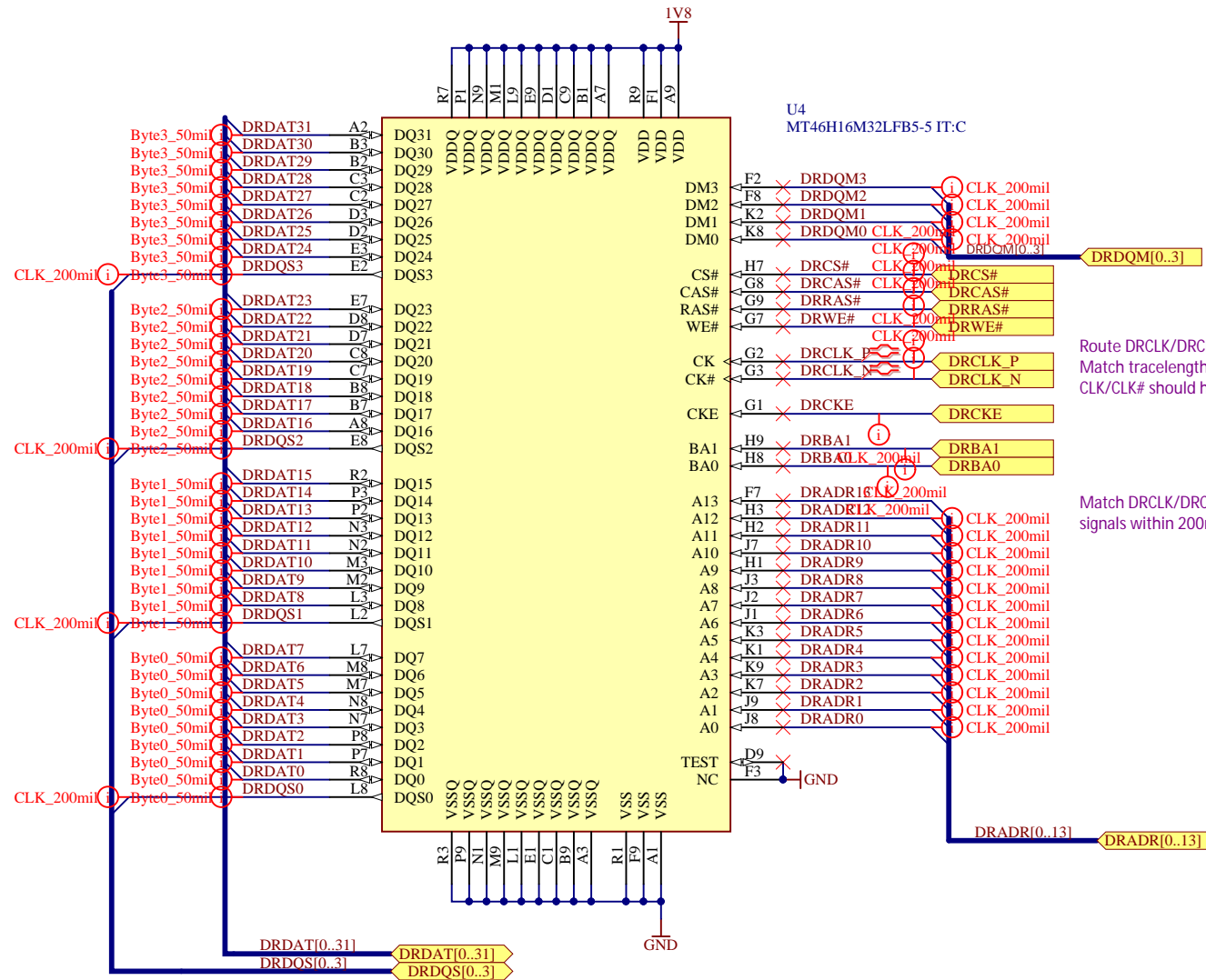
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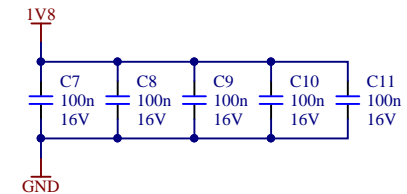
VSNEG! VGNEG damages Vcom switch

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Route DRCLK/DRCLK# together on the same layer
Match trancelengths to within +20mil
CLK/CLK# should have differential characteristic impedance of 100-120 Ohm

Match DRCLK/DRCLK# Trace Length to DRDQSX signals within 200mils (recommended), 400mils max.



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Technical Information

Title: **DDR SDRAM**

Revision: 1.1

Author: MJ

Date: November 2016

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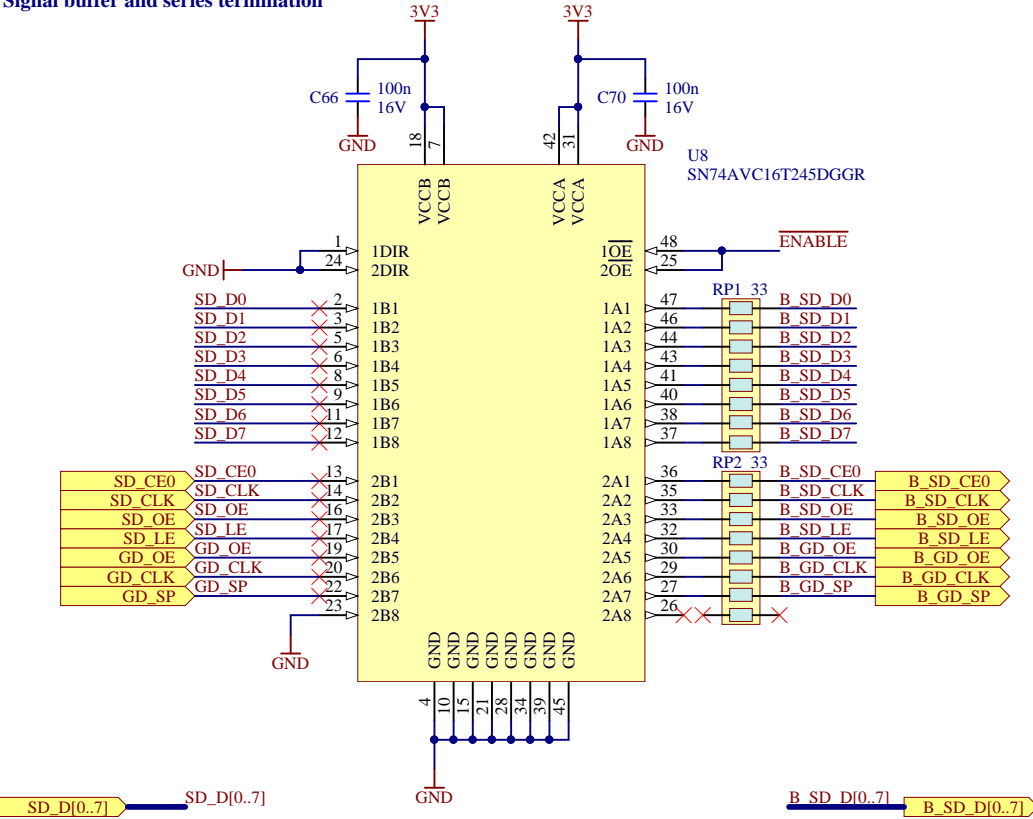
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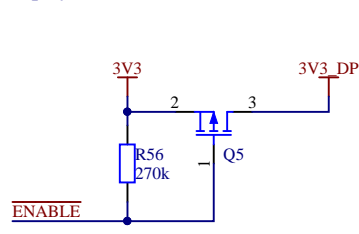
Falcon 2.1



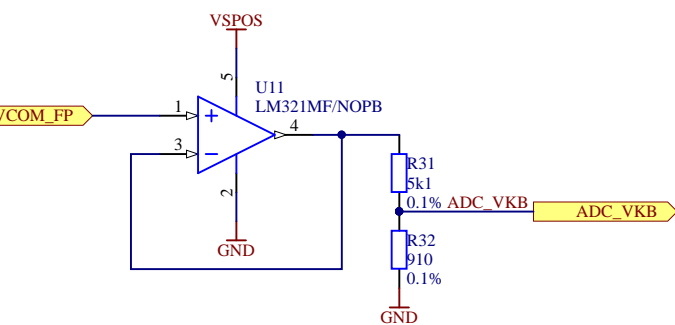
Signal buffer and series termination



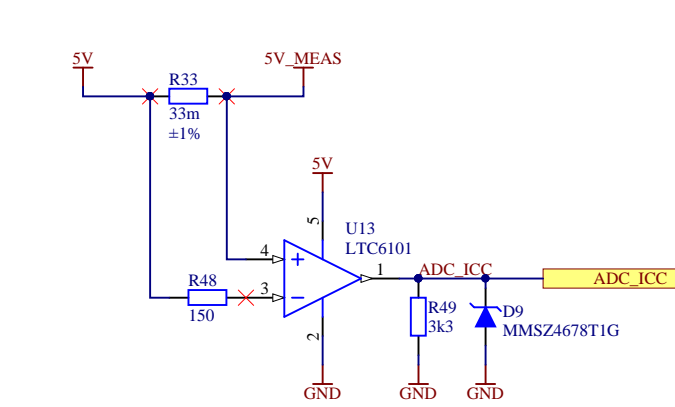
Display load switch



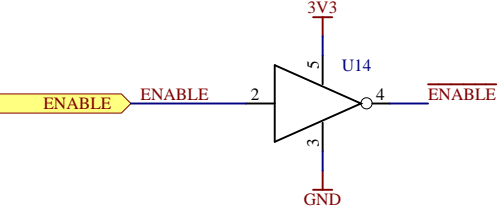
Vkb measurement



Icc measurement 5V



Shutdown Inverter



SPI buffer

