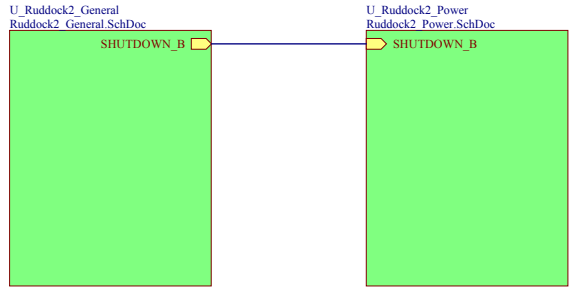
Ruddock 2.0.1



Designer : A. Ethrington/G.Smallwood/G.Husbands
Date : 2nd May 2014
Board revision : V2.0.1
Board description : Ruddock2 - Processor Interface for Raven and HBZ6

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Revision History

Revision number		Date	Engineer	Description
Major	Minor			
	2.0.1	2nd May 2014	GJH	Change I2C resistors R27 and R28 to NF Change RP10 to 0R

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Milton Road
Cambridge
CB4 0FX

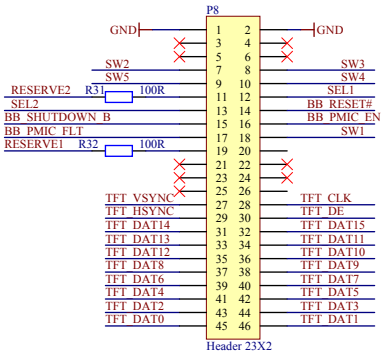
Title: *Ruddock 2 Front Page* Revision: V2.0.1
Author: A. Ethrington/G.Smallwood/G.Husbands
Date: 2nd May 2014
Sheet: 1 of 3 Document Number: Dxxxxx Sheet Size: A3

Ruddock 2.0.1

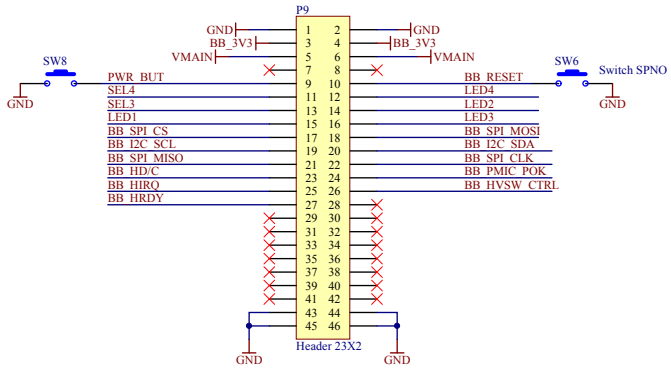
eMMC memory uses mmc1 port - pins 3-6 and 22-25 on B'bone Black, P8.
Port must remain o/c if eMMC memory will be used.

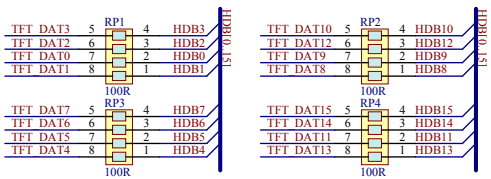
Interface to Beaglebone

Connects to BeagleBone P8

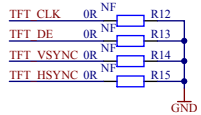


Connects to Beagle Bone P9

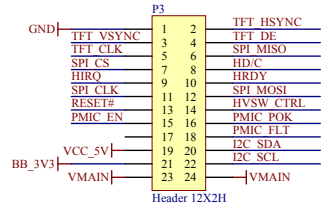




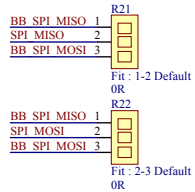
S1D13524 p27 - Connect to VSS in 16 bit host config.
Do Not fit if TFT interface is used



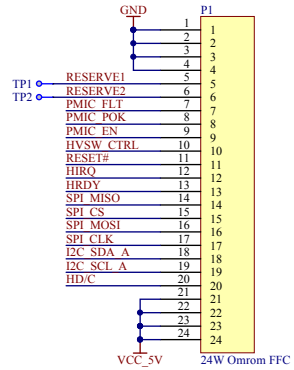
TestPoints - 2.54mm header pads



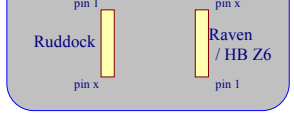
SPI SWAP



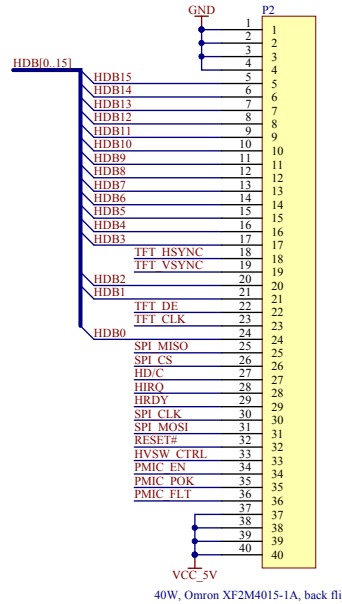
Output Interface to HBZ6/ Raven



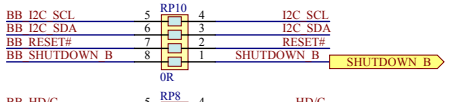
Note Pin Swap!



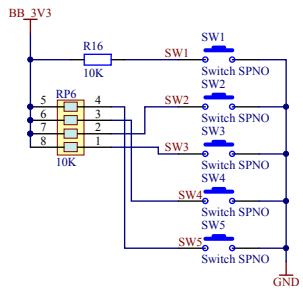
Output Interface to Raven



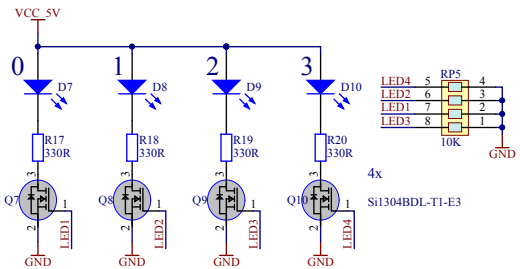
40W, Omron XF2M4015-1A, back flip FFC conn



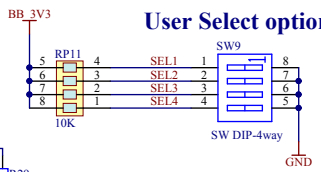
User configurable Switches

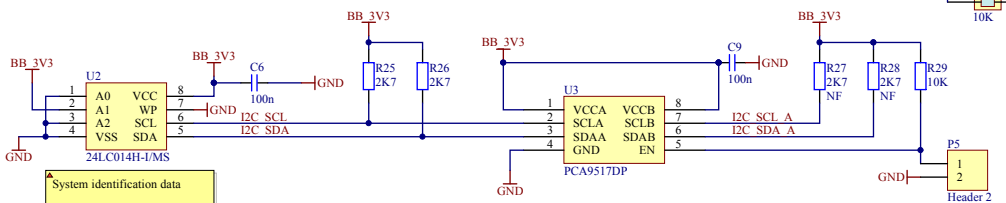


User configurable LEDs



User Select options





System identification data

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Technical Information
U34 Cambridge Science Park
Milton Road
Cambridge
CB4 0FX
Title: Ruddock 2 - General
Revision: V2.0.1
Author: GSS
Date: 2nd May 2014
Sheet: 2 of 3
Document Number: Dxxxxx
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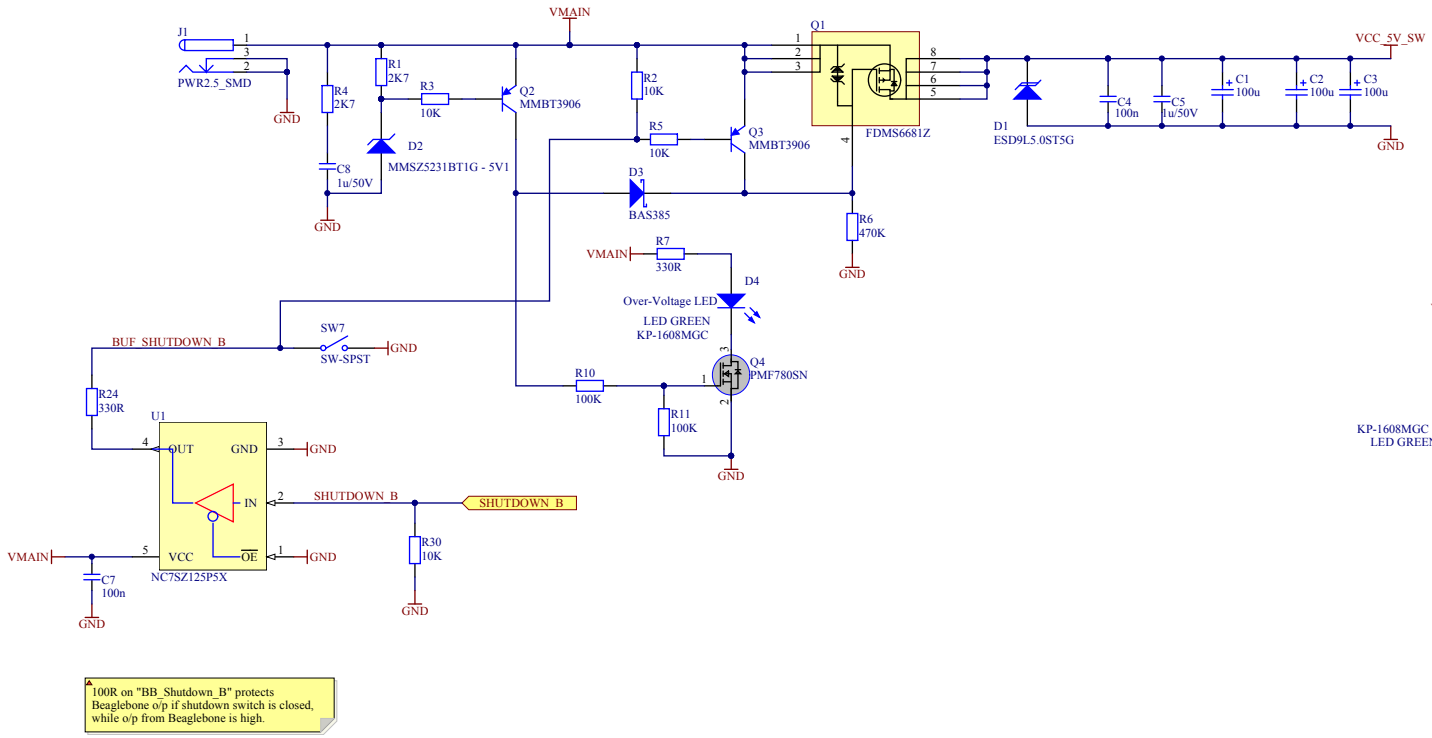
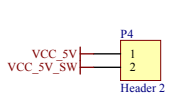
Ruddock 2.0.1

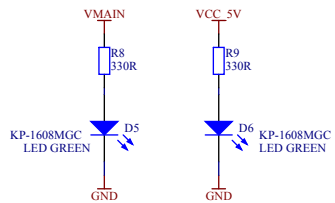
DC Supply, Overvoltage Protection



100R on "BB_Shutdown_B" protects Beaglebone o/p if shutdown switch is closed, while o/p from Beaglebone is high.



Power Status



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