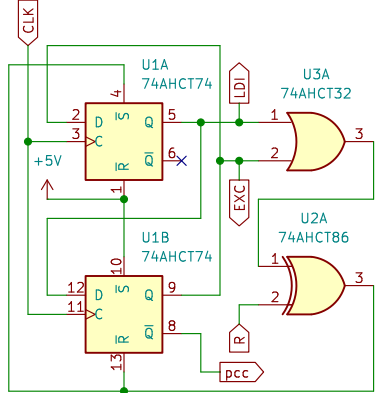
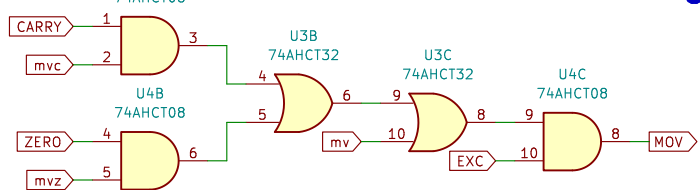


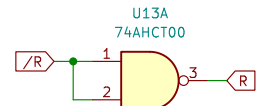
State Machine



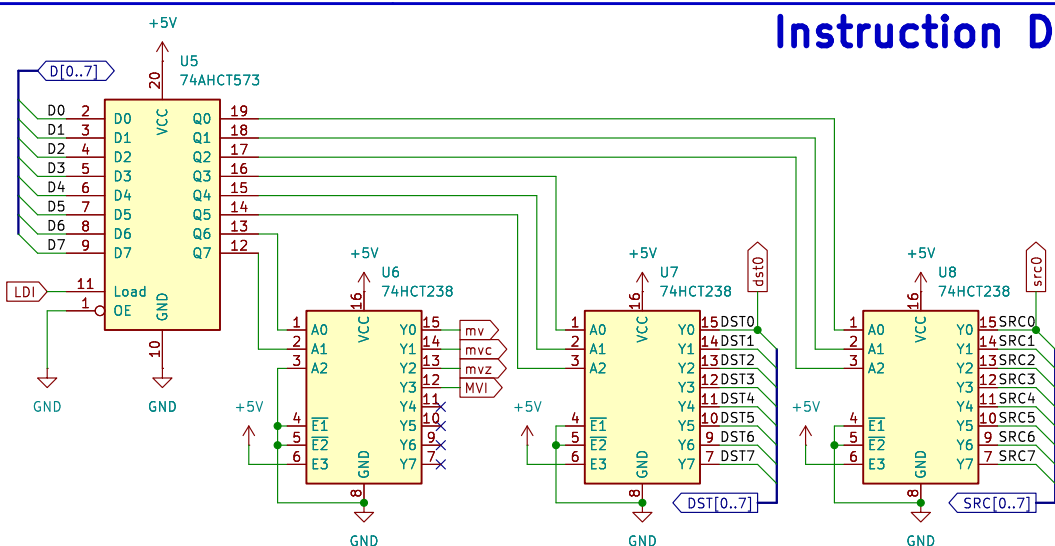
Conditional Logic



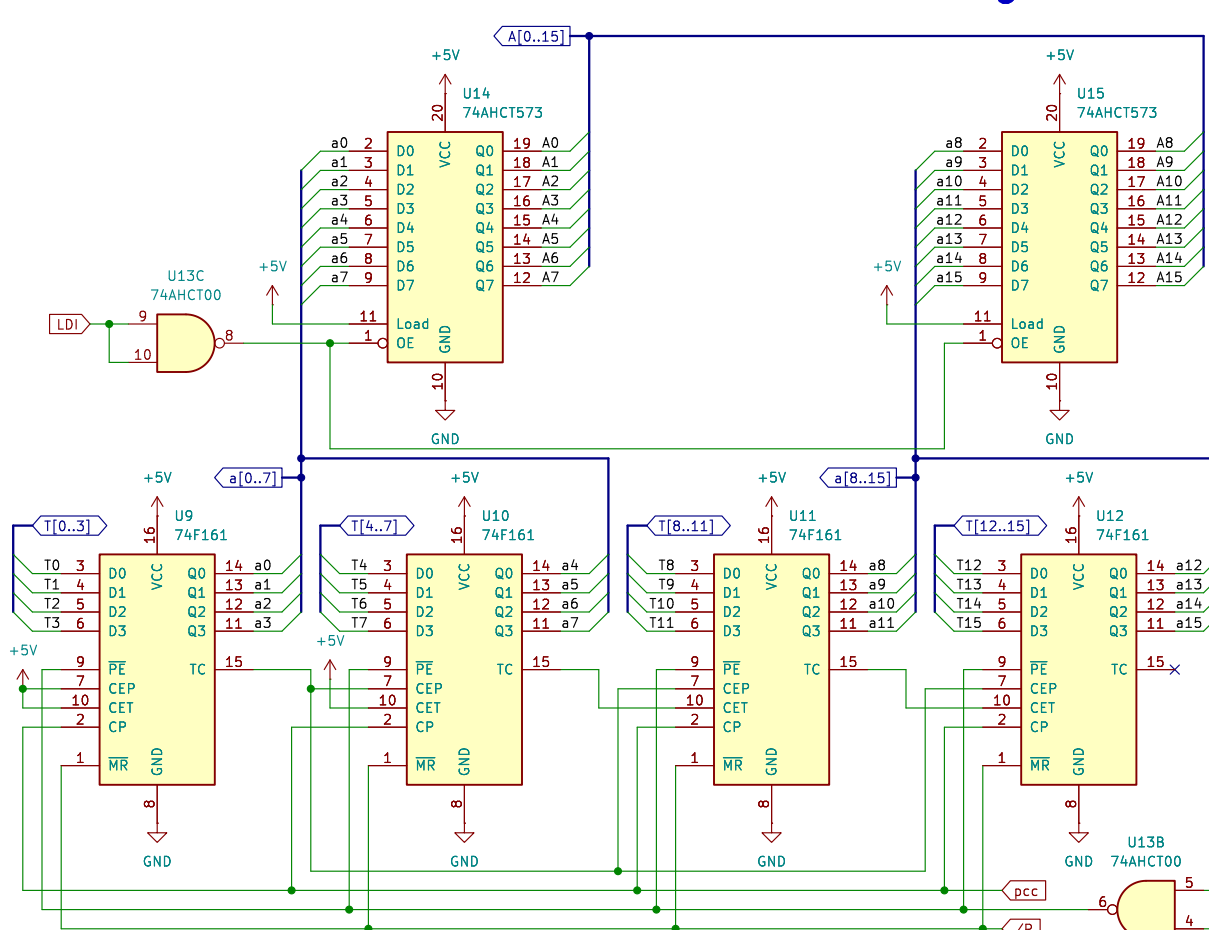
Reset Logic



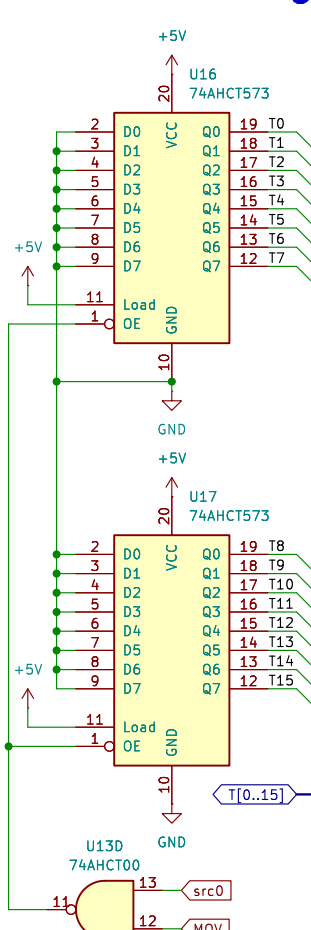
Instruction Decoder



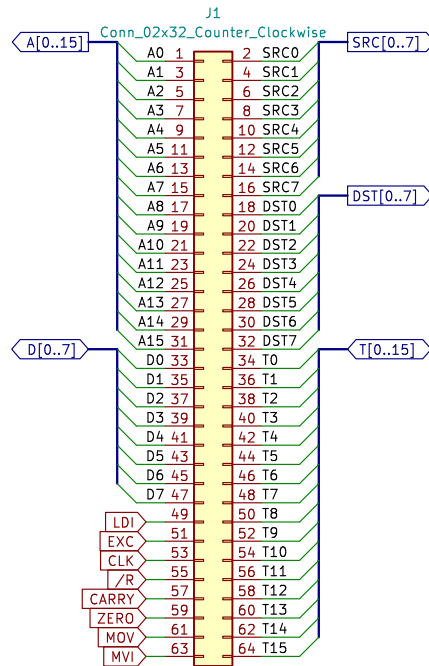
Program Counter



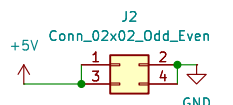
Zero Register



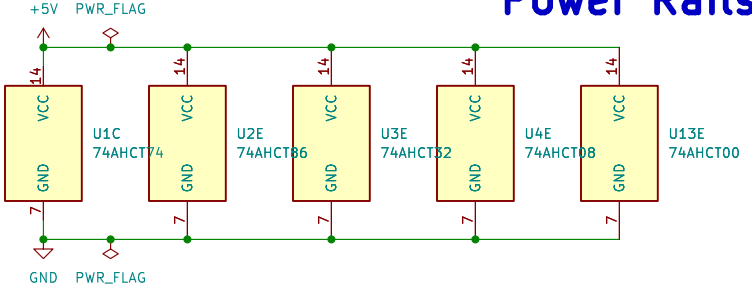
Stacking Header



PWR Conn



Power Rails



Unused

