TTA8 Outline

16-bit Transport Bus
16-bit Address Bus
8-bit External Data Bus

Function Units
- Control Unit 16-bit [0]
    - Linked Registers
        - Destination Register, Program Counter {dst0}[16-bit]
        - Source Register, Zero {src0}[16-bit]
- Load/Store Unit 16/8-bit [1..2]
    - Destination/Source Register, Address {dst1}{src1}[16-bit]
    - Linked Registers
        - Destination Register, Data {dst2}[8-bit]
        - Source Register, Data {src2}[8-bit]
- ALU Unit 8-bit [3..5]
    - Destination/Source Register, Data A {dst3}{src3}[8-bit]
    - Destination/Source Register, Data B {dst4}{src4}[8-bit]
    - Linked Registers
        - Destination Register, ALU Control {dst5}[8-bit]
        - Source Register, Data {src5}[8-bit]
- Register Unit 16/8-bit [6..7]
    - Destination/Source Register, Data Low {dst6}{src6}[8-bit DEST, 16-bit SRC]
    - Destination/Source Register, Data High {dst7}{src7}[8-bit]

bit	7	6	5…3	2…0
mv	0	0	dest	source
mvc	0	1	dest	source
mvz	1	0	dest	source

bit	7	6	5…4	3	2…0
mvi	1	1	nibble select	hi-bit	lo-data