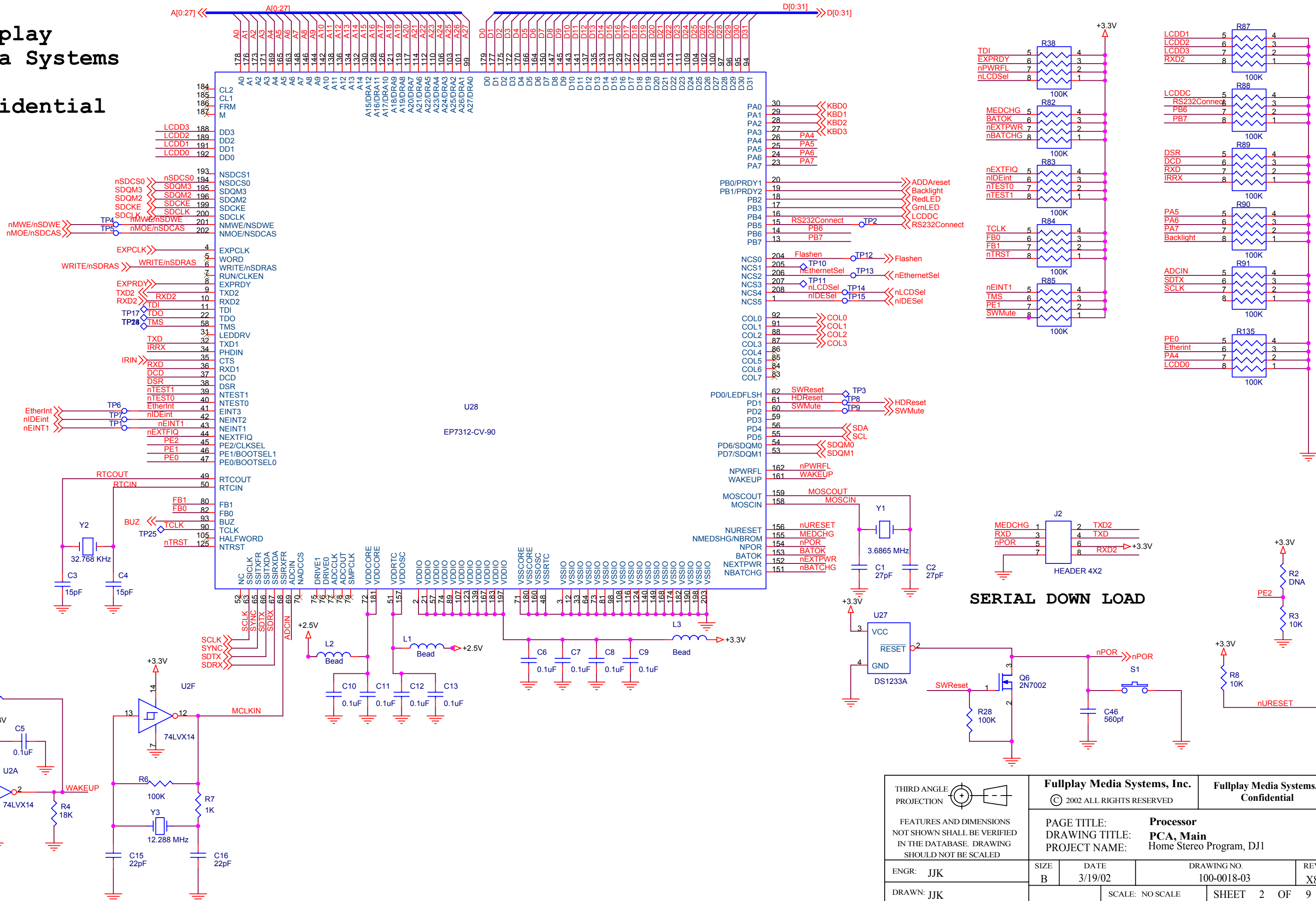


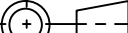
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| REVISION:    | X1   |         |  |
| CHECKED BY:  |  |         |  |
| APPROVED BY: |  |         |  |
| X1           | PRE RELEASE  |         |  |
| X2           | ADDED AUDIO AMP CIRCUIT AND XILINX CPLD  |         |  |
| X3           | Added C127 and C132 Changed Filter caps on 42L50 to Analog Ground. 7/25/02   |         |  |
| X4           | Fixed the symbols for QQ3,Q4,Q5,Q6 Source and Drain were swapped. 8/02/02  |         |  |
| X5           | Moved RS232Connect from PD3 (u23 Pin 59) to PB5 (u23 Pin 15) Net at PB5 was named ACLKCSel. Added GNDTP 1-10. Renamed NET PB2 to RedLED renamed NET PB3 to GrnLED. 8/08/02<br>Added C133 470pF NPO 0603. |         |  |
| X6           | Created the NET +12VF, moved L6 to +12VF   | 8/12/02 |  |
| X7           | Changed C46 from 0.1uf to 560pF  | 8/15/02 |  |
| X8           | Changed S1 to PN 960005  | 8/16/02 |  |

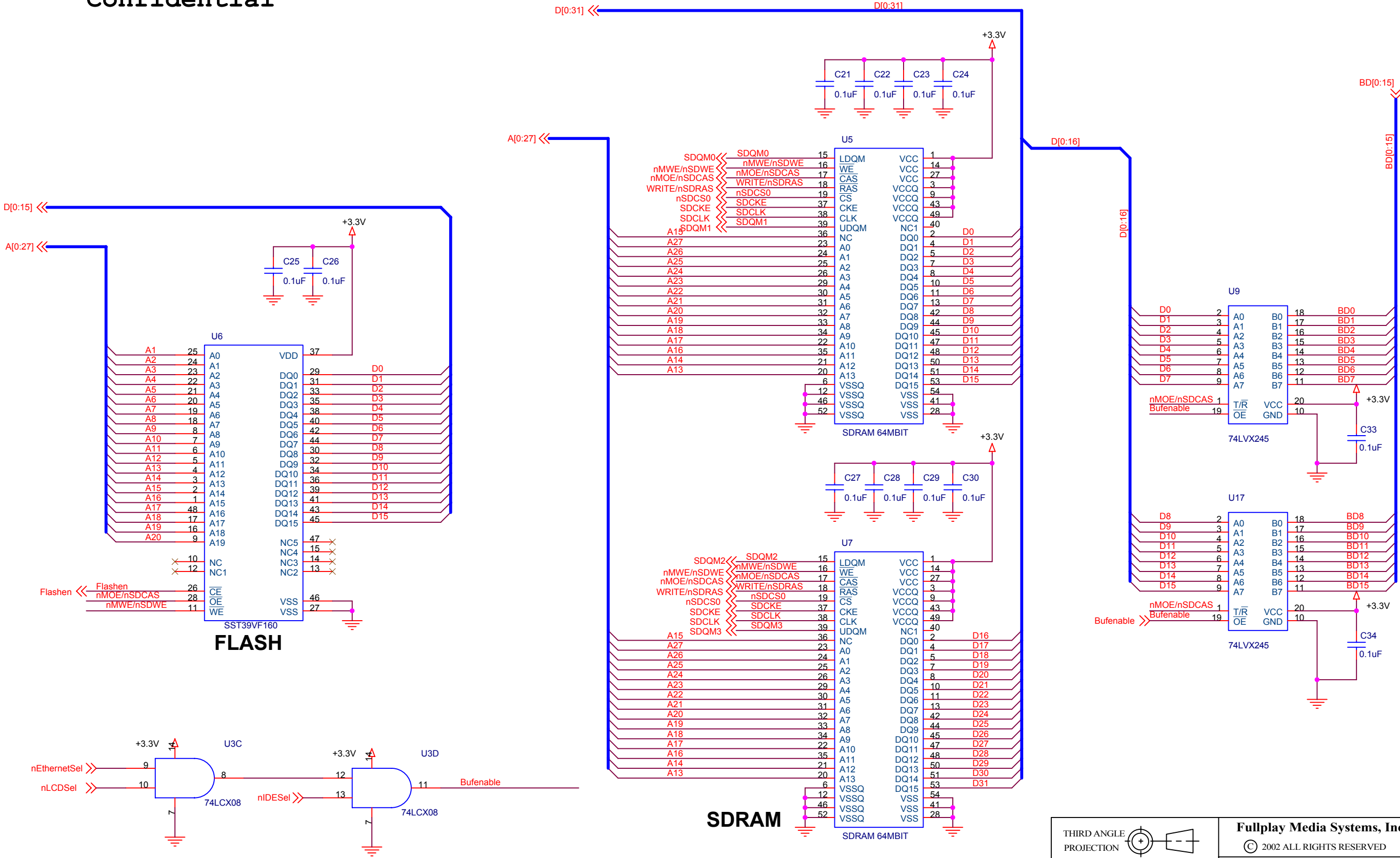
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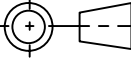
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|  | <p>PAGE TITLE: <b>Processor</b></p> <p>DRAWING TITLE: <b>PCA, Main</b></p> <p>PROJECT NAME: Home Stereo Program, DJ1</p> |                 |   |           |
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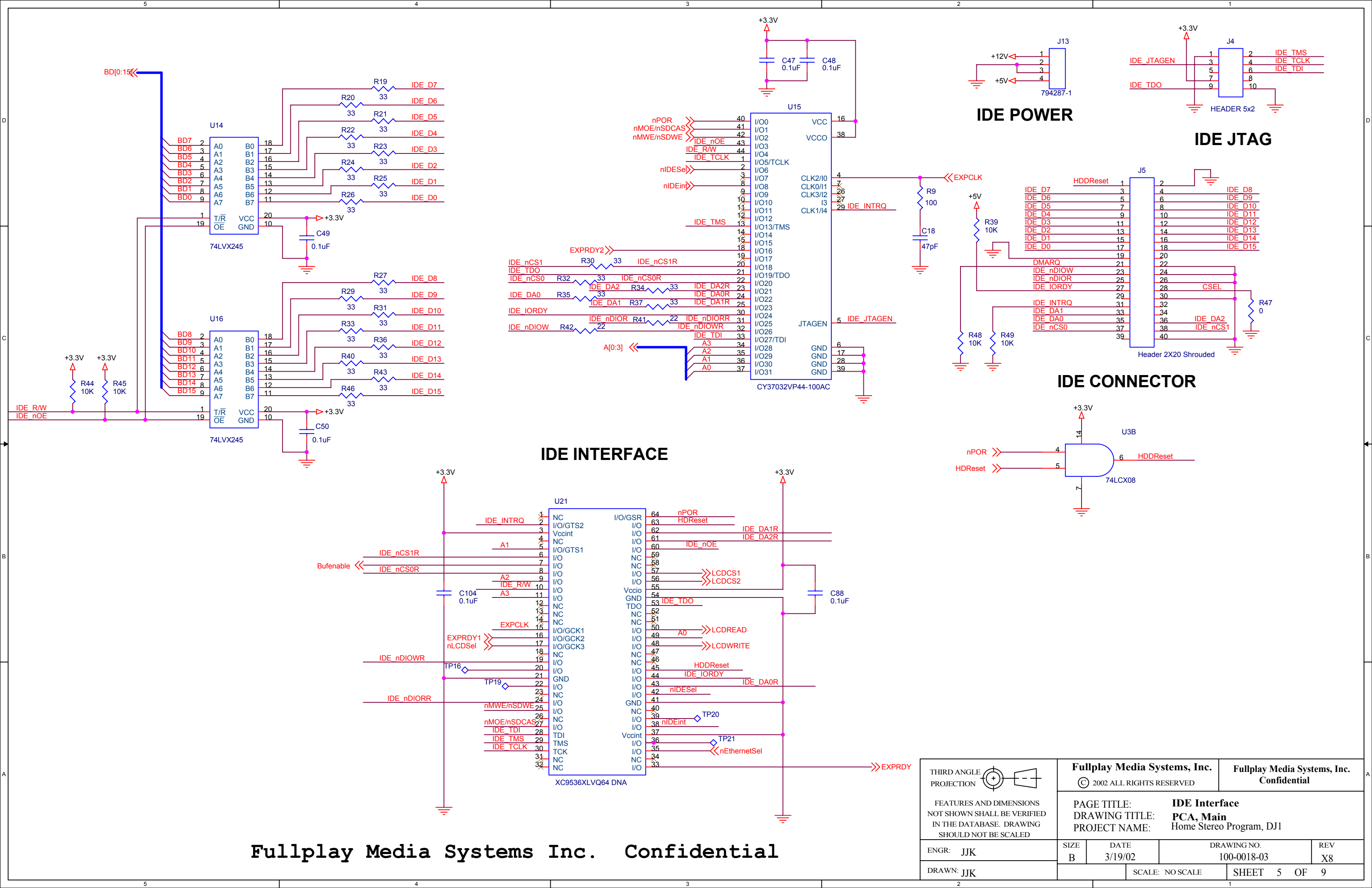
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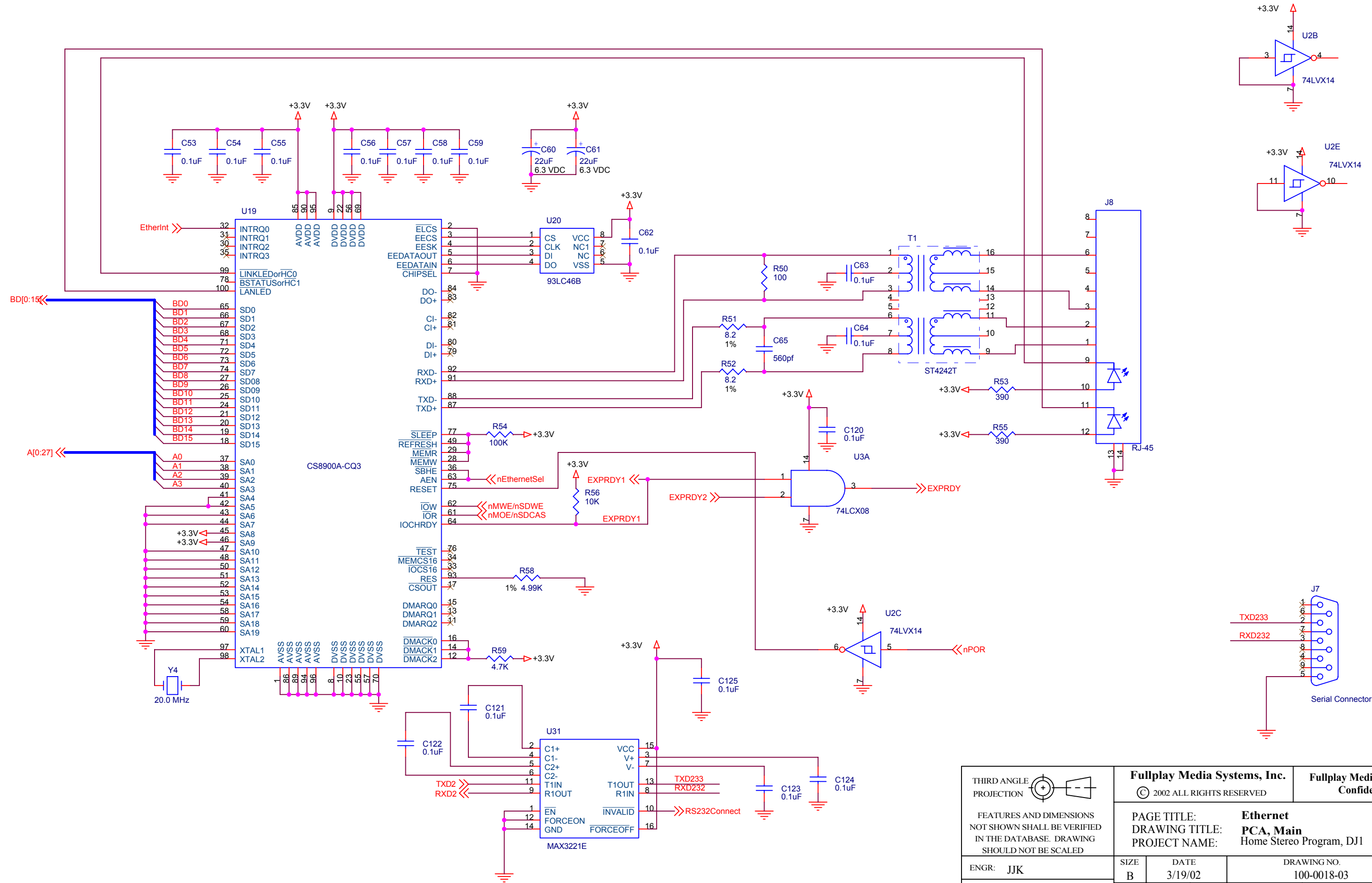
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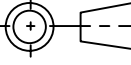


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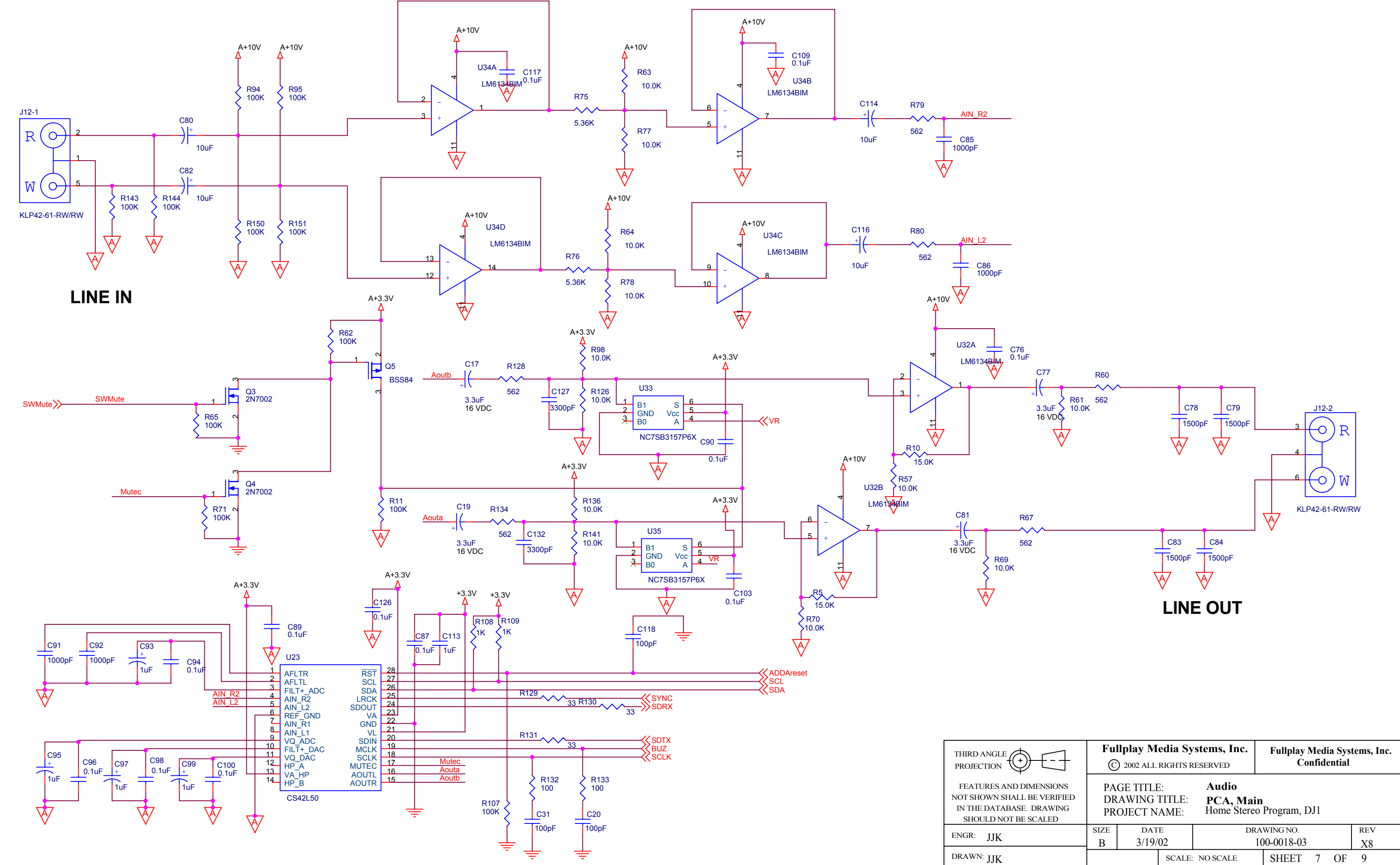


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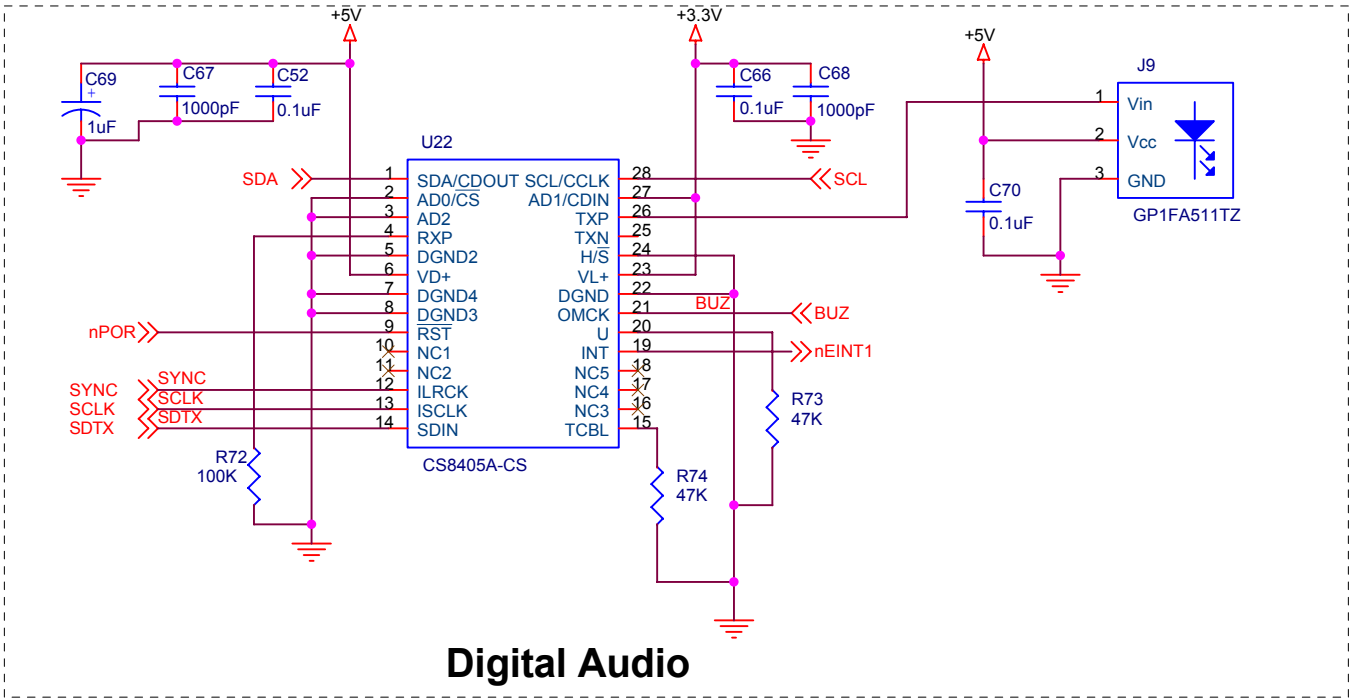
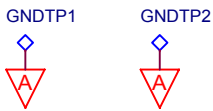
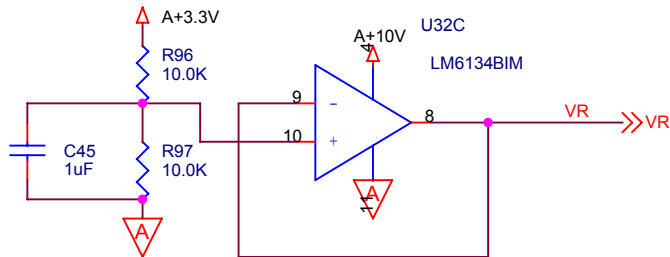
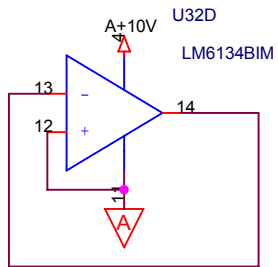
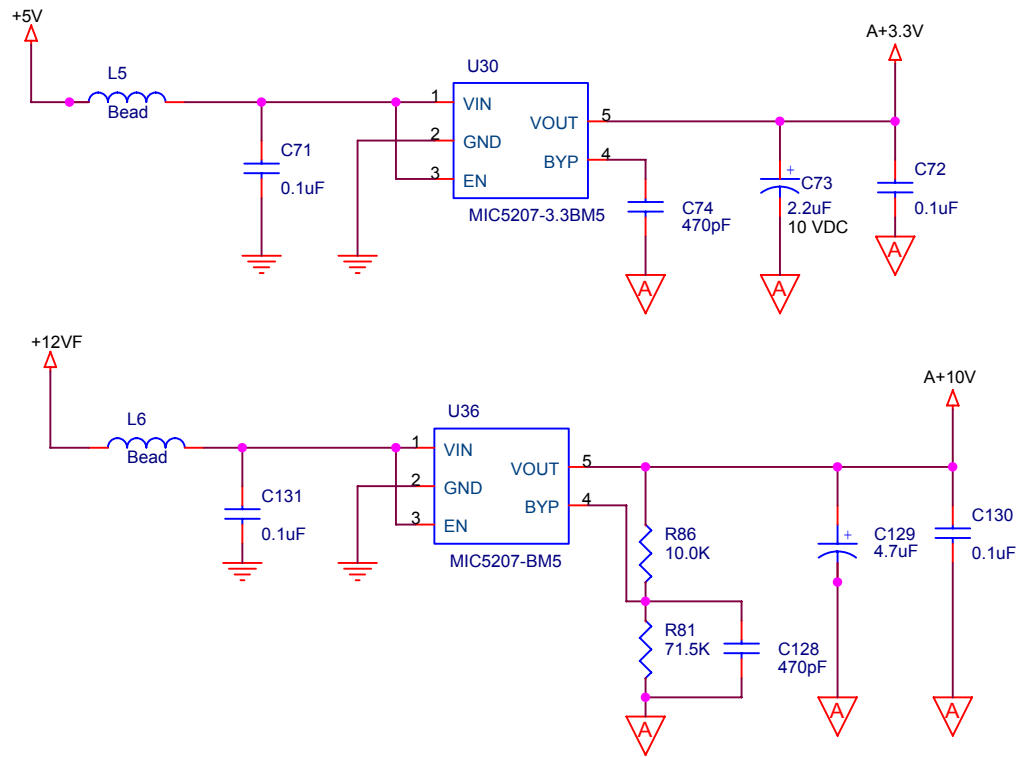


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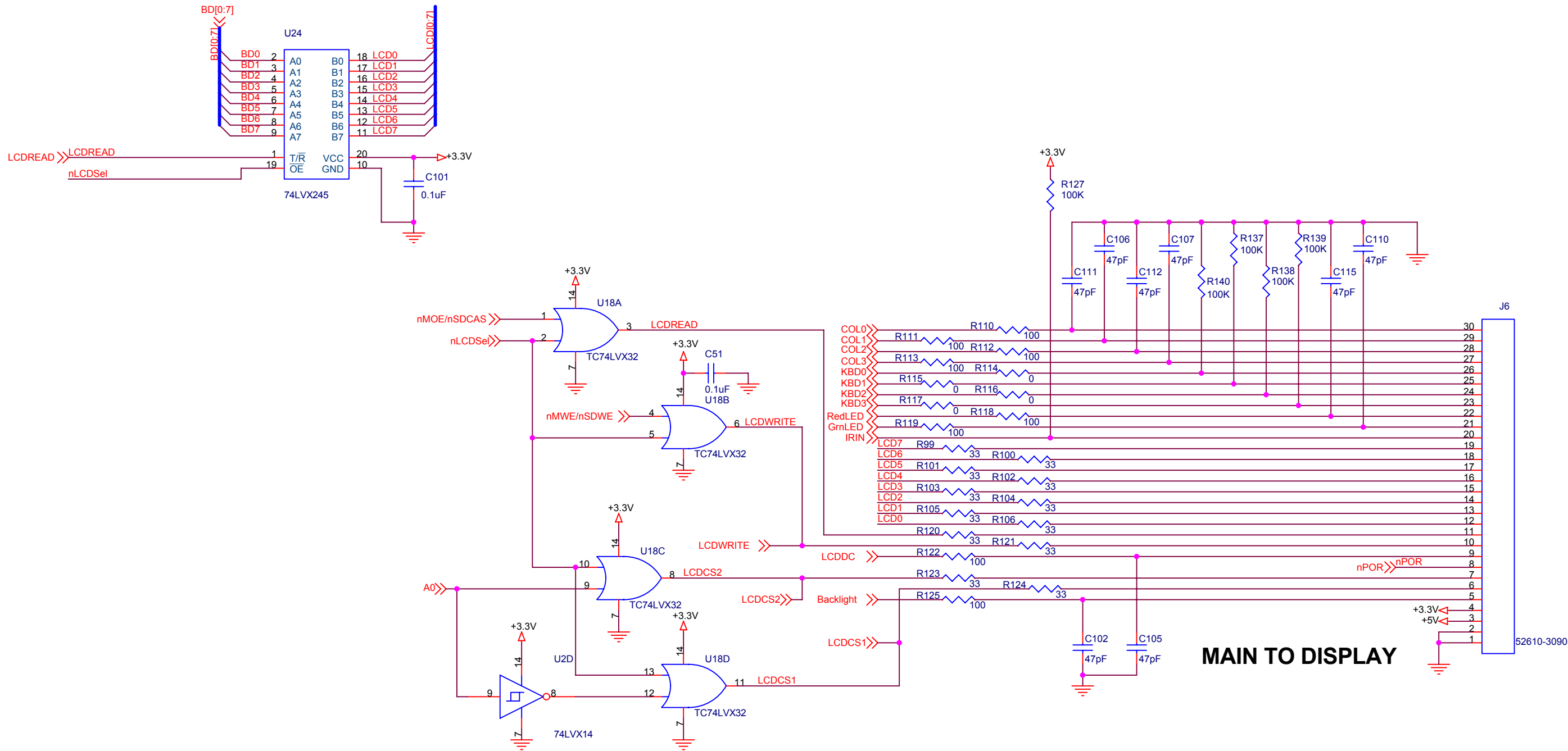


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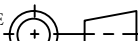


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|   | PAGE TITLE: <b>Display Interface</b><br>DRAWING TITLE: <b>PCA, Main</b><br>PROJECT NAME: Home Stereo Program, DJ1 |                 |  |              |
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