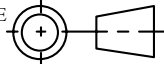
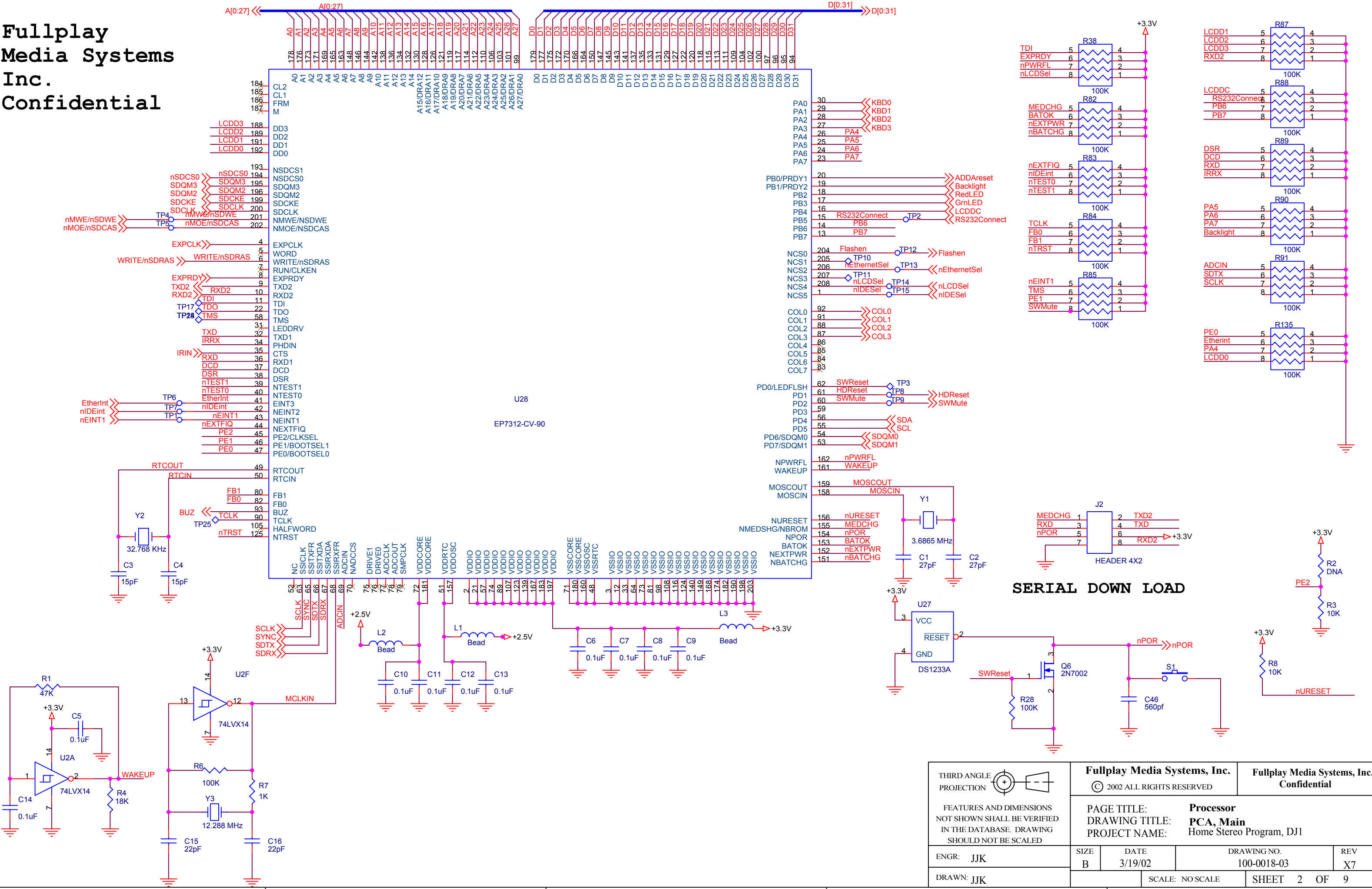


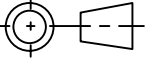
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REVISION:	X1
CHECKED BY:	
APPROVED BY:	
X1	PRE RELEASE
X2	ADDED AUDIO AMP CIRCUIT AND XILINX CPLD
X3	Added C127 and C132 Changed Filter caps on 42L50 to 7/25/02 Analog Ground.
X4	Fixed the symbols for QQ3,Q4,Q5,Q6 Source and Drain were swapped. 8/02/02
X5	Moved RS232Connect from PD3 (u23 Pin 59) to PB5 (u23 Pin 15) Net at PB5 was named ACLKCSel. Added GNDTP 1-10. Renamed NET PB2 to RedLED renamed NET PB3 to GrnLED. 8/08/02 Added C133 470pF NPO 0603.
X6	Created the NET +12VF, moved L6 to +12VF
X7	Changed C46 from 0.1uf to 560pF

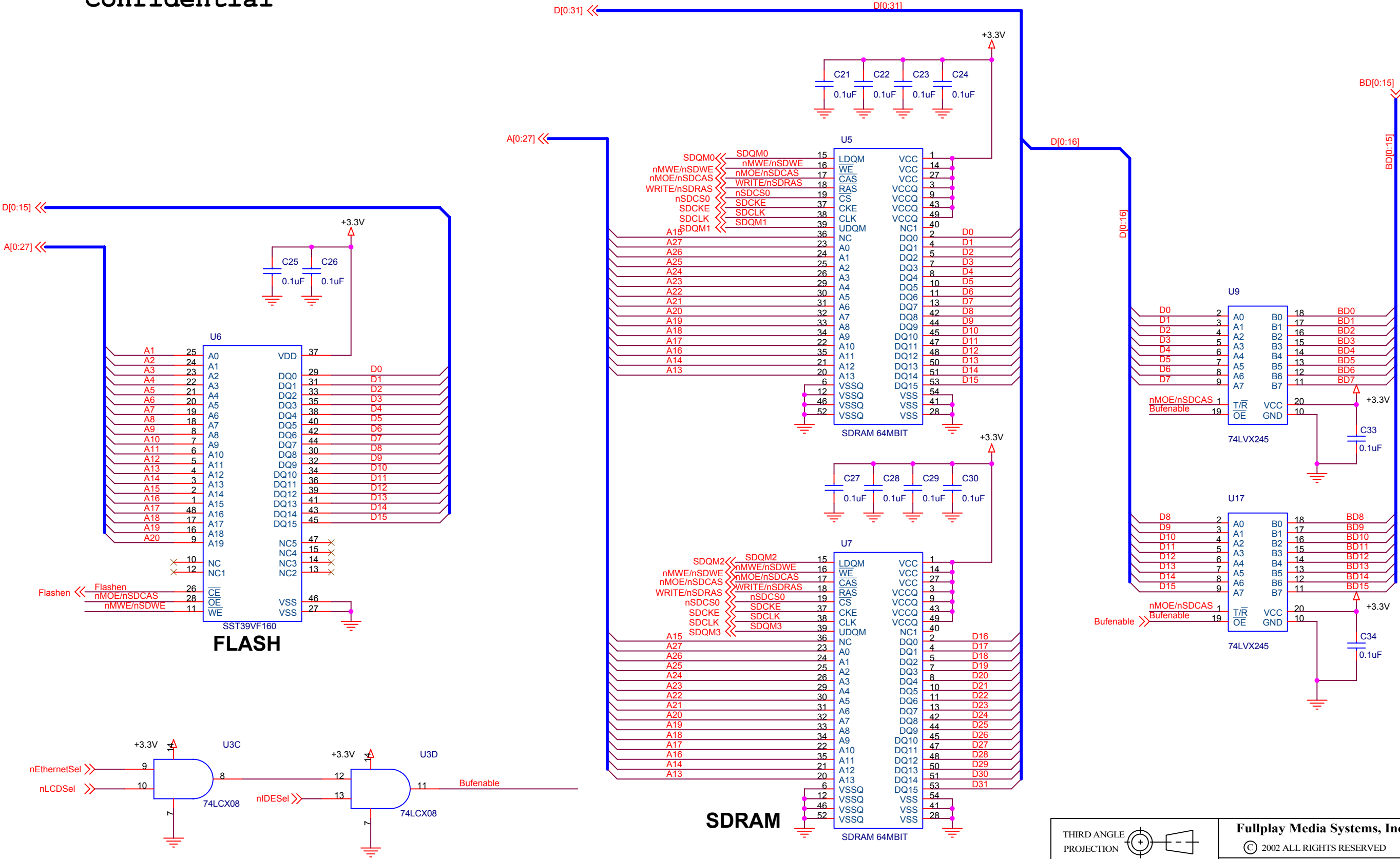
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	<div>PAGE TITLE: Documentation</div> <div>DRAWING TITLE: PCA, Main</div> <div>PROJECT NAME: Home Stereo Program, DJ1</div>			
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DRAWN: JJK	SCALE: NO SCALE		SHEET 1 OF 9	

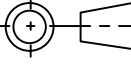
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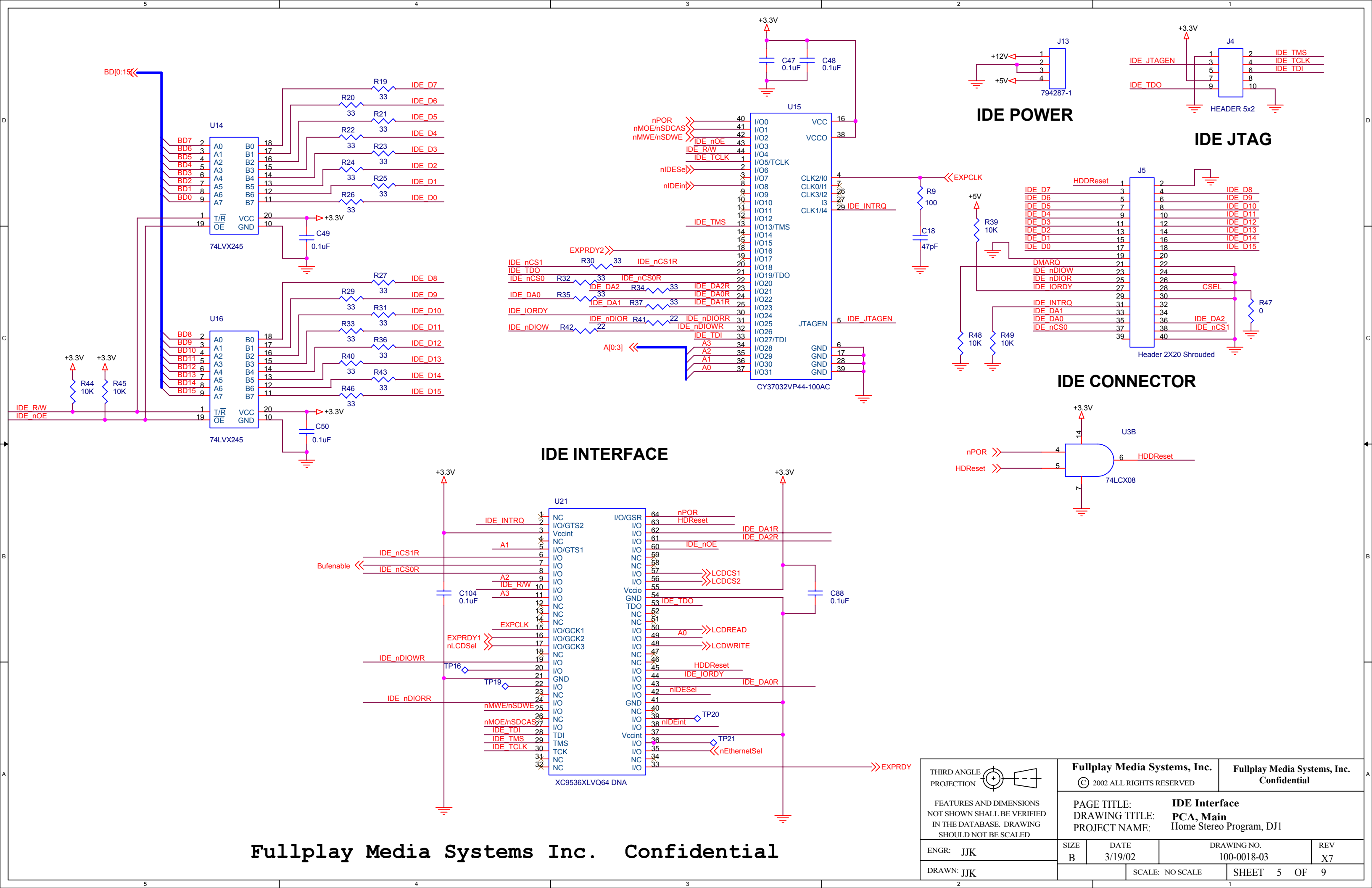


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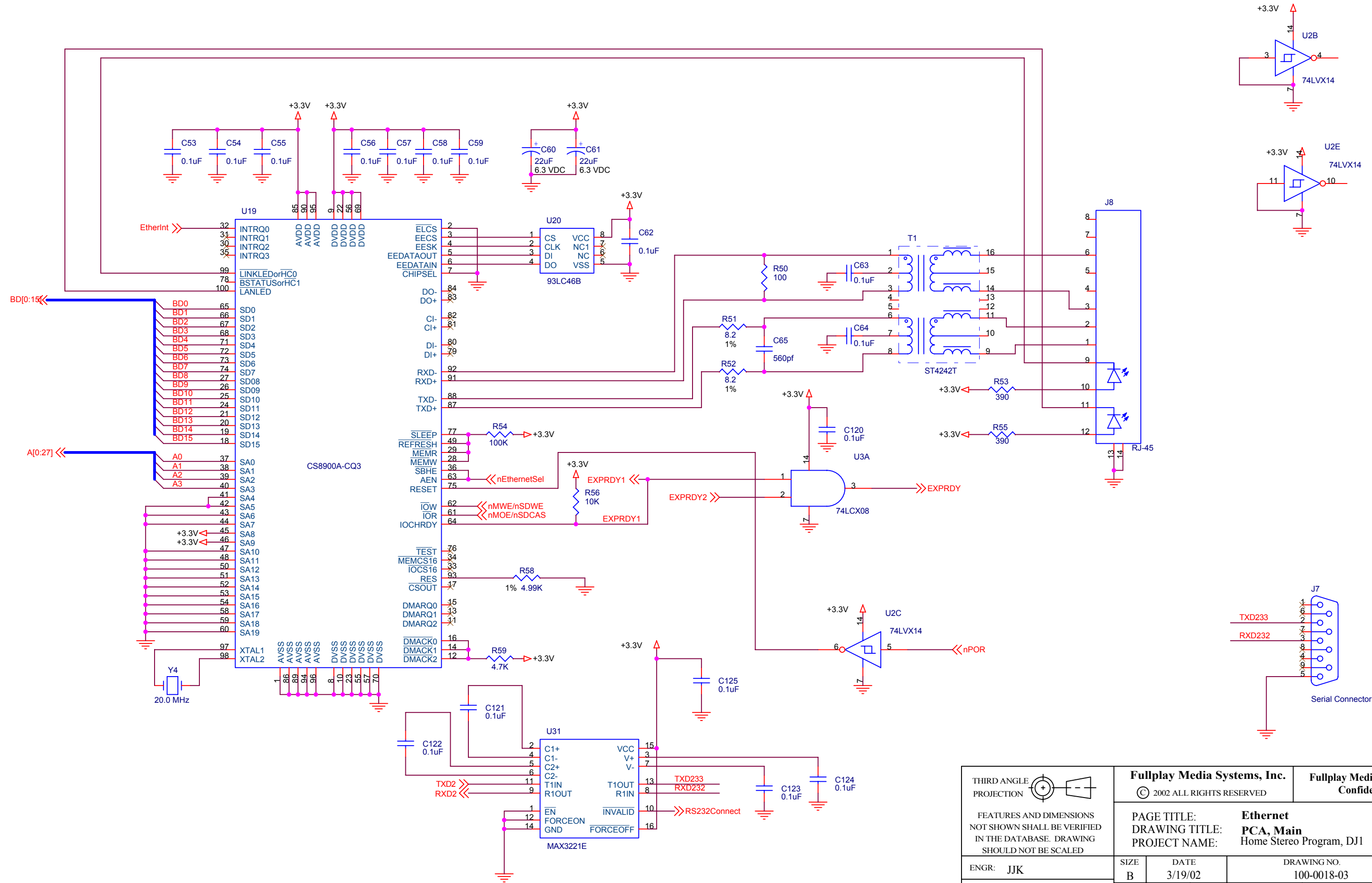
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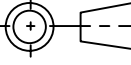


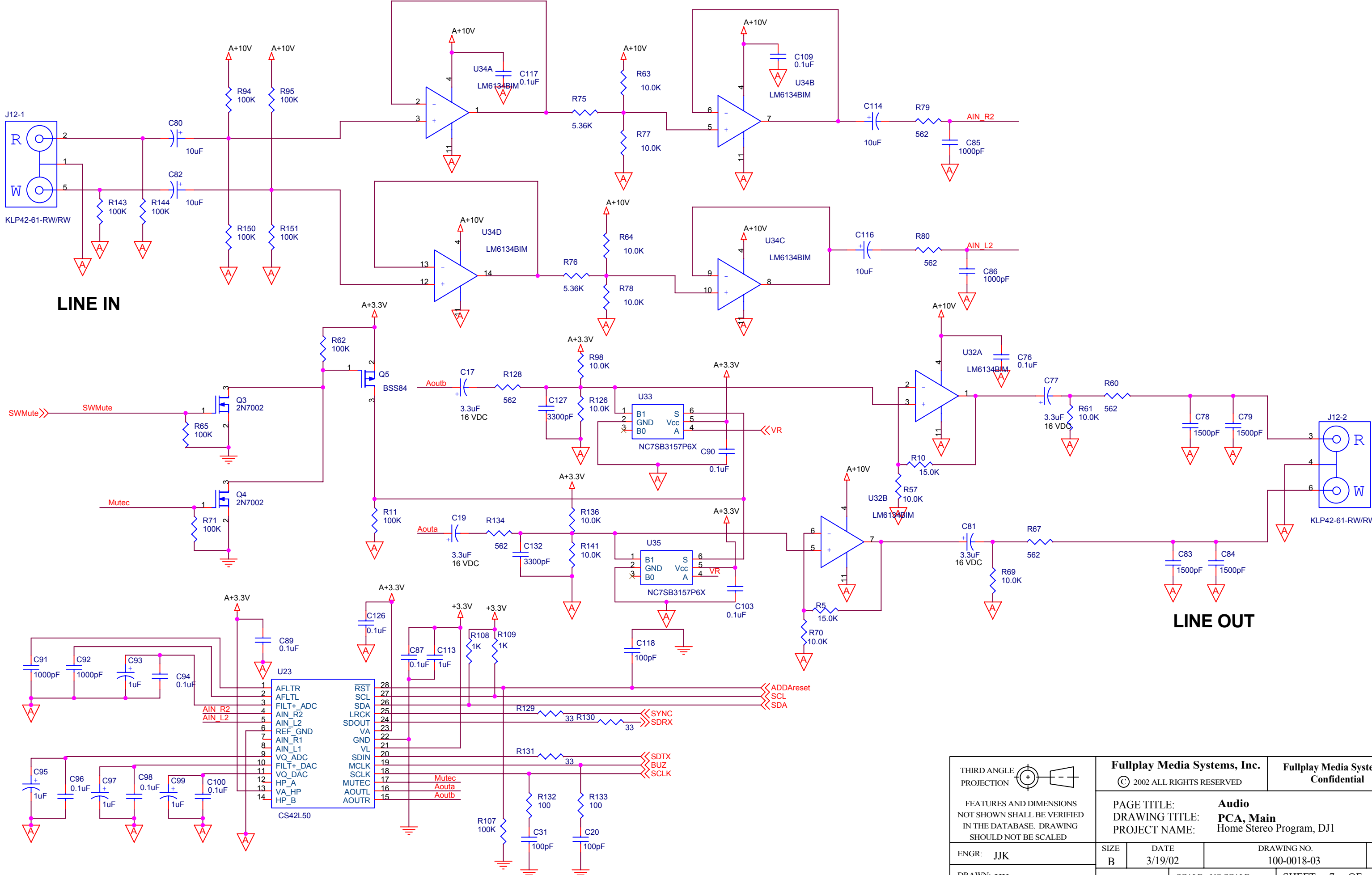
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DRAWN: JJK	SCALE: NO SCALE		SHEET 3 OF 9	REV X7

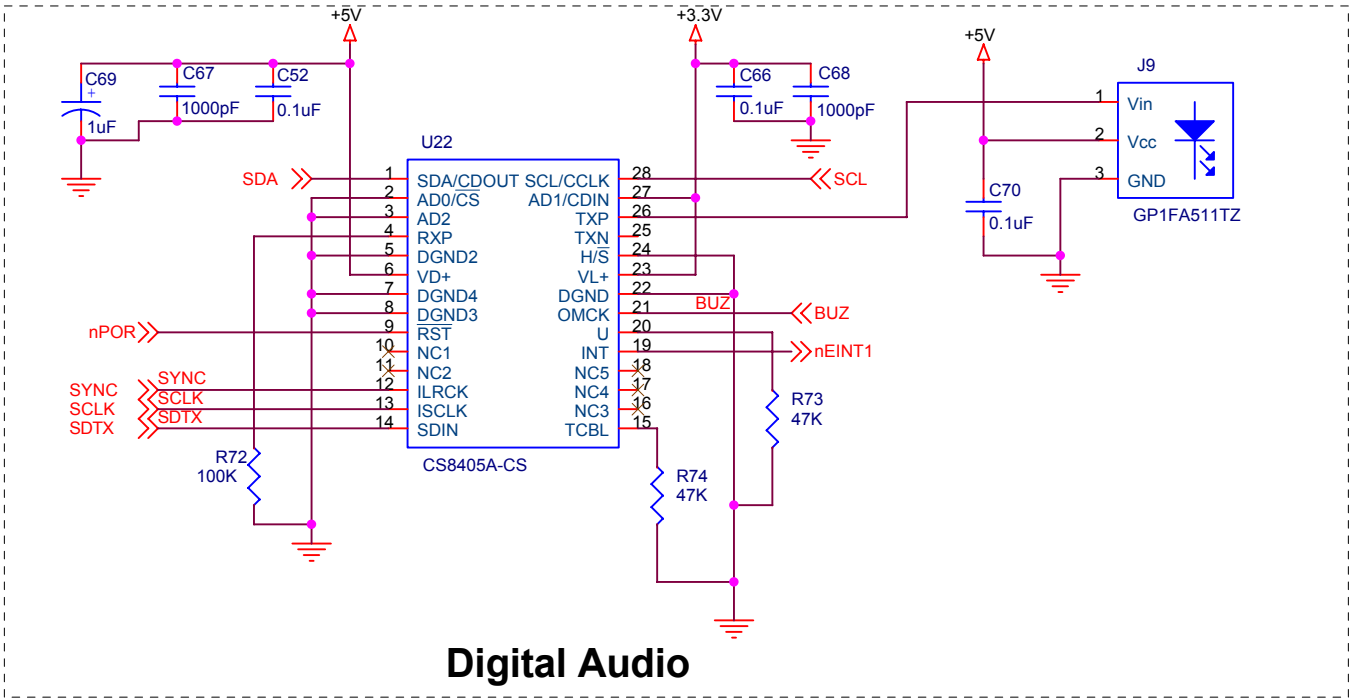
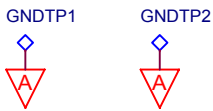
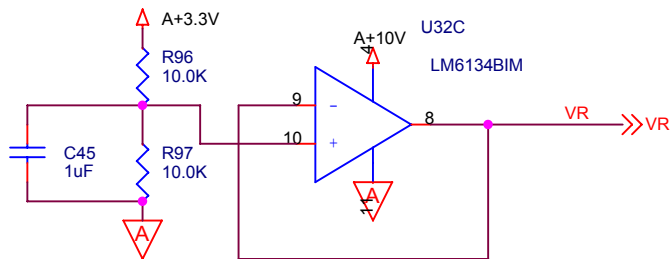
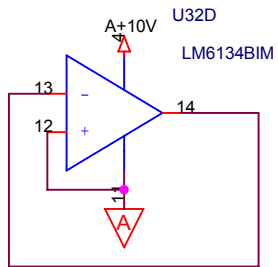
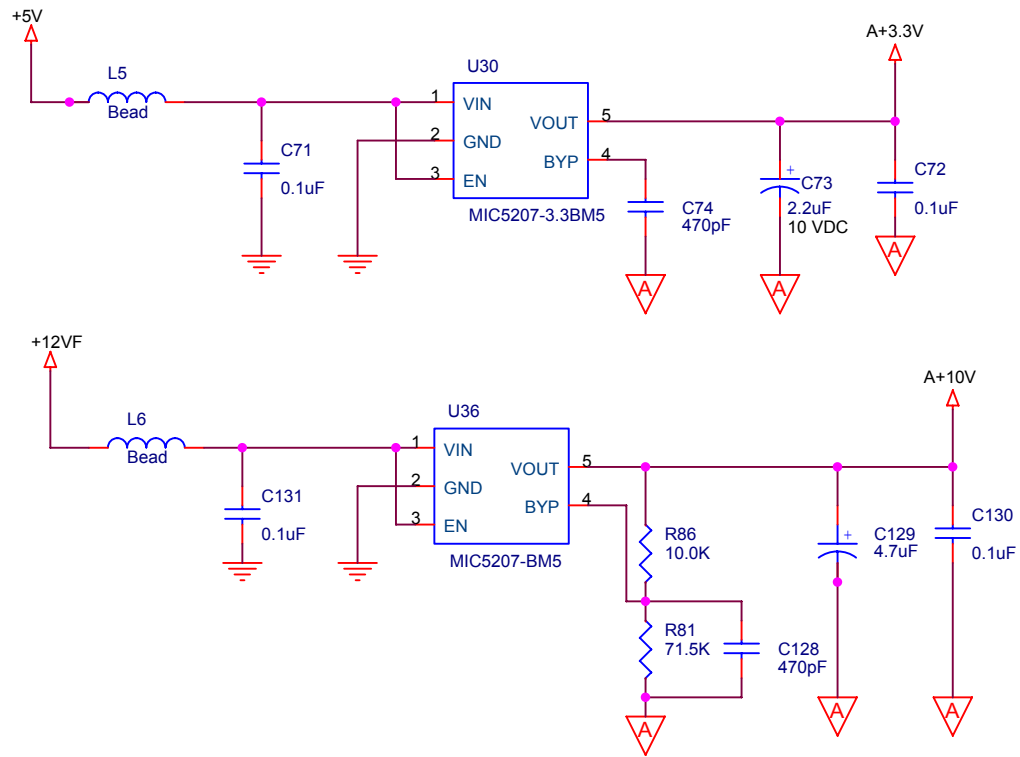


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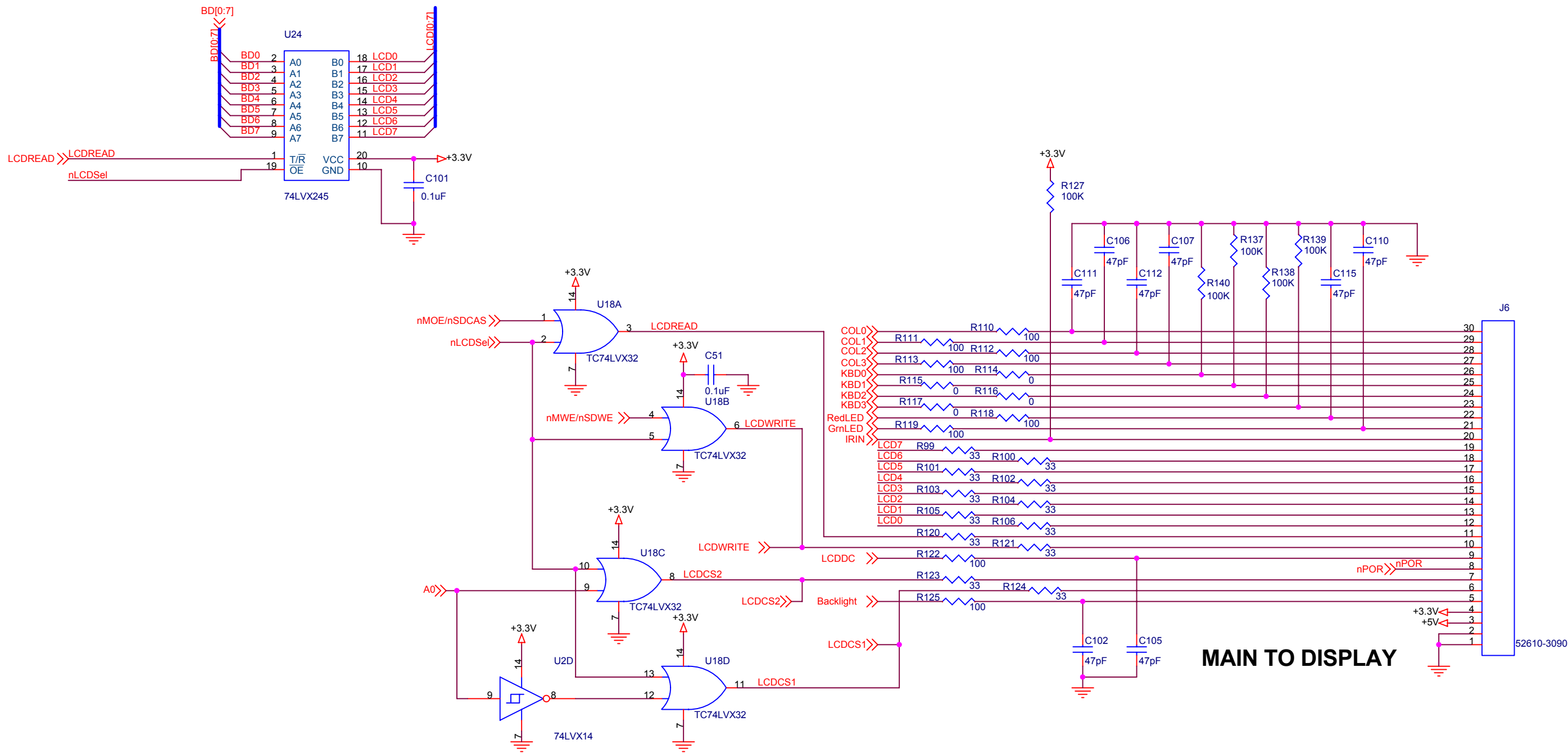


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		SHEET 9 OF 9		