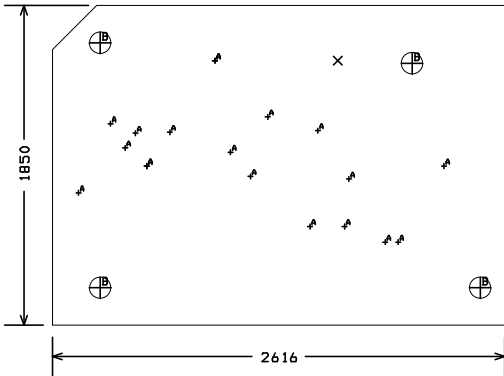



NOTES:

1. MATERIAL (TYPE FR-4) WITH 1 OZ. COPPER ON ALL LAYERS AND A FINAL COMPOSITE THICKNESS 0.062 INCHES +/- 0.002 INCHES (INCLUDING SOLDER MASK). MATERIAL MUST MEET UL RATING 94V-0.
2. DRILL BOARD IN ACCORDANCE WITH THE DRILL CHART SHOWN. ALL HOLE SIZES ARE FINISHED HOLES.
3. HASL PLATING MUST BE APPLIED TO ALL EXPOSED PADS AND HOLES.
4. THE SOLDER MASK (BLUE SOLDER RESIST) SHALL BE APPLIED TO BOTH SIDES OF THE PRINTED WIRING BOARD PER IPC-SM-840, TYPE B, CLASS 2.
5. ALL SOLDER JOINT, CONTACT PADS AND LOCATING HOLES SHALL BE FREE OF SOLDER MASK.
6. NOMENCLATURE SHALL BE SCREENED IN WHITE EPOXY INK ON BOTH SIDES OF THE BOARD. THERE SHOULD BE NO INK ON ANY LAND.
7. BOARD EDGES SHALL BE SMOOTH AND FREE OF BURRS.
8. BOARD MATERIAL AND MANUFACTURING PROCESSES MUST MEET UL REQUIREMENTS. SUPPLIERS UL IDENTIFICATION, LOGO AND DATE CODE ARE TO BE AFFIXED IN AN AREA FREE OF CIRCUITRY ON THE BOARD.
9. THE PWB WARPAGE SHALL BE NO MORE THAN 0.020 INCH/INCH (.020 MM/MM).
10. THE BOARD MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND OPENS.
11. THE BOARD OUTLINE GERBER SHOWS THE OVERALL SIZE AND SHAPE. IT ALSO SHOWS ANY INTERNAL CUT-OUTS.
12. PCB LAYERS STACK UP AND INFORMATION:
CSILK
CSOLDER
CSIDE
WSIDE
WSOLDER
WSILK

GERBER FILES WERE GENERATED IN DrCAD (() NAME USED ON ARTWORK).
FOLLOWING IS AN EXPLANATION OF FILE EXTENSION:
.SST TOP SIDE SILK SCREEN (CSILK)
.SMT TOP SIDE SOLDER MASK (CSOLDER)
.TOP TOP SIDE LAYER (CSIDE)
.GND GND PLANE LAYER (GND)
.PWR POWER PLANE LAYER (PWR)
.BOT BOTTOM SIDE LAYER (WSIDE)
.SMB BOTTOM SIDE SOLDER MASK (WSOLDER)
.SSB BOTTOM SIDE SILK SCREEN (WSILK)
.SPB SOLDER PASTE BOTTOM (WPASTE)
.SPT SOLDER PASTE TOP (CPASTE)
.GTD GERB TOOL DATA
.LIS POST PROCESSOR REPORT
.DRD DRILL INFORMATION
.DTS DRILL TAPE SUMMARY
.TAP DRILL INFORMATION EXCELLON FORMAT
.FAB THIS FABRICATION DRAWING
.AST ASSEMBLY TOP
.ASB ASSEMBLY BOTTOM
13. INTERPRET THIS DRAWING IN ACCORDANCE WITH IPC-D-325
14. BOARDS TO FABRICATED PER IPC-6011 CLASS 2 EXCEPT AS SPECIFIED HEREIN.
15. BAREBOARD TEST PER ANSI/IPC-6012
 - A. CONTINUITY TESTING PER PARAGRAPH 3.9.2.1
 - B. CIRCUIT ISOLATION TESTING PER PARAGRAPH 3.9.2.2



DRILL CHART				
SYM	DIAM	TOL	QTY	NOTE
♂	0.028		17	
x	0.038		1	
⊥	0.125		4	NON-PLATED
TOTAL			22	

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<p>FEATURES AND DIMENSIONS NOT SHOWN SHALL BE VERIFIED IN THE DATABASE. DRAWING SHOULD NOT BE SCALED</p>	<p>PAGE TITLE: FABRICATION</p> <p>DRAWING TITLE: PCB, DISPLAY RIGHT</p> <p>PROJECT NAME: HOME STEREO PROGRAM, DJ1</p>			
<p>ENGR: R.C. TRUE</p>	<p>SIZE B</p>	<p>DATE 11-07-01</p>	<p>DRAWING NO. 200-0023-01</p>	<p>REV X3</p>
<p>DRAWN: M.J. KIDD</p>	<p>SCALE: NO SCALE</p>		<p>SHEET 1 OF 1</p>	