

Dharma Memory Map

Chip Select	Chip Select Address Range	Dharma Vaild Range	External Address Bus Used	Word Size
CS[7] (Internal only) Boot ROM (128 bytes)	7000.0000 - 7FFF.FFFF	7000.0000 - 7000.007F	N/A	32bit
CS[6] (Internal only) SRAM (38400 bytes)	6000.000 - 6FFF.FFFF	6000.0000 - 6000.85FF	N/A	32bit
nCS5 (IDE)	5000.0000 - 5FFF.FFFF	5000.000 - 5000.000F	A0 - A3	16bit
nCS[4] (USB)	4000.0000 - 4FFF.FFFF	4000.0000 - 4000.0001	A0	8bit
nCS[3] (PCMCIA)	3000.0000 - 3FFF.FFFF	3200.0000 - 3201.FFFF	A0 - A16, A25	8bit or 16bit
nCS[3] (Memory Mapped Inputs)	3000.0000 - 3FFF.FFFF	3040.0000	A22	16bit
nCS[3] (Memory Mapped Outputs)	3000.0000 - 3FFF.FFFF	3080.0000	A23	8bit
nCS[3] (Memory Mapped Interrupts)	3000.0000 - 3FFF.FFFF	3100.0000	A24	8bit
nCS[2] (Compact Flash)	2000.0000 - 2FFF.FFFF	2000.0000 - 2000.003E	A1 - A5	16bit
nCS[1] (Ethernet)	1000.0000 - 1FFF.FFFF	1000.0000 - 1000.000F	A0 - A3	16bit
nCS[0] (Flash Rom, 4 Mbyte)	0000.0000 - 0FFF.FFFF	0000.0000 - 003F.FFFE	A1 - A21	16bit
nCS[0] (Flash Rom, 8 Mbyte)	0000.0000 - 0FFF.FFFF	0000.0000 - 007F.FFFF	A2 - A22	32bit