Fast and high-fidelity state preparation and measurement in triple-quantum-dot spin qubits

Jacob Z. Blumoff,* Andrew S. Pan, Tyler E. Keating, Reed W. Andrews, David W. Barnes, Teresa L. Brecht,
Edward T. Croke, Larken E. Euliss, Jacob A. Fast, Clayton A. C. Jackson, Aaron M. Jones, Joseph Kerckhoff, Robert K. Lanza, Kate Raach, Bryan J. Thomas, Roland Velunta, Aaron J. Weinstein,
Thaddeus D. Ladd, Kevin Eng, Matthew G. Borselli, Andrew T. Hunter, and Matthew T. Rakher HRL Laboratories, LLC, 3011 Malibu Canyon Road, Malibu, California 90265, USA
(Dated: January 31, 2022)

We demonstrate rapid, high-fidelity state preparation and measurement in exchange-only Si/SiGe triple-quantum-dot qubits. Fast measurement integration (980 ns) and initialization (\approx 300 ns) operations are performed with all-electrical, baseband control. We emphasize a leakage-sensitive joint initialization and measurement metric, developed in the context of exchange-only qubits but applicable more broadly, and report an infidelity of $2.5\pm0.5\times10^{-3}$. This result is enabled by a high-valley-splitting heterostructure, initialization at the 2-to-3 electron charge boundary, and careful assessment and mitigation of T_1 during spin-to-charge conversion. The ultimate fidelity is limited by a number of comparably-important factors, and we identify clear paths towards further improved fidelity and speed. Along with an observed single-qubit randomized benchmarking error rate of 1.7×10^{-3} , this work demonstrates initialization, control, and measurement of Si/SiGe triple-dot qubits at fidelities and durations which are promising for scalable quantum information processing.

I. INTRODUCTION

Spins in semiconductor quantum dots are promising foundational elements for scalable quantum information processing [1–6]. This technology leverages the hardearned knowledge and exquisite tools of the semiconductor industry, which routinely mass-produces devices with billions of nanoscale structures. In this work we employ quantum dots which are laterally confined by lithographically-patterned gate electrodes and vertically confined within a high-mobility Si/SiGe heterostructure [7, 8]. This construction yields suppressed sensitivity to gate-oxide disorder and noise relative to MOS-based devices [9–11]. Additionally, the use of isotopicallyenriched ²⁸Si permits long spin-coherence times relative to control times [12–14]. Rather than form a qubit from an individual electron spin, one can encode a qubit in the collective spin state of three electrons in three quantum dots—the exchange-only qubit (more specifically, a decoherence-free subsystem [DFS] qubit) [15–18]. These qubits are insensitive to global magnetic noise processes and allow universal control with only baseband electrical pulses, not requiring lasers, large RF circuits, or micromagnets.

High-fidelity single-qubit operations for Si/SiGe exchange-only qubits have been previously demonstrated [19], and we here show high-fidelity state preparation and measurement (SPAM) in a similar device. State preparation and measurement are integral to quantum computing—indeed they are the first and last steps in any algorithm, be it for NISQ, fault-tolerant computing, networking, or sensing applications. A reasonable ambition for SPAM performance is that it maintain fidelities

and durations comparable to those of coherent manipulations, minimizing its likelihood of being a limiting factor in a larger application which may require multiple rounds of initialization and measurement. This establishes concrete goals and forms a natural basis of comparison, and using these criteria we show that exchange-only-qubit SPAM can maintain performance parity.

Measurement of spin qubits has a deep history and admits a large space of approaches [2, 3, 5]. With few exceptions, most schemes involve a spin-to-charge conversion (S2C) mechanism followed by a measurement of the resulting charge-occupancy state. Spin-to-charge conversion is most frequently achieved with spin-dependent tunneling (SDT) [20] or with Pauli spin blockade (PSB) [21, 22]. There are myriad quantum-dot charge detection technologies, foremost the use of quantum point contacts (QPCs) [20, 23], single-electron transistors (SETs) [24– 32], or dot charge sensors (DCSs, also called sensor quantum dots) [33–35], all of which can be measured at DC or RF [36–38]. In the RF case, gate reflectometry can enable measurement at almost any gate electrode, and has yielded remarkable results [39-41]. This approach bolsters applicability to dense gate architectures [31, 42– 45] but requires a bulky LC resonator elsewhere in the system. Here we employ PSB, which permits higher measurement fidelity and greater flexibility in applied magnetic field than does SDT, and a DCS, which requires only a structure of similar scale to—and simultaneously fabricated with—the qubits themselves. Furthermore, DCSs allow for greater measurement sensitivity than do QPCs [37].

Our key advancements relative to prior work in PSB/DCS spin qubit measurements—enabling the SPAM performance reported here—include details of the device, the control and amplifier chains, and our voltage biasing protocols. We also employ careful met-

^{*} jzblumoff@hrl.com

rics for device-relevant characterization of SPAM fidelity, and much of this analysis is applicable to measurement modalities other than the PSB/DCS approach. One goal of this work is to provide an updated basis upon which to evaluate the potential of the PSB/DCS approach; whether it is sufficient for high-quality SPAM, or whether more read-out resources will be required for acceptable performance.

Spin qubit initialization also has several approaches, including the use of microwave pumping [46], relaxation [20, 47], and measurement-based mechanisms [48], but in light of the desirably-weak interaction of spins with electromagnetic fields, these approaches are often impractically slow. A faster method available in spin systems is to exchange quantum-dot electrons with those from a cold electron reservoir. This contrasts with initialization in other architectures (e.g., superconducting qubits or trapped ion qubits), for which it is impossible or inefficient to deterministically replace the particles hosting the qubit.

This article is organized into the following sections: first we describe the measurement approach used for our devices and quantify some aspects of its performance, paying attention to both fidelity and duration. We then do the same for state preparation. We conclude by discussing our preferred technique for quantifying SPAM performance using a joint metric ($F_{\rm BC}$, with BC standing for Benchmarking Contrast) and compare the results to other key qubit device measures.

II. MEASUREMENT

In this work we employ a device fabricated similarly to that of Ref. 19. This device comprises a linear array of six quantum dots and two DCSs, which share a common source but have independent drain leads. Only half of the device is utilized in this work, and a nominally identical device is shown in the electron microscopy inset of Fig. 1. The overlapping gates are formed from aluminum, 60 nm above a 3-nm-wide, 800-ppm ²⁹Si quantum well [13, 49, 50].

Singlet-triplet measurement via Pauli spin blockade exploits the fermionic nature of electrons, which are constrained to anti-symmetric wavefunctions. At low magnetic fields, the ground state of two electrons in one dot must correspond to an anti-symmetric spin singlet; higher-energy triplet states which are spin-symmetric require valley and/or orbital excitations to provide overall antisymmetry. Here we notate charge in a set of dots as (n, m); for example (2,0) represents two electrons in dot 1, zero electrons in dot 2, etc. A spin singlet is indicated by S, so "S(2,0)" is the ground state of the (2,0)charge state of a double-dot. In the (2,0) charge state, valley and/or orbital excitations provide a singlet-triplet energy splitting δ_{ST} which can be exploited for both spin initialization and measurement, with the magnitude of that splitting being critical to the fidelity of both opera-

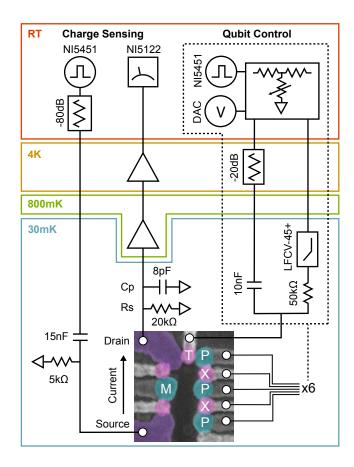


FIG. 1. The signal chain. Square waves are generated at room temperature, attenuated, and sent into the dilution refrigerator, where they act as a bias between source and drain two-dimensional electron gas (2DEG) leads. These square waves probe the conductance of the DCS (labeled M), which is sensitive to the TQD charge state, and generate a current across a subsequent 20 k Ω sense resistor. This signal is amplified by a two-stage cryogenic HEMT, then demodulated and thresholded at room temperature. Signal adder circuits (dashed contour) enable fast and accurate baseband control of the plunger (blue, "P" & "M") and barrier (pink "T" and "X") gate biases. The adder circuit (simplified in this figure) is described in further detail in appendix A. Note that the source signal attenuation has a low-impedance output.

tions.

In a triple-quantum-dot (TQD) DFS, the encoded- $|0\rangle$ state is given by a spin singlet on two (hereafter "outer") dots, "S(1,1)". The third dot charge-state is omitted for brevity but is singly occupied by an electron which is sometimes referred to as the "gauge spin" and is crucial for exchange-only universal control. This electron need not be initialized into any particular spin state [18, 19], but reliable exchange coupling requires that it not be in an excited orbital or valley state. The encoded- $|1\rangle$ state of the DFS qubit is a superposition of triplet-states $[T_{0,\pm}(1,1)]$ on the outer dot spins, entangled with this third spin. Since PSB measures whether or not the two outer spins are in a spin singlet or triplet, the preparation

and measurements of exchange-only qubits can proceed in the same fashion as in double-quantum-dot singlettriplet qubits [51, 52].

Charge-states of the triple-quantum-dot are probed by the electrostatic interaction with a nearby DCS (underneath the "M" gate in the device depicted in Fig. 1). The DCS is biased into the Coulomb blockade regime, where its conductance is highly sensitive to the potential environment and, accordingly, to the charge occupancy of the TQD. We interrogate this conductance by applying a bias between the source and drain 2DEGs connected to this dot, modulated by a square wave at a frequency of about 2 MHz. The resulting DCS drain current flows through a 20 k Ω sense resistor R_s , and the generated voltage signal is amplified by a two-stage cryogenic HEMT (Avago ATF-38133) [53, 54], shown in Fig. 1. Placing a low-dissipation stage at the still plate $(T \approx 800 \text{ mK})$ before a second stage of amplification (at $T \approx 4 \text{ K}$) allows for reduced added noise while respecting the thermal constraints of the dilution refrigerator. A cold finger locates that 800 mK amplifier in spatial proximity (several cm) to the device chip, minimizing the (bandwidthlimiting) parasitic capacitance C_p , which we estimate to be roughly 8 pF from the signal bandwidth. The signal then continues to room temperature where it is digitized and demodulated in software.

We measure the spin state of the device by performing spin-to-charge conversion—detuning the device from an "idle" bias configuration (or "coordinate") of low exchange energy in the (1,1) charge cell towards the (2,0)regime. Because of PSB and the finite singlet-triplet energy splitting, the charge-state level crossing occurs at higher detuning bias for spin triplets than for singlets (see the energy diagram in Fig. 2a.) This sets a "measure window" of bias where the spin states differ in their charge character. At an intermediate bias near one half of the two-electron excited-state splitting, the singlet-triplet degree of freedom and the charge occupancy are maximally correlated, and measurement of charge also yields spin-state information. This is directly evident from spin blockade spectroscopy—histograms of DCS conductance as a function of detuning bias (Fig. 2b) [55].

Near the charge-state transition the bias is ramped in time so as to minimize Landau-Zener processes which might excite the spins into elevated valley-orbit states. This is discussed further in Section III. Due to imperfect signal integrity engineering, with the current design we require a "settling" period of order one to ten μ s before the source-drain square waves are applied to maintain consistent measurement results. Generally this delay, rather than integration, dominates the obtainable measurement cadence, but we expect to ameliorate this with further signal chain engineering.

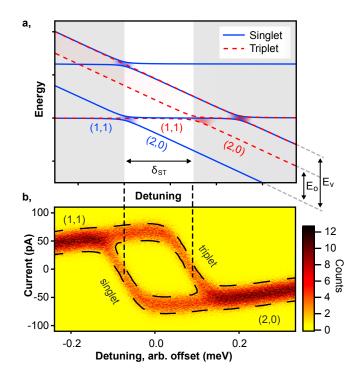
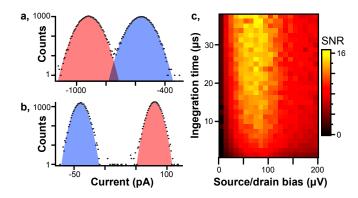


FIG. 2. Energy level diagram and spin blockade spectroscopy. a, Energy level diagram near the measurement window, shown at zero magnetic field for clarity. Horizontal lines (energy independent of detuning) represent the (1,1) charge state used for exchange and idle operations, while the diagonal lines represent the (2,0) charge state. In this illustration the relevant singlet-triplet splitting δ_{ST} (and accordingly the region of spin-to-charge conversion) is set by the two-electron orbital energy (E_o) rather than the excited valley state energy (E_v) . b, Spin blockade spectroscopy, showing histograms of DCS current as a function of detuning between the (2,0) and (1,1) charge states. The fully-resolved gap between the singlet and triplet branches is reflective of highly-efficient spin-to-charge conversion.

A. SNR

We now probe the quality of that measurement, starting with the signal to noise ratio (SNR). The experimental routine begins with preparation of a dephased spin state, which is created by waiting at the idle bias for a time longer than the hyperfine-dominated T_2^* . This is followed by the previously-described S2C and charge-state measurement processes, and we histogram the resulting measurement current. We use this routine to explore the obtainable spin SNR as a function of measurement duration and strength (i.e. source-drain bias amplitude) in Fig. 3. As we increase the integration duration from 980 ns to roughly 40 μ s, the SNR first improves as we better average away white noise, then saturates as it becomes limited by the 1/f charge noise on the DCS. Similarly, the SNR increases quickly as we increase the source-drain bias amplitude but then decreases after a critical value. This reflects nonlinearity in the DCS con-



Readout histograms and SNR as a function of time and bias amplitude. a, Log-scale histograms of DCS current (data in black), with Gaussian fits indicating singlet (blue) and triplet (red) readout signals, here with a total integration time of 980 ns. Thresholding a distribution with this SNR (6.5) provides a bound on $1 - F_{BC} \ge 6 \times 10^{-4}$. b, Histograms taken at an integration time of 40 μ s and with an additional measurement subtracted, giving an SNR of 15.5. This distribution bounds $1 - F_{\rm BC} \gtrsim 5 \times 10^{-15}$, but other limiting effects (such as relaxation during measurement) are evident. c, Histogram SNR as a function of both sourcedrain bias amplitude and integration time. SNR saturates as a function of increasing integration time due to 1/f noise on the DCS potential. SNR increases with bias amplitude, then decreases after a critical point.

ductance, for instance due to excited states. Quantitative predictions for both components of SNR are discussed in detail in appendix B.

We can further look at two extremal cases: the shortest-duration measurement which maintains a "sufficiently high" SNR, and the long-duration measurement with maximized SNR. Fig. 3a depicts a rapid measurement, integrating for only 980 ns. This timescale compares favorably to spin-relaxation T_1 (>10 ms, discussed further in Section II B), quantum-dot dynamical-decoupling timescales [56], and anticipated two-qubit gate durations [18, 19]. In that time we obtain an SNR of 6.5, which limits $^1 - F_{\rm BC}$ to 6×10^{-4} ($F_{\rm BC}$ is defined carefully in Section IV).

Alternately we can saturate the high-SNR limit by integrating for 40 μ s (and subtracting a reference measurement), as shown in Fig. 3b, which achieves an SNR of 15.5. This SNR yields an exceptionally small theoretical bound on infidelity, but other factors limit fidelity far more strongly. An examination of the histogram of Fig. 3b shows evidence of T_1 (points scattered between the two Gaussian distributions) and additional

$$1 - F_{\rm BC} \ge \frac{1}{2} \left(1 - \operatorname{erf} \left(\frac{\operatorname{SNR}}{2\sqrt{2}} \right) \right). \tag{1}$$

spurious counts, the latter of which may indicate more pathological noise mechanisms or a probability of populating other charge states. We now go on to discuss those T_1 effects.

B. Relaxation during measurement

As we access larger signal-to-noise ratios, measurement fidelity quickly becomes limited by relaxation during the measurement. This was explored experimentally, and a key result is shown in Fig. 4a. These data were gathered by preparing a dephased qubit state and transitioning to a trial measurement bias, then sweeping the detuning of that bias. For each detuning we apply a typical modulated source-drain bias for a variable amount of time (called "measurement time" in the figure), but do not record a measurement signal. After that time we perform a calibrated measurement at a fixed bias to examine the resulting state, and we show that result as a function of both trial measurement detuning and duration. The detuning axis has an arbitrary offset but is referenced to the SNR at each point (extracted from spin blockade spectroscopy), which serves to demarcate the singlet and triplet charge-state transitions.

The locations of the sharp reductions in T_1 at negative detuning are consistent with anticrossings between the S(1,1) and $T_{-}(1,1)$ states, given our 1.5 mT applied magnetic field. We can estimate the bound on measurement fidelity imposed by finite SNR and T_1 , as shown in Fig. 4b². Combining those two effects, we can locate the bias for optimal contrast—which, notably, is not the bias at which SNR is maximized. Importantly, we see T_1 decay times of over 20 ms within biases that yield high SNR, which compares favorably to our achievable measurement rates of μ s order. We additionally performed the same experimental sequence and analysis but with the amplitude of the source-drain bias as the independent variable, and discuss that result in appendix C. That measurement shows that large source-drain bias can induce T_1 , and accordingly a compromise must be made in choosing that amplitude.

The rich T_1 behavior displayed in Fig. 4a involves a complex interplay of microscopic semiconductor physics. One extrinsic relaxation pathway is via cotunneling with the electron bath, but this can typically be exponentially suppressed by increasing the relevant tunnel barrier. The intrinsic decay channels within the device require both charge-transition and spin-flip mechanisms. Charge decay proceeds via electron-phonon coupling or electromagnetic interactions with the gate, bath, or charge noise

$$1 - F_{\rm BC} \ge \frac{1}{2} \left(1 - \exp\left(-\frac{T_{\rm measurement}}{T_1}\right) \right)$$
 (2)

 $^{^{1}}$ When noise on the singlet and triplet readout signals is Gaussian and symmetric, SNR bounds fidelity by the relation

² Relaxation during measurement yields a bound

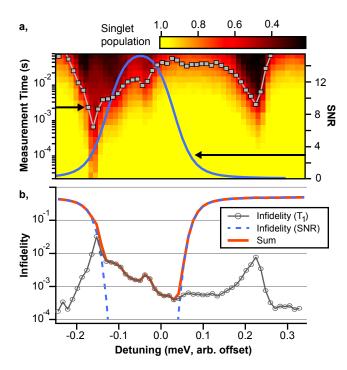


FIG. 4. T_1 and SNR as a function of measurement bias and the resulting limits on SPAM fidelity. a, In color, the resulting singlet probability after trial measurements at swept bias (x axis) and duration (left vertical axis). In gray, the fit 1/e times at each bias point. In blue, the SNR as a function of bias, extrapolated from a fit of spin blockade spectroscopy (right vertical axis). b, Calculating the limits on $F_{\rm BC}$ that result both from finite SNR and from relaxation during measurement. Combining those sources, we see clearly that the maximum fidelity measurement bias does not coincide with maximal SNR for this choice of measurement parameters.

sources, which can have different densities of states and hence differing dependence on detuning. Microscopically, singlet and triplet states can couple to each other via nuclear hyperfine interaction with $^{29}\mathrm{Si}$ and $^{73}\mathrm{Ge}$ atoms, as well as the spin-orbit interaction present in Si/SiGe quantum wells. T_1 "hot spots" can appear at detuning biases where excited singlet and triplet states anticross, leading to strong spin mixing and rapid charge decay, as observed experimentally in Fig. 4a. In appendix D we address how these features are expected to connect with the "measure window" energy spectrum.

III. STATE PREPARATION

We initialize the spin state by biasing fully into the (2,0) charge configuration, maximizing the energy splitting between the triplet states and the ground-state singlet. At thermal equilibrium, we expect to preferentially populate the encoded- $|0\rangle$ (singlet) state. Waiting for relaxation to equilibrium is impractical, as singlet-triplet decay rates $(1/T_1)$ can be quite long due to the necessity

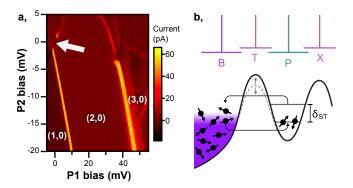


FIG. 5. Sweeping initialization bias and initialization schematic. a, This signal (color) is the difference of spin measurements between preparations of an initialized state and of a dephased state. When initialization is effective, these measurement results are not identical. Sweeping the plungergate initialization bias of the outer two dots, we see two bands at the (1,0)-(2,0) and (2,0)-(3,0) charge cell boundaries, which are both usable regions for initialization. The light gray arrow indicates the bias region used for S2C during measurement. **b,** Near the (2,0)-(1,0) charge state transition the tunneling rate between the outermost dot and the 2DEG bath is greatly accelerated, leading to rapid thermalization and depopulation of the (2,0) excited states. The detuning span of this region is set by the two-electron singlet-triplet splitting. The rate can be further increased by dynamically depressing the dotbath barrier (dashed line), physically realized as the "T" gate visible in Fig. 1.

for spin relaxation [57, 58]. Instead, as previously mentioned, we speed up the process by exchanging electrons with a cold reservoir—here a 2DEG which also serves as the drain for the DCS current. Typically initialization is achieved by biasing the outermost dot to the vicinity of the (2,0)-(1,0) charge boundary where the chemical potential of the (1,0) charge state lies between that of the S(2,0) and T(2,0) states (as shown empirically in Fig. 5a and schematically in Fig. 5b). This enables bathdot tunneling to more rapidly equilibrate or "flush" to the ground state. We sweep the duration of this flush period and choose a duration at which the triplet population stabilizes. In this work, we exploit the (2,0)-(3,0)charge boundary [rather than (1,0)-(2,0)], seen on the right side of Fig. 5b, which to our knowledge has not been previously discussed for this purpose.

Initialization is completed by transitioning back to the idle bias via a ramped trajectory. First, we change the bias quickly from the initialization charge boundary to an "entry" point just outside the (2,0)-(1,1) boundary. Then we ramp slowly—with ramping times of order 10-100 ns—into and through the PSB coordinate, ending at a second entry point just inside the (1,1) charge cell. This is designed so as to move through the anti-crossing without triggering a Landau-Zener transition. Then, we jump quickly from that point to idle, minimizing the time spent at low-exchange regions where magnetic dephasing is more rapid. To return from idle to measurement, we

repeat latter half of this process in reverse. The optimal choices of ramp times and the entry coordinates balance many factors, and are a subject for future study.

A. Initialization fidelity

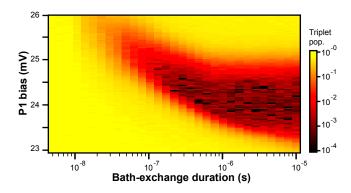
After tunneling to and from the bath, at equilibrium we expect to populate the ground-state singlet state with probability given by the partition function

$$P_{|0\rangle}^{-1} \approx Z_{2e} \approx 1 + 3e^{-E_o\beta} + 4e^{-E_v\beta} \approx (5 \times 10^{-4})^{-1}, (3)$$

where $\beta = 1/(k_B T_e)$ (here, the effective electron temperature $T_e \approx 220$ mK, measured from tunneling linewidth as a function of mixing chamber temperature), E_v is the excited valley state energy in the outermost dot $(\approx 250 \mu \text{eV}, \text{ measured with detuning axis pulsed spec-}$ troscopy [59]), E_0 is the two-electron orbital excited state energy ($\approx 160 \mu eV$, measured with spin blockade spectroscopy), and we neglect higher excited valley-orbital states (assuming their contributions are negligible at sufficiently low temperatures). The factors of three and hour come from the respective degeneracies of the excited valley and orbital states. Zeeman splittings of the polarized triplet states are also neglected in this equation as they are small at the low and moderate magnetic fields at which we typically operate exchange-only qubits (here, 1.5 mT). We have also neglected the third electron, which we estimate to be separable from the outer-dot initialization, and may eventually reach an equilibrium set by its own (one-electron) excited-state energy. It is possible to end in the wrong charge state during this process, so for example the partition function in Eq. 3 can be extended to account for (1,0) occupation as well (if initializing at that charge boundary), though empirically this is not a common outcome.

B. Initialization speed

This speed of this initialization process benefits from the fact that tunneling between the outer dot and bath depends exponentially on the height of the insulating tunnel barrier. Pulsing the voltage bias on the relevant barrier gate strongly and quickly modulates that potential. In practice, we observe a speed limit enforced by two main mechanisms: orbital suppression and waveform stability. First, depressing the barrier height can distort and desymmetrize the quantum dot confining potential, which decreases the orbital excited-state energy [60] and limits the ultimate obtainable fidelity (Eq. 3). In principle, this effect could be countered by shaping the potential further with appropriate pulses on other nearby gate electrodes. Second, the stability of the bias waveform strongly influences this process. Ideally, when sweeping the initialization bias and duration (Fig. 6), we expect the resulting population to be strongly bias-dependent



Sweeping initialization bias and duration. Sweeping the outer-dot plunger gate bias across the (2,0)-(3,0)boundary and sweeping time. This population is the thresholded result of a spin-state measurement immediately after bath-exchange following preparation of a dephased state. The populations are not monotonic in duration, which is indicative of the bias waveforms settling in time. While our highspeed adder circuit (appendix A) attempts to provide a flat response, not all non-idealities are accounted for. This settling limits the minimum initialization time in this case to roughly 300 ns to reach an asymptotic population of 6×10^{-4} . P1 biases differ from Fig. 5a because additional (pulsed) tunnelbarrier bias has been introduced and that T gate has a nontrivial cross-capacitance to the first dot potential. This result is degraded from the Boltzmann distribution prediction for initialization fidelity alone (Eq. 3, $\approx 5 \times 10^{-4}$), due to measurement and mapping errors.

and to change monotonically with time. In contrast, we believe the observed non-monotonic signal is a signature of bias drift. This effect limits us to a minimum duration of roughly 300 ns to reach the asymptotic population; however this limitation should be reduced with improved engineering for signal integrity. We note also that a wider initialization bias window (resulting from larger excited-state splittings) should also reduce the sensitivity to this drift.

We find that using the alternate (2,0)-(3,0) "initialization window" leads to the same asymptotic singlet population but often more than an order of magnitude more quickly. Several factors likely contribute to this operational improvement; for one, the increased dot plunger (P) gate voltage required at this boundary decreases the effective tunnel barrier. Second, the increased bias-space width of this initialization window (evident in Fig. 5) likely mitigates the effects of waveform drift. Finally, the spin and charge character of the multi-electron states also play an important role. For instance, it can be shown from the relevant Clebsch-Gordan coefficients that the tunneling transition rate between an N-electron state of total spin S_i and an N+1 state with spin S_j is weighted by a factor [61]

$$\gamma_{ij} = \frac{S_i + 1}{2S_i + 1} \delta_{S_i + 1/2, S_j} + \frac{S_i}{2S_i + 1} \delta_{S_i - 1/2, S_j}. \tag{4}$$

As a result, while both the one- and three-electron

ground states have total spin S=1/2 at typical magnetic fields, the single-electron state has a three-times larger tunneling rate to the triplets compared to singlets where both are energetically available, whereas the opposite is true for the three-electron ground state, which has a three-times larger tunneling rate for singlets compared to triplets. Details of the three-electron excitation spectrum may also play a role and depend sensitively on the confining electrostatics as well as valley mixing; quantifying the effects of these states on the initialization process is an interesting area for further study.

IV. JOINT SPAM FIDELITY & BUDGET

A. Quantifying SPAM performance

In this section we move on from the mechanics of individual SPAM operations to focus on the quantification of their performance. Measurement SNR and T_1 both contribute to SPAM infidelity and are useful diagnostic quantities, but tell only part of the story. We can probe initialization fidelity by performing a measurement directly after initialization (as in Fig. 6) however we do not consider this quantity to be a reliable summary metric of initialization quality, nor of integrated SPAM quality. Among other concerns, it can be gamed by biased measurement error, it doesn't include any errors in "mapping" when biasing to and from the idle point, and it does not guarantee what fraction of the population will exhibit proper qubit evolution. The last criterion is generally relevant due to the possibility of leakage outside of the qubit space, the effects of which vary widely between physical-qubit implementations. One plausible alternative metric is the ultimate contrast that can be observed in exchange oscillations (appendix E), but this is also degraded by charge noise and other decoherence mechanisms whose infidelity is better associated with coherent manipulation.

Instead, we emphasize an integrated preparation and measurement metric (first presented in Ref. 19 and reproduced mathematically in appendix F) that avoids the previously mentioned flaws while providing several other benefits. This measure, dubbed $F_{\rm BC}$ for Benchmarking Contrast, probes the fundamental concern of imperfect preparation and measurement: what fraction of the time do we obtain the correct result after an arbitrary qubit evolution, independent of the fidelity of that evolution. $F_{\rm BC}$ is derived from "blind" randomized benchmarking, a method originally designed to detect leakage out of the computational space during coherent manipulation of exchange-only qubits. In this procedure an ordinary randomized benchmarking experiment (with sequences that compile to the identity) is executed along with a second benchmarking experiment, whose sequences are instead engineered to compile to a population-inverting gate. To assess SPAM performance, we evaluate the contrast of these fit curves at zero Clifford operations, which

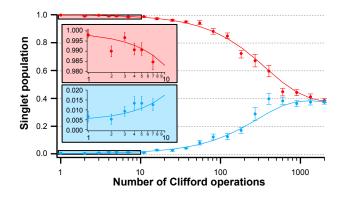


FIG. 7. Measuring joint initialization and measurement fidelity from benchmarking contrast. The return-to-singlet (red) and return-to-triplet (blue) traces from blind randomized benchmarking. The contrast of these fit curves, extrapolated to 0 Clifford operations, indicates $1 - F_{\rm BC} = 2.5(5) \times 10^{-3}$, with a per-Clifford qubit error of $1.7(2) \times 10^{-3}$, and a per-Clifford leakage rate of $3.3(9) \times 10^{-4}$. Upper and lower insets emphasize full contrast at the shortest sequences.

attempts to subtract any contrast loss due to imperfect qubit rotations.

 $F_{\rm BC}$ transposes randomized benchmarking's typical objective-measuring Clifford fidelity independently of SPAM performance. This measure differs from assignment fidelity³, which might be quite coarsely approximated by the same curves evaluated at 0.5 Clifford operations. Importantly for exchange-only qubits, we find $F_{\rm BC}$ to be more robust than assignment fidelity in the presence of leakage, though that import depends on the qubit physical implementation. $F_{\rm BC}$ also implicitly disallows the use of different processes for preparation or measurement of $|0\rangle$ and $|1\rangle$ which may boost assignment fidelity at the expense of operational utility. We note that this metric is essentially universal, and allows fair comparison between diverse physical implementations of qubits. One final desirable feature of $F_{\rm BC}$ as a "summary" metric is its resilience to manipulation, or being increased at the expense of the fidelity of other qubit operations. Any tradeoffs between SPAM and single-qubit errors are readily apparent in this extraction.

Blind randomized benchmarking for the device described in this work is shown in Fig. 7a. These data indicate a per-Clifford error of 1.7×10^{-3} and $F_{\rm BC}$ of $2.5(5)\times10^{-3}$. This latter estimate is highly consistent with the visible contrast of exchange oscillations (appendix E).

 $^{^3}$ $F_A = 1 - \frac{1}{2} (P[0, 1] + P[1, 0])$, where the latter terms indicate the probability of registering an erroneous binarized measurement result after nominal preparation of a computational state.

Factor	Error contribution
Boltzmann distribution	5×10^{-4}
SNR (980 ns integration)	6×10^{-4}
T_1 (measurement & settle)	2×10^{-4}
(1,1) mapping errors	2×10^{-4}
Total	1.5×10^{-3}

TABLE I. Budget for $F_{\rm BC}$.

B. Compiled budget & missing error

To understand this SPAM performance, we can consider three components: initialization, measurement, and mapping. Initialization quality is foremost bound by the magnitude of the 2e and 1e excited state energies relative to the effective electron temperature T_e (Eq. 3). Measurement performance can be determined from a trio of "active" factors: signal amplitude, noise amplitude, and relaxation during measurement—the latter of which was discussed in Section IIB and the former two in appendix B. These three factors are tightly interrelated and sometimes in tension, so that overall optimization is often a balancing act between them. Mapping errors, which occur in the transitions from initialization to the idle bias and from the idle bias to the measurement bias, can reasonably be grouped with one of the above or considered as a separate category, but should be included in any operationally-representative integrated SPAM metric. These errors are further discussed in appendix G. Here we consider and assemble the effects of those components into a budget for $F_{\rm BC}$, shown in table I.

This budget leaves roughly 1×10^{-3} infidelity unexplained relative to the observed $1 - F_{BC} = 2.5 \times 10^{-3}$. We hypothesize that the missing fidelity is attributable to probabilistically initializing with the gauge electron in an excited state. During these events, the exchange rate on one control axis will exhibit a different voltage dependence (J(V)) than occurs when the gauge electron is in its ground state. As a result, Clifford operations composed of pulses calibrated for the J(V) mapping of the ground state quickly scramble the spin-state when that electron is excited, resulting in an apparent SPAM error. This effect, sometimes described as "dual exchange frequencies," has been reported in other works [9], where the Fourier transform of time-domain exchange oscillations clearly shows two peaks. For the present device under study, we are unable to observe a clear second peak in the spectra of exchange evolution on either exchange axis, however it is unclear whether this experiment has sufficient resolution to differentiate a second frequency at the 1×10^{-3} level. Development of a better method to quantify such population is an important area for future

Though the energy of the gauge electrons first excitedstate was not measured, as a one-electron property we can reasonably associate its first excited state with the valley degree of freedom. Assuming equilibrium statistics in a Boltzmann distribution, the missing infidelity implies an excited valley-state energy of roughly 130 μeV , a value which is consistent with the measured valley splitting distribution for this heterostructure [59]. We note that this population would not be evident in initialization metrics which did not require fairly measuring qubit evolution, pointing again to the importance of the use of blind randomized benchmarking as a fair SPAM fidelity metric.

V. CONCLUSION

By our preferred, conservative metric, we have demonstrated preparation and measurement in exchange-only qubits with $1-F_{\rm BC}{=}2.5(5){\times}10^{-3}$. These are obtained using only baseband control electronics, using a measurement integration time of 980 ns, an initialization duration of roughly 300 ns, and a charge-transition ramp time of 100 ns. We present a budget which explains ${\approx}60\%$ of the observed error, and hypothesize that the remaining error is due to thermal excited states of the gauge electron.

Priorities for improving initialization performance are obtaining larger excited-state energies and suppressing the effective electron temperature, which should yield both improved singlet and gauge electron preparation. Initialization may be accelerated by achieving more stable voltage waveforms. Related signal integrity engineering is also key to reducing the "settling" period, which presently dominates the possible measurement cadence. Other paths to improved PSB/DCS measurement performance include lower-noise amplification with lower parasitic capacitances, further exploration of "latching" [62– 64] and "avalanche" [65] approaches, and improved T_1 decay rates. T_1 effects can likely be reduced by further decreasing charge and magnetic noise, which are already desiderata for improved coherent manipulation. This may also require using smaller source-drain biases, further emphasizing the need for improvements in signal and noise amplitudes.

Importantly, we conclude that high-fidelity and rapid SPAM (maintaining performance parity with coherent manipulation) is achievable in spin qubits using only simple electronics and components with high flexibility in temperature and magnetic field. Routes to even further improvement without addition of other components are clear.

VI. ACKNOWLEDGMENTS

The authors gratefully acknowledge the important contributions of Andrey Kiselev, Matthew Reed, Laura de Lorenzo, Emily Pritchett, Cody Jones, and Ed Chen, and support in figure preparation from John Carpenter.

- D. Loss and D. P. DiVincenzo, Quantum computation with quantum dots, Phys. Rev. A 57, 120 (1998).
- [2] W. G. van der Wiel, S. De Franceschi, J. M. Elzerman, T. Fujisawa, S. Tarucha, and L. P. Kouwenhoven, Electron transport through double quantum dots, Rev. Mod. Phys. 75, 1 (2002).
- [3] R. Hanson, L. P. Kouwenhoven, J. R. Petta, S. Tarucha, and L. M. K. Vandersypen, Spins in few-electron quantum dots, Rev. Mod. Phys. 79, 1217 (2007).
- [4] T. D. Ladd, F. Jelezko, R. Laflamme, Y. Nakamura, C. Monroe, and J. L. O'Brien, Quantum computers, Nature 464, 45 (2010).
- [5] F. A. Zwanenburg, A. S. Dzurak, A. Morello, M. Y. Simmons, L. C. L. Hollenberg, G. Klimeck, S. Rogge, S. N. Coppersmith, and M. A. Eriksson, Silicon quantum electronics, Rev. Mod. Phys. 85, 961 (2013).
- [6] T. D. Ladd and M. S. Carroll, Silicon qubits, in *Ency-clopedia of Modern Optics (Second Edition)*, edited by B. D. Guenther and D. G. Steel (Elsevier, Oxford, 2018) pp. 467–477.
- [7] M. A. Eriksson, M. Friesen, S. N. Coppersmith, R. Joynt, L. J. Klein, K. Slinker, C. Tahan, P. M. Mooney, J. O. Chu, and S. J. Koester, Spin-based quantum dot quantum computing in silicon, Quant. Inf. Proc. 3, 133 (2004).
- [8] B. M. Maune, M. G. Borselli, B. Huang, T. D. Ladd, P. W. Deelman, K. S. Holabird, A. A. Kiselev, I. Alvarado-Rodriguez, R. S. Ross, A. E. Schmitz, M. Sokolich, C. A. Watson, M. F. Gyure, and A. T. Hunter, Coherent singlet-triplet oscillations in a silicon-based double quantum dot, Nature 481, 344 (2012).
- [9] M. D. Reed, B. M. Maune, R. W. Andrews, M. G. Borselli, K. Eng, M. P. Jura, A. A. Kiselev, T. D. Ladd, S. T. Merkel, I. Milosavljevic, E. J. Pritchett, M. T. Rakher, R. S. Ross, A. E. Schmitz, A. Smith, J. A. Wright, M. F. Gyure, and A. T. Hunter, Reduced sensitivity to charge noise in semiconductor spin qubits via symmetric operation, Phys. Rev. Lett. 116, 110402 (2016).
- [10] E. Kawakami, T. Jullien, P. Scarlino, D. R. Ward, D. E. Savage, M. G. Lagally, V. V. Dobrovitski, M. Friesen, S. N. Coppersmith, M. A. Eriksson, and L. M. K. Vandersypen, Gate fidelity and coherence of an electron spin in an Si/SiGe quantum dot with micromagnet, Proc. Nat. Acad. Sci. USA 113, 11738 (2016).
- [11] E. J. Connors, J. Nelson, H. Qiao, L. F. Edge, and J. M. Nichol, Low-frequency charge noise in Si/SiGe quantum dots, Phys. Rev. B 100, 165305 (2019).
- [12] A. M. Tyryshkin, S. Tojo, J. J. L. Morton, H. Riemann, N. V. Abrosimov, P. Becker, H.-J. Pohl, T. Schenkel, M. L. W. Thewalt, K. M. Itoh, and S. A. Lyon, Electron spin coherence exceeding seconds in high-purity silicon, Nat. Mater. 11, 143 (2012).
- [13] K. Eng, T. D. Ladd, A. Smith, M. G. Borselli, A. A. Kiselev, B. H. Fong, K. S. Holabird, T. M. Hazard, B. Huang, P. W. Deelman, I. Milosavljevic, A. E. Schmitz, R. S. Ross, M. F. Gyure, and A. T. Hunter, Isotopically enhanced triple-quantum-dot qubit, Sci. Adv. 1, e1500214 (2015).
- [14] T. Struck, A. Hollmann, F. Schauer, O. Fedorets, A. Schmidbauer, K. Sawano, H. Riemann, N. V. Abrosimov, L. Cywiński, D. Bougeard, et al., Low-frequency

- spin qubit energy splitting noise in highly purified $^{28}{\rm Si/SiGe,}$ npj Quantum Inf. 6, 1 (2020).
- [15] D. A. Lidar, I. L. Chuang, and K. B. Whaley, Decoherence-free subspaces for quantum computation, Phys. Rev. Lett. 81, 2594 (1998).
- [16] D. P. DiVincenzo, D. Bacon, J. Kempe, G. Burkard, and K. B. Whaley, Universal quantum computation with the exchange interaction, Nature 408, 339 (2000).
- [17] J. Kempe, D. Bacon, D. A. Lidar, and K. B. Whaley, Theory of decoherence-free fault-tolerant universal quantum computation, Phys. Rev. A 63, 042307 (2001).
- [18] B. H. Fong and S. M. Wandzura, Universal quantum computation and leakage reduction in the 3-qubit decoherence free subsystem, Quantum Info. Comput. 11, 1003 (2011).
- [19] R. W. Andrews, C. Jones, M. D. Reed, A. M. Jones, S. D. Ha, M. P. Jura, J. Kerckhoff, M. Levendorf, S. Meenehan, S. T. Merkel, A. Smith, B. Sun, A. J. Weinstein, M. T. Rakher, T. D. Ladd, and M. G. Borselli, Quantifying error and leakage in an encoded Si/SiGe triple-dot qubit, Nat. Nanotechnol. 14, 747 (2019).
- [20] J. M. Elzerman, R. Hanson, L. H. Willems van Beveren, B. Witkamp, L. M. K. Vandersypen, and L. P. Kouwenhoven, Single-shot read-out of an individual electron spin in a quantum dot, Nature 430, 431 (2004).
- [21] K. Ono, D. Austing, Y. Tokura, and S. Tarucha, Current rectification by pauli exclusion in a weakly coupled double quantum dot system, Science 297, 1313 (2002).
- [22] A. C. Johnson, J. R. Petta, C. M. Marcus, M. P. Hanson, and A. C. Gossard, Singlet-triplet spin blockade and charge sensing in a few-electron double quantum dot, Phys. Rev. B 72, 165308 (2005).
- [23] M. Field, C. G. Smith, M. Pepper, D. A. Ritchie, J. E. F. Frost, G. A. C. Jones, and D. G. Hasko, Measurements of Coulomb blockade with a noninvasive voltage probe, Phys. Rev. Lett. 70, 1311 (1993).
- [24] B. E. Kane, N. S. McAlpine, A. S. Dzurak, R. G. Clark, G. J. Milburn, H. B. Sun, and H. Wiseman, Single-spin measurement using single-electron transistors to probe two-electron systems, Phys. Rev. B 61, 2961 (2000).
- [25] G. J. Podd, S. J. Angus, D. A. Williams, and A. J. Ferguson, Charge sensing in intrinsic silicon quantum dots, Appl. Phys. Lett. 96, 082104 (2010).
- [26] A. Morello, J. J. Pla, F. A. Zwanenburg, K. W. Chan, K. Y. Tan, H. Huebl, M. Möttönen, C. D. Nugroho, C. Yang, J. A. van Donkelaar, A. D. C. Alves, D. N. Jamieson, C. C. Escott, L. C. L. Hollenberg, R. G. Clark, and A. S. Dzurak, Single-shot readout of an electron spin in silicon, Nature 467, 687 (2010).
- [27] J. P. Dehollain, J. T. Muhonen, K. Y. Tan, A. Saraiva, D. N. Jamieson, A. S. Dzurak, and A. Morello, Singleshot readout and relaxation of singlet and triplet states in exchange-coupled ³¹P electron spins in silicon, Phys. Rev. Lett. 112, 236801 (2014).
- [28] T. F. Watson, B. Weber, M. G. House, H. Büch, and M. Y. Simmons, High-fidelity rapid initialization and read-out of an electron spin via the single donor D^- charge state, Phys. Rev. Lett. **115**, 166806 (2015).
- [29] M. A. Broome, T. F. Watson, D. Keith, S. K. Gorman, M. G. House, J. G. Keizer, S. J. Hile, W. Baker, and M. Y. Simmons, High-fidelity single-shot singlet-triplet

- readout of precision-placed donors in silicon, Phys. Rev. Lett. 119, 046802 (2017).
- [30] D. Keith, M. G. House, M. B. Donnelly, T. F. Watson, B. Weber, and M. Y. Simmons, Single-shot spin readout in semiconductors near the shot-noise sensitivity limit, Phys. Rev. X 9, 041003 (2019).
- [31] E. Chanrion, D. J. Niegemann, B. Bertrand, C. Spence, B. Jadot, J. Li, P.-A. Mortemousque, L. Hutin, R. Maurand, X. Jehl, M. Sanquer, S. De Franceschi, C. Bäuerle, F. Balestro, Y.-M. Niquet, M. Vinet, T. Meunier, and M. Urdampilleta, Charge detection in an array of CMOS quantum dots, Phys. Rev. Appl. 14, 024066 (2020).
- [32] A. E. Seedhouse, T. Tanttu, R. C. Leon, R. Zhao, K. Y. Tan, B. Hensen, F. E. Hudson, K. M. Itoh, J. Yoneda, C. H. Yang, et al., Pauli blockade in silicon quantum dots with spin-orbit control, PRX Quantum 2, 010303 (2021).
- [33] F. Hofmann, T. Heinzel, D. Wharam, J. Kotthaus, G. Böhm, W. Klein, G. Tränkle, and G. Weimann, Single electron switching in a parallel quantum dot, Phys. Rev. B 51, 13872 (1995).
- [34] E. Onac, F. Balestro, L. H. W. van Beveren, U. Hart-mann, Y. V. Nazarov, and L. P. Kouwenhoven, Using a quantum dot as a high-frequency shot noise detector, Phys. Rev. Lett. 96, 176601 (2006).
- [35] E. P. Nordberg, H. L. Stalford, R. Young, G. A. Ten Eyck, K. Eng, L. A. Tracy, K. D. Childs, J. R. Wendt, R. K. Grubbs, J. Stevens, M. P. Lilly, M. A. Eriksson, and M. S. Carroll, Charge sensing in enhancement mode double-top-gated metal-oxide-semiconductor quantum dots, Appl. Phys. Lett. 95, 202102 (2009).
- [36] D. J. Reilly, C. M. Marcus, M. P. Hanson, and A. C. Gossard, Fast single-charge sensing with a RF quantum point contact, Appl. Phys. Lett. 91, 162101 (2007).
- [37] C. Barthel, M. Kjærgaard, J. Medford, M. Stopa, C. M. Marcus, M. P. Hanson, and A. C. Gossard, Fast sensing of double-dot charge arrangement and spin state with a radio-frequency sensor quantum dot, Phys. Rev. B 81, 161308 (2010).
- [38] R. J. Schoelkopf, P. Wahlgren, A. A. Kozhevnikov, P. Delsing, and D. E. Prober, The radio-frequency singleelectron transistor (RF-SET): A fast and ultrasensitive electrometer, Science 280, 1238 (1998).
- [39] K. D. Petersson, C. G. Smith, D. Anderson, P. Atkinson, G. A. C. Jones, and D. A. Ritchie, Charge and spin state readout of a double quantum dot coupled to a resonator, Nano Letters 10, 2789 (2010).
- [40] K. Petersson, L. McFaul, M. Schroer, M. Jung, J. Taylor, A. Houck, and J. Petta, Circuit quantum electrodynamics with a spin qubit, Nature 490, 380 (2012).
- [41] J. Stehlik, Y.-Y. Liu, C. M. Quintana, C. Eichler, T. R. Hartke, and J. R. Petta, Fast charge sensing of a cavity-coupled double quantum dot using a Josephson parametric amplifier, Phys. Rev. Appl. 4, 014018 (2015).
- [42] A. Crippa, R. Ezzouch, A. Aprá, A. Amisse, R. Laviéville, L. Hutin, B. Bertrand, M. Vinet, M. Urdampilleta, T. Meunier, M. Sanquer, X. Jehl, R. Maurand, and S. De Franceschi, Gate-reflectometry dispersive readout and coherent control of a spin qubit in silicon, Nat. Commun. 10, 2776 (2019).
- [43] M. Urdampilleta, D. J. Niegemann, E. Chanrion, B. Jadot, C. Spence, P.-A. Mortemousque, C. Bäuerle, L. Hutin, B. Bertrand, S. Barraud, R. Maurand, M. Sanquer, X. Jehl, S. De Franceschi, M. Vinet, and T. Meunier, Gate-based high fidelity spin readout in a CMOS

- device, Nat. Nanotechnol. 14, 737 (2019).
- [44] V. N. Ciriano-Tejel, M. A. Fogarty, S. Schaal, L. Hutin, B. Bertrand, L. Ibberson, M. F. Gonzalez-Zalba, J. Li, Y.-M. Niquet, M. Vinet, and J. J. Morton, Spin readout of a CMOS quantum dot by gate reflectometry and spindependent tunneling, PRX Quantum 2, 010353 (2021).
- [45] F. Ansaloni, A. Chatterjee, H. Bohuslavskyi, B. Bertrand, L. Hutin, M. Vinet, and F. Kuemmeth, Single-electron operations in a foundry-fabricated array of quantum dots, Nat. Commun. 11, 6399 (2020).
- [46] M. Friesen, C. Tahan, R. Joynt, and M. A. Eriksson, Spin readout and initialization in a semiconductor quantum dot, Phys. Rev. Lett. 92, 037901 (2004).
- [47] J. R. Petta, A. C. Johnson, J. M. Taylor, E. A. Laird, A. Yacoby, M. D. Lukin, C. M. Marcus, M. P. Hanson, and A. C. Gossard, Coherent manipulation of coupled electron spins in semiconductor quantum dots, Science 309, 2180 (2005), http://science.sciencemag.org/content/309/5744/2180.full.pdf.
- [48] T. Nakajima, A. Noiri, J. Yoneda, M. R. Delbecq, P. Stano, T. Otsuka, K. Takeda, S. Amaha, G. Allison, K. Kawasaki, et al., Quantum non-demolition measurement of an electron spin qubit, Nat. Nanotechnol. 14, 555 (2019).
- [49] D. M. Zajac, T. M. Hazard, X. Mi, K. Wang, and J. R. Petta, A reconfigurable gate architecture for Si/SiGe quantum dots, Appl. Phys. Lett. 106, 223507 (2015).
- [50] M. G. Borselli, K. Eng, R. S. Ross, T. M. Hazard, K. S. Holabird, B. Huang, A. A. Kiselev, P. W. Deelman, L. D. Warren, I. Milosavljevic, A. E. Schmitz, M. Sokolich, M. F. Gyure, and A. T. Hunter, Undoped accumulation-mode Si/SiGe quantum dots, Nanotechnology 26, 375202 (2015).
- [51] J. Levy, Universal quantum computation with spin-1/2 pairs and Heisenberg exchange, Phys. Rev. Lett. 89, 147902 (2002).
- [52] C. Barthel, D. J. Reilly, C. M. Marcus, M. P. Hanson, and A. C. Gossard, Rapid single-shot measurement of a singlet-triplet qubit, Phys. Rev. Lett. 103, 160503 (2009).
- [53] L. A. Tracy, D. R. Luhman, S. M. Carr, N. C. Bishop, G. A. Ten Eyck, T. Pluym, J. R. Wendt, M. P. Lilly, and M. S. Carroll, Single shot spin readout using a cryogenic high-electron-mobility transistor amplifier at sub-Kelvin temperatures, Appl. Phys. Lett. 108, 063101 (2016).
- [54] I. T. Vink, T. Nooitgedagt, R. N. Schouten, L. M. K. Vandersypen, and W. Wegscheider, Cryogenic amplifier for fast real-time detection of single-electron tunneling, Appl. Phys. Lett. 91, 123512 (2007).
- [55] A. Jones, E. Pritchett, E. Chen, T. Keating, R. Andrews, J. Blumoff, L. De Lorenzo, K. Eng, S. Ha, A. Kiselev, S. Meenehan, S. Merkel, J. Wright, L. Edge, R. Ross, M. Rakher, M. Borselli, and A. Hunter, Spin-blockade spectroscopy of Si/Si-Ge quantum dots, Phys. Rev. Appl. 12, 014026 (2019).
- [56] M. Veldhorst, J. C. C. Hwang, C. H. Yang, A. W. Leenstra, B. de Ronde, J. P. Dehollain, J. T. Muhonen, F. E. Hudson, K. M. Itoh, A. Morello, and A. S. Dzurak, An addressable quantum dot qubit with fault-tolerant control-fidelity, Nat. Nanotechnol. 9, 981 EP (2014).
- [57] R. R. Hayes, A. A. Kiselev, M. G. Borselli, S. S. Bui, E. T. C. III, P. W. Deelman, B. M. Maune, I. Milosavljevic, J.-S. Moon, R. S. Ross, A. E. Schmitz, M. F. Gyure, and A. T. Hunter, Lifetime measurements (T₁) of electron

- spins in Si/SiGe quantum dots, (2009), arXiv:0908.0173 [cond-mat.mes-hall].
- [58] J. Prance, Z. Shi, C. Simmons, D. Savage, M. Lagally, L. Schreiber, L. Vandersypen, M. Friesen, R. Joynt, S. Coppersmith, et al., Single-shot measurement of triplet-singlet relaxation in a Si/SiGe double quantum dot, Phys. Rev. Lett. 108, 046808 (2012).
- [59] E. H. Chen, K. Raach, A. Pan, A. A. Kiselev, E. Acuna, J. Z. Blumoff, T. Brecht, M. D. Choi, W. Ha, D. R. Hulbert, M. P. Jura, T. E. Keating, R. Noah, B. Sun, B. J. Thomas, M. G. Borselli, C. Jackson, M. T. Rakher, and R. S. Ross, Detuning axis pulsed spectroscopy of valley-orbital states in Si/Si-Ge quantum dots, Phys. Rev. Appl. 15, 044033 (2021).
- [60] D. V. Melnikov and J.-P. Leburton, Dimensionality effects in the two-electron system in circular and elliptic quantum dots, Phys. Rev. B 73, 085320 (2006).
- [61] D. Weinmann, W. Häusler, W. Pfaff, B. Kramer, and U. Weiss, Spin blockade in non-linear transport through quantum dots, EPL 26, 467 (1994).
- [62] S. Studenikin, J. Thorgrimson, G. Aers, A. Kam, P. Zawadzki, Z. Wasilewski, A. Bogan, and A. Sachrajda, Enhanced charge detection of spin qubit readout via an intermediate state, Appl. Phys. Lett. 101, 233101 (2012).
- [63] J. Mason, S. Studenikin, A. Kam, Z. Wasilewski, A. Sachrajda, and J. Kycia, Role of metastable charge states in a quantum-dot spin-qubit readout, Phys. Rev. B 92, 125434 (2015).
- [64] P. Harvey-Collard, B. D'Anjou, M. Rudolph, N. T. Jacobson, J. Dominguez, G. A. Ten Eyck, J. R. Wendt, T. Pluym, M. P. Lilly, W. A. Coish, M. Pioro-Ladrière, and M. S. Carroll, High-fidelity single-shot readout for a spin qubit via an enhanced latching mechanism, Phys. Rev. X 8, 021046 (2018).
- [65] C. J. van Diepen, T.-K. Hsiao, U. Mukhopadhyay, C. Reichl, W. Wegscheider, and L. M. Vandersypen, Electron cascade for distant spin readout, Nat. Commun. 12, 1 (2021).
- [66] C. W. J. Beenakker, Theory of Coulomb-blockade oscillations in the conductance of a quantum dot, Phys. Rev. B 44, 1646 (1991).
- [67] H. Van Houten, C. Beenakker, and A. Staring, Coulombblockade oscillations in semiconductor nanostructures, in Single charge tunneling (Springer, 1992) pp. 167–216.
- [68] K. Horibe, T. Kodera, and S. Oda, Back-action-induced excitation of electrons in a silicon quantum dot with a single-electron transistor charge sensor, Appl. Phys. Lett. 106, 053119 (2015).
- [69] H. Li, M. Xiao, G. Cao, C. Zhou, R. Shang, T. Tu, G. Guo, H. Jiang, and G. Guo, Back-action-induced nonequilibrium effect in electron charge counting statistics, Appl. Phys. Lett. 100, 092112 (2012).
- [70] D. Culcer, L. Cywiński, Q. Li, X. Hu, and S. Das Sarma, Quantum dot spin qubits in silicon: multivalley physics, Phys. Rev. B 82, 155312 (2010).
- [71] D. Stepanenko, M. Rudner, B. I. Halperin, and D. Loss, Singlet-triplet splitting in double quantum dots due to spin-orbit and hyperfine interactions, Phys. Rev. B 85, 075416 (2012).
- [72] J. D. Whittaker, F. Da Silva, M. S. Allman, F. Lecocq, K. Cicak, A. Sirois, J. Teufel, J. Aumentado, and R. W. Simmonds, Tunable-cavity QED with phase qubits, Phys. Rev.d B 90, 024513 (2014).

[73] A. Myerson, D. Szwer, S. Webster, D. Allcock, M. Curtis, G. Imreh, J. Sherman, D. Stacey, A. Steane, and D. Lucas, High-fidelity readout of trapped-ion qubits, Phys. Rev. Lett. 100, 200502 (2008).

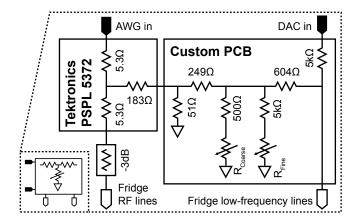


FIG. 8. Room temperature high speed adder circuit. This circuit allows high-bandwidth but baseband control, combining inputs both from NI AWGs and custom lownoise DACs. Together with the fridge components shown in Fig. 1 (attenuation, bias tee and low-pass filter), this forms the full signal chain, providing the benefits discussed in the text. $R_{\rm fine}$ and $R_{\rm coarse}$ are highly-stable potentiometers that allow for balancing the attenuation of the high-frequency and low-frequency signal paths.

Appendix A: High-speed Adder Circuit

Sending high-bandwidth baseband signals into a dilution refrigerator is a non-trivial problem. To protect sensitive quantum devices from thermal Johnson noise and instrumentation noise, microwave or all-AC signals are generally attenuated heavily at intermediate temperature stages within a refrigerator. However, it is not desirable to send large DC signals through attenuators, as they will constantly dissipate power inside of the cryostat. Fortunately we can instead aggressively low-pass filter DC signals. In our adder circuit we use both of these techniques—sending the AC and DC components of a signal in separately, which are then combined at a base-temperature bias tee (see Fig. 8).

An added complication is that the baseband pulses (generated from NI5451 AWGs) contain both AC and DC components. We get around this by splitting the AWG signal. The AC component travels directly through the (attenuated) AC lines. The DC component is attenuated at room temperature via a simple resistive divider. then combined with the true DC signal from a low-noise DAC, before traveling through the (filtered) DC lines. It is critical that the attenuation of both of these paths is matched, or the mismatch will result in long-timescale waveform distortion. This is particularly evident in the shift of measurement signals during long experimental sequences, such as randomized benchmarking. Treated improperly, this shift of signal with time can be mistaken as resulting from some other dot physics. We test this using the stabilization of a dot loading line bias after a stepedge pulse, and the attenuation of the resistive divider is fine-tuned with a highly-stable Vishay potentiometer.

Appendix B: Contributions to SNR

1. Noise amplitude

The budget for measurement noise is dominated by three effectively white noise sources of comparable magnitude (input-referred HEMT noise, sense-resistor Johnson noise, DCS current shot noise) and by the noise on the DCS potential. In our measurement chain configuration, the former three sources each generate a noise spectral density of roughly 200-300 pV/ $\sqrt{\rm Hz}$. The quantum dot charge noise spectrum is typically of the form A^2/f , with effective gate-referred voltage spectral density $A\approx 5~\mu{\rm V}/\sqrt{\rm Hz}$ at 1 Hz.

The effect of this noise source on readout depends on how the DCS is operated. In particular, two extreme choices are to bias the DCS gate voltage to maximize either the transconductance G_m (peak of the derivative of conductance with respect to M voltage) or the conductance (the conventional Coulomb blockade peak point). The former maximizes the signal when the magnitude of the charge-dependent potential shift at the DCS (appendix B2) is small compared to the width of the Coulomb peak, and we can approximate the effect of the DCS-potential noise as a linear shift of the conductance. The latter maximizes the signal when the magnitude of the charge-dependent potential shift is much larger than the width of the Coulomb peak and the state-dependent conductance difference is maximal. In this case, we are insensitive to DCS potential noise to first order. From these two extrema it is evident that the DCS bias that maximizes signal may not be the correct bias to maximize SNR, particularly in the 1/f-dominated long-integration regime.

To predict the ultimate expected noise amplitude we integrate the white noise sources with the filter function appropriate for our square-wave modulated measurement tone, yielding a current noise of σ_{SD}^2 . We account for the charge noise in a conservative analysis, taking the small-shift approximation such that, to first order, the output measured current is modulated by $G_m d(t)$, where d(t) is the (noisy) potential shift. If we add that to the previous analysis, assume d(t) has a power-spectral density given by A^2/f , and account for our averaging scheme when incorporating an (approximately instantaneously) subsequent reference measurement, the total histogram variance can be expressed as

$$\sigma_{\rm I}^2 = 2\sigma_{SD}^2 + 16\ln 2G_m^2 A^2. \tag{B1}$$

This sets an upper bound on the effects of DCS charge noise on measurement histograms. For relevant measurement parameters, we predict $\sigma_I \approx 5 \mathrm{pA}$ at long duration, which is in line with observed values (e.g. Fig. 3b, though note in that figure the current values for referenced measurements have been divided by a factor of 4 for inconsequential implementation details.)

As subtracting a reference measurement increases the white noise contribution, it is only preferable when the noise is dominated by 1/f or other slow processes. This can occur either because the 1/f contribution is generally large or because one seeks the highest possible SNR and is willing to average long enough to reduce the white noise contribution. Even in such a regime, performing a reference measurement for every experimental query is unlikely to be optimal.

2. Signal amplitude

We can estimate the magnitude of the HEMT-inputreferred voltage signal as a product of five factors:

- 1. the spin-to-charge conversion efficiency,
- 2. the inter-charge-state DCS potential shift,
- 3. the DCS transconductance G_m ,
- 4. the maximum applicable source-drain bias, and
- 5. the value of the sense resistor.

The spin-to-charge conversion efficiency describes how effectively the spin states correspond to different charge states at the measurement bias, and approaches unity when the two-electron excited-state splitting is larger than the singlet- or triplet-selective charge transition widths. That width can be reduced by decreasing the effective electron temperature and tunnel coupling, but it is also sensitive to charge noise (see the supplemental material of [55]). In practice, we find an excited-state splitting of roughly 100 μeV is sufficient to push this term near unity at typical operating electron temperature and tunnel coupling. The magnitude of the potential shift is a straightforward calculation from the device geometry and Coulomb's law, accounting for the effects of image charges due to the electrostatic environment. The transconductance around a Coulomb peak is a wellstudied question, depending on tunneling rates of the DCS tunnel barriers and the effective electron temperature [66, 67].

The maximum applicable source bias is limited by two factors. First (as noted in Section II A) nonlinearity in the DCS causes the conductance to decrease after a critical value. Second, we also observe that at sufficiently large source-drain bias, the spin relaxation lifetime begins to decrease, discussed in appendix C. The voltage signal can be linearly increased with high-resistance sense resistors, but at the expense of increased Johnson and shot noise. Additionally, due to parasitic capacitance, the accessible measurement frequency decreases quickly, limiting the ability to mitigate low-frequency noise.

Appendix C: T₁ vs Source-Drain Bias Amplitude

We have also explored the effect of measurement-biasdriven T_1 decay using the experiment discussed in Section II B. Fig. 9 shows that T_1 decreases strongly when

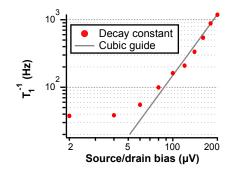


FIG. 9. T_1 as a function of applied measurement bias. The decay rate increases after a certain bias amplitude of energy comparable to the singlet-triplet splitting. We also provide a cubic guide to the eye, which appears to track the asymptotic behavior.

we bias beyond roughly 75 μ V, a value suggestively close in correspondence to the S-T splitting at the middle of the measurement window, e.g., equal to the gap at that detuning between the ground (2,0) state and the lowest (1,1) excited triplet state. These observations show similarities to other reports in the literature [68, 69]. The onset value and the nonlinear scaling with bias are potentially indicative of enhanced relaxation due to phonons generated by the enhanced dissipation of the DCS. Further work is needed to understand the microscopic underpinnings of this effect.

Appendix D: Measure Window Energy Spectrum

The level structure in the vicinity of a (1,1)-(2,0) anticrossing is quite complex, especially in Si/SiGe quantum dots where the valley degree of freedom introduces additional excitations. The valley splitting E_v is usually 10s-100s μ eV while the single-electron orbital energies tend to be 1 meV and higher. However, the two-electron (2e) orbital energy E_o can be considerably smaller (on order of 10s-100s μeV) than the valley splitting due to the interplay of the Coulomb interaction and spatial asymmetry. A simplified model spectrum for the device in this paper is depicted in Fig. 2a. The first excited two electron orbital state in a single dot is a spin triplet and hence three-fold degenerate in the absence of a magnetic field. By contrast, a two electron valley excitation can be either singlet or triplet and is nearly fourfold degenerate because of the negligible intervalley exchange interaction [70]. These degeneracies alter the occupation of excited states and hence the initialization fidelity, as reflected in the partition function of Eq. 3. The level structure in practice will be further complicated by the presence of other doubly-excited states, valley-spin-orbit mixing, and Zeeman splitting of polarized triplets due to external magnetic fields.

This energy spectrum also provides some hints about the physics of T_1 relaxation within the measurement window, which requires both spin- and charge-mixing of the singlet and triplet states. Thus, for example, the hyperfine interaction can couple the $T_x(1,1)$ states to the nearly degenerate and parallel excited S(1,1) state, which can then decay via phonon or photon emission to the ground S(2,0) state. The spin-orbit interaction likewise introduces magnetic gradients mixing T(1,1) and S(1,1)as well as spin-flip tunneling, which couples $T_x(1,1)$ to S(2,0) directly, both of which are sensitive to the magnetic field orientation [71]. Similarly the reverse processes are also possible. These terms will create additional excited singlet-triplet anticrossings within the measure window when the polarized triplet states are split by external magnetic fields (not pictured), which we believe are connected to the T_1 "hot spots" visible in Fig. 4. These features are qualitatively reproduced in calculations directly diagonalizing the (1,1)-(2,0) Hamiltonian including spin-mixing terms, though quantitative comparison is currently inhibited because several key parameters (particularly for the spin-orbit coupling) have not been characterized. In such a model, the relaxation rate may be sensitive in both magnitude and hot spot location to magnetic field, tunnel coupling, and valley mixing, consistent with the variable, tuneup- and device-dependent T_1 features we observe experimentally.

Appendix E: Exchange oscillation contrast

The contrast of exchange oscillations is another plausible joint SPAM performance metric in our system. We prefer $F_{\rm BC}$ from benchmarking for the reasons noted in the text, but in this case the resulting estimates are quite similar (see Fig. 10). In other physical architectures this approach might be compared to the contrast of Rabi oscillations.

Appendix F: Definition of F_{BC}

This integrated preparation and measurement metric (discussed in Section IV) was first presented in Ref. 19). $F_{\rm BC}$ is derived from "blind" randomized benchmarking, a procedure in which an ordinary randomized benchmarking experiment (with sequences that compile to the identity) is executed along with a second benchmarking experiment, whose sequences are instead engineered to compile to a population-inverting gate. We then fit the results of these two experiments to the form

$$y_0 = A + B(1-p)^N + C(1-q)^N$$
 (F1a)

$$y_1 = A - B(1 - p)^N + C(1 - q)^N$$
. (F1b)

To assess SPAM performance, we evaluate the contrast of these fit curves at zero Clifford operations, which attempts to subtract any contrast loss due to imperfect

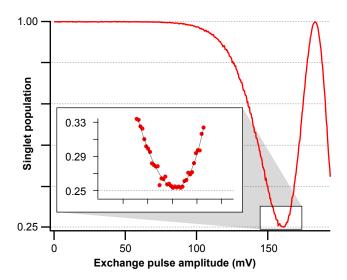


FIG. 10. Exchange oscillation contrast. Exchange oscillations on the n axis, plotting thresholded singlet population vs magnitude of throw on the symmetric axis, referenced to the voltage change on the X2 gate. The n axis should rotate the logical 0 state from 100% singlet to 25% singlet. The inset has magnified the same data near first near-25% trough. Interpreted directly, half of the difference between this contrast and 0.75 would indicate a SPAM infidelity of 2.8×10^{-3} .

qubit rotations. The SPAM infidelity is given by half of the missing contrast,

$$1 - F_{BC} := 0.5 - B.$$
 (F2)

Appendix G: Mapping errors

During DFS operation, bias configurations used for initialization and measurement are different than the idle bias used in-between coherent manipulations. Transitioning between these operating points can give rise to "mapping" errors. Similar mapping steps are not uncommon in other architectures, e.g., flux-tunable superconducting qubits [72] and some trapped-ion qubit encodings [73]. Within the architecture of this work, alternate mapping operations (e.g., "latching" [62–64] and "avalanche" [65] mechanisms) can provide more favorable SNR and/or relaxation rates, but potentially at added cost in mapping error. In this work mapping errors are principally caused by dephasing due to magnetic noise and with Landau-Zener processes, and to a lesser extent relaxation and the risk of transiting the boundaries of other charge cells. We are also at risk of state degradation during the settling period required before source-drain biases are applied, which we believe is constrained by signal integrity limitations. We budget some of this error by comparing two experiments: one where we bias directly from initialization to measurement, and a second where we bias from initialization to the idle bias before measurement. These two measurements yield triplet fractions of 6×10^{-4} and 8×10^{-4} , respectively, with the difference reflecting mapping error due to that additional bias trajectory.

While mapping errors could be reduced by engineering devices with, e.g. longer singlet-triplet dephasing time T_2^* or better signal integrity, they can also be limited somewhat by careful pulse design. Landau-Zener transitions occur when gate voltages jump between SPAM and idle configurations too quickly, such that the ground state cannot adiabatically move between the (2,0) to (1,1) charge configurations. Such transitions are most likely when the charge states are near an anti-crossing, as they

are during initialization and measurement, and as such can be suppressed by ramping slowly to and from the SPAM configurations. On the other hand, magnetic dephasing is in some ways a symptom of ramping too slowly. Near the charge boundary, the same large singlet-triplet energy splitting that enables SPAM suppresses dephasing between the logical states. Close to idle, the energy splitting is much smaller, and dephasing is more significant. Consequently, long-duration ramps deep within the (1,1) charge cell unnecessarily accumulate dephasing error. We employ a two-step approach to ramping (as noted in Section III) to minimize both types of error.