input   clk,
input   clk2x,
input   clk1x_follower,
input   reset,
input   sect,
output   finish,
output   memory_controller_waitrequest,
output   memory_controller_enable_a,
output   memory_controller_address_a,
output   memory_controller_write_enable_a,
output   memory_controller_in_a,
output   memory_controller_size_a,
output   memory_controller_out_a,
input   memory_controller_enable_b,
output   memory_controller_address_b,
output   memory_controller_write_enable_b,
output   memory_controller_in_b,
output   memory_controller_size_b,
input   memory_controller_out_b,
output   return_value.

1为忙 表示要
往外 他写
32 bit
1为写. 0为读
让 memory 读

写进 memory

finish 为低 直到 计算 求数 到菜后一句 结束, waitrequest 为低

写地址: write_enable = 0. 读操作.   out 进信号
          write_enable = 1. 写操作.   in 出信号

Size : 0 for byte.
       1 for shot.
       2 for integer.
       3 for long.