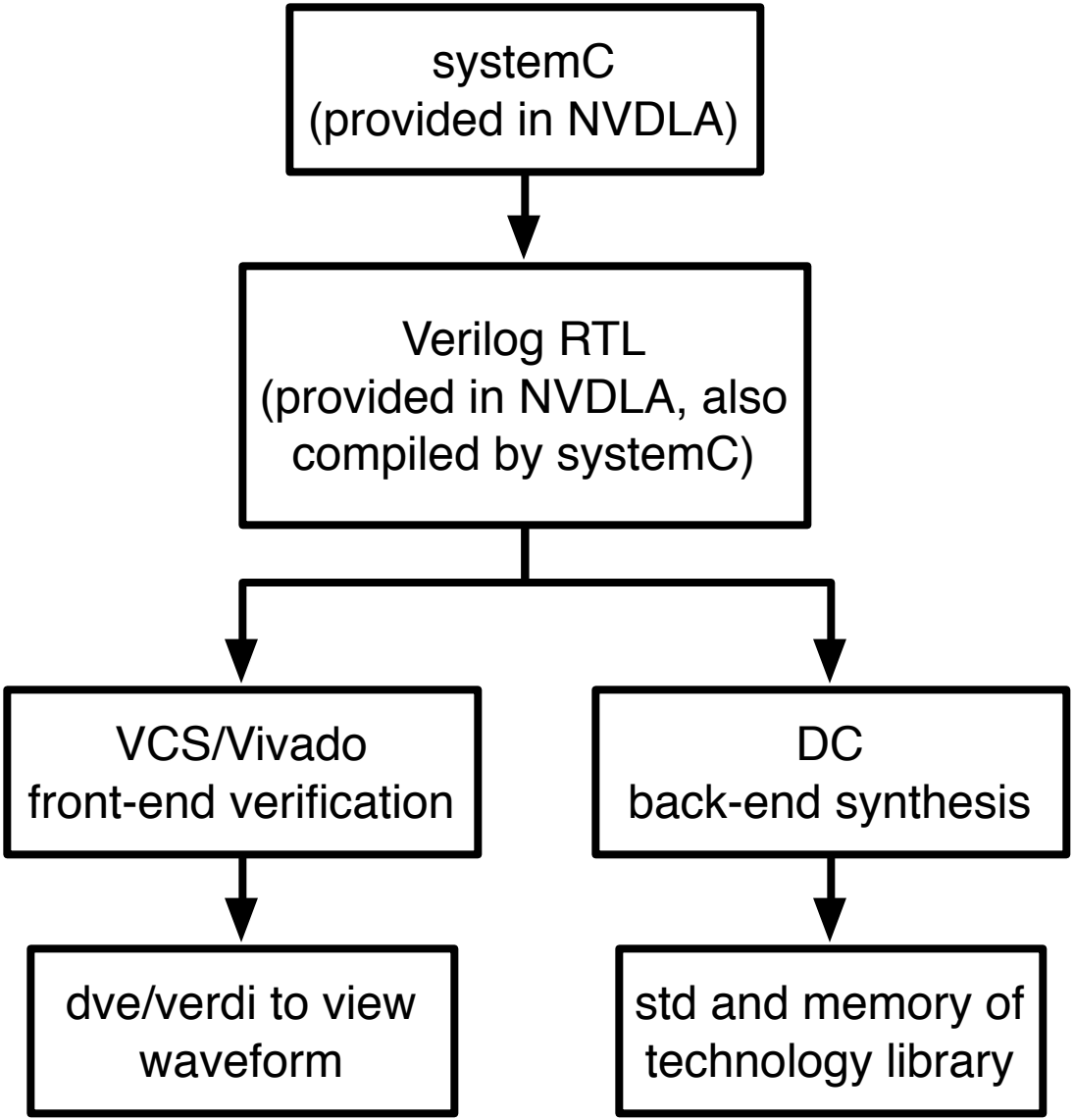
systemC
(provided in NVDLA)
Verilog RTL
(provided in NVDLA, also
compiled by systemC)
VCS/Vivado
front-end verification
DC
back-end synthesis
dve/verdi to view
waveform
std and memory of
technology library