Component Name pcie_x8_gen3_3_0

| Basic | Capabilities | PF0 IDs | **PF0 BAR** | Legacy/MSI Cap | MSIx Cap | Power Management | Extd. Capabilities-1 | Extd. Capabilities-2 |

Base Address Registers (BARs) serve two purposes. Initially, they serve as a mechanism for the device to request blocks of address space map. After the BIOS or OS determines what addresses to assign to the device, the Base Address Registers are programmed with addresses this information to perform address decoding.

☑ Bar0

Type   Memory ▼   ☐ 64 bit   ☐ Prefetchable

Size Unit   Megabyt... ▼   Size Value 1 ▼

Value (Hex): FFF00000

☑ Bar1

Type   Memory ▼   ☐ 64 bit   ☐ Prefetchable

Size Unit   Megabyt... ▼   Size Value 1 ▼

Value (Hex): FFF00000

☑ Bar2

Type   Memory ▼   ☐ 64 bit   ☐ Prefetchable

Size Unit   Megabyt... ▼   Size Value 1 ▼

Value (Hex): FFF00000

☐ Bar3

Type   N/A ▼   ☐ 64 bit   ☐ Prefetchable

Size Unit   Kilobytes ▼   Size Value 2 ▼

Value (Hex): 00000000