

Physical Functions

☒ Enable Physical Function 0

☐ Enable Physical Function 1

Device Capabilities Register PF

PF0 Max Payload Size 1024 bytes ▼

PF1 Max Payload Size 512 bytes ▼

☐ Extended Tag Field

☐ SRIOV Capability

Device Capabilities Register 2

☐ 32-bit AtomicOp Completer Supported

☐ 64-bit AtomicOp Completer Supported

☐ 128-bit CAS Completer Supported

☐ TPH Completer Supported

OBFF Supported 00 Not Supported ▼

Link Status Register

Selects whether the device reference clock is provided by the connector (Synchronous) or generated via an onboard PLL(Asynchronous)

☒ Enable Slot Clock Configuration

☐ Function Level Reset