HAL/S COMPILER PHASE 1 -- VERSION OF JULY 15, 2024. CLOCK TIME = 12:11:17.00.

TODAY IS JULY 15, 2024. CLOCK TIME = 20:22:5.29.

PARM FIELD: LISTING2, LIST, NOADDRS, NOTABLES, TRACE

COMPLETE LIST OF COMPILE-TIME OPTIONS IN EFFECT

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*** TYPE 1 OPTIONS ***
```

NOADDRS

NODECK

NODUMP

NOHALMAT

NOHIGHOPT

LFXI LIST

LISTING2

MICROCODE

NOREGOPT

NOSDL

NOSREF NOSRN

NOTABDMP

NOTABLES

NOTABLST

NOTEMPLATE

NOVARSYM ZCON

\*\*\* TYPE 2 OPTIONS \*\*\*

BLOCKSUM = 400

CARDTYPE =

COMPUNIT = 0

DSR = 1

LABELSIZE = 1200

LINECT = 59

LITSTRINGS = 2000

MACROSIZE = 500

MFID =

PAGES = 2500

SYMBOLS = 200

TITLE =

XREFSIZE = 2000

\*\*\* NO LANGUAGE SUBSET IN EFFECT \*\*\*

HAL/S REL32V0	TITAN SYSTEMS CORP.	JULY 15, 2024	20:22:5.29	PAGE 2

STMT SOURCE REVISION

		l = = 1.0
1 M		DEMO
1 M	PROGRAM;	DEMO
cl		DEMO
cl	THIS IS A DEMONSTRATION PROCESS TO CHOK THE LIGHTNE PROPRIET BY	DEMO
	THIS IS A DEMONSTRATION PROGRAM TO SHOW THE LISTING PRODUCED BY	
c	THE HAL/S-360 COMPILER	DEMO
С		DEMO
2 M	REPLACE PRINTER BY "6";	DEMO
3 M	DECLARE INTEGER INITIAL(1),	DEMO
3 M	А, В, С;	DEMO
4 M	DECLARE D, F, G;	DEMO
5 M	DECLARE E VECTOR(4);	DEMO
6 M	DECLARE H, I, J,	DEMO
6 M	K ARRAY(5) MATRIX(3, 4),	DEMO
6 M	L, M, N,	DEMO
6 M	O SCALAR;	DEMO
7 M	STRUCTURE AA:	DEMO
7 M	1 BB,	DEMO
7 M	2 CC MATRIX(4, 3),	DEMO
7 M	1 DD,	DEMO
7 M	2 EE ARRAY(4) MATRIX(3, 4);	DEMO
8 M	STRUCTURE 00:	DEMO
8 M	1 RR,	DEMO
8 M	2 AAREF AA-STRUCTURE,	DEMO
8 M	2 SS CHARACTER(5);	DEMO
9 M	, , ,	DEMO
2 11	Elektric in_elikelieit && elikelieite(e),	DBH IO

HAL/S STMT	REL32V0	TITAN	S Y S T E M	S C O R P . SOURCE	JULY 15, 2024	20:22:5.29	PAGE 3 CURRENT SCOPE	
SIMI				SOURCE			CORRENT SCOPE	
10 M 11 M 12 M 14 M	PROC1: PROCEDURE; DECLARE A INTEGER; IF A = B THEN DO; LABEL1: 1 B = C;					P   P   D   P	ROC1 ROC1 ROC1 O=ST#13 ROC1	
E  15 M  S	1 {MY_STRUCTURE.RR.A		= {M*;3:2,*	_ Y_STRUCTURE.RR.AAREF.BB.CC	} *;*,2	P	PROC1	
16 M 17 M 17 M 18 M	END; ELSE A = C; CLOSE PROC1;					P P	T#13 ROC1 ROC1 ROC1	

\*\*\*\* B L O C K S U M M A R Y \*\*\*\*

OUTER VARIABLES USED

B, B\*, C, MY\_STRUCTURE\*, MY\_STRUCTURE

OUTER STRUCTURE TEMPLATES USED

QQ, AA

HAL/S REL32V0 STMT	TITAN SYSTEMS CORP.	JULY 15, 2024	20:22:5.29 PAGE 4 CURRENT SCOPE	
19 M DO FOR C = 1 TO 100; 20 M 1 D = K ; S C:2,3			DEMO DEMO	
21 M END; 22 M DO CASE A; 23 M 1 A = B;			ST#19 DEMO CASE 1	
E			CASE 2	
25 M END;			ST#22	
E *			DEMO	
E   27 M   WRITE (PRINTER) MY_STE	* RUCTURE.RR.AAREF.BB.CC , [MY	* _STRUCTURE.RR.AAREF.DD.EE]	, D, DEMO	
s	3;1 TO 3,*	2;2 T	ro 4:	
E   - * 27 M E, F, G, H, I, J, [K] 28 M CLOSE DEMO;	], L, M, N, O;		   DEMO   DEMO	

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\*\*\*\* COMPILATION LAYOUT \*\*\*\*

DEMO: PROGRAM;

PROC1: PROCEDURE;

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STRUCTURE TEMPLATE SYMBOL & CROSS REFERENCE TABLE LISTING:

(CROSS REFERENCE FLAG KEY: 4 = ASSIGNMENT, 2 = REFERENCE, 1 = SUBSCRIPT USE, 0 = DEFINITION)

DCL	NAME	TYPE	ATTRIBUTES & CROSS REFERENCE
7	AA	STRUCTURE TEMPLATE	ALIGNED XREF: 0 0007 2 0008 2 0015 2 0027
			USED BY: AAREF
7	BB	1 MINOR NODE	ALIGNED XREF: 0 0007 POSSIBLY NOT USED
7	CC	2 4 X 3 MATRIX	SINGLE, ALIGNED XREF: 0 0007 2 0015 2 0027 POSSIBLY NOT ASSIGNED
7	DD	1 MINOR NODE	ALIGNED XREF: 0 0007 POSSIBLY NOT USED
7	EE	2 3 X 4 MATRIX ARRAY	ARRAY(4), SINGLE, ALIGNED XREF: 0 0007 4 0015 2 0027
8	QQ	STRUCTURE TEMPLATE	ALIGNED XREF: 0 0008 2 0009 2 0015 2 0027
			USED BY: MY_STRUCTURE
8	RR	1 MINOR NODE	ALIGNED XREF: 0 0008 POSSIBLY NOT USED
8	AAREF	2 STRUCTURE	AA-STRUCTURE, ALIGNED XREF: 0 0008 6 0015 2 0027
8	SS	2 CHARACTER (5)	ALIGNED XREF: 0 0008 POSSIBLY NOT USED

SYMBOL & CROSS REFERENCE TABLE LISTING:

(CROSS REFERENCE FLAG KEY: 4 = ASSIGNMENT, 2 = REFERENCE, 1 = SUBSCRIPT USE, 0 = DEFINITION)

DCL	NAME	TYPE	ATTRIBUTES & CROSS REFERENCE
3	A	INTEGER	SINGLE, ALIGNED, STATIC, INITIAL XREF: 0 0003 2 0022 4 0023 1 0024
11	A	INTEGER	SINGLE, ALIGNED, STATIC XREF: 0 0011 2 0012 4 0017
8	AAREF	STRUCTURE	**** SEE STRUCTURE TEMPLATE OO
3	В	INTEGER	SINGLE, ALIGNED, STATIC, INITIAL XREF: 0 0003 2 0012 4 0014 2 0023
			1 0024
7	BB	MINOR NODE	**** SEE STRUCTURE TEMPLATE AA
3	C	INTEGER	SINGLE, ALIGNED, STATIC, INITIAL XREF: 0 0003 2 0014 2 0017 4 0019
			1 0020
7	CC	4 X 3 MATRIX	**** SEE STRUCTURE TEMPLATE AA
4	D	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0004 4 0020 2 0027
7	DD	MINOR NODE	**** SEE STRUCTURE TEMPLATE AA
1	DEMO	PROGRAM	1 0020  **** SEE STRUCTURE TEMPLATE AA  SINGLE, ALIGNED, STATIC XREF: 0 0004 4 0020 2 0027  **** SEE STRUCTURE TEMPLATE AA  XREF: 0 0001
5	E	4 - VECTOR	SINGLE, ALIGNED, STATIC XREF: 0 0005 4 0024 2 0027
7	EE	3 X 4 MATRIX ARRAY	**** SEE STRUCTURE TEMPLATE AA
4	F	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0004 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
4	G	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0004 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	H	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	I	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	J	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	K	3 X 4 MATRIX ARRAY	ARRAY(5), SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0020 2 0024
			4 0026 2 0027
6	L	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
			***** ERROR **** REFERENCED BUT NOT ASSIGNED
14	LABEL1	STATEMENT LABEL	XREF: 0 0014 NOT REFERENCED
6	M	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
9	MY_STRUCTURE	STRUCTURE (5)	QQ-STRUCTURE, ALIGNED, STATIC XREF: 0 0009 6 0015 2 0027
6	N	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027  ***** ERROR **** REFERENCED BUT NOT ASSIGNED  ***** BUTCHED STATIC XPER: 0 006 2 0027
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	0	SCALAR	SINGLE, ALIGNED, STATIC AREF: 0 0000 2 002/
			**** ERROR **** REFERENCED BUT NOT ASSIGNED
2	PRINTER	REPLACE MACRO	MACRO TEXT="6" XREF: 0 0002 2 0027
10	PROC1	PROCEDURE	XREF: 0 0010 NOT REFERENCED
8	RR	MINOR NODE	**** SEE STRUCTURE TEMPLATE QQ
8	SS	CHARACTER (5)	**** SEE STRUCTURE TEMPLATE QQ

<sup>\*\*\*\*\*</sup> BI105 ERROR #2 OF SEVERITY 2 OCCURRED \*\*\*\*\*

<sup>\*\*\*\*\*</sup> DURING CONVERSION OF HAL/S STATEMENT 29.\*\*\*\*

<sup>\*\*\*\*</sup> ERROR ONE OR MORE VARIABLES REFERENCED BUT NOT ASSIGNED.

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OPTIONAL TABLE SIZES

REQUESTED USED NAME ^^^ LITSTRINGS 2000 0 30 SYMBOLS 200 500 2 MACROSIZE 2000 XREFSIZE 75 400 14 BLOCKSUM

CALLS TO SCAN = 311 CALLS TO IDENTIFY = 85 NUMBER OF REDUCTIONS = 857 MAX STACK SIZE = 11 MAX IND. STACK SIZE = 10 END IND. STACK SIZE = 1 END ARRAY STACK SIZE = 0 MAX EXT\_ARRAY INDEX = 5 STATEMENT COUNT = 28 MINOR COMPACTIFIES = 0 MAJOR COMPACTIFIES = 0 = 0 REALLOCATIONS MAX NESTING DEPTH = 2 FREE STRING AREA = 16628536

END OF HAL/S PHASE 1, JULY 15, 2024. CLOCK TIME = 20:22:5.30.

39 CARDS WERE PROCESSED.

2 ERRORS WERE DETECTED IN PHASE 1.

\*\*\*\* SUMMARY OF ERRORS DETECTED IN PHASE 1 \*\*\*\*
ERROR #1 OF SEVERITY 1 IN CROSS-REFERENCE
ERROR #2 OF SEVERITY 2 IN PHASE 1 SET UP

NUMBER OF FILE 6 LOCATES = 1 NUMBER OF FILE 6 READS = 0 NUMBER OF FILE 6 WRITES = 0

TOTAL CPU TIME FOR PHASE 1 0:0:0.01.
CPU TIME FOR PHASE 1 SET UP 0:0:0.00.
CPU TIME FOR PHASE 1 PROCESSING 0:0:0.01.
CPU TIME FOR PHASE 1 CLEAN UP 0:0:0.00.
PROCESSING RATE: 234000 CARDS PER MINUTE.

\*\*\*\* COMPILATION ABANDONED \*\*\*\*\*