

HAL/S COMPILER PHASE 1 -- VERSION 7.0 OF MARCH 29, 1974. CLOCK TIME = 12:56:33.73.

TODAY IS MARCH 30, 1974. CLOCK TIME = 15:25:47.95.

OVERRIDING OPTIONS: LISTING2,LIST

COMPLETE LIST OF COMPILE-TIME OPTIONS IN EFFECT

TYPE 1 OPTIONS

NODUMP	INSTEAD OF DUMP
LISTING2	INSTEAD OF NOLISTING2
LIST	INSTEAD OF NOLIST
TRACE	INSTEAD OF NOTRACE
NODECK	INSTEAD OF DECK
TABLES	INSTEAD OF NOTABLES
NOTABLST	INSTEAD OF TABLST
NOADDRS	INSTEAD OF ADDRS
NOSRN	INSTEAD OF SRN
NOSDL	INSTEAD OF SDL

TYPE 2 OPTIONS

PAGES = 250
LINE COUNT = 59

STMT	SOURCE	CURRENT SCOPE
1 MI DEMO:		DEMO
1 MI PROGRAM;		DEMO
CI		DEMO
CI	THIS IS A DEMONSTRATION PROGRAM TO SHOW THE LISTING PRODUCED BY	DEMO
CI	THE HAL/S-360 COMPILER	DEMO
CI		DEMO
2 MI	REPLACE PRINTER BY "6";	DEMO
3 MI	DECLARE INTEGER INITIAL(1),	DEMO
3 MI	A, B, C;	DEMO
4 MI	DECLARE D, F, G;	DEMO
5 MI	DECLARE E VECTOR(4);	DEMO
6 MI	DECLARE H, I, J,	DEMO
6 MI	K ARRAY(5) MATRIX(3, 4),	DEMO
6 MI	L, M, N,	DEMO
6 MI	O SCALAR;	DEMO
7 MI	STRUCTURE AA:	DEMO
7 MI	1 BB,	DEMO
7 MI	2 CC MATRIX(4, 3),	DEMO
7 MI	1 DD,	DEMO
7 MI	2 EE ARRAY(4) MATRIX(3, 4);	DEMO
8 MI	STRUCTURE QQ:	DEMO
8 MI	1 RR,	DEMO
8 MI	2 AAREF AA-STRUCTURE,	DEMO
8 MI	2 SS CHARACTER(5);	DEMO
9 MI	DECLARE MY_STRUCTURE QQ-STRUCTURE(5);	DEMO

STMT	SOURCE	CURRENT SCOPE
10 MI PROC1:		PROC1
10 MI PROCEDURE;		PROC1
11 MI DECLARE A INTEGER;		PROC1
12 MI IF A = B THEN		PROC1
13 MI DO;		PROC1
14 MI LABEL1: B = C;		PROC1
15 MI SI	(MY_STRUCTURE.RR.AAREF.DD.EE) = (MY_STRUCTURE.BR.AAREF.BB.CC) ; *;3:2,* *;*,2	PROC1
16 MI END;		PROC1
17 MI ELSE		PROC1
17 MI A = C;		PROC1
18 MI CLOSE PROC1;		PROC1

BLOCK SUMMARY

OUTER VARIABLES USED:

B, B*, C, MY_STRUCTURE*, MY_STRUCTURE

STMT	SOURCE	CURRENT SCOPE
19 M	DO FOR C = 1 TO 100;	DEMO
20 M	D = K 24 ;	DEMO
SI	C:2,3	
21 M	END;	DEMO
22 M	DO CASE A;	DEMO
23 M	A = B;	DEMO CASE 1
E	- -	
24 M	E = K :	DEMO CASE 2
SI	B:A,*	
25 M	END;	DEMO DO CASE END
E	*	
26 M	[K] = 0;	DEMO
E	*	
27 M	WRITE(PRINTER) MY_STRUCTURE.RR.AAREF.BB.CC	DEMO
SI	3;1 TO 3,* [MY_STRUCTURE.RR.AAREF.DD.EE] 2;2 TO 4: D,	
E	- *	
27 M	E, F, G, H, I, J, [K], L, M, N, O;	DEMO
28 M	CLOSE DEMO;	DEMO

HAL/S COMPILATION

I N T E R M E T R I C S I N C .

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P R O G R A M L A Y O U T

DEMO: PROGRAM:

PROC1: PROCEDURE;

SYMBOL & CROSS REFERENCE TABLE LISTING:

(CROSS REFERENCE FLAG KEY: 4 = ASSIGNMENT, 2 = REFERENCE, 1 = SUBSCRIPT USE, 0 = DEFINITION)

DCL	NAME	TYPE	ATTRIBUTES & CROSS REFERENCE
7	AA	STRUCTURE TEMPLATE	ALIGNED XREF: 0 0007 2 0008
7	BB	1 MINOR NODE	ALIGNED XREF: 0 0007 POSSIBLY NOT USED
7	CC	2 4 X 3 MATPIX	SINGLE, ALIGNED XREF: 0 0007 2 0015 2 0027 POSSIBLY NOT ASSIGNED
7	DD	1 MINOR NODE	ALIGNED XREF: 0 0007 POSSIBLY NOT USED
7	EE	2 3 X 4 MATRIX ARRAY	ARRAY(4), SINGLE, ALIGNED XREF: 0 0007 4 0015 2 0027
8	OQ	STRUCTURE TEMPLATE	ALIGNED XREF: 0 0008 2 0009
8	RR	1 MINOR NODE	ALIGNED XREF: 0 0008 POSSIBLY NOT USED
8	AAREF	2 STRUCTURE	AA-STRUCTURE, ALIGNED XREF: 0 0008 6 0015 2 0027
8	SS	2 CHARACTER(5)	ALIGNED XREF: 0 0008 POSSIBLY NOT USED
3	A	INTEGER	SINGLE, ALIGNED, STATIC, INITIAL XREF: 0 0003 2 0022 4 0023 1 0024
11	A	INTEGER	SINGLE, ALIGNED, STATIC XREF: 0 0011 2 0012 4 0017
8	AAREF	STRUCTURE	**** SEE STRUCTURE TEMPLATE OQ
3	B	INTEGER	SINGLE, ALIGNED, STATIC, INITIAL XREF: 0 0003 2 0012 4 0014 2 0023 1 0024
7	BB	MINOR NODE	**** SEE STRUCTURE TEMPLATE AA
3	C	INTEGER	SINGLE, ALIGNED, STATIC, INITIAL XREF: 0 0003 2 0014 2 0017 4 0019 1 0020
7	CC	4 X 3 MATRIX	**** SEE STRUCTURE TEMPLATE AA
4	D	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0004 4 0020 2 0027
7	DD	MINOR NODE	**** SEE STRUCTURE TEMPLATE AA
1	DEMO	PROGRAM	XREF: 0 0001
5	E	4 - VECTOR	SINGLE, ALIGNED, STATIC XREF: 0 0005 4 0024 2 0027
7	EZ	3 X 4 MATRIX ARRAY	**** SEE STRUCTURE TEMPLATE AA
4	F	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0004 2 0027
4	G	SCALAR	**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	H	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0004 2 0027
6	I	SCALAR	**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	J	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
6	K	3 X 4 MATRIX ARRAY	**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	L	SCALAR	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
14	LABEL1	STATEMENT LABEL	**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	M	SCALAR	XREF: 0 0014 NOT REFERENCED
9	MY_STRUCTURE	STRUCTURE(5)	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
6	N	SCALAR	**** ERROR **** REFERENCED BUT NOT ASSIGNED
6	O	SCALAR	QO-STRUCTURE, ALIGNED, STATIC XREF: 0 0009 6 0015 2 0027
2	PPINTER	REPLACE MACRO	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
10	PROC1	PROCEDURE	**** ERROR **** REFERENCED BUT NOT ASSIGNED
8	RR	MINOR NODE	SINGLE, ALIGNED, STATIC XREF: 0 0006 2 0027
8	SS	CHARACTER(5)	**** ERROR **** REFERENCED BUT NOT ASSIGNED
			MACRO-TEXT INDEX=1 XREF: 0 0002 2 0027
			XREF: 0 0010 NOT REFERENCED
			**** SEE STRUCTURE TEMPLATE OQ
			**** SEE STRUCTURE TEMPLATE OQ

DCL NAME	TYPE	ATTRIBUTES & CROSS REFERENCE
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**** ERROR **** ONE OR MORE VARIABLES REFERENCED BUT NOT ASSIGNED.

MACRO TEXT LISTING:

LOC TEXT

1 6

CALLS TO SCAN = 311
CALLS TO IDENTIFY = 85
NUMBER OF REDUCTIONS = 866
MAX STACK SIZE = 11
MAX IND. STACK SIZE = 10
END IND. STACK SIZE = 1
END ARRAY STACK SIZE = 0
MAX EXT_ARRAY INDEX = 6
XREF LIST ENTRIES = 71
STATEMENT COUNT = 28
NUMBER OF SYMBOLS = 30
MINOR COMPACTIFIES = 1
MAJOR COMPACTIFIES = 0
MAX NESTING DEPTH = 2
FREE STRING AREA = 33063

END OF HAL/S PHASE 1, MARCH 30, 1974. CLOCK TIME = 15:25:52.46.

39 CARDS WERE PROCESSED.
ONE ERROR WAS DETECTED IN PHASE 1.

**** SUMMARY OF ERRORS DETECTED IN PHASE 1 ****
ERROR #1 OF SEVERITY 1 IN CROSS-REFERENCE

TOTAL CPU TIME FOR PHASE 1 0:0:1.93.
CPU TIME FOR PHASE 1 SET UP 0:0:0.05.
CPU TIME FOR PHASE 1 PROCESSING 0:0:1.63.
CPU TIME FOR PHASE 1 CLEAN UP 0:0:0.25.
PROCESSING RATE: 1435 CARDS PER MINUTE.

***** COMPILATION ERRORS INHIBITED TEMPLATE GENERATION