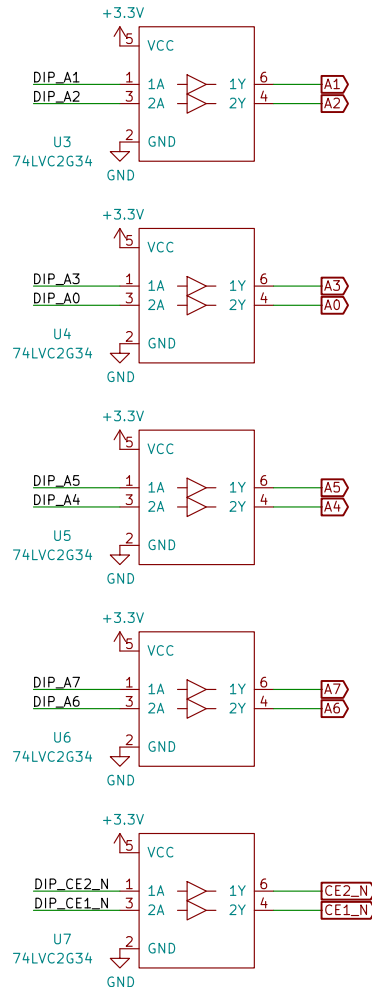
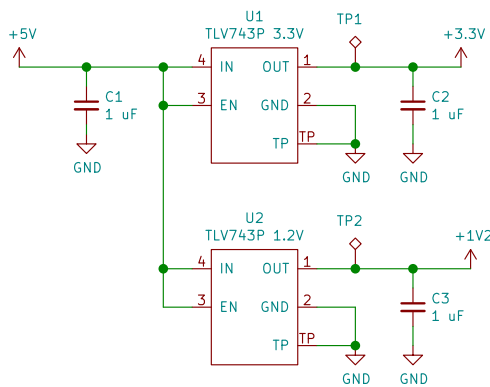
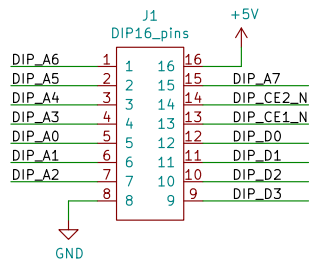
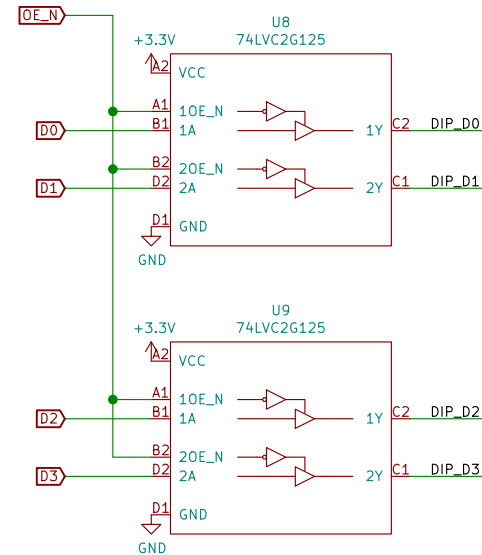


1	2	3	4	5	6	
A	<div>256x4 PROM</div> <div>replaces MMI 6301 or equivalent; 16-pin DIP; TTL levels</div>					A
B						B
C						C
D	<div><div>Sheet: Sheet5E85C2D6</div><div>File: prom_io_power.sch</div><div>Sheet: Sheet5E85CF8C</div><div>File: prom_fpga.sch</div><div><div>Sheet: /</div><div>File: prom.sch</div><div>Title:</div><div><div>Size: A4</div><div>Date:</div></div><div><div>KiCad E.D.A. kicad 5.1.5-1.fc31</div><div>Rev:</div><div>Id: 1/3</div></div></div></div>					D
1	2	3	4	5	6	



TTL inputs  
5V tolerant



TTL outputs  
5V tolerant when in  
hi-z state

## Pin I/O and power

Sheet: /Sheet5E85C2D6/  
File: prom\_io\_power.sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad 5.1.5-1.fc31

**Rev:**

Id: 2/3



Sheet: /Sheet5E85CF8C/  
File: prom\_fpga.sch

<b>Title:</b>		
Size: A4	Date:	<b>Rev:</b>
KiCad E.D.A.    kicad 5.1.5-1.fc31		Id: 3/3