MuntsOS Embedded Linux

Application Note #19: BeaglePlay Target Platform Notes

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Introduction

The BeaglePlay is a small Linux microcomputer board with industry standard interfaces for add-on I/O modules (a mikroBUS socket, a QWIIC socket, and a Grove socket) instead of a general purpose expansion header. It has a Texas Instruments AM6254 ARMv8 Cortex-A53 quad core CPU and comes with 2 GB of RAM. The BeaglePlay has one USB-A receptacle for peripheral devices and one USB-C receptacle for power and tethering. It has 10/100/1000BASE-T and 10BASE-T1L wired Ethernet interfaces, a WL1807MOD wireless Ethernet interface, and a CC1352P7 wireless microcontroller capable of supporting a wide variety of radio networks.

Standard Hardware Configuration

After installing a **MuntsOS Embedded Linux** <u>Thin Server</u>, the BeaglePlay will have all of the mikroBUS hardware subsystems (I²C bus master, PWM output, SPI bus master, and UART) active and the Grove and QWIIC sockets configured as I²C bus masters.

mikroBUS Socket **GPIO3.10** AN PWM **PWM0.0** GPIO3.11 **GPIO3.12** GPIO3.9 **RST** INT GPIO3.13 SPI0 CS0 CS RX **UART5 RXD** GPIO3.24 GPIO3.14 SPIO SCLK SCK TX **UART5 TXD** GPIO3.25 GPIO3.22 GPIO3.7 SPI0 MISO MISO SCL 12C3 SCL GPIO3.8 SPI0 MOSI MOSI SDA 12C3 SDA GPIO3.23 +5V +5V 3.3V +3.3V **GND** GND **GND** GND **Grove Socket QWIIC Socket** 12C5 SCL I2C1 SCL 1 1 GPIO3.28 PWM1.0 12C5 SDA PWM1.1 2 2 12C1 SCL GPIO3.29 **UART1 TXD** 3 3 3.3V 3.3V **GND** 4 4 **GND**

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GPIOX.Y denotes /dev/gpiochipX line Y.

PWMX.Y denotes /sys/class/pwm/pwmchipX output Y.

Alternate GPIO pin functions (shown as gray in the above diagram) can be configured with device tree overlays described in the following section.

Device Tree Overlays

MuntsOS includes the following device tree overlays for altering the BeaglePlay hardware configuration. Device tree overlays are applied by the boot loader and selected by editing /boot/config.txt, changing OVERLAYS= to e.g. OVERLAYS=grove-gpio mikrobus-gpio.

grove-gpio.dtbo

Disables hardware subsystem **I2C1** and configures the Grove socket **D0** pin as **GPI03.28** and **D1** pin as **GPI03.29**.

grove-motor1.dtbo

Disables hardware subsystem **I2C1** and configures the Grove socket **D0** pin as **PWM1.0** and **D1** pin as **GPI03.29**.

grove-motor2.dtbo

Disables hardware subsystem **I2C1** and configures the Grove socket **D0** pin as **PWM1.0** and **D1** pin as **PWM1.1**.

grove-motor3.dtbo

Disables hardware subsystem **I2C1** and configures the Grove socket **D0** pin as **GPIO3.28** and **D1** pin as **PWM1.1**.

grove-serial.dtbo

Disables hardware subsystem **I2C1** and configures the Grove socket as serial port /dev/ttyS1.

mikrobus-qpio.dtbo

Configures all mikroBUS pins as GPIO *and* disables the corresponding SPI, I²C, PWM, and UART hardware subsystems to conserve power consumption. This is the preferred overlay for supporting unusual Click Boards such as the <u>Relay Click</u> which drives one of the relays with **CS**, normally the SPI slave select signal.

mikrobus-i2c-qpio.dtbo

Configures both of the mikroBUS I²C pins as GPIO. Does *not* disable the corresponding I²C hardware subsystem.

mikrobus-pwm-gpio.dtbo

Configures the mikroBUS PWM pin as GPIO. Does *not* disable the corresponding PWM hardware subsystem.

mikrobus-spi-qpio.dtbo

Configures all of the mikroBUS SPI pins as GPIO. Does *not* disable the corresponding SPI hardware subsystem.

mikrobus-uart-gpio.dtbo

Configures both of the mikroBUS serial port pins as GPIO. Does *not* disable the corresponding UART hardware subsystem.

Serial Ports

Unlike the Debian distribution for the BeaglePlay, there is a direct and sane mapping between the hardware serial ports and their device nodes (**UART0** is **/dev/ttyS0** etc.).

Hardware Subsystem Device Node

UART0	/dev/ttyS0	Console Serial Port Header J6
UART1	/dev/ttyS1	Grove Socket (requires grove-serial.dtbo)
UART5	/dev/ttyS5	mikroBUS socket RX and TX
UART6	/dev/ttyS6	CC1352 wireless microcontroller

WiFi Antennas

The BeaglePlay WiFi interface works best with two **2.4G/5G** antennas, connected to coax sockets **J3** and **J5**.