

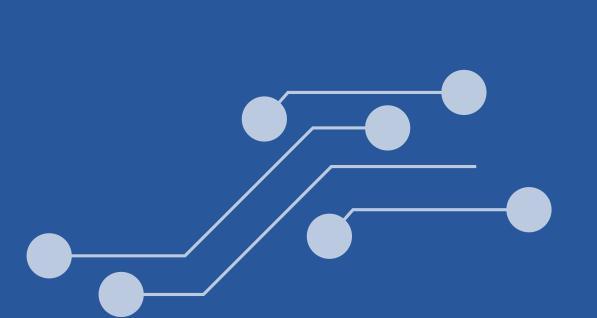






**PRESENTS** 

### IdEEEas-2K21 Are you ready









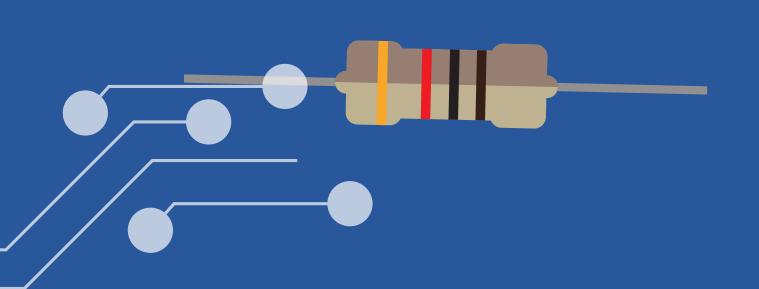


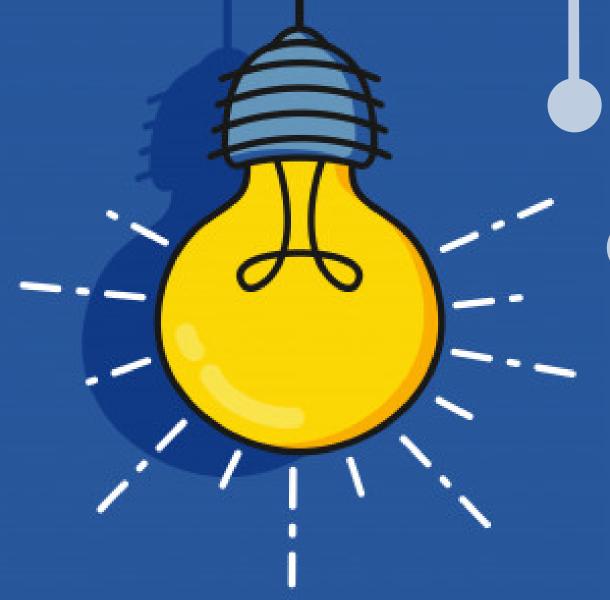


**PRESENTS** 

IdEEEas-2K21

### REGIFIER CIRCUIT DEBUGGING



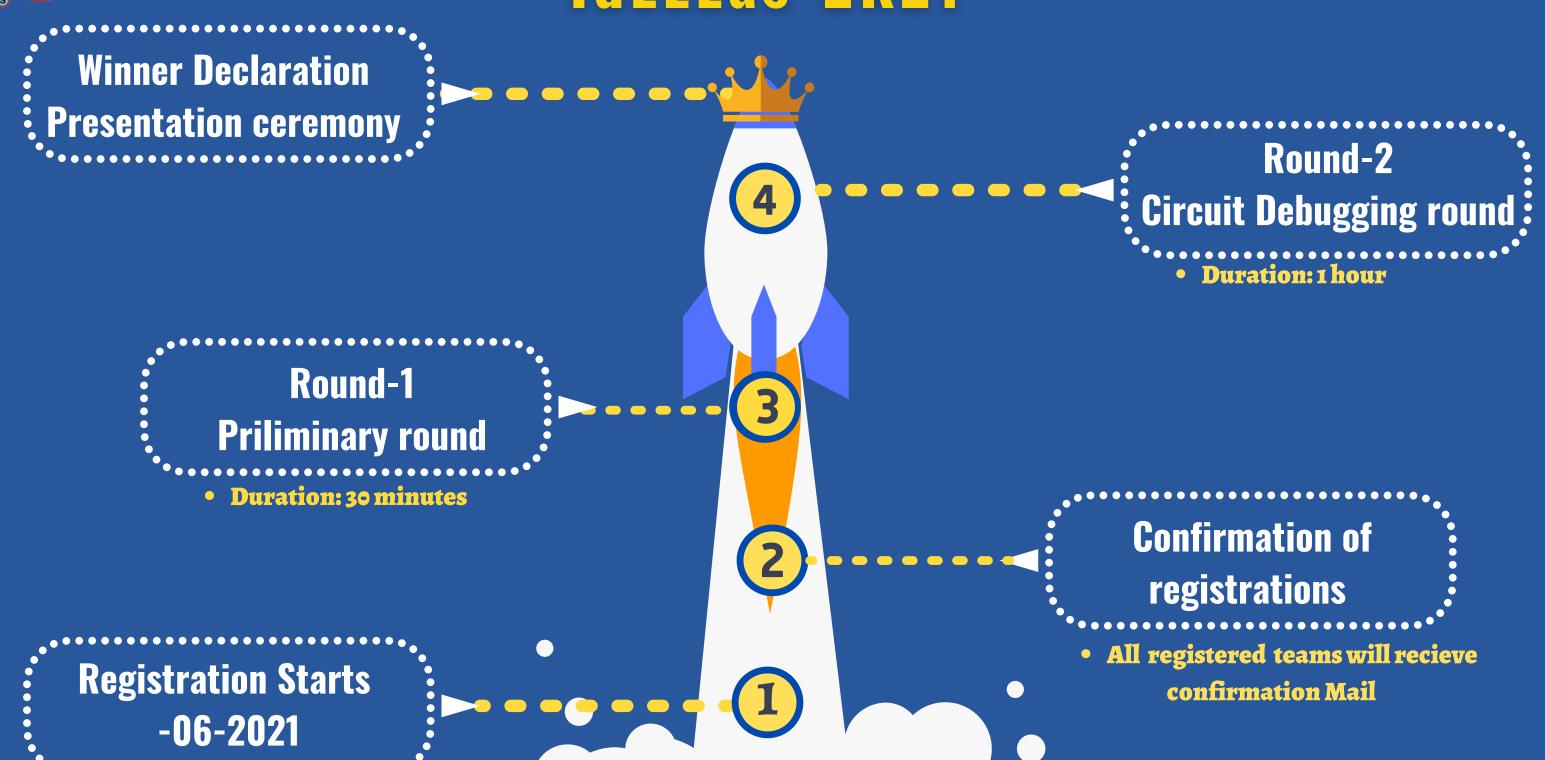




**PRESENTS** 

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# IEEE SIT SB IdEEEas-2K21 PRESENTS RECTIFIER CIRCUIT DEBUGGING

" An intelligent person creates things using new resources, but a wise person uses existing things to create new ones"

Debugging gives us various opportunities to rectify the mistake done and sometimes to understand the circuit better. You develop intuition with a simulation. Analyzing electronic circuits and applying theoretical knowledge into a practical scenario is the main focus of this event.



## IEEE SIT SB IdEEEas-2K21 PRESENTS RECTIFIER

CIRCUIT DEBUGGING

#### PRELIMNARY ROUND

- In the preliminary round, participants are given within a test that has to be solved in 30 minutes.
- The questions comprise Multiple choice questions (MCQs) and Reasoning
- The top five teams will be allowed for finals.



# IEEE SIT SB IdEEEas-2K21 PRESENTS RECTIFIER CIRCUIT DEBUGGING

### FINAL ROUND

The final round involves debugging of the given circuit.

#### Simulators:

- <u>Tinkercad</u>,
- Ltspice
- NI simulator



## IEEE SIT SB IdEEEas-2K21 PRESENTS RECTIFIER

CIRCUIT DEBUGGING

### Registration

- Registration fees are ₹50.
- 20% Discount will be provided to IEEE Membership holders
- All registered Candidates will get the Confirmation mail
- Participants are requested to arrange PCs. If not possible inform us earlier while Registration.



## IEEE SIT SB IdEEEas-2K21 PRESENTS RECTIFIER

#### CIRCUIT DEBUGGING

#### CODE OF CONDUCT

- Abide by the rules made by the organiser.
- Malpractice is **not entertained**.

Decisions of the judges and coordinators are final.

**Student Coordinators:** 

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