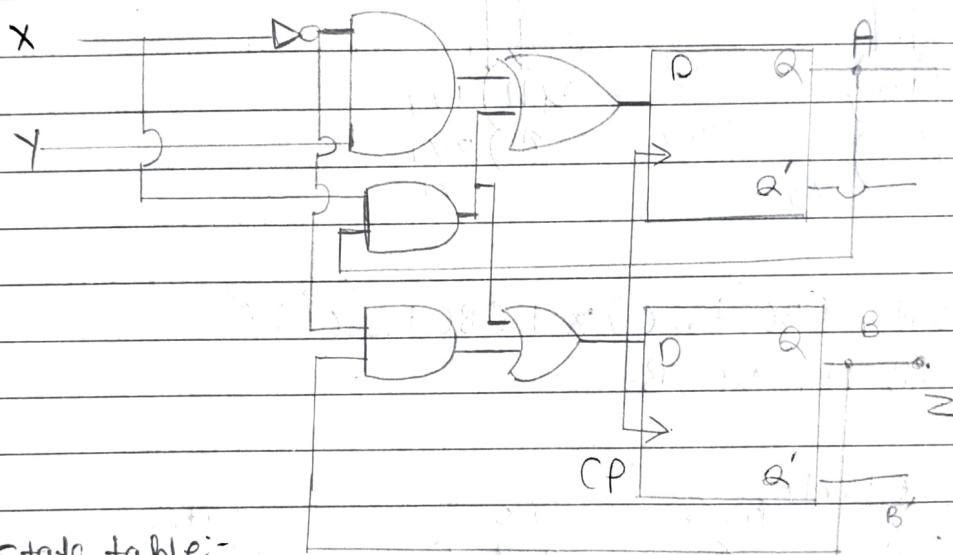


DE Assignment

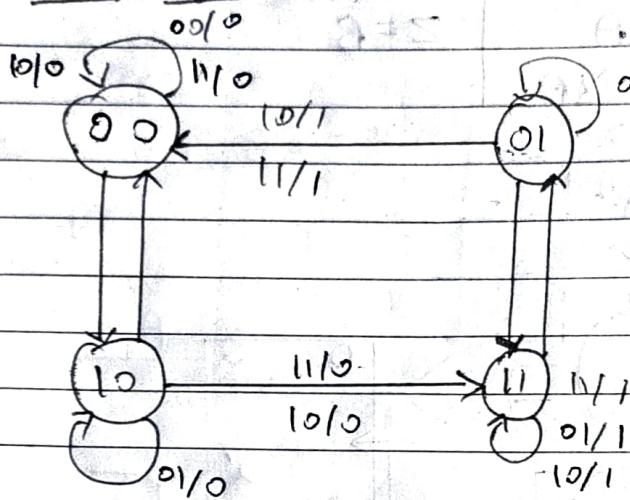
(1)

$$\begin{aligned} Q-1 \quad A(t+1) &= X'Y + XA & Z = B \\ \Rightarrow B(t+1) &= X'B + XA \end{aligned}$$

=) (a) Logic Diagram:-★ State table:-

P.S	Inputs		N.S.		Output	
A	B	X	Y	A	B	Z
0	0	0	0	0	0	0
0	0	0	1	1	0	0
0	0	1	0	0	0	0
0	0	1	1	0	0	0
0	1	0	0	0	1	1
0	1	0	1	1	1	1
0	1	1	0	0	0	1
0	1	1	1	0	0	0
1	0	0	0	0	0	0
1	0	0	1	1	0	0
1	0	1	0	1	1	0
1	0	1	1	1	1	1
1	0	0	0	0	1	1
1	0	0	1	1	1	1
1	1	0	0	1	1	1
1	1	0	1	1	1	1
1	1	1	0	0	1	1
1	1	1	1	1	1	1

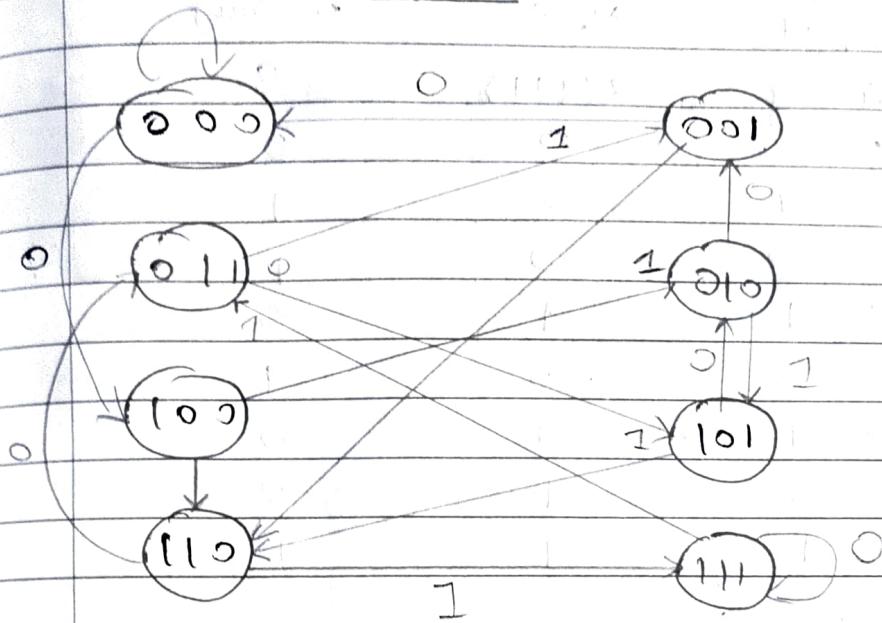
(c) State Diagram:-



Q-2 $DA = (BC' + B'C)x + (BC + B'C')x'$
 $= DB = A, DC = B$

(a) State table:

PS A B C	Inputs X	N.S.			Output		
		A	B	C	DA	DB	DC
0 0 0	0	1	0	0	1	0	0
0 0 1	0	0	0	0	0	0	0
0 1 1	1	1	0	1	0	1	0
0 1 0	0	0	1	0	0	0	1
1 0 0	1	0	0	1	0	0	1
1 0 1	0	1	0	1	1	1	0
1 1 0	1	0	1	0	1	1	1
1 1 1	0	1	1	1	1	1	1
1 1 1	1	0	1	1	0	1	1

(b) State Diagram

Q-3 Full Adder Circuit's equations are:-

$$S = x \oplus y \oplus Q$$

$$C = xy + xQ + yQ$$

$$\Rightarrow \text{Output} \Rightarrow S = x \oplus y \oplus Q$$

Input equation:-

$$D_Q = C$$

$$= xy + xQ + yQ$$

Characteristic equation

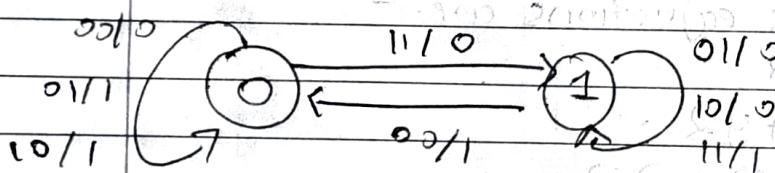
$$Q(t+1) = 0 = xy + xQ + yQ$$

$$\text{State eqn: } Q(t+1) = C$$

(a) State equation:-

P.S.	Input		N. S.	Output
$Q(t)$	x	y	$Q(t+1)$	s
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

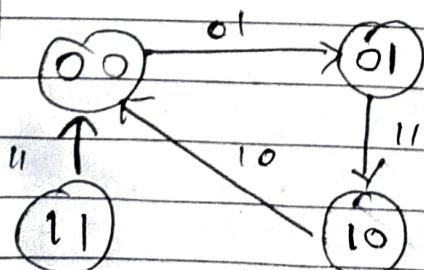
(b) State Diagram :-



Q-4 $TA = A + B$; $TB = A' + B$

P.S.		Input		N. S.	
A	B	TA	TB	A	B
0	0	0	1	0	1
0	1	1	1	1	0
1	0	1	0	0	0
1	1	1	1	0	0

(b) State Diagram:-



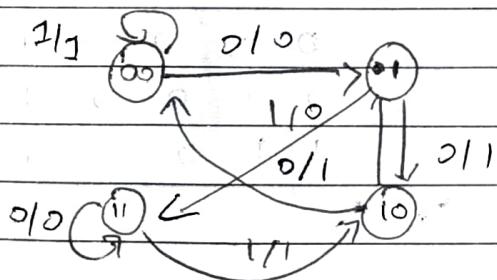
$$\begin{aligned} Q-5 \quad & JA = B, \quad JB = (A + X') \\ \equiv \quad & KA = B', \quad KB = (A + X)' \end{aligned}$$

$$Y = A + X + B$$

* State table :-

P. S.	Input	N. S.	F. F.	O/P		
A	B	X	A(t+1)	B(t+1)	JA KA JB KB	Y
0	0	0	0	1	0 1 1 1	0
0	0	1	0	0	0 1 0 0	1
0	1	0	1	0	1 0 1 1	1
0	1	1	1	1	1 0 0 0	0
1	0	0	0	0	0 1 0 0	1
1	0	1	0	1	0 1 1 1	0
1	1	0	1	1	1 0 0 0	0
1	1	1	1	0	1 0 1 1	1

* State Diagram:-



$$Q-6 \quad JA = BX + B'Y'$$

$$\equiv \quad KA = B'XY$$

$$Z = AX + BX'Y'$$

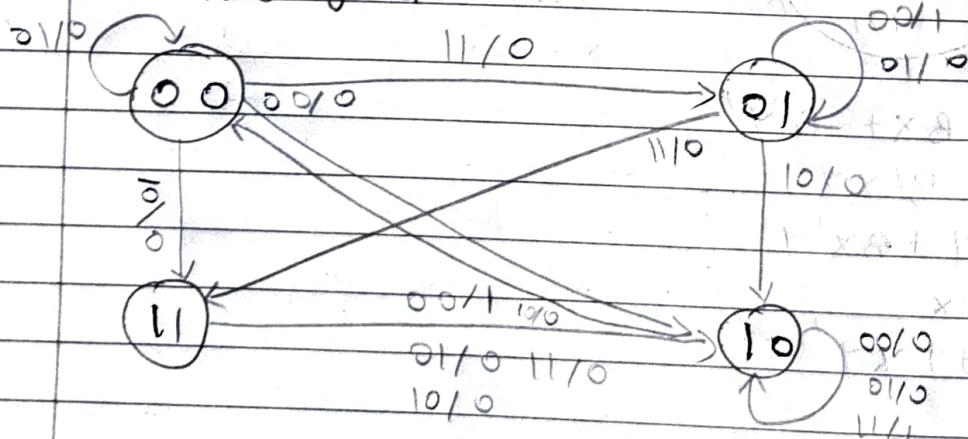
$$JB = A'X$$

$$KB = A + XY'$$

(6)

P. S.	Input		N. S.		O/P						
	A	B	X	Y	A	B	Z	J _A	J _A	J _B	K _B
	0	0	0	0	1	0	0	1	0	0	0
	0	0	1	0	0	1	0	0	0	0	0
	0	0	0	1	0	1	1	0	1	1	1
	0	0	1	1	0	1	0	0	0	1	0
	0	1	0	0	0	1	1	0	0	0	0
	0	1	0	1	0	1	0	0	0	0	0
	0	1	1	0	1	0	0	1	0	1	1
F	0	1	1	1	1	1	0	1	0	1	0
G	1	0	0	0	1	0	0	1	0	0	1
H	1	0	0	1	1	0	0	0	0	0	1
I	1	0	1	0	0	0	0	1	1	0	1
J	1	0	1	1	1	0	1	0	0	0	1
K	1	1	0	0	1	0	1	0	0	0	1
L	1	1	0	1	1	0	0	0	0	0	1
M	1	1	1	0	1	0	0	1	0	0	1
N	1	1	1	1	1	0	1	1	0	0	1

(b) State Diagram



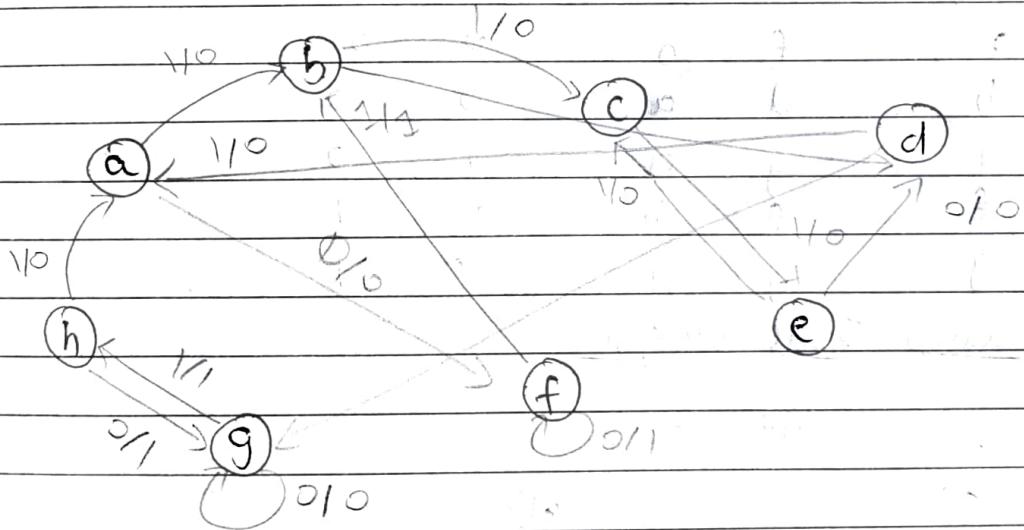
Q-7 P.S.

N. S

Output

	$x=0$	$x=1$	$x=0$	$x=1$
a	f	b	o	o
b	d	c	o	o
c	f	e	o	o
d	g	a	1	o
e	d	c	o	o
f	f	b	1	1
g	g	h	o	1
h	g	a	1	o

* State Diagram:-

* Reduced State table:-

- State d & h both go to same states & f & a have same outputs
- So, out of d & h, one can be removed.

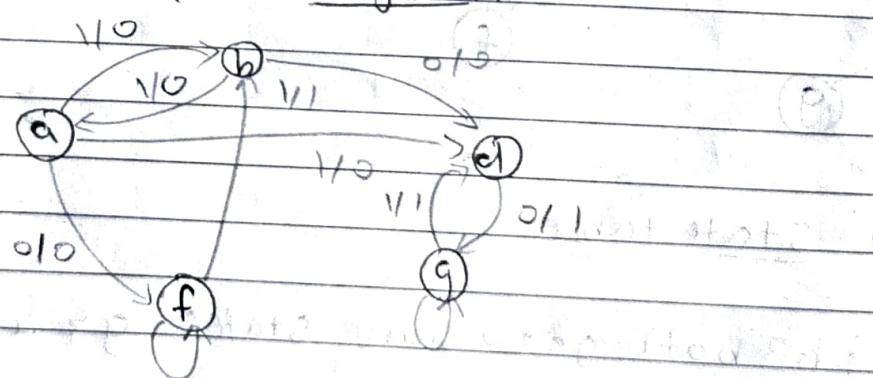
(8)

P.S.	N.S.		O/P	
	$x=0$	$x=1$	$x=0$	$x=1$
a	f	b	0	0
b	d	c	0	0
c	f	b	0	0
d	g	a	1	0
f	f	b	1	1
g	g	d	0	1

- Here, states a, c & f go to same sides & f & b also, a & c have same output of 0 for $x=0$ &
- a, c are equivalent.

P.S.	N.S.		O/P	
	$x=0$	$x=1$	$x=0$	$x=1$
a	f	b	0	0
b	d	a	0	0
c	g	a	1	0
f	f	b	1	1
g	g	d	0	1

* Reduced State Diagram: -



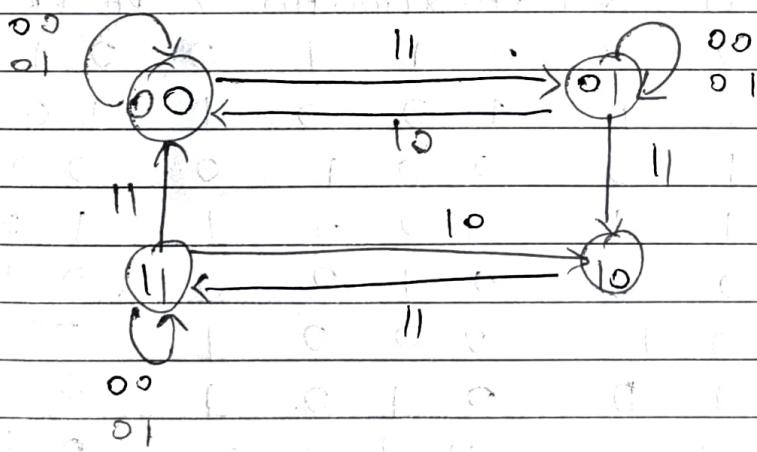
Q-8 Equations Derived from Figure:-

$$D = T \oplus Q$$

P.S.	Input	N.S.	F.F I/P
$Q(t)$	T	$Q(t+1)$	D
0	0	0	0
0	1	1	1
1	0	1	1
1	1	0	0

- Here, if input is 0, next state will be same as present state (memory).
- If input 1 then next state will be complement of present state.
- Hence, it is equivalent to T flip flop.

Q-9 State Diagrams:-



* JA :-

KA :-

EX ₀₀		01	11	10
AB	00	0	0	W
01	0	0	X	0
11	X	X	X	X
10	X	X	X	X

EX ₀₀		01	11	10
AB	00	X	X	X
01	0	X	X	X
11	0	0	1	0
10	0	0	0	1

K_B

EX ₀₀		01	11	10
AB	00	X	X	X
01	0	0	1	1
11	0	0	1	1
10	X	X	X	X

Q-10 A(t) B(t) C(t) X A(t+1) B(t+1) C(t+1) Y DA DB DC

$$\begin{array}{ccccccccccccc}
 = & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 1 & 1 \\
 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 1 & 0 & 1 & 0 \\
 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\
 & 0 & 0 & 1 & 1 & 1 & 0 & 0 & 1 & 0 & 1 & 0 \\
 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 \\
 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\
 & 0 & 1 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 1 \\
 & 0 & 1 & 1 & 1 & 1 & 0 & 1 & 0 & 0 & 1 & 0 \\
 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\
 & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 1 \\
 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 \\
 & 1 & 1 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 1 \\
 & 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 \\
 & 1 & 1 & 1 & 1 & 1 & 0 & 0 & 0 & 0 & 1 & 0
 \end{array}$$

* K-maps -

O_A :

AB		00	01	11	10
		00	1	1	0
		01	0	0	0
		11	X	X	X
		10	0	0	X

$$O_A = A'B'C'$$

O_B :

AB		00	01	11	10
		00	1	0	0
		01	1	0	1
		11	X	X	X
		10	1	1	X

$$O_B = A + C'X' + BCX$$

O_C :

AB		00	01	11	10
		00	1	0	0
		01	0	0	0
		11	X	X	X
		10	0	1	X

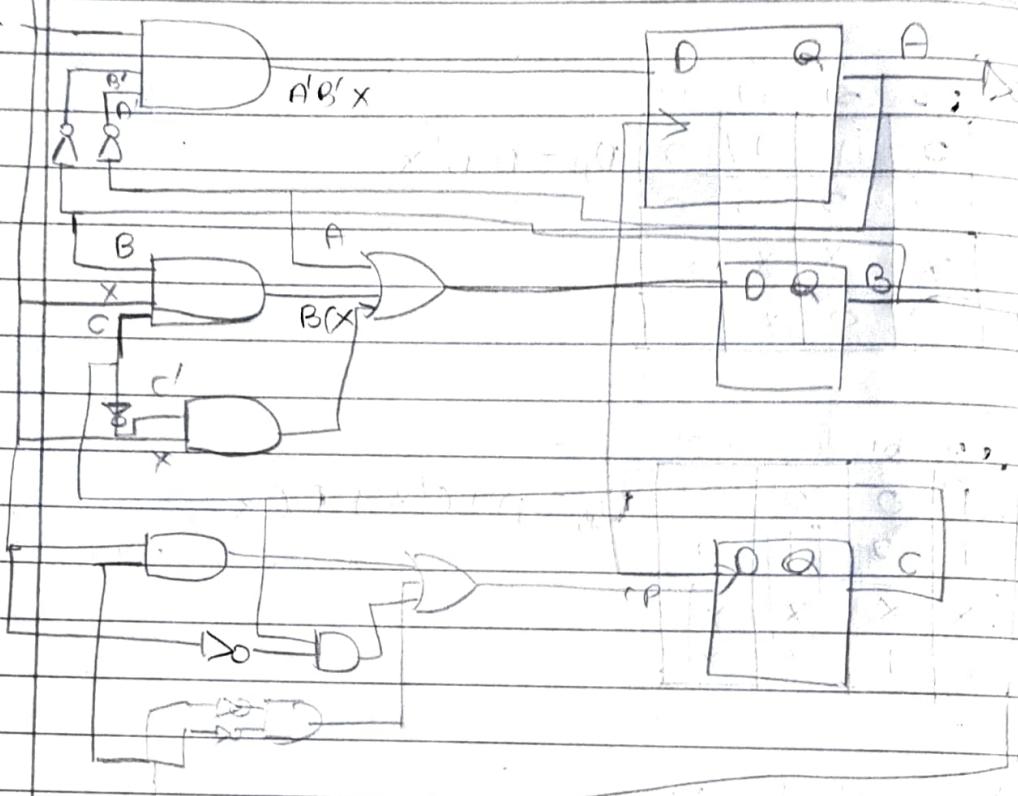
$$O_C = AX + CX' + A'B'C'$$

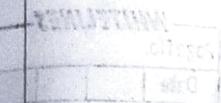
Y :

AB		00	01	11	10
		00	0	1	1
		01	0	1	1
		11	X	X	X
		10	0	0	X

$$Y = A'B'C'$$

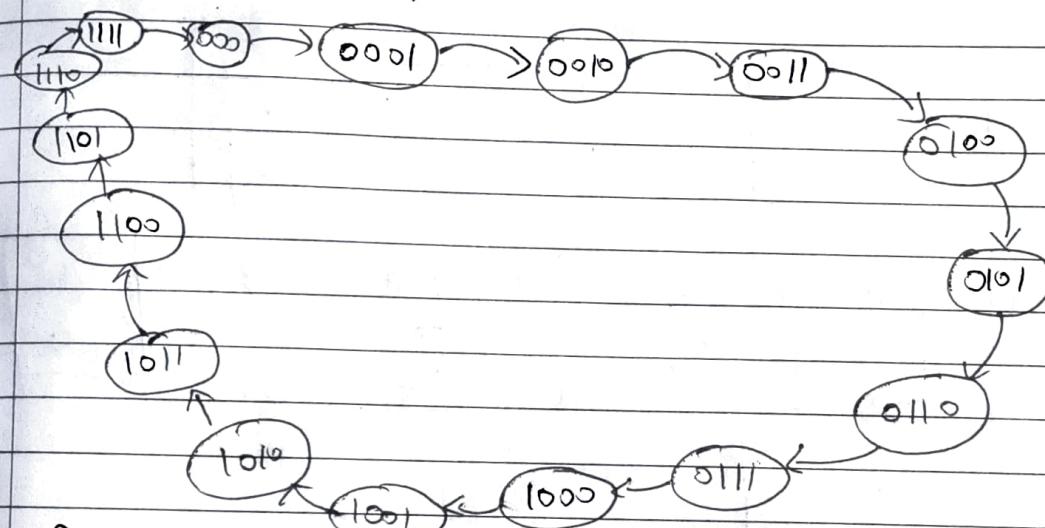
(12)





(13)

Q-11 State Diagram :-

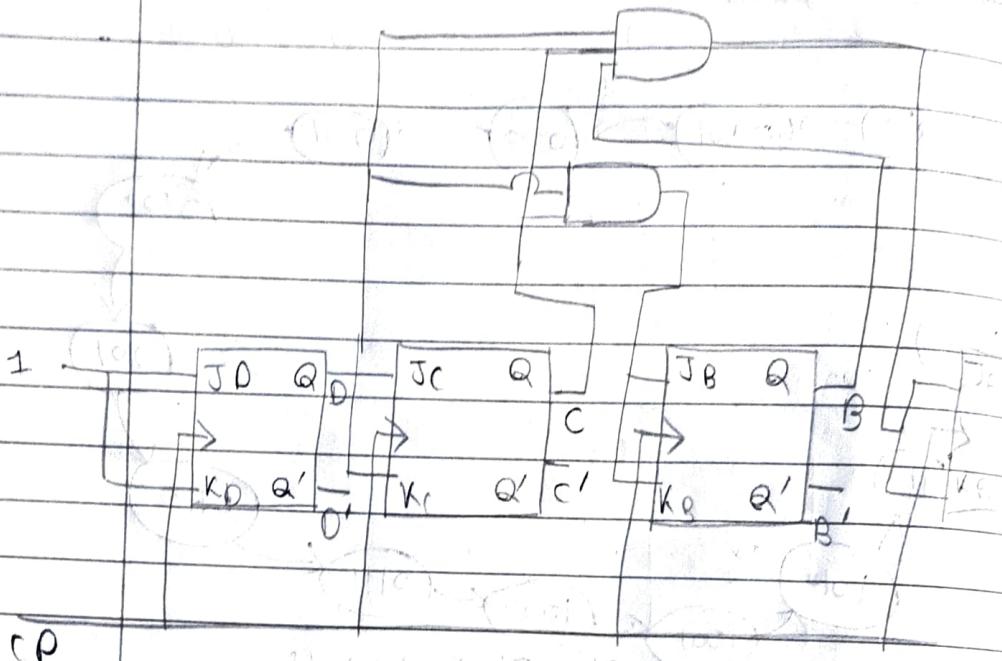


PS N.S.

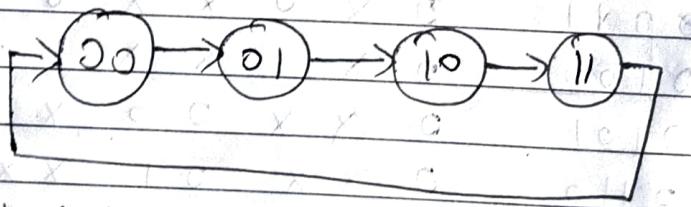
Flip Flop Inputs

A	B	C	D	A	B	C	D	J _A	K _A	J _B	K _B	J _C	K _C	J _D	K _D
0	0	0	0	0	0	0	1	0	X	0	X	0	X	1	X
0	0	0	1	0	0	1	0	0	X	0	X	1	X	X	1
0	0	1	0	0	0	1	1	0	X	0	X	X	0	1	X
0	0	1	1	0	1	0	0	0	0	X	1	X	X	1	X
0	1	0	0	0	1	0	0	0	X	X	1	X	0	0	X
0	1	0	1	0	1	1	0	0	X	X	0	1	X	X	1
0	1	1	0	0	1	1	1	0	X	X	1	X	X	X	1
0	1	1	1	0	0	0	0	1	X	X	1	X	X	X	1
1	0	0	0	1	0	0	1	X	0	X	0	X	1	X	X
1	0	0	1	1	0	1	0	X	0	X	1	X	X	1	X
1	0	1	0	1	0	1	1	X	0	X	0	X	1	X	X
1	0	1	1	1	0	1	1	X	0	X	0	X	0	1	X
1	1	0	0	0	0	0	0	X	1	X	X	1	X	X	1
1	1	0	1	0	0	0	1	X	1	X	X	1	X	X	1
1	1	1	0	0	0	0	0	X	0	X	0	X	1	X	X
1	1	1	1	0	0	0	0	X	1	X	X	0	1	X	X
1	1	1	1	1	0	0	0	X	1	X	X	1	X	X	X
1	1	1	1	1	0	0	0	X	1	X	X	1	X	X	X

(15)



Q-13 State Diagram:



State table:-

Present state	Next state	Flip Flop input
A x B x	A . B.	T A
0 1 0 0	0 1	0
0 1 1 1	1 0	1
1 0 1 1	1 1	1
1 1 1 1	0 0	0

19 DC SOA 8

17

* K-Maps:-

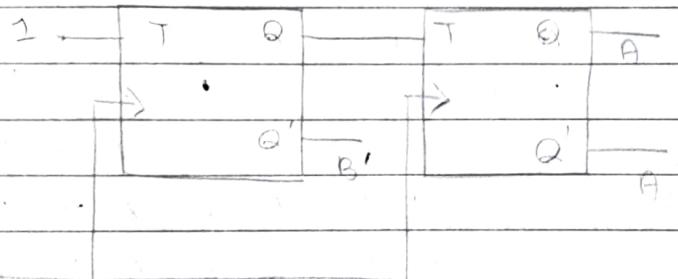
A	B	0	1
0	0	0	1
1	0	1	1

$$T_A = B$$

A	B	0	1
0	0	1	1
1	1	1	1

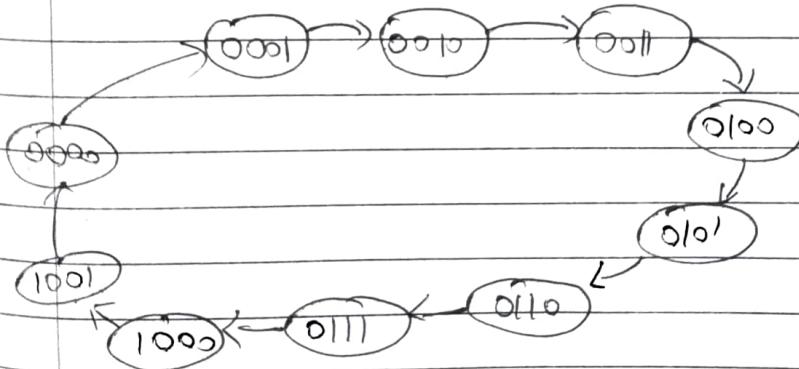
$$T_B = 1$$

* Sequential circuit



(P)

Q-14 State Diagram



* State table:-

P. S.	N. S.	FF Inputs							
A B C D	A B C D	S _A	R _A	S _B	R _B	S _C	R _C	S _D	R _D
0 0 0 0	0 0 0 1	0	X	0	X	0	X	1	0
0 0 0 1	0 0 1 0	0	X	0	X	1	0	0	1
0 0 1 0	0 0 1 1	0	X	0	X	0	X	0	1
0 0 1 1	0 1 0 0	0	X	1	0	0	X	0	1
0 1 0 0	0 1 0 1	0	X	X	0	0	X	1	0
0 1 0 1	0 1 1 0	0	X	X	0	1	0	0	1
0 1 1 0	0 1 1 1	0	X	X	0	X	0	1	0
0 1 1 1	1 0 0 0	1	0	0	1	0	1	0	1
1 0 0 0	1 0 0 1	X	0	0	X	0	X	1	0
1 0 0 1	0 0 0 0	0	1	0	X	0	X	0	1
1 0 1 0	- - - -	X	X	X	X	X	X	X	X
1 0 1 1	- - - -	X	X	X	X	X	X	X	X
1 1 0 0	- - - -	X	X	X	X	X	X	X	X
1 1 0 1	- - - -	X	X	X	X	X	X	X	X
1 1 1 0	- - - -	X	X	X	X	X	X	X	X
1 1 1 1	- - - -	X	X	X	X	X	X	X	X

* SA

		CD			
AB		00	01	11	10
00	0	0	0	0	0
01	0	0	1	0	0
11	X	X	X	X	X
10	X	0	X	X	X

$$S_A = BCD$$

RA

		CD			
AB		00	01	11	10
00	X	X	X	X	X
01	X	X	0	X	X
11	X	X	X	X	X
10	0	1	X	X	X

$$R_A = C'D$$

		CD			
AB		00	01	11	10
00	0	0	1	0	0
01	X	X	0	X	X
11	X	X	X	X	X
10	0	0	X	X	X

		CD			
AB		00	01	11	10
00	X	X	0	X	X
01	0	0	1	0	0
11	X	X	X	X	X
10	X	X	X	X	X

		CD			
AB		00	01	11	10
00	0	1	0	X	X
01	0	1	0	X	X
11	X	X	X	X	X
10	0	0	X	X	X

$$S_C = A' C' D$$

		CD			
AB		00	01	11	10
00	X	0	1	0	0
01	X	0	1	0	0
11	X	X	X	X	X
10	X	X	X	X	X

$$R_C = CD$$

		CD			
AB		00	01	11	10
00	1	0	0	1	1
01	1	0	0	1	1
11	X	X	X	X	X
10	1	0	X	X	X

$$S_D = D'$$

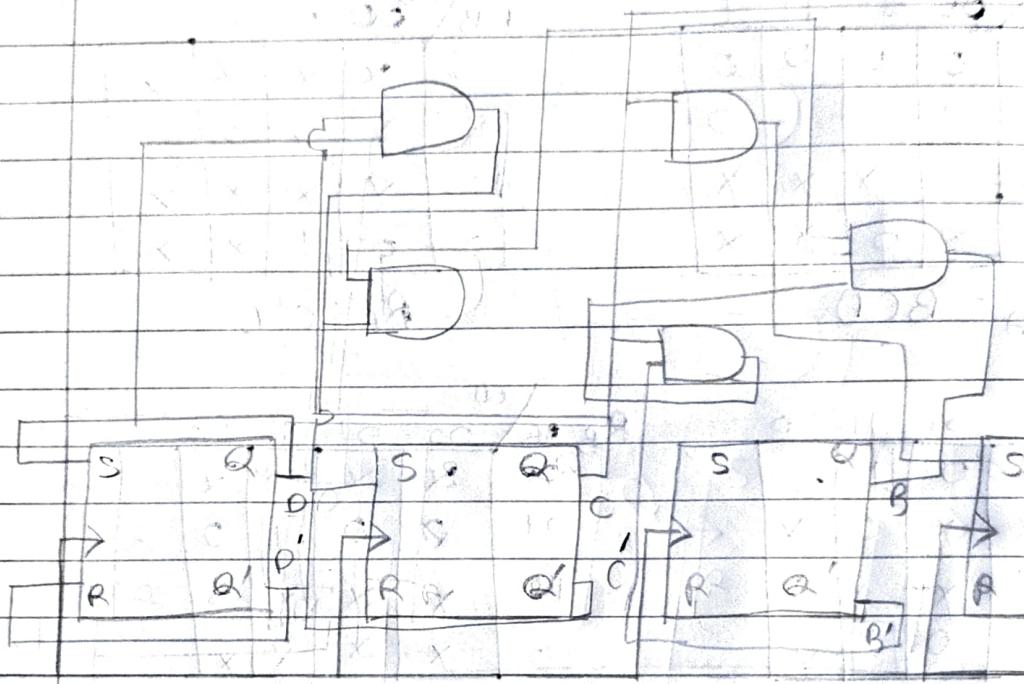
		CD			
AB		00	01	11	10
00	0	1	1	0	0
01	0	1	1	0	0
11	X	X	X	X	X
10	0	1	X	X	X

$$R_D = 0$$

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Date _____

(29)



CP

Teacher's Signature _____