

SMD Version  
V1 - Initial design

Title		
CPLD Tester SMD Version		
Size	Number	Revision
A4		V1
Date:	17/11/2019	Sheet of
File:	C:\Users\.\CPLDTester.SchDoc	Drawn By:

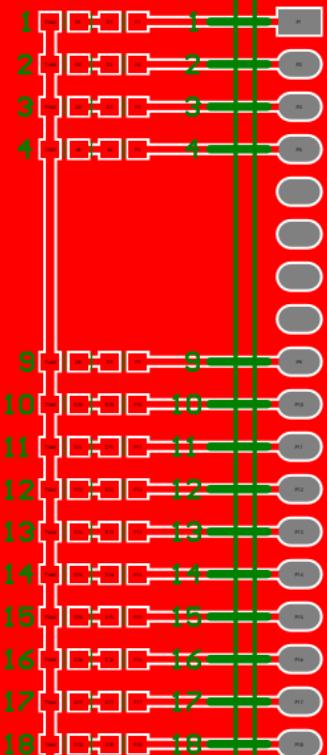
Tester

CPU

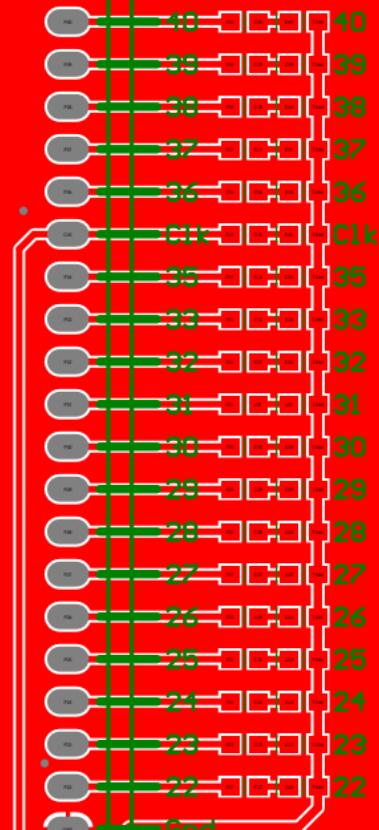
Memory

1

S1



JLCJLCJLCJLC



Gnd Vdd TVdd

Power SW1

Clock

R2

P1

U1

U2

R3

D5

C4

R3

D5

C4

R3

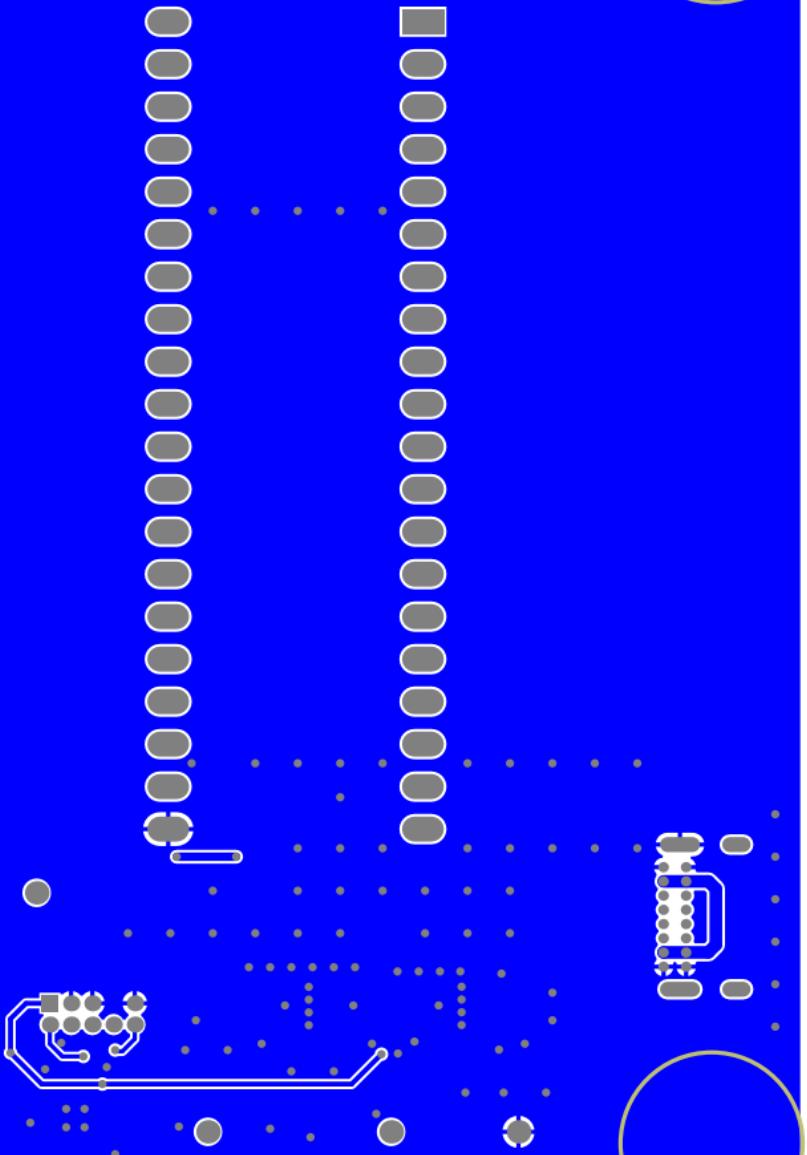
D5

C4

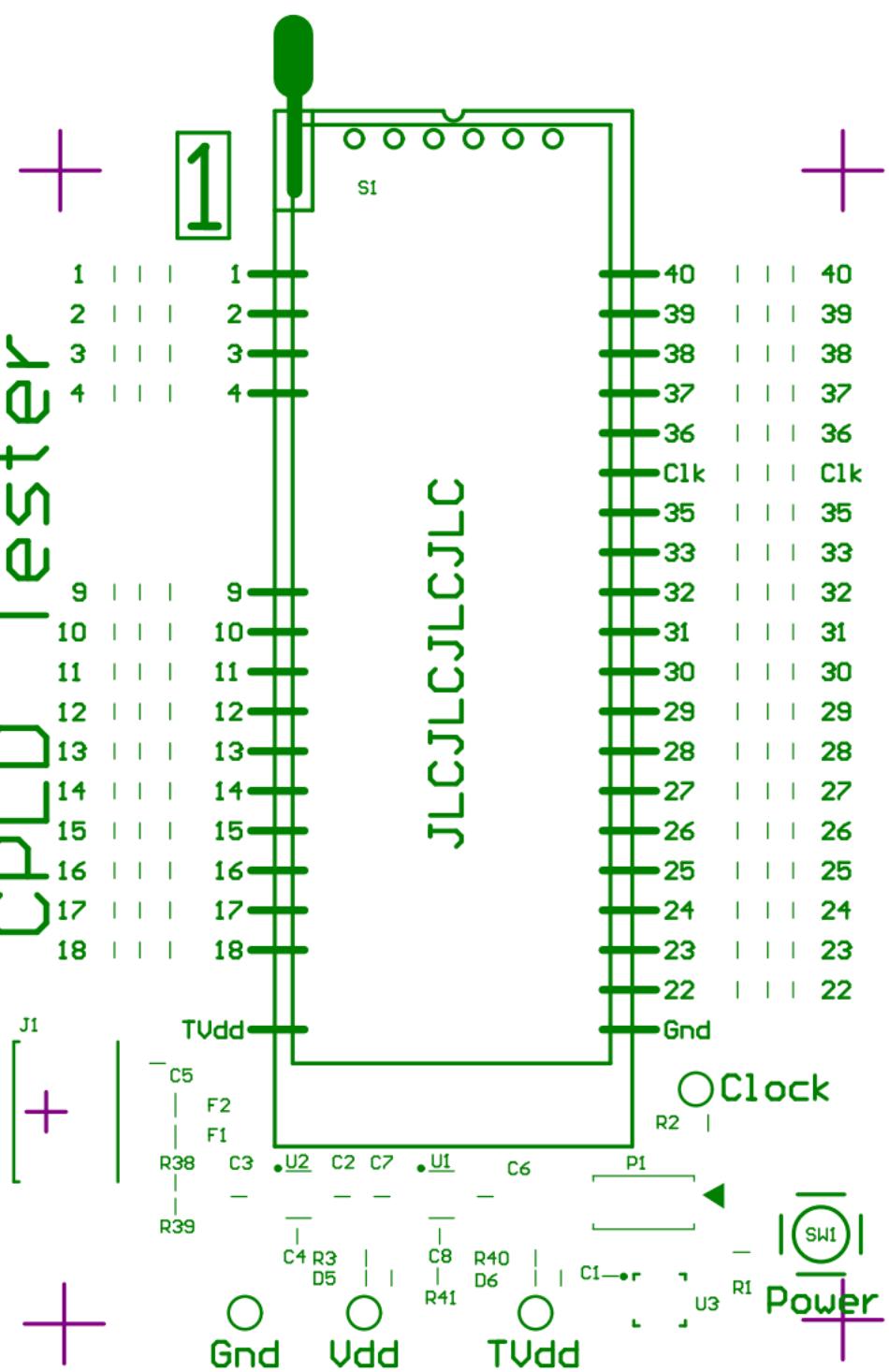
R3

D5

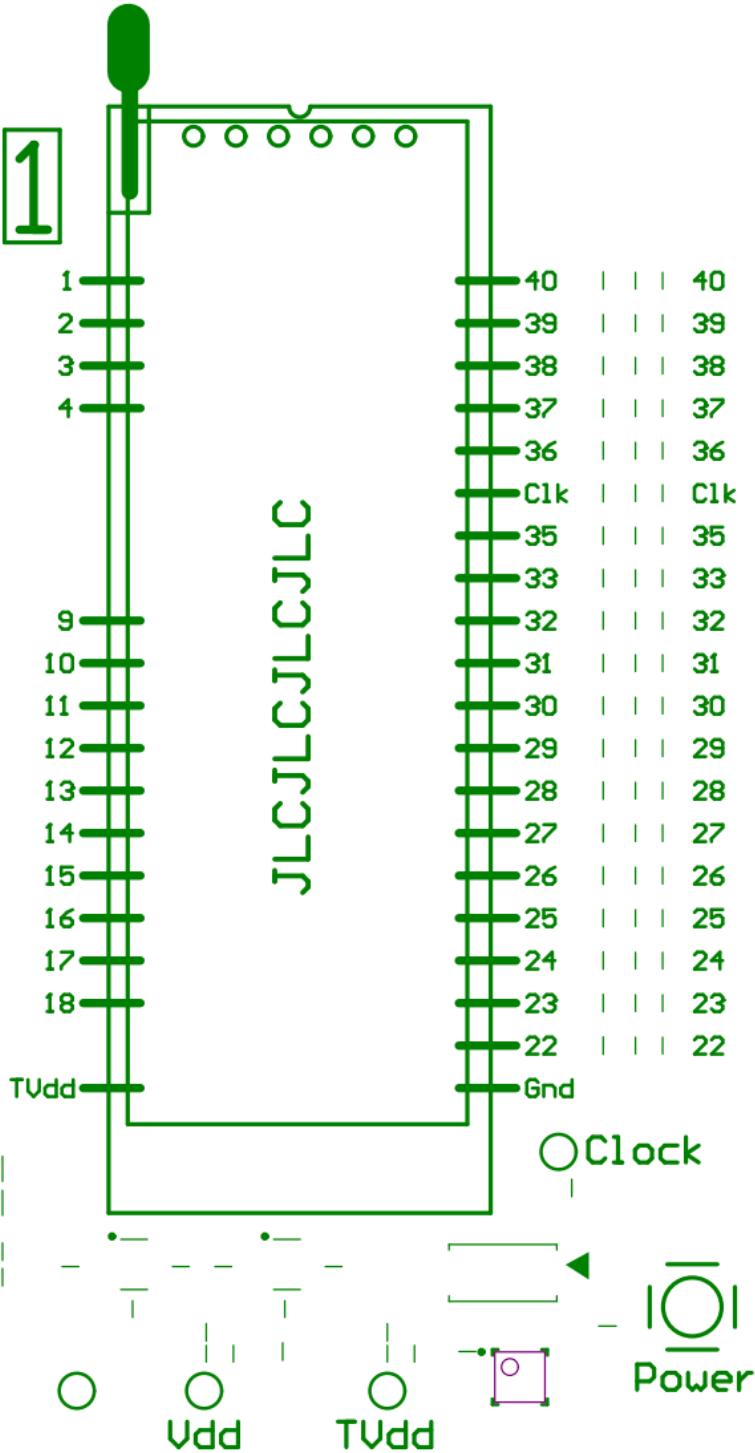
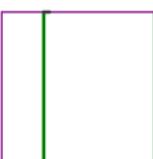
CPLD Tester SMD Version  
Version = V1

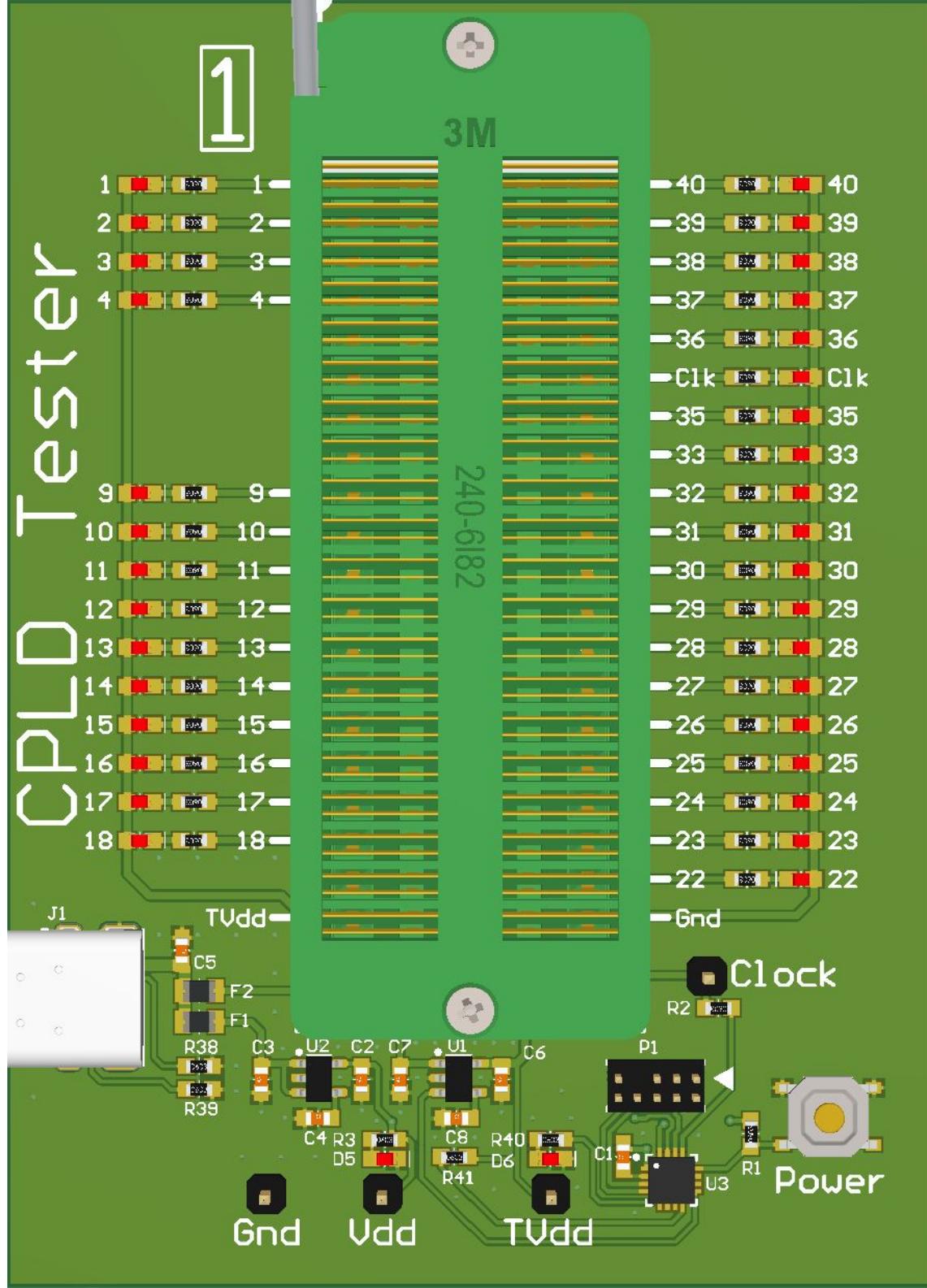


# CPLD Tester



# CPLD Tester







# Bill of Materials

Bill of Materials for Project [C]

Source Data From:

CPLDTester.PriPCB

Project:

CPLDTester.PriPCB

Variant:

None

Creation Date:

17/11/2019

10:59 PM

Print Date:

17-Nov-19

10:59:41 PM

Footprint	Comment	Farnell PN	Digikey	LCSC
CCNP1608-0603	2.2uF			C23630
CCNP1608-0603	22nF			C21122
CCNP1608-0603	100nF	1186187, [NoValue]		C14663
LED1608-0603-RED	LED Red	1581239		C2286
QFN-16	MKL03Z8VFG4			C159845
Bumper_RBS-2	Plastic Bumper		RPC4821-ND	
CR1608-0603	1K			C21190
CR1608-0603	5.1K			C22126