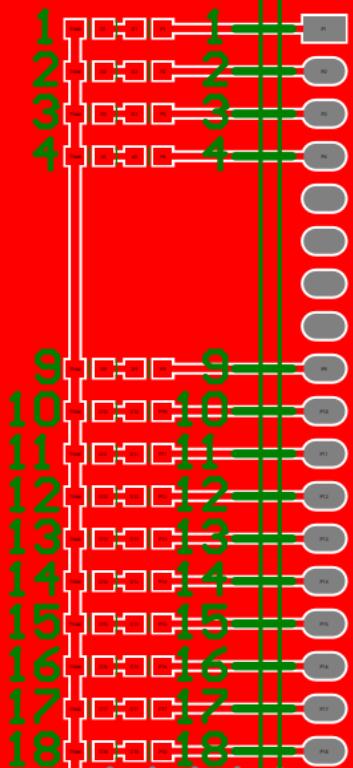
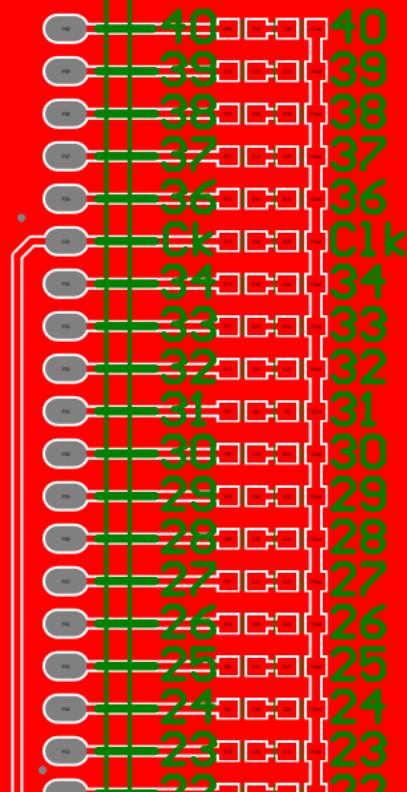


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Size	Number	Revision
A4		V1.1
Date:	18/11/2019	Sheet of
File:	C:\Users...\CPLDTester.SchDoc	Drawn By:

1



JLC JLC JLC JLC



39 38 37 36

Ck 34 33 32

31 30 29 28

27 26 25 24

23 22 Gnd

Clock

TUdd

Time

R2

C5

F2

F1

P28

C3

U2

S2

Q2

B1

Q1

Q6

Q5

Q4

R3

D5

R39

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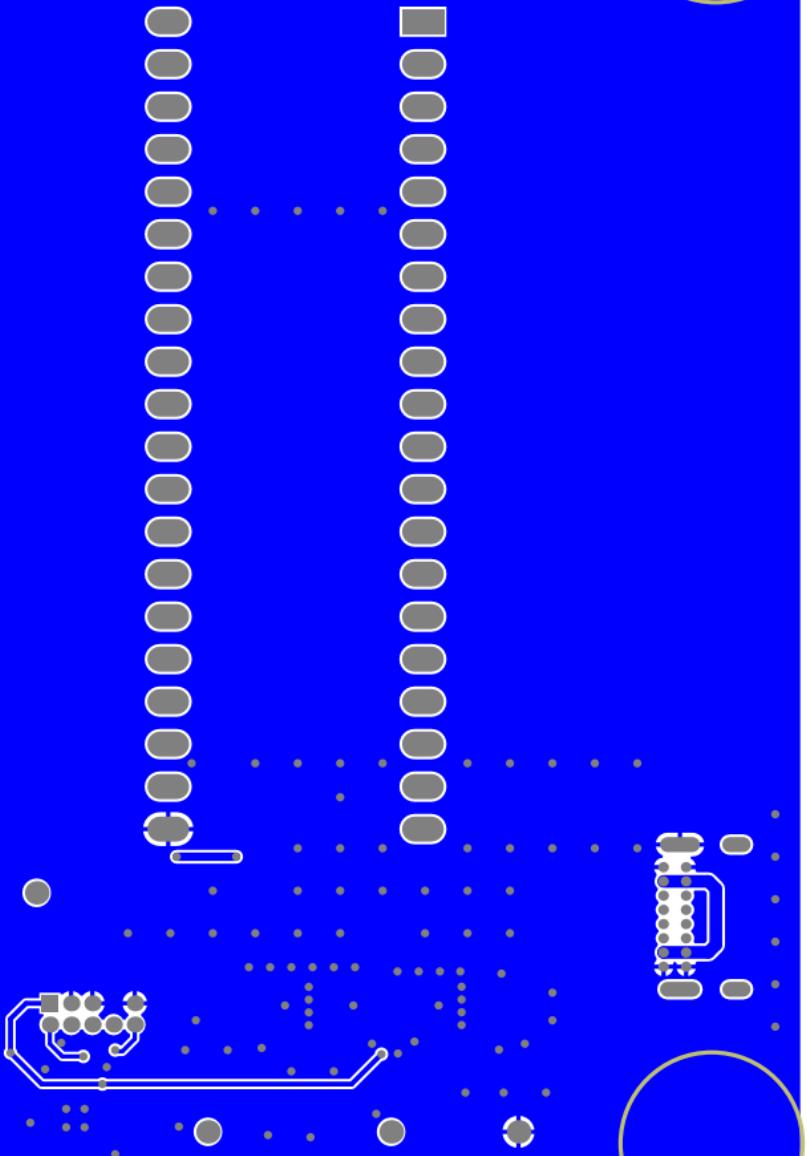
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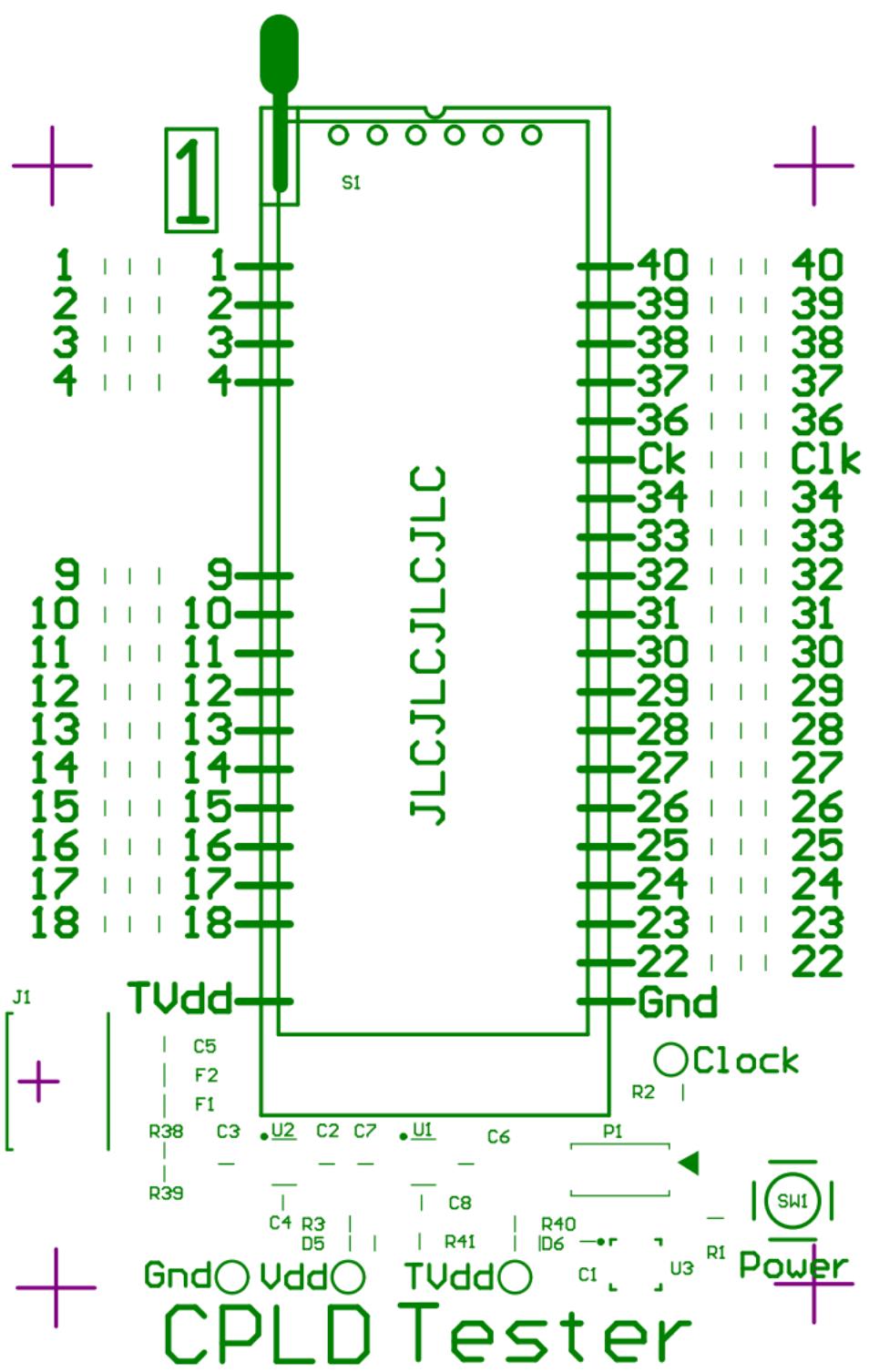
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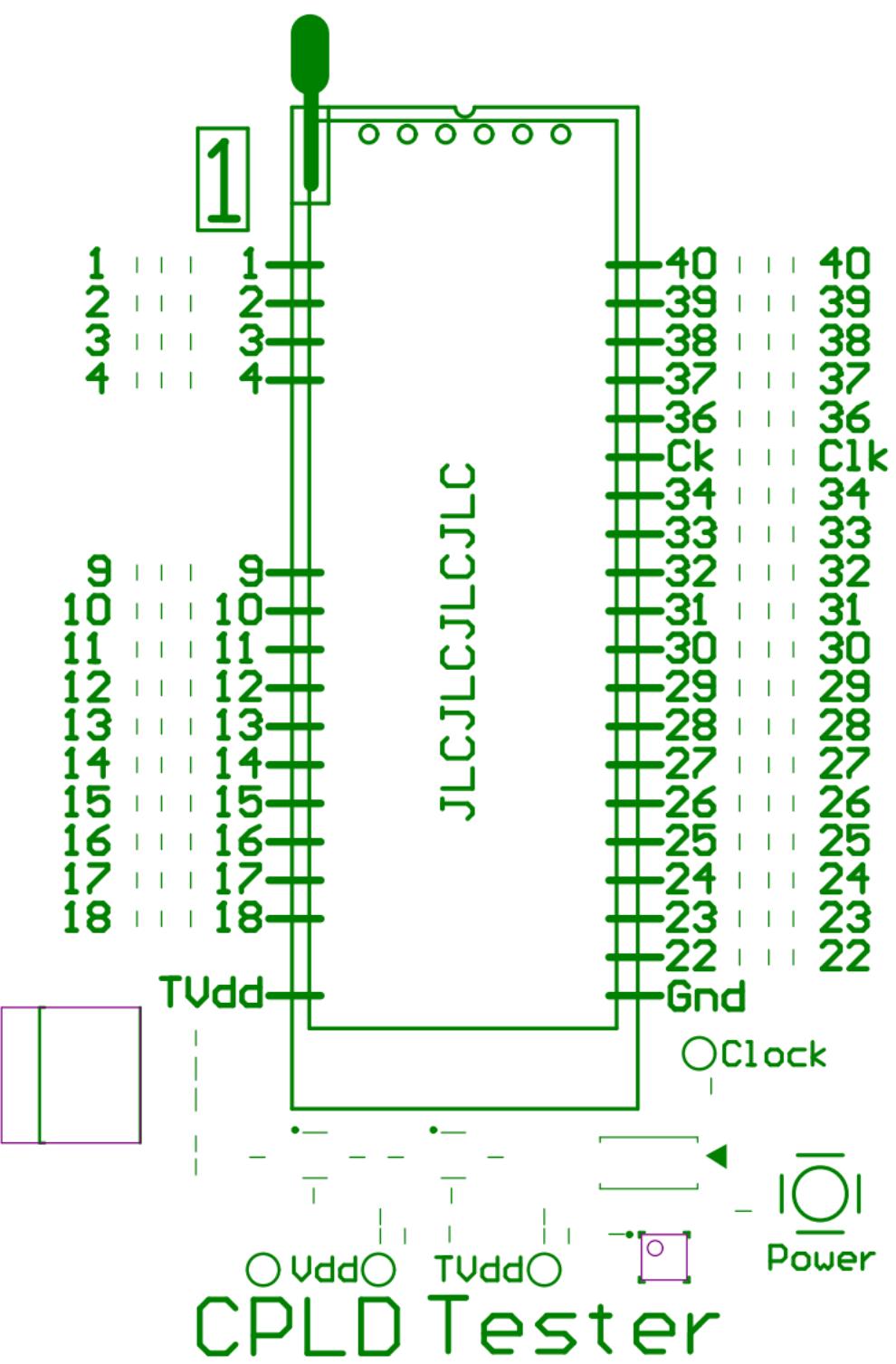
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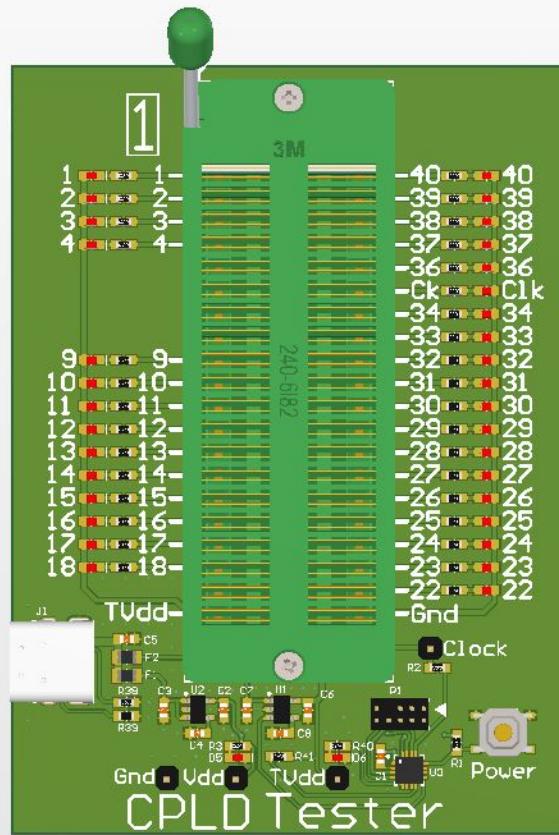
R39

CPLD Tester SMD Version
Version = V1.1









Bill of Materials

Bill of Materials for Project [C]

Source Data From:

CPLDTester.PriPCB

Project:

CPLDTester.PriPCB

Variant:

None

Creation Date:

18/11/2019

9:58 PM

Print Date:

18-Nov-19

9:58:30 PM

Footprint	Comment	Farnell PN	Digikey	LCSC
CCNP1608-0603	2.2uF			C23630
CCNP1608-0603	22nF			C21122
CCNP1608-0603	100nF	1186187, [NoValue]		C14663
LED1608-0603-RED	LED Red	1581239		C2286
QFN-16	MKL03Z8VFG4			C159845
Bumper_RBS-2	Plastic Bumper		RPC4821-ND	
CR1608-0603	1K			C21190
CR1608-0603	5.1K			C22126