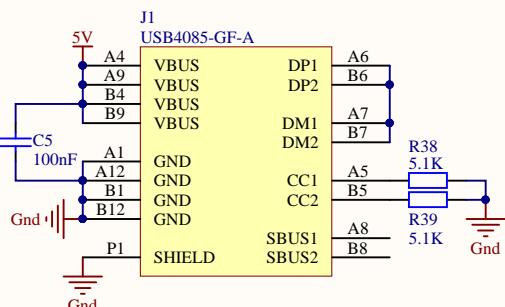
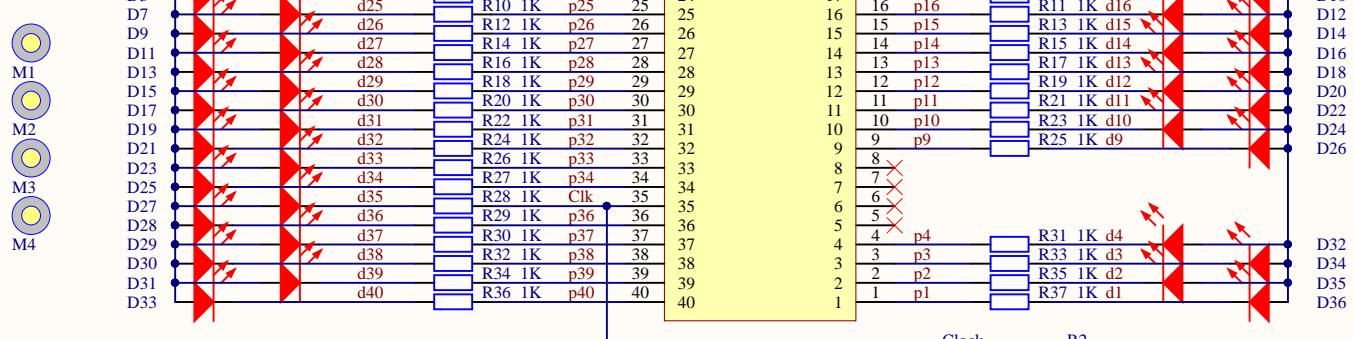


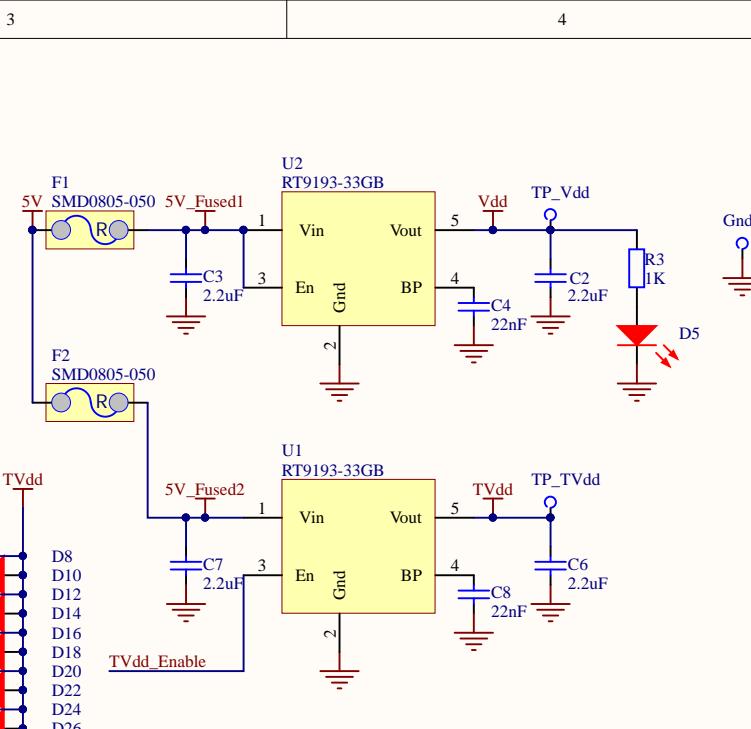
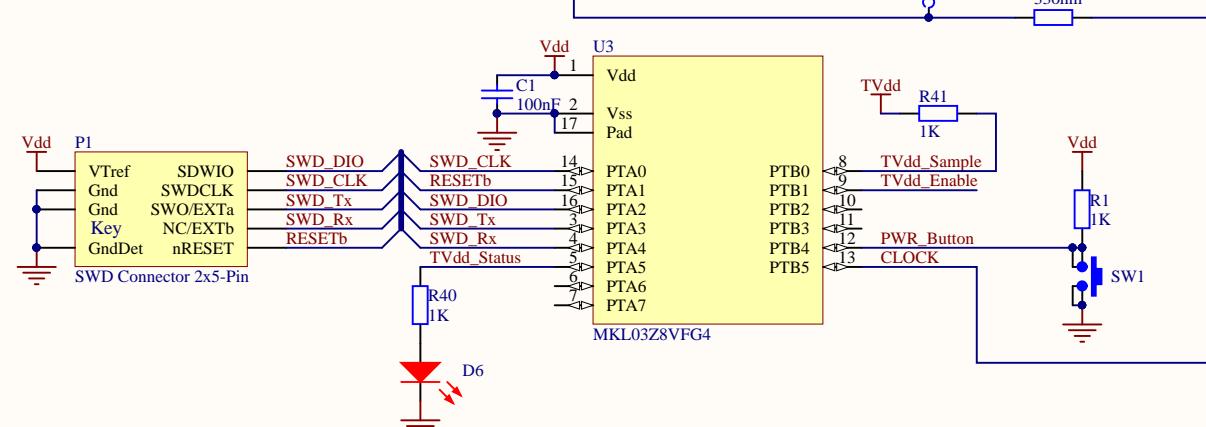
A



B



M1
M2
M3
M4



SMD Version
V1 - Initial design

Title CPLD Tester SMD Version		
Size	Number	Revision
A4		V1
Date:	11/11/2019	Sheet of
File:	C:\Users...\CPLDTester.SchDoc	Drawn By:

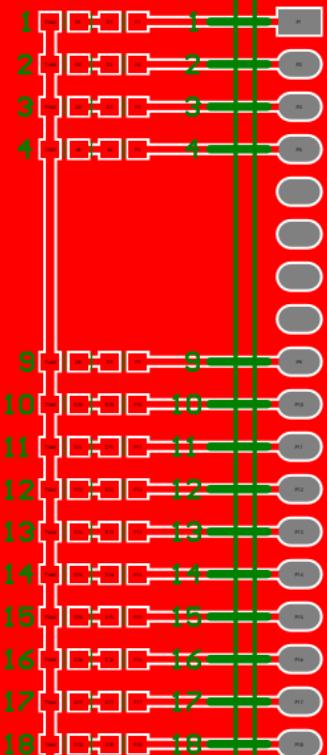
Tester

CPU

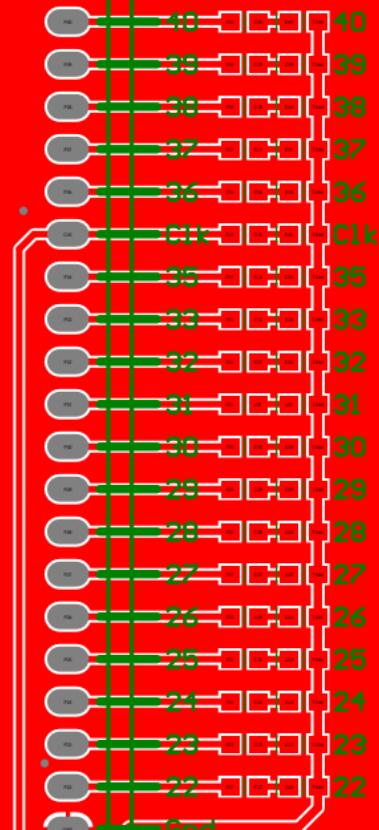
Memory

1

S1



JLCJLCJLCJLC



Gnd Vdd TVdd

Power SW1

Clock

R2

P1

U1

U2

R3

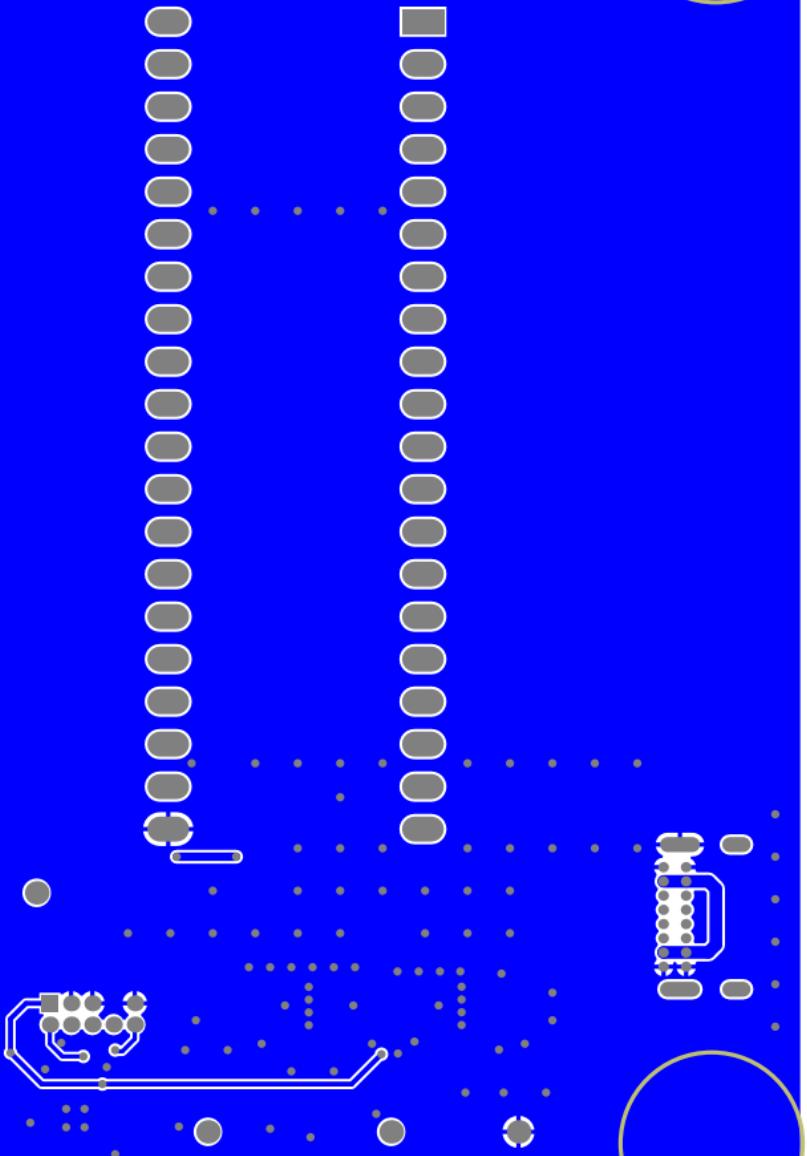
D5

C4

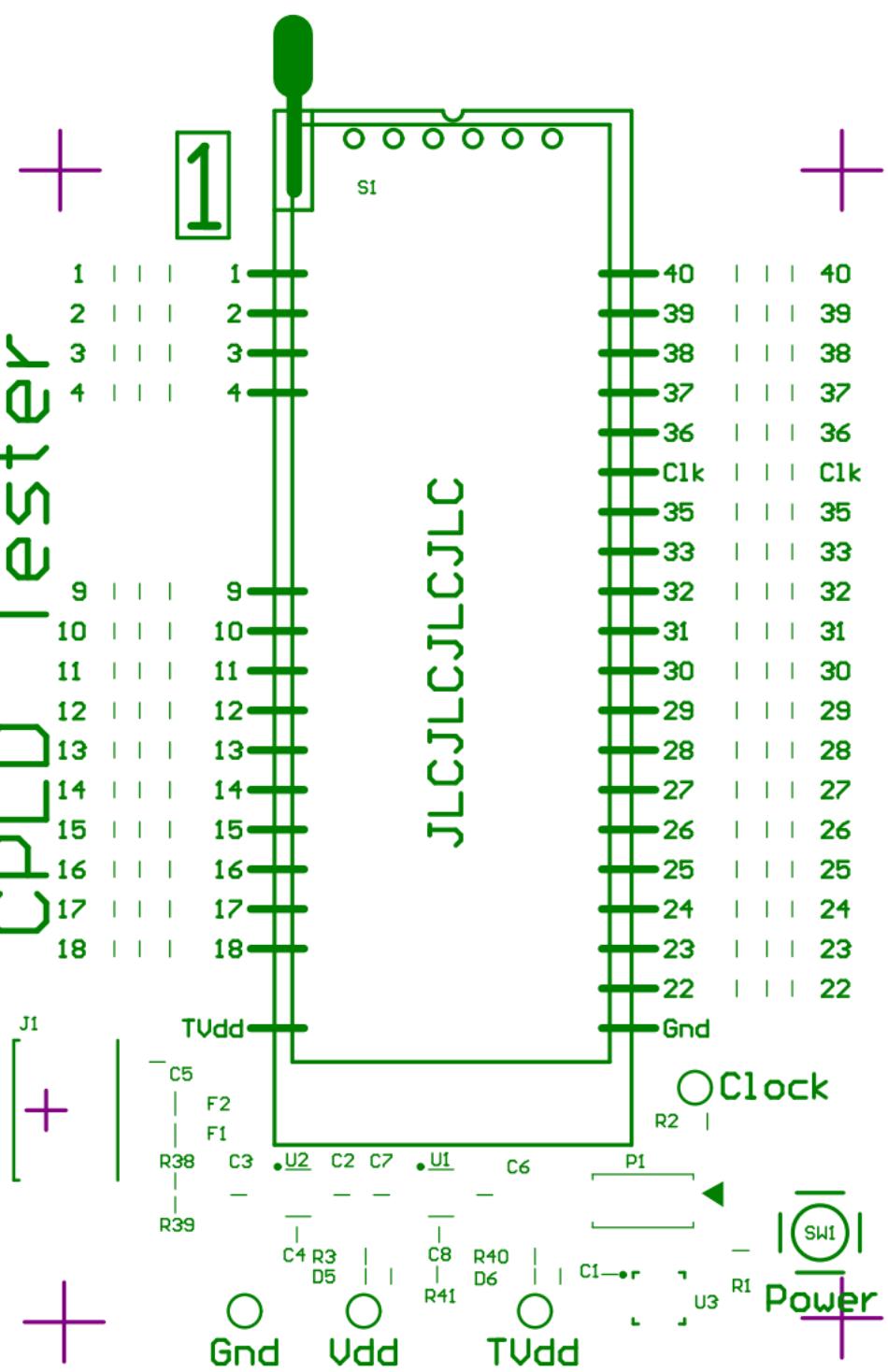
R3

D5

CPLD Tester SMD Version
Version = V1



CPLD Tester



CPLD Tester

