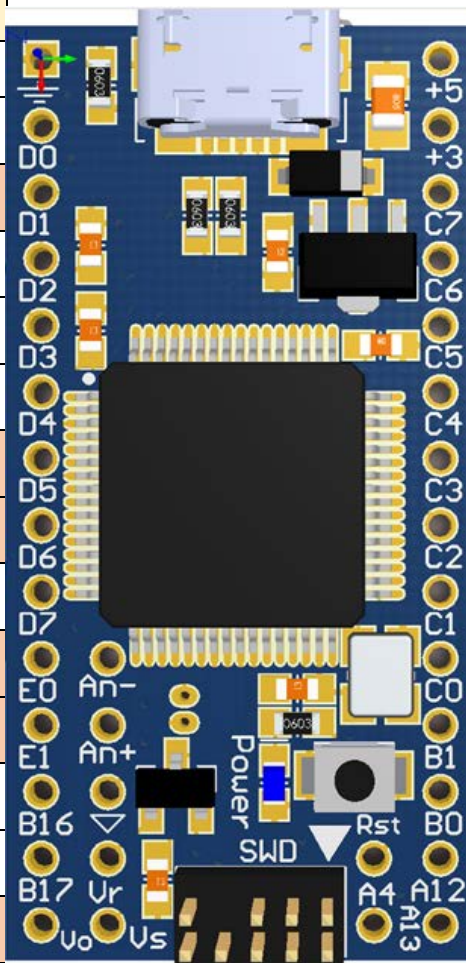


MUX(7)	MUX(6)	MUX(4)	MUX(3)	MUX(2)	MUX(1)	MUX(0)
						Gnd
	LPUART0_RTS	FTM3_CH0	UART2_RTS	SPI0_PCS0	PTD0 LLWU_P12	
	LPUART0_CTS	FTM3_CH1	UART2_CTS	SPI0_SCK	PTD1 ADC0_SE5b	
I2C0_SCL	LPUART0_RX	FTM3_CH2	UART2_RX	SPI0_SOUT	PTD2 LLWU_P13	
I2C0_SDA	LPUART0_TX	FTM3_CH3	UART2_TX	SPI0_SIN	PTD3	
SPI1_PCS0	EWM_IN	FTM0_CH4	UART0_RTS	SPI0_PCS1	PTD4 LLWU_P14	
SPI1_SCK	EWM_OUT	FTM0_CH5	UART0_CTS	SPI0_PCS2	PTD5 ADC0_SE6b	
SPI1_SOUT	FTM0_FLT0	FTM0_CH6	UART0_RX	SPI0_PCS3	PTD6 LLWU_P15	ADC0_SE7b
SPI1_SIN	FTM0_FLT1	FTM0_CH7	UART0_TX		PTD7	
RTC_CLKOUT	I2C1_SDA		UART1_TX	SPI1_PCS1	PTE0 CLKOUT32K	ADC1_SE4a
SPI1_SIN	I2C1_SCL		UART1_RX	SPI1_SOUT	PTE1 LLWU_P0	ADC1_SE5a
	EWM_IN	FTM_CLKIN0	UART0_RX	SPI1_SOUT	PTB16	
	EWM_OUT	FTM_CLKIN1	UART0_TX	SPI1_SIN	PTB17	
			DAC0_OUT/CMP1_IN3/ADC0_SE23			



MUX(0)	MUX(1)	MUX(2)	MUX(3)	MUX(4)	MUX(6)	MUX(7)
5V						
Vdd = 3.3V						
CMP0_IN1	PTC7	SPI0_SIN	USB_SOF_OUT	I2S0_RX_FS		
CMP0_IN0	PTC6 LLWU_P10	SPI0_SOUT	PDB0_EXTRG	I2S0_RXCLK	I2S0_MCLK	
	PTC5 LLWU_P9	SPI0_SCK	LPTMR0_ALT2	I2S0_RXD0	CMP0_OUT	FTM0_CH2
	PTC4 LLWU_P8	SPI0_PCS0	UART1_TX	FTM0_CH3	CMP1_OUT	LPUART0_TX
CMP1_IN1	PTC3 LLWU_P7	SPI0_PCS1	UART1_RX	FTM0_CH2	I2S0_TXCLK	LPUART0_RX
ADC0_SE4b CMP1_IN0	PTC2	SPI0_PCS2	UART1_CTS	FTM0_CH1	I2S0_TX_FS	LPUART0_CTS
ADC0_SE15	PTC1 LLWU_P6	SPI0_PCS3	UART1_RTS	FTM0_CH0	I2S0_TXD0	LPUART0_RTS
ADC0_SE14	PTC0	SPI0_PCS4	PDB0_EXTRG	USB_SOF_OUT		
ADCx_SE9	PTB1	I2C0_SDA	FTM1_CH1		FTM1_QD_PHB	
ADCx_SE8	PTB0 LLWU_P5	I2C0_SCL	FTM1_CH0		FTM1_QD_PHA	
	PTA12		FTM1_CH0		I2S0_TXD0	FTM1_QD_PHA
	PTA13 LLWU_P4		FTM1_CH1		I2S0_TX_FS	FTM1_QD_PHB