

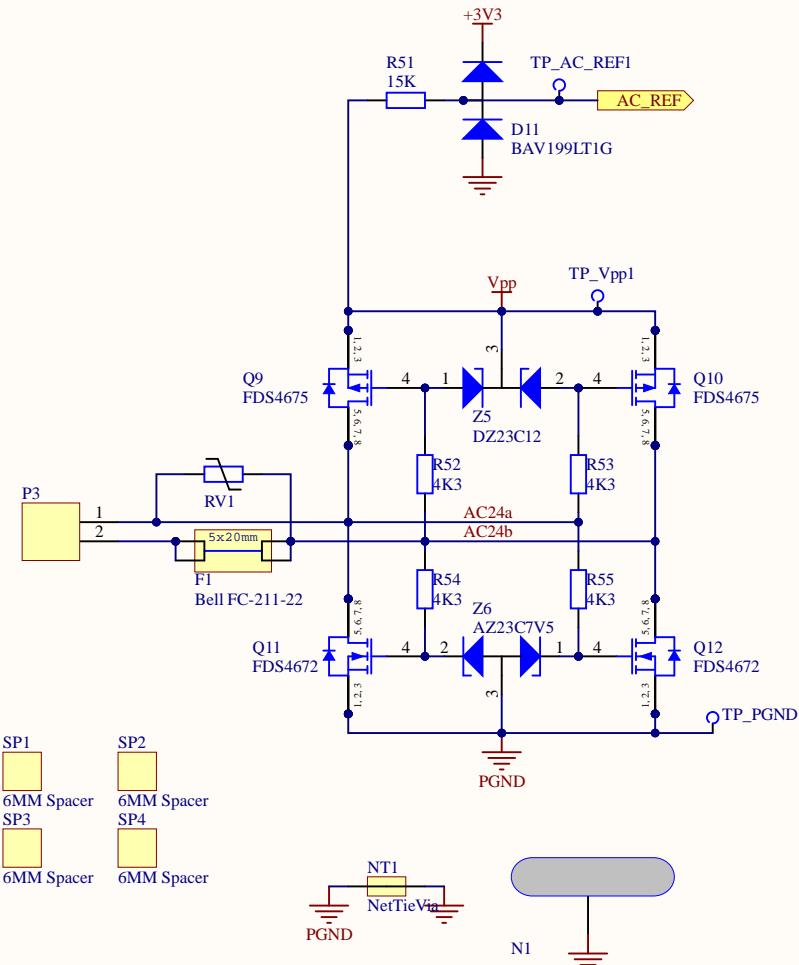
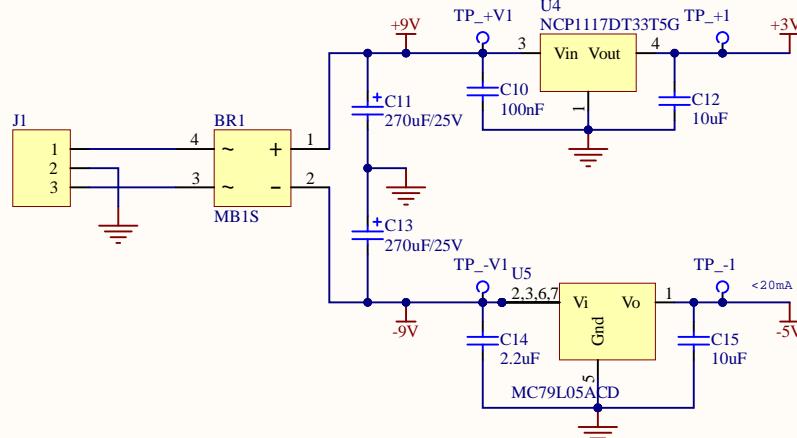
$$V_{oc} = (I_1 + I_2)/2 \times 0.05R \times (51K + 10K)/10K \\ = .153(I_1 + I_2)$$

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- Initial release
- Added Setback
- Changed current source IC etc.
- Added voltage reference for ADC

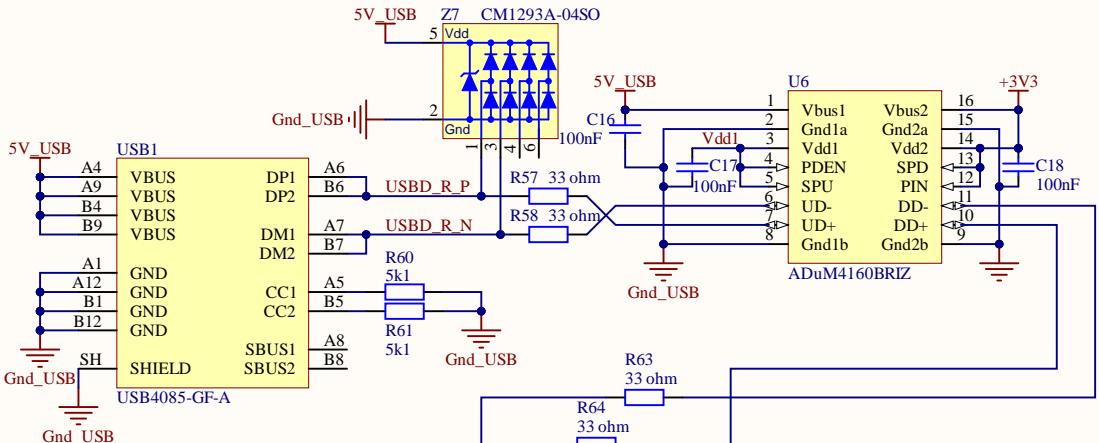
V3

- Board Dimensions altered
- Simplified NTC cct. (linearity not required)
- Removed unused components
- Isolated thermocouple
- Isolated USB
- Prevented TC amp saturating
- Power LEDs directly driven
- Overcurrent directly measured
- Decoupled 3V3 supplies (noise from OLED)

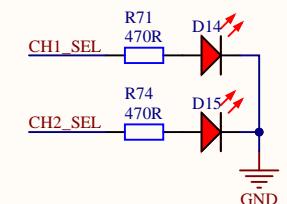
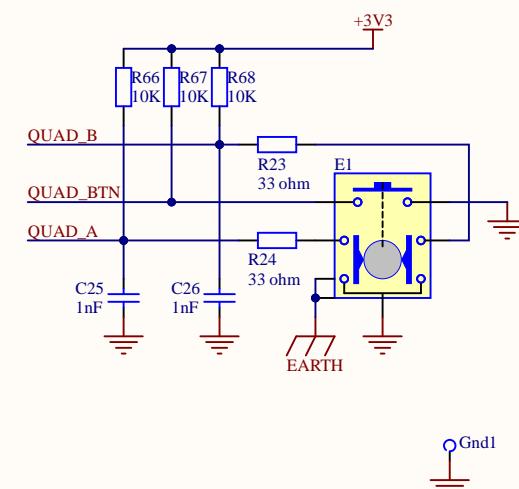
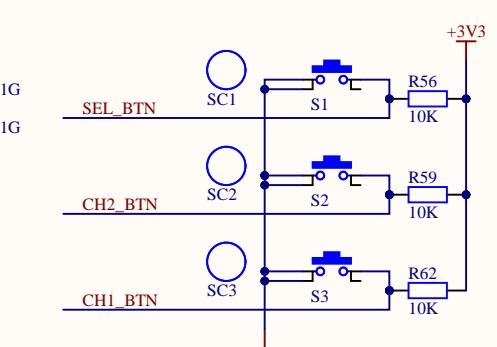
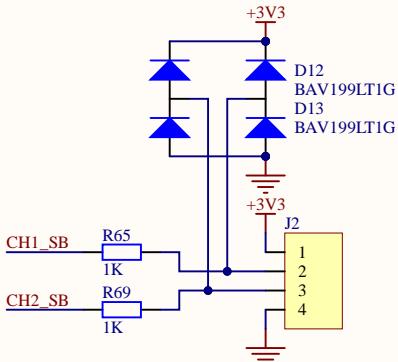
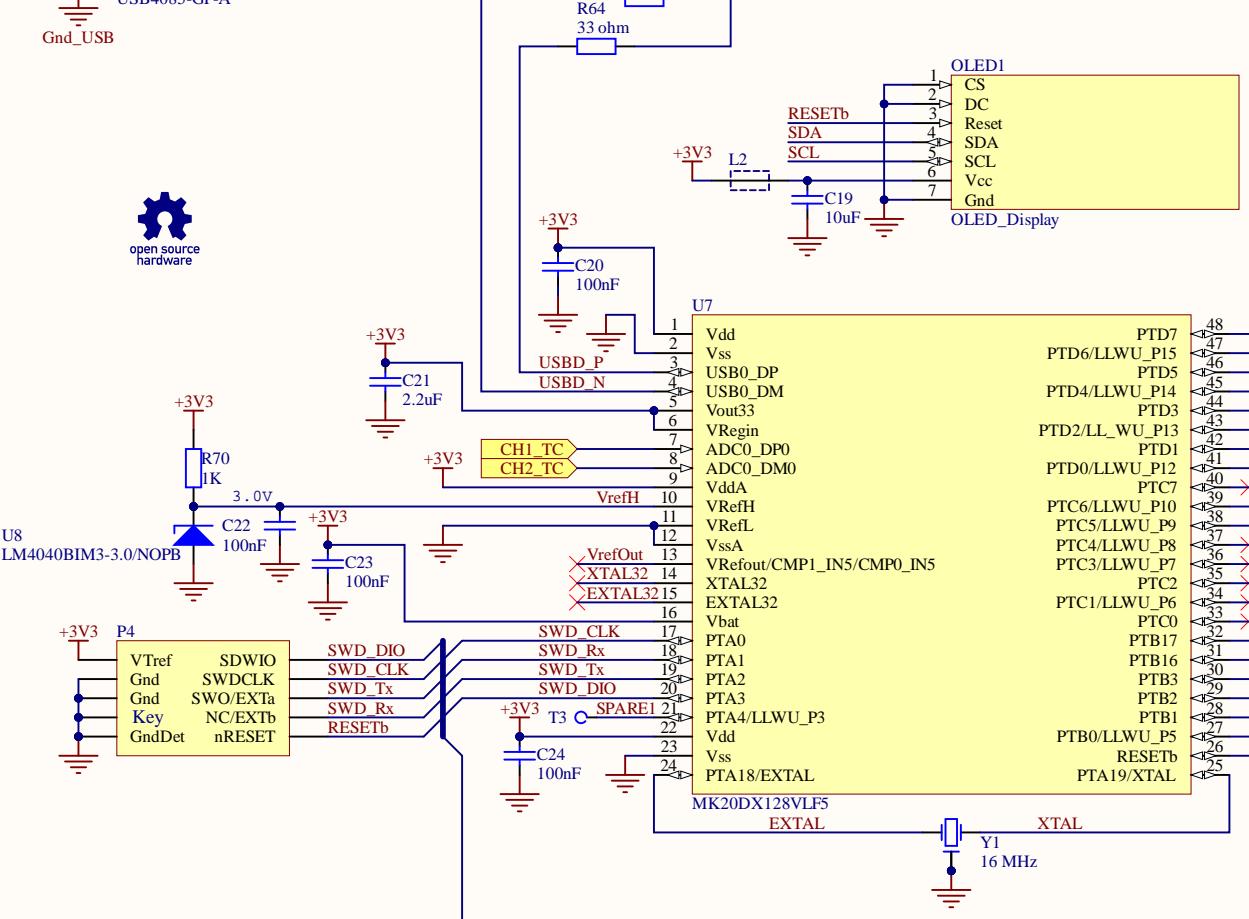


Title SolderingStation\_Power SchDoc

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## open source hardware



Title	SolderingStation_Processor.SchDoc	JLC_Serial_Number
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A4		3
Date:	7/13/2021	Sheet of
File:	Z:\Documents\_\SolderingStation_Processor.SchDoc	DrSwLBy:

