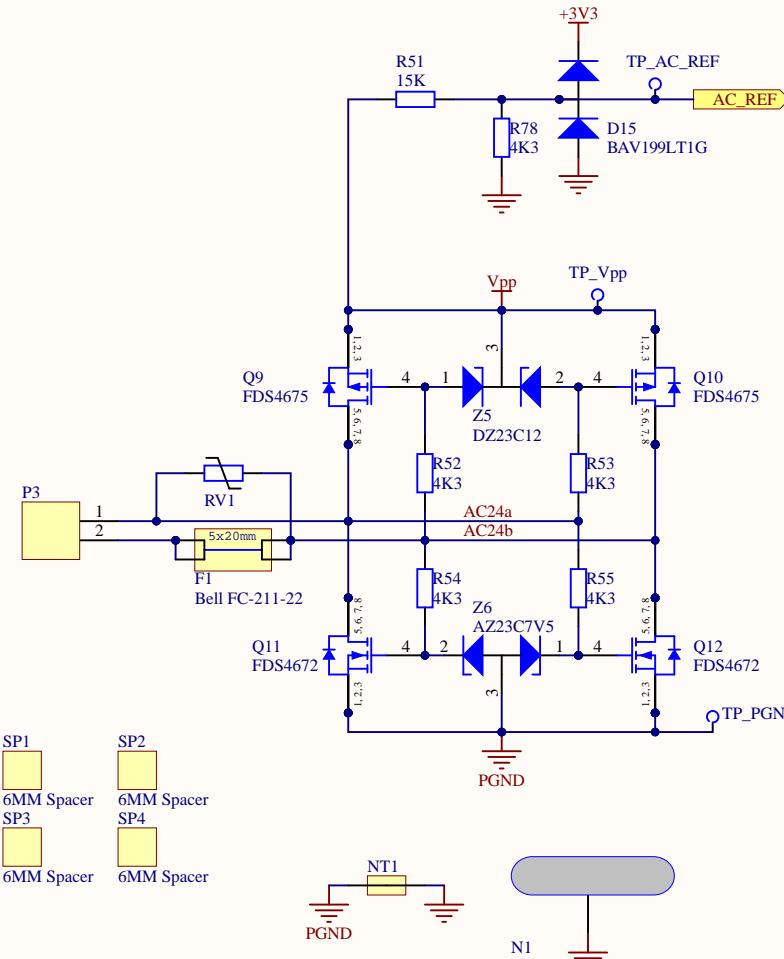
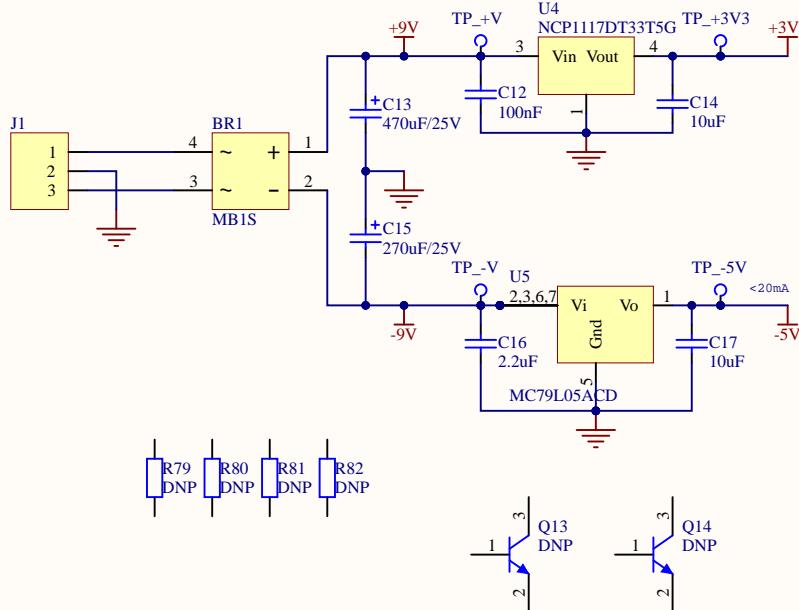


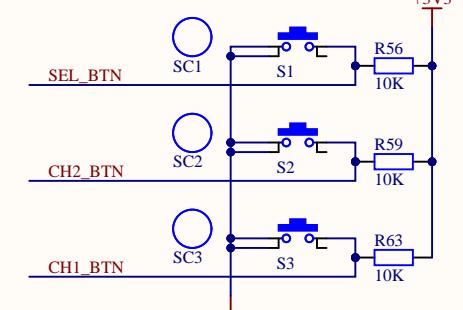
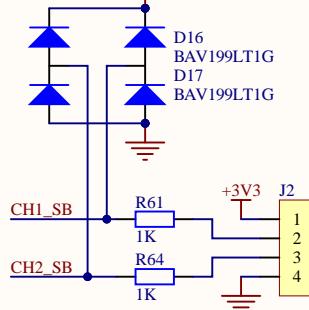
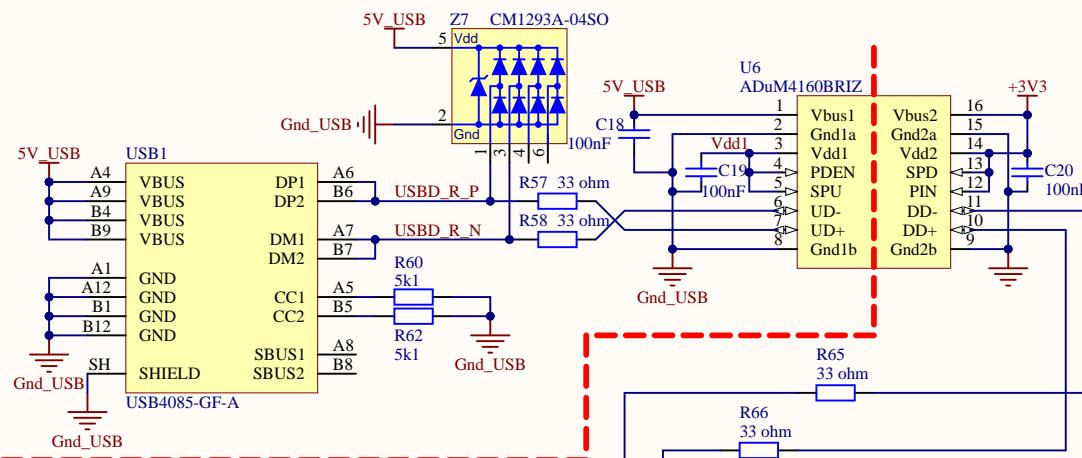
V1
 - Initial release
 V2
 - Added Setback
 - Changed current source IC etc.
 - Added voltage reference for ADC
 V3
 - Board Dimensions altered
 - Simplified NTC cct. (linearity not required)
 - Removed unused components
 - Isolated thermocouple
 - Isolated USB
 - Prevented TC amp saturating
 - Power LEDs directly driven
 - Overcurrent directly measured
 - Decoupled 3V3 supplies (noise from OLED)



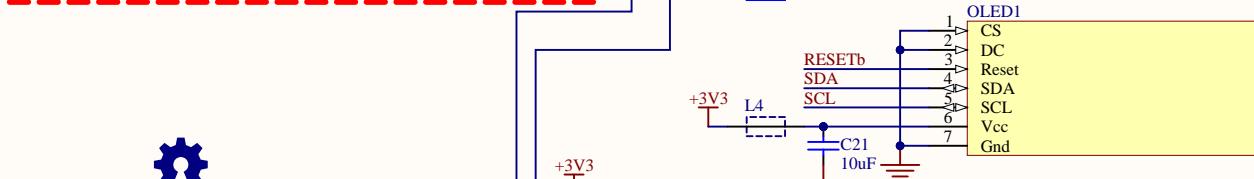
Title
SolderingStation_Power.SchDoc

Size	Number	Revision
A4		3
Date:	7/19/2021	Sheet of
File:	Z:\Documents\...\SolderingStation_Power.SchDoc	Created By:

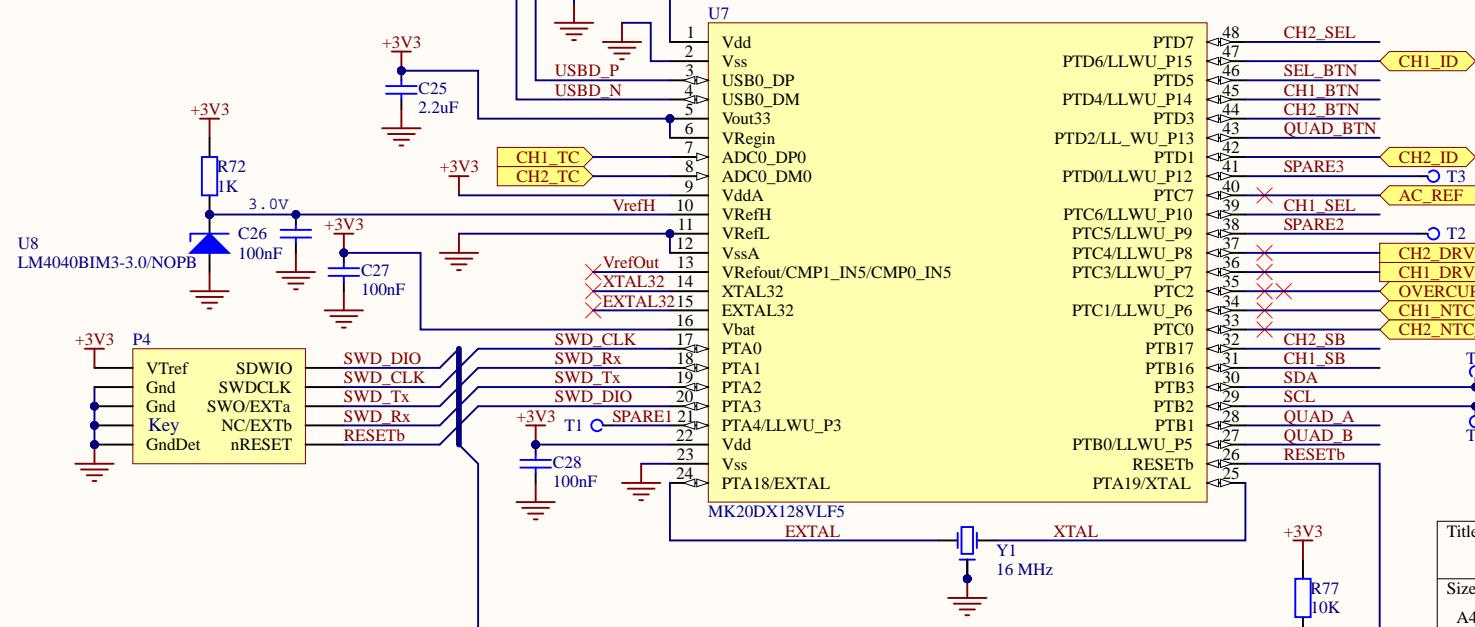
A



B



C



D

Title		SolderingStation_Processor.SchDoc	JLC_Serial_Number
Size	Number	Revision	
A4		3	
Date:	7/19/2021	Sheet of	
File:	Z:\Documents\...\SolderingStation_Processor.SchDoc	3	

