

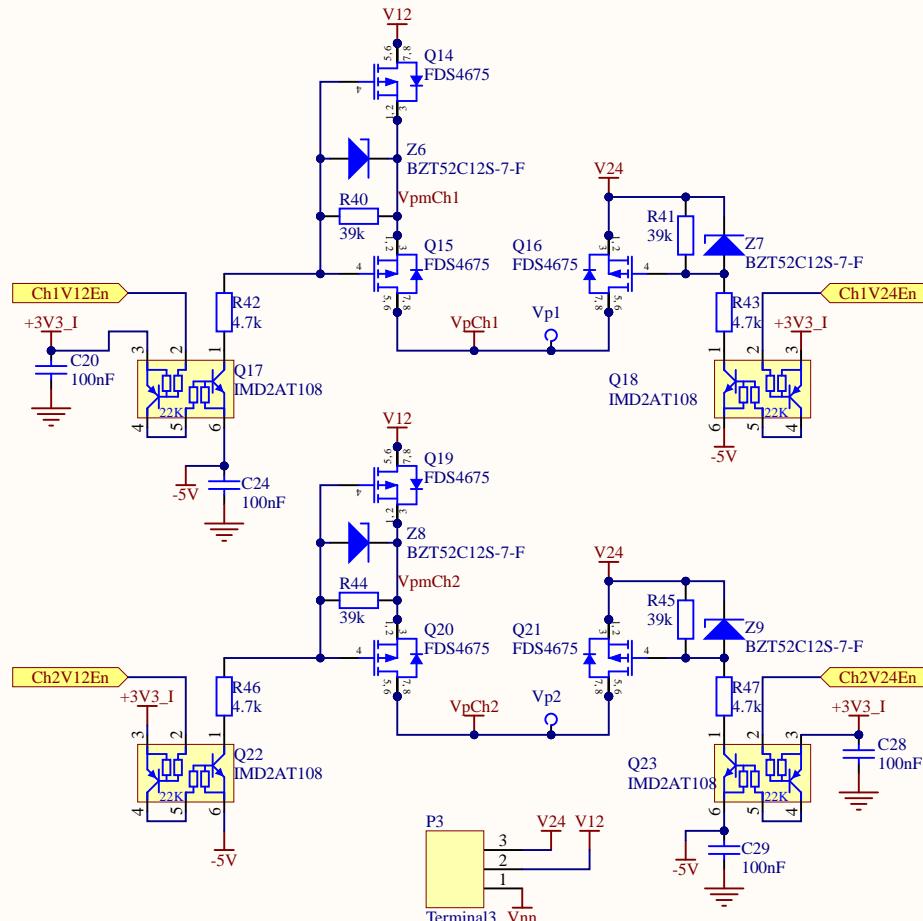
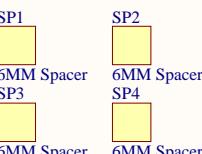
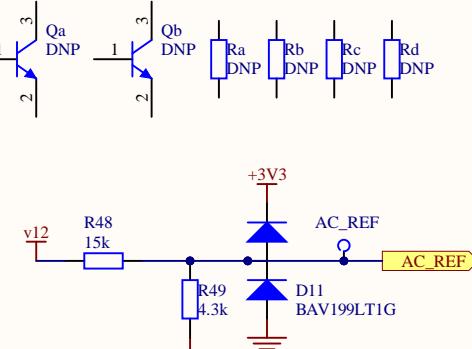
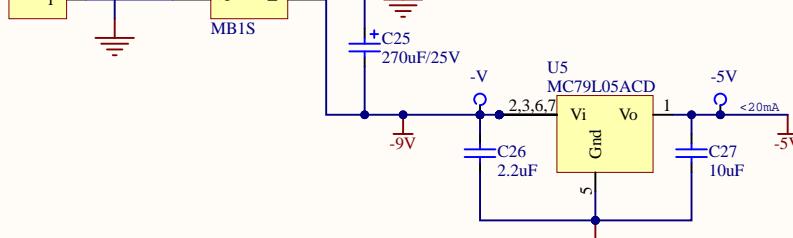
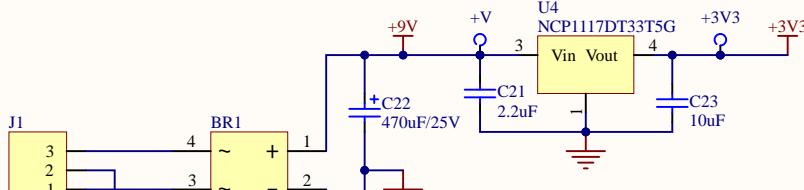
V1
- Initial release

V2
- Added Setback
- Changed current source IC etc.
- Added voltage reference for ADC

V3
- Board Dimensions altered
- Simplified NTC cct. (linearity not required)
- Removed unused components
- Isolated USB
- Prevented TC amp saturating
- Power LEDs directly driven
- Overcurrent directly measured
- Decoupled 3V3 supplies (noise from OLED)
- Added MUX and lots of related changes
- Changed to 4 channels (2 pairs)

V4
- Change to dual supply wth external synchronous rectifier
- Changed to processor wth PGA
- Fixed IMD2AT108 footprint

V4.2
- Fixed U5 legend / Removed via-in-pin / moved dot on IMD2AT108
- Fixed some component values / Increase Vdd for op-amp + assoc. changes



Title		
SolderingStation_Power.SchDoc		
Size	Number	Revision
A4		4.2
Date:	2-11-2022	Sheet of
File:	Z:\Documents\..\SolderingStation_Power.SchDoc	

