

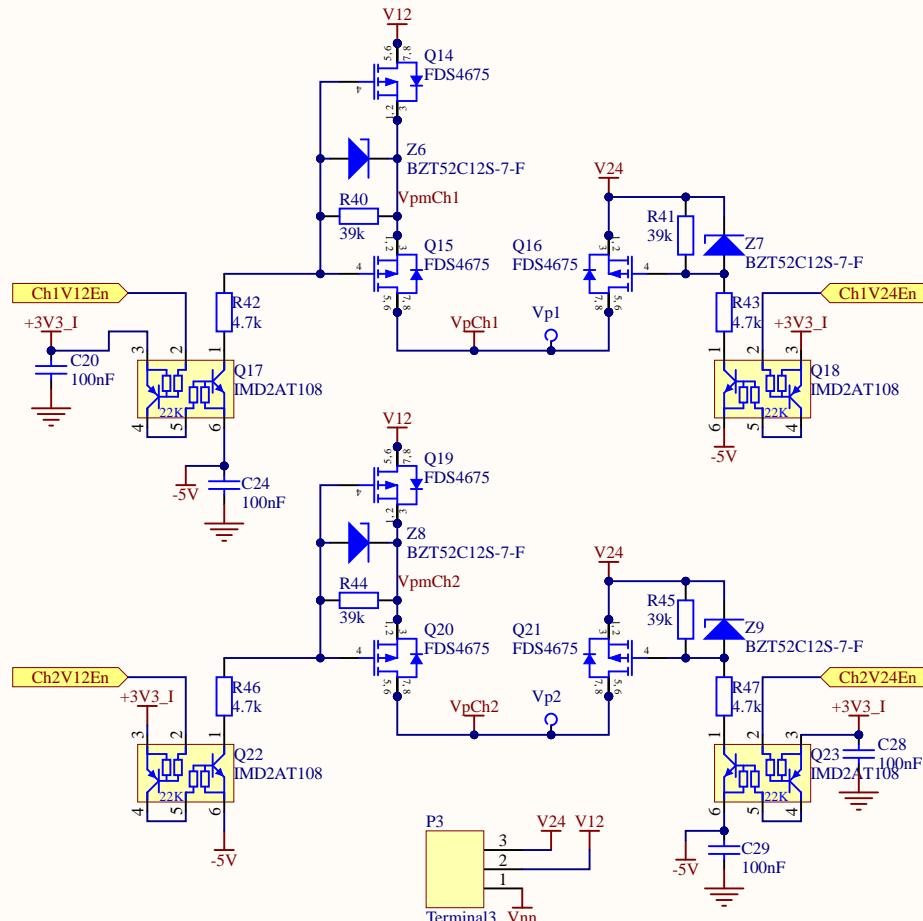
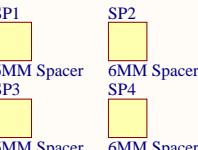
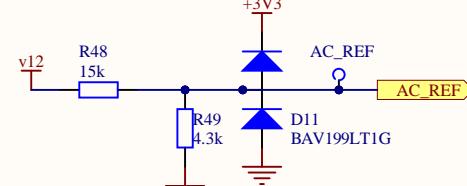
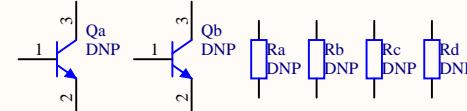
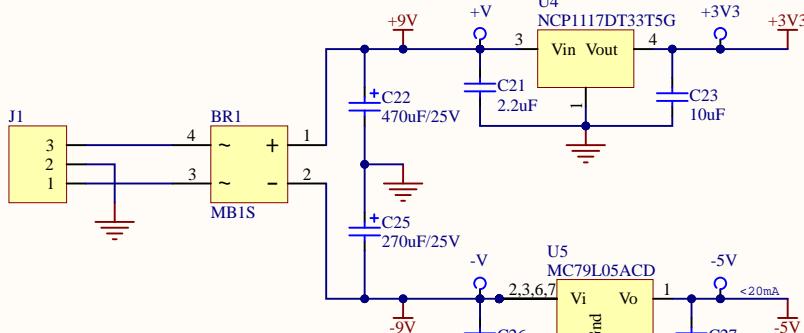
V1  
- Initial release

V2  
- Added Setback  
- Changed current source IC etc.  
- Added voltage reference for ADC

V3  
- Board Dimensions altered  
- Simplified NTC cct. (linearity not required)  
- Removed unused components  
- Isolated USB  
- Prevented TC amp saturating  
- Power LEDs directly driven  
- Overcurrent directly measured  
- Decoupled 3V3 supplies (noise from OLED)  
- Added MUX and lots of related changes  
- Changed to 4 channels (2 pairs)

V4  
- Change to dual supply with external synchronous rectifier  
- Changed to processor with PGA

V4.1  
- Fixed U5 legend / Removed via-in-pin



Title  
**SolderingStation\_Power.SchDoc**

Size	Number	Revision
A4		4.1
Date:	12-30-2021	Sheet of
File:	Z:\Documents\..\SolderingStation_Power.SchDoc	

