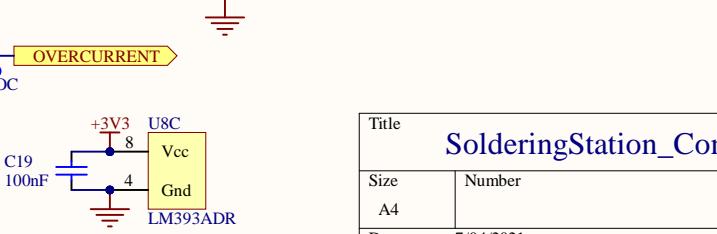
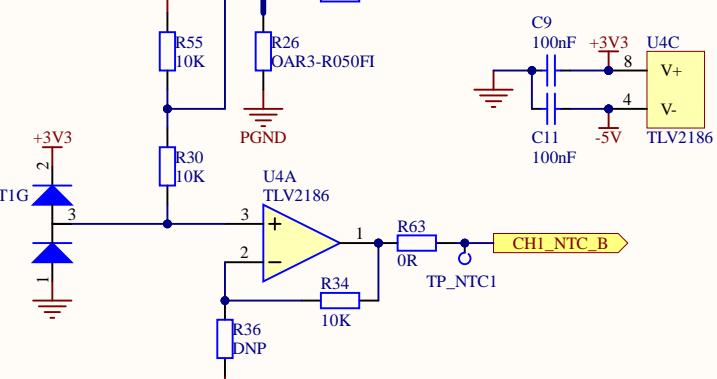
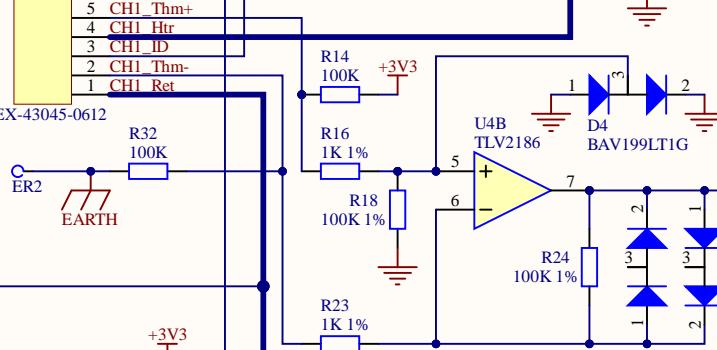
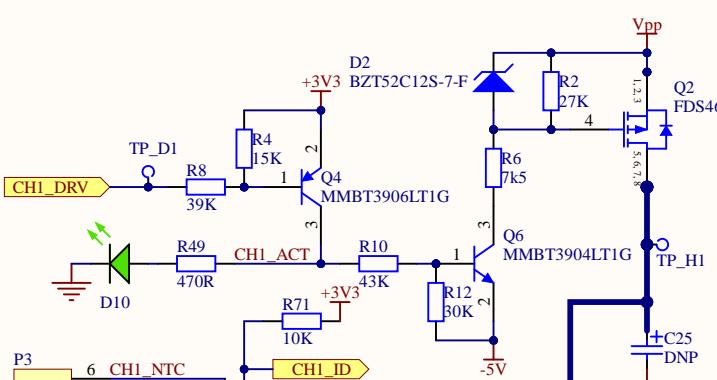
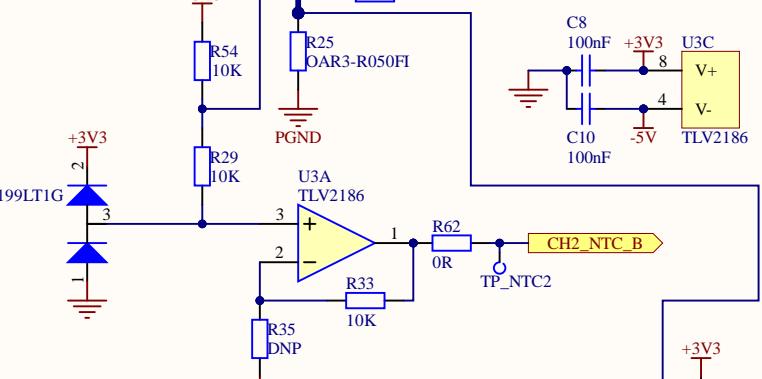
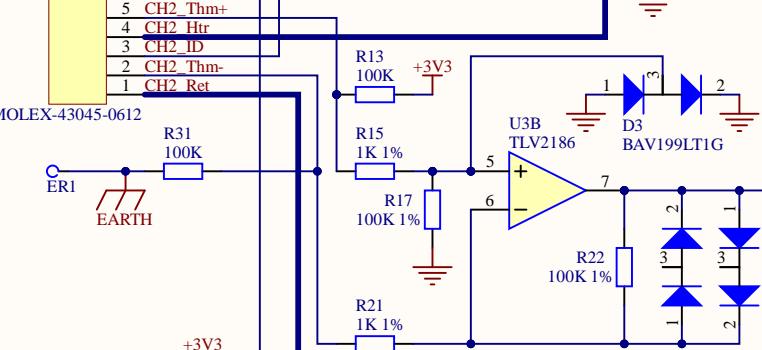
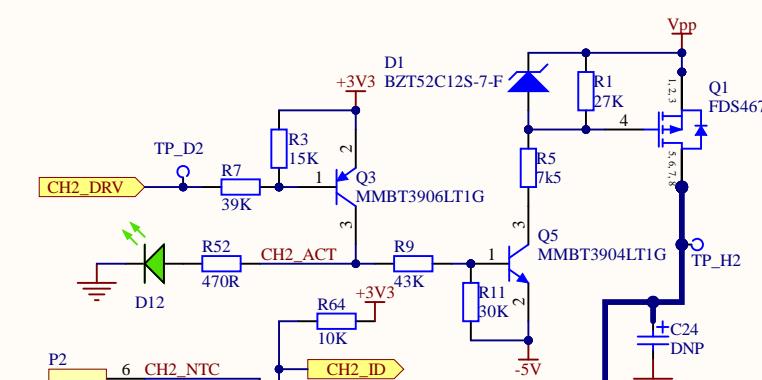


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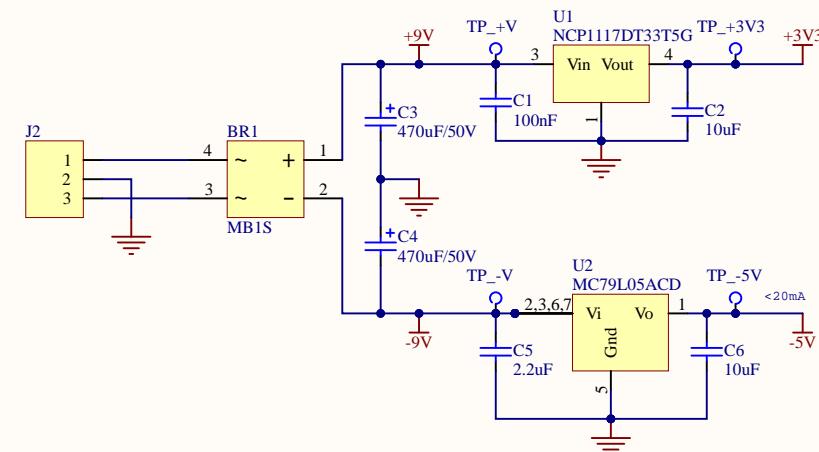
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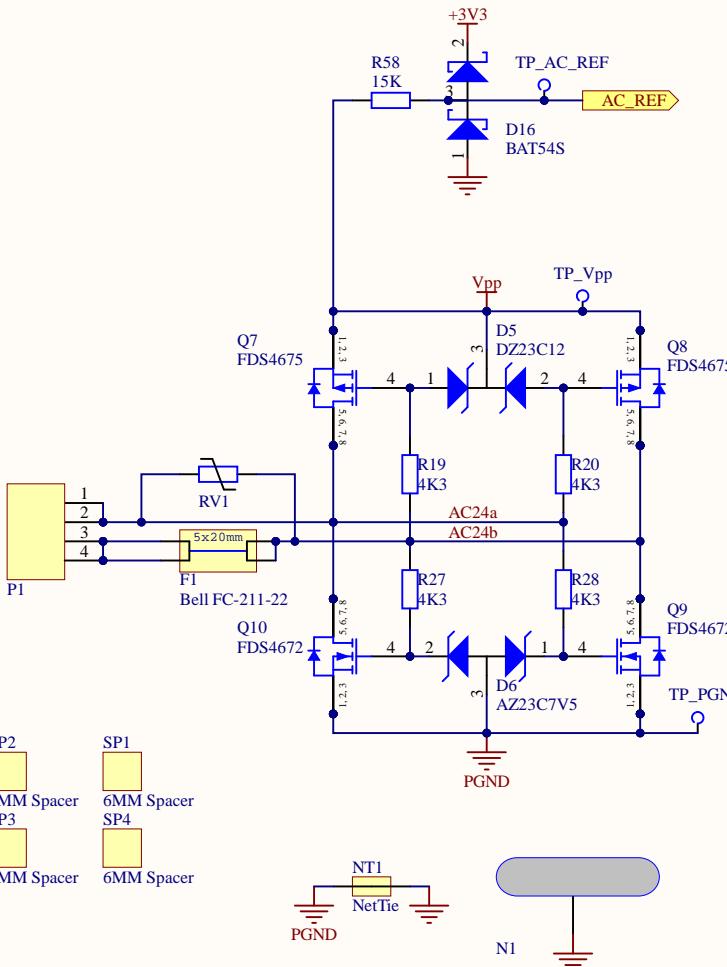
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Date:	7/04/2021	Sheet of
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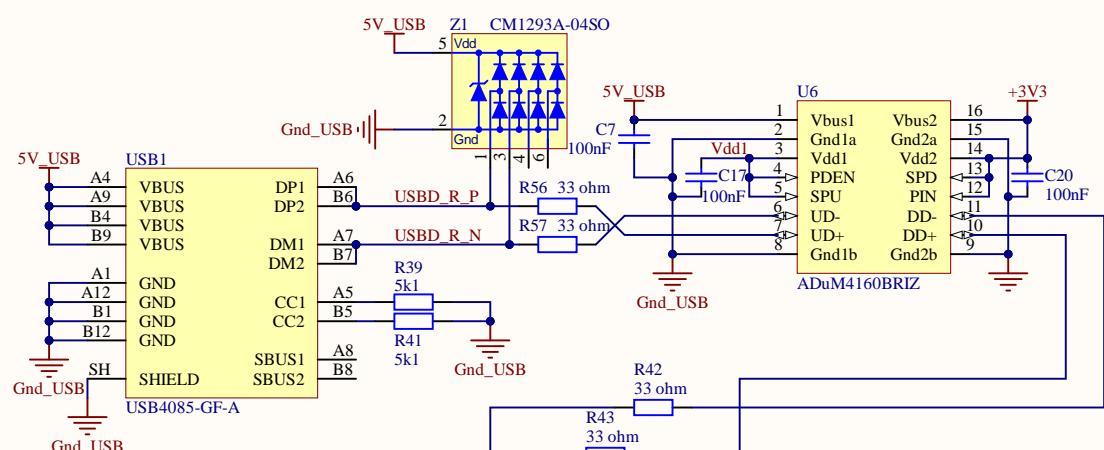


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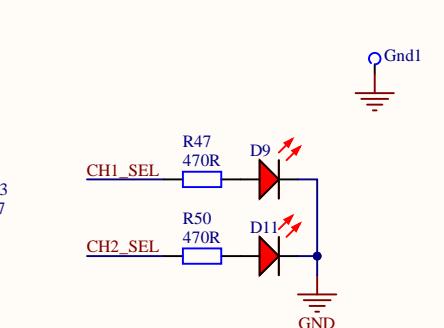
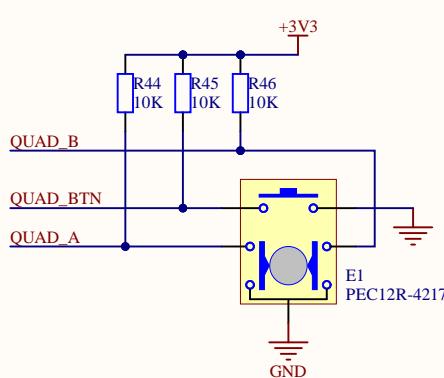
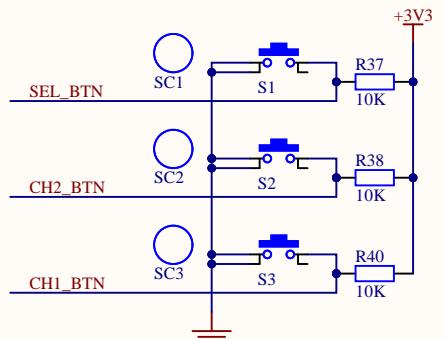
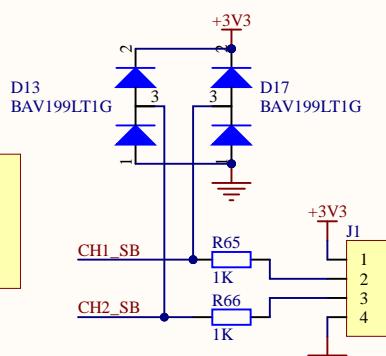


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File: H:\Documents\...\SolderingStation_Power	Solder By:	



V1
- Initial release
V2
- Added Setback
- Changed current source IC etc.
- Added voltage reference for ADC
V3
- Board Dimensions altered
- Simplified NTC cct. (linearity not required)
- Removed unused components
- Isolated thermocouple
- Isolated USB



PTD7	CH2_SEL	OVERCURRENT
PTD6/LLWU_P15	SEL_BTN	
PTD5	CH1_BTN	
PTD4/LLWU_P14	CH2_BTN	
PTD3	QUAD_BTN	
PTD2/LL_WU_P13	PTD1	
PTD1	CH2_ID	
PTD0/LLWU_P12	SPARE3	T3
PTC7	SPARE4	
PTC6/LLWU_P10	CH1_SEL	
PTC5/LLWU_P9	SPARE2	
PTC4/LLWU_P8	T2	
PTC3/LLWU_P7	CH2_DRV	
PTC2	CH1_DRV	
PTC1/LLWU_P6	CH1_ID	
PTC0	CH1_NTC_B	
PTB17	CH2_NTC_B	
PTB16	CH1_SB	
PTB3	SDA	
PTB2	SCL	
PTB1	QUAD_A	
PTA19/XTAL	QUAD_B	
PTA18/EXTAL	RESETb	
Y1 16 MHz	TP_SDA	
XTAL	TP_SCL	

D

Title SolderingStation_Processor.SchDoc		
Size	Number	Revision
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Date:	7/04/2021	Sheet of
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