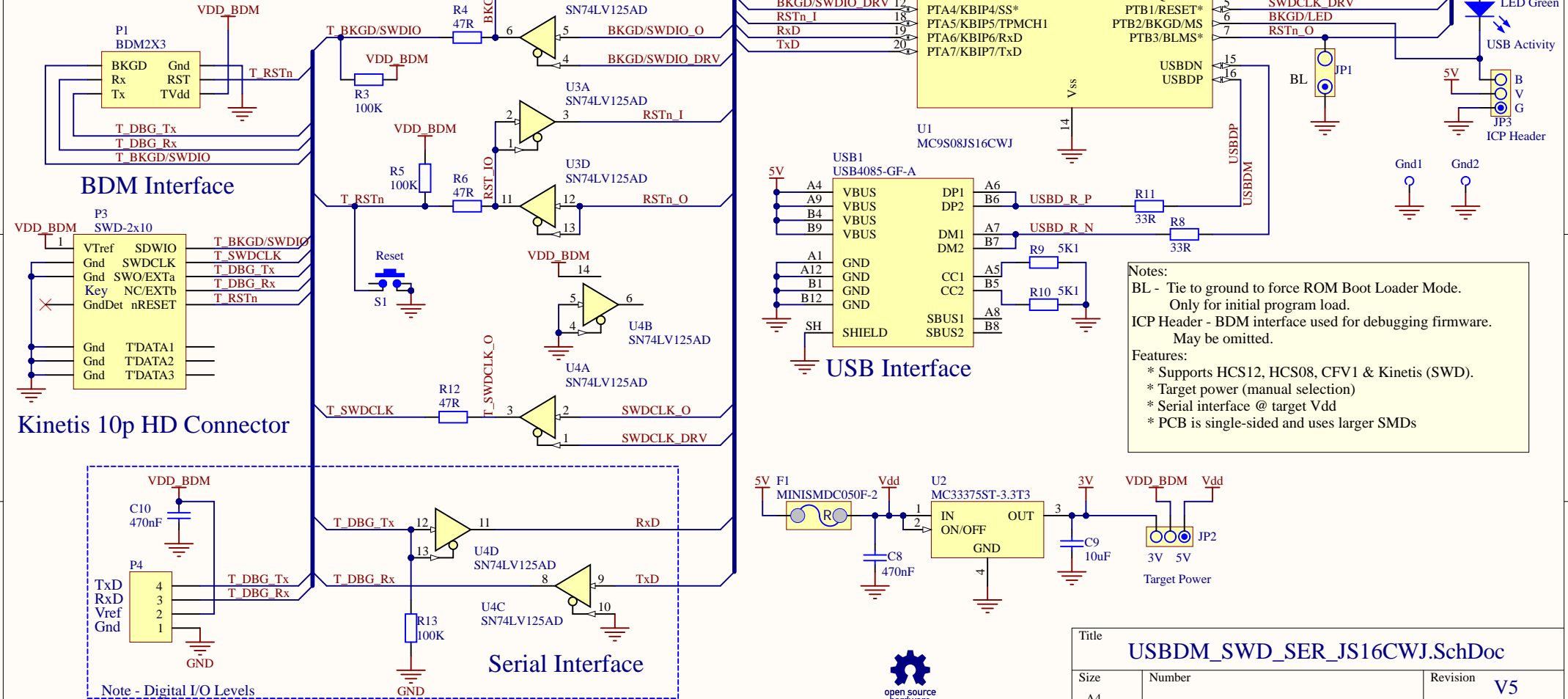


Revision:  
3.0 Deleted CTS,RTS added SWD interface  
3.1 Deleted pins from P3 to fit 10p/20p plug  
3.2 Added Rx/Tx to P1 & P3, Gnd TPs  
3.3 Board shrink  
3.4 Rationalised component values  
3.5 Removed SWO, changed J1 to microUSB  
3.6 Added reset switch  
4.0 Changed to USB 3 connector  
5.0 Updated for JLCPCB (Crystal and some component values)



Title		
USBDM_SWD_SER_JS16CWJ.SchDoc		
Size	Number	Revision
A4		V5
Date:	10-02-2024	Sheet of
File:	Z:\Documents\...\USBDM_SWD_SER_JS16CWJ.SchDoc	1 of 1

