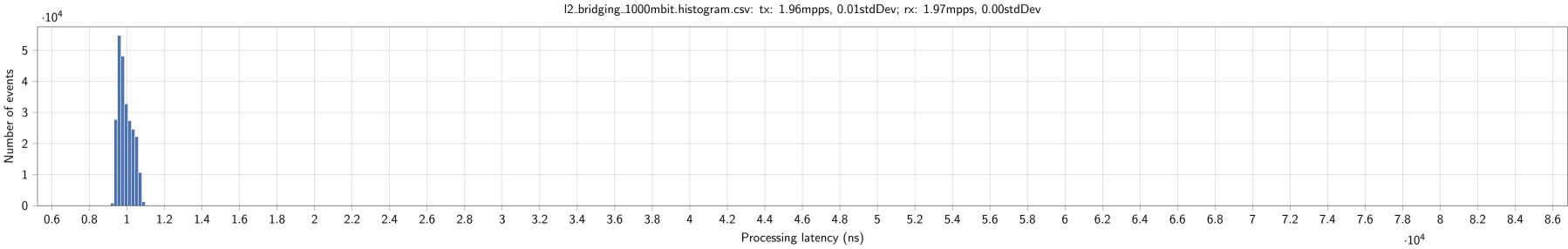
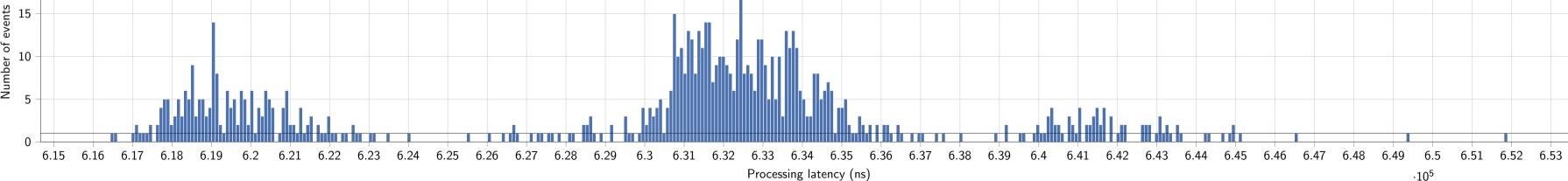


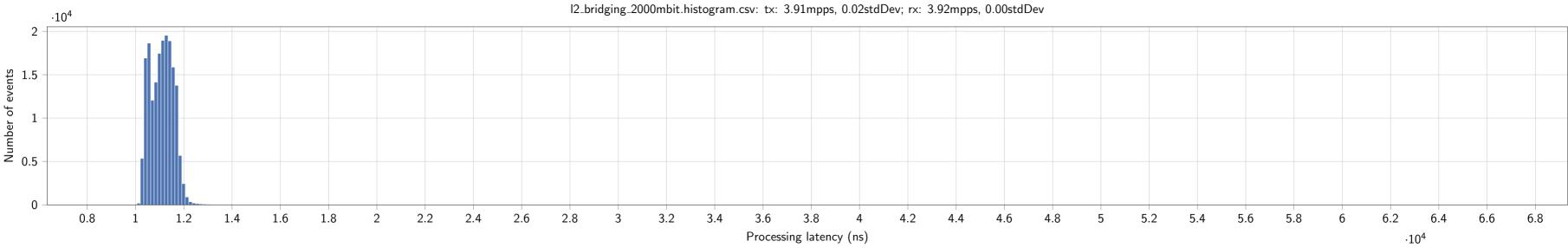
I2\_bridging\_0\_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.37mpps, 0.00stdDev

Processing latency (ns)

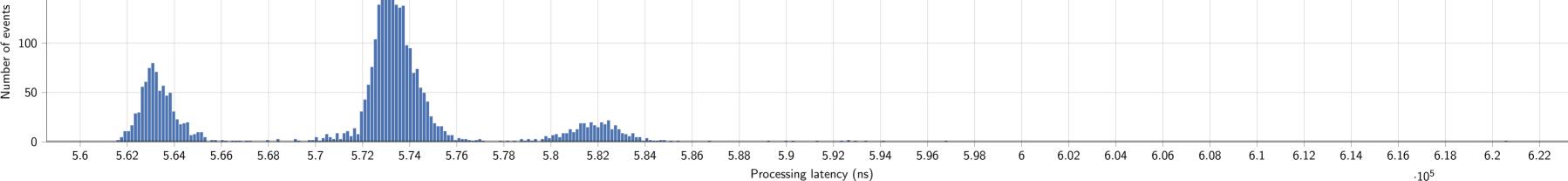




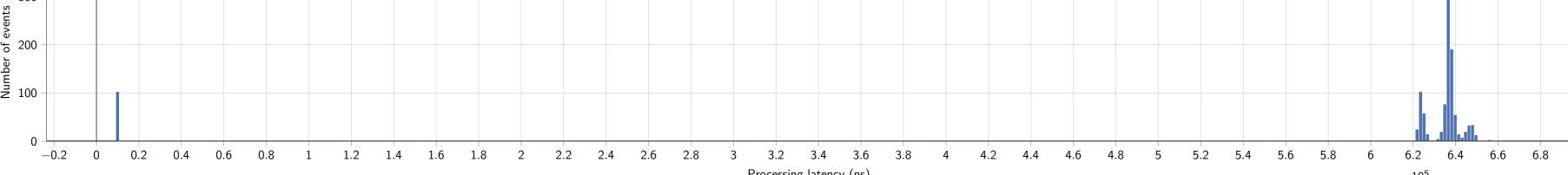
I2\_bridging\_1\_load.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 8.49mpps, 0.00stdDev

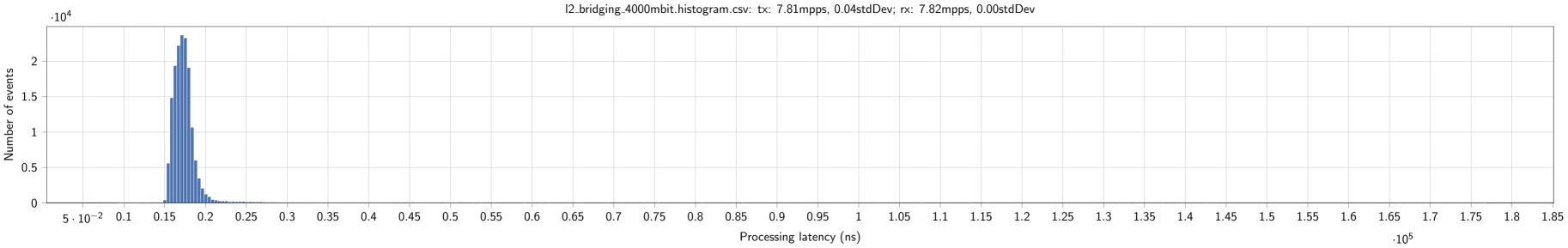


l2\_bridging\_2\_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.36mpps, 0.00stdDev

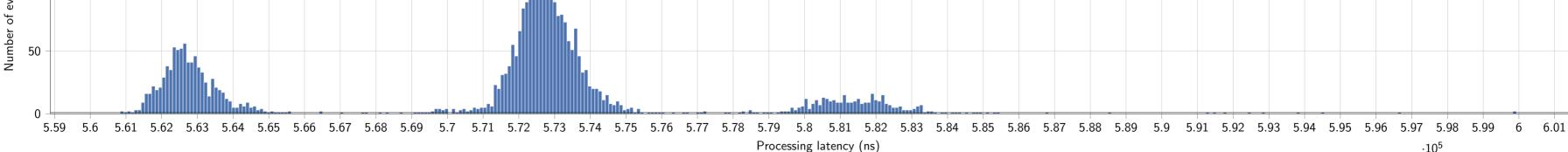


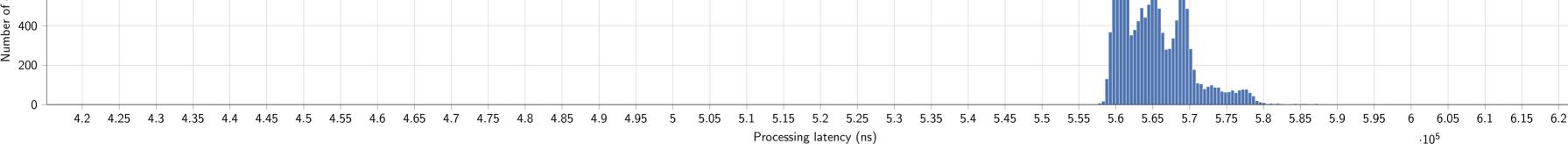
I2\_bridging\_3\_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 8.42mpps, 0.00stdDev

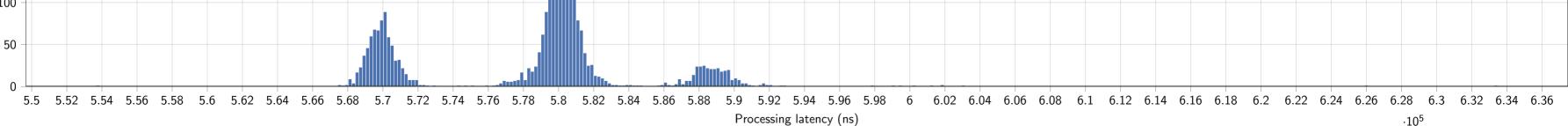




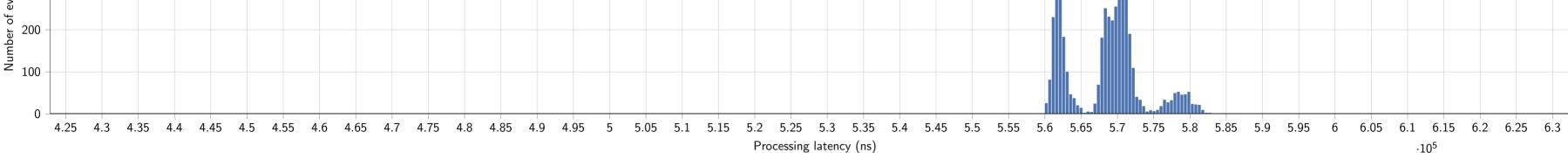
I2\_bridging\_4\_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.37mpps, 0.00stdDev

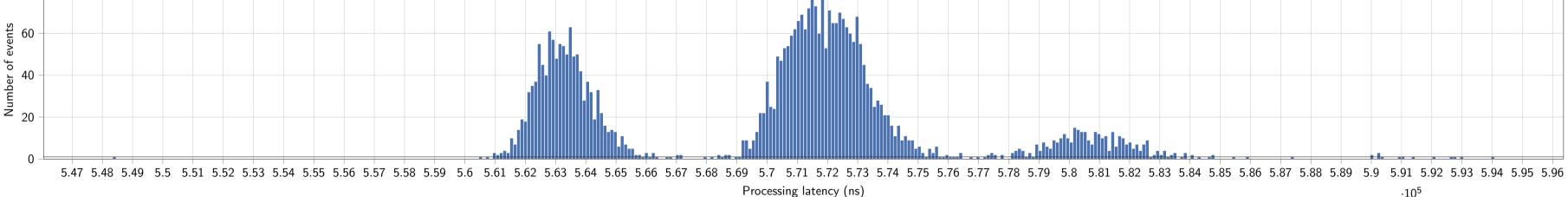




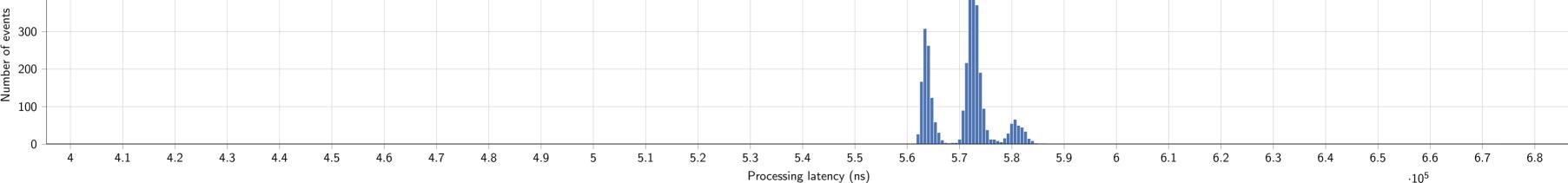


l2\_bridging\_6000mbit.histogram.csv: tx: 11.78mpps, 0.06stdDev; rx: 9.37mpps, 0.00stdDev

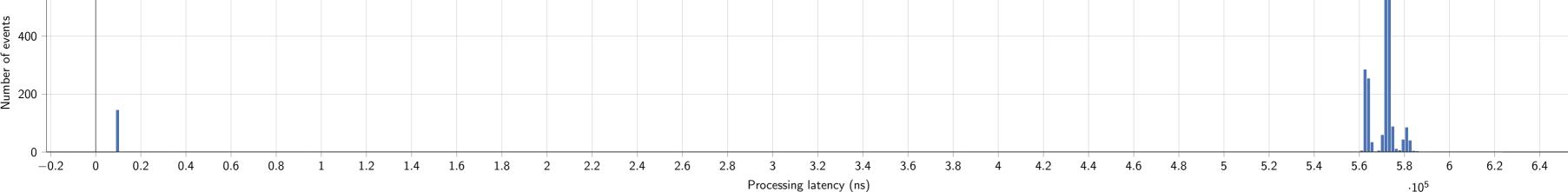


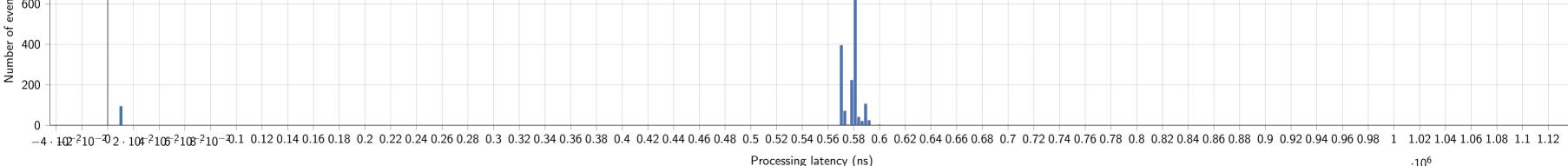


l2\_bridging\_6600mbit.histogram.csv: tx: 13.01mpps, 0.06stdDev; rx: 9.35mpps, 0.00stdDev

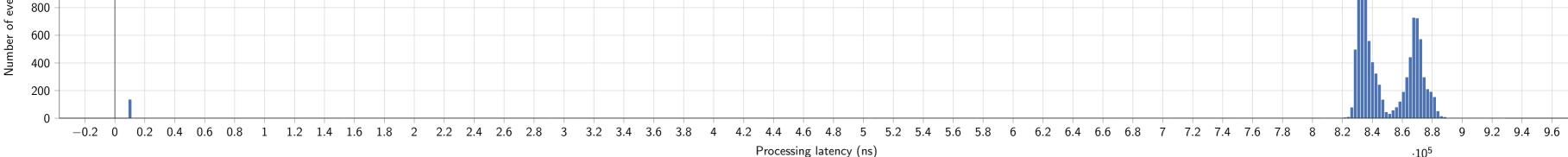


l2\_bridging\_6800mbit.histogram.csv: tx: 13.28mpps, 0.06stdDev; rx: 9.36mpps, 0.00stdDev



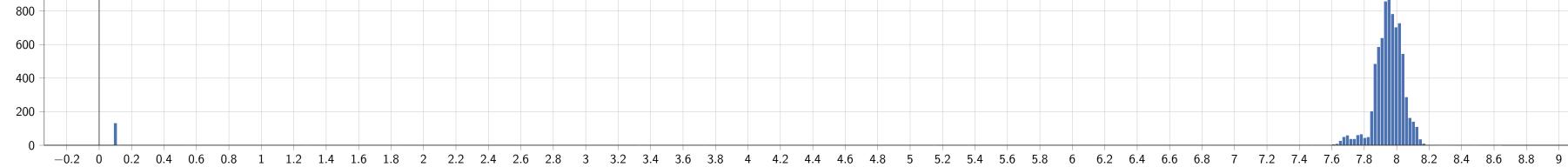


l2\_multimac\_100.histogram.csv: tx: 10.14mpps, 0.05stdDev; rx: 6.16mpps, 0.00stdDev





Processing latency (ns)



Processing latency (ns)

l2\_multimac\_10000.histogram.csv: tx: 10.15mpps, 0.05stdDev; rx: 6.72mpps, 0.00stdDev

l2\_multimac\_100000.histogram.csv: tx: 10.19mpps, 0.05stdDev; rx: 6.77mpps, 0.00stdDev

