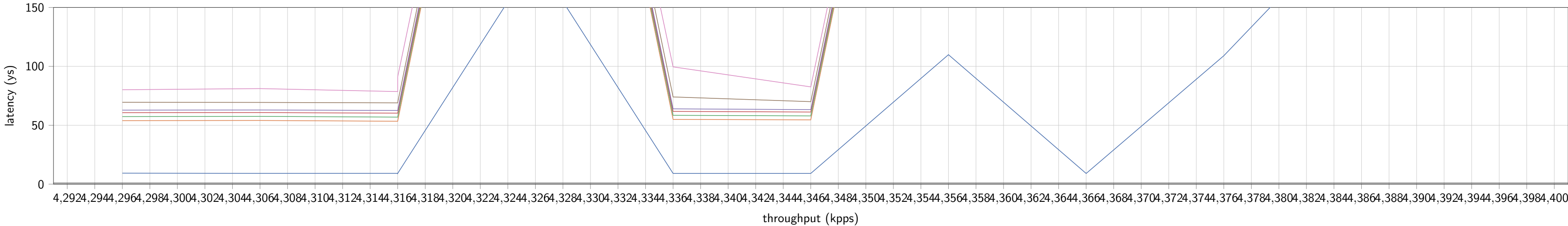
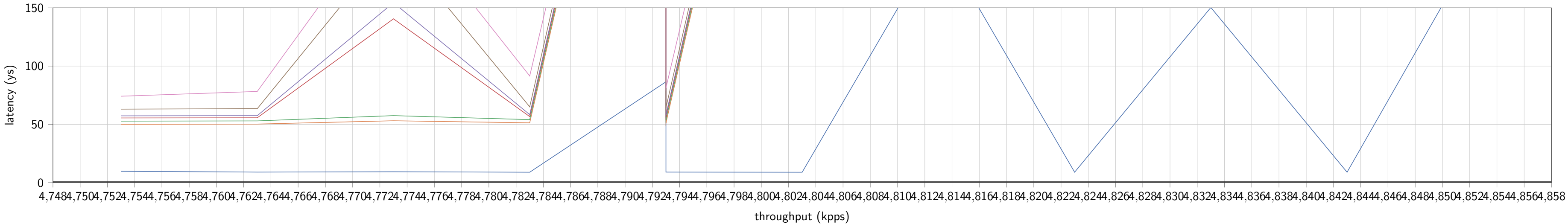
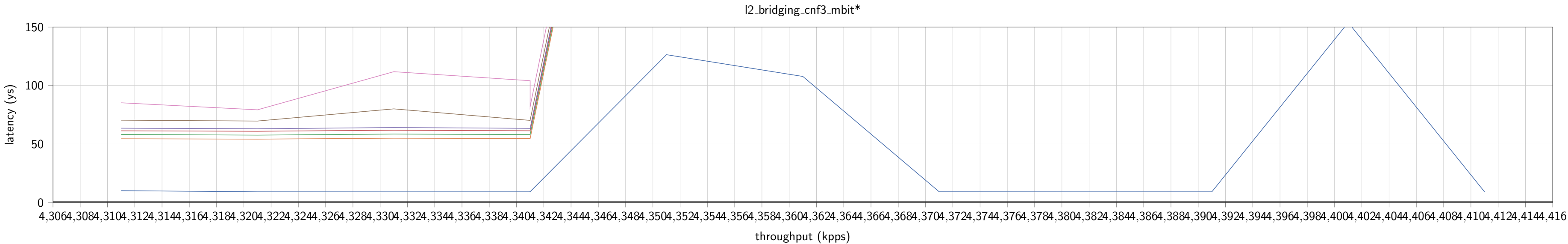


l2_bridging_cnf1_mbit*

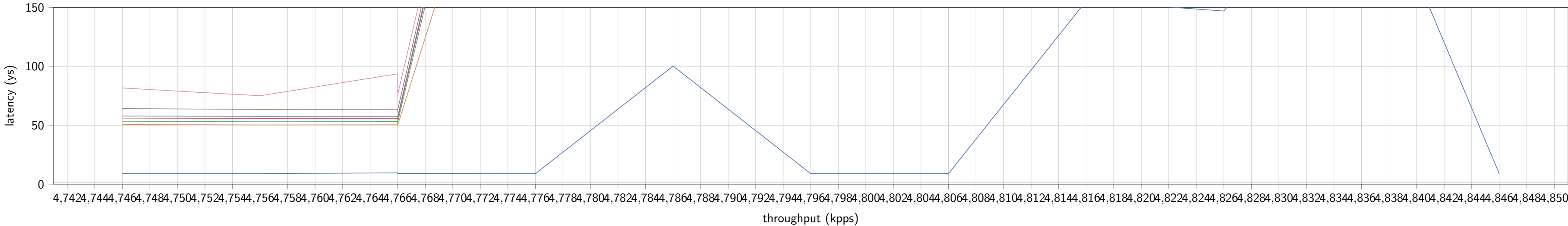


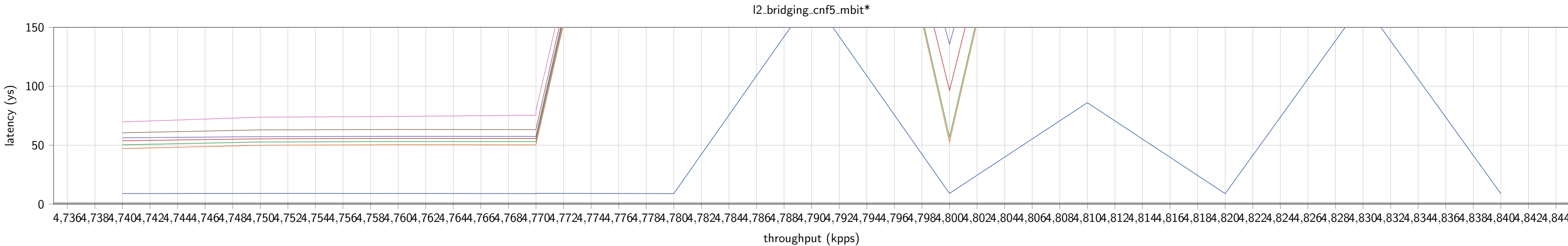
l2_bridging_cnf2_mbit*



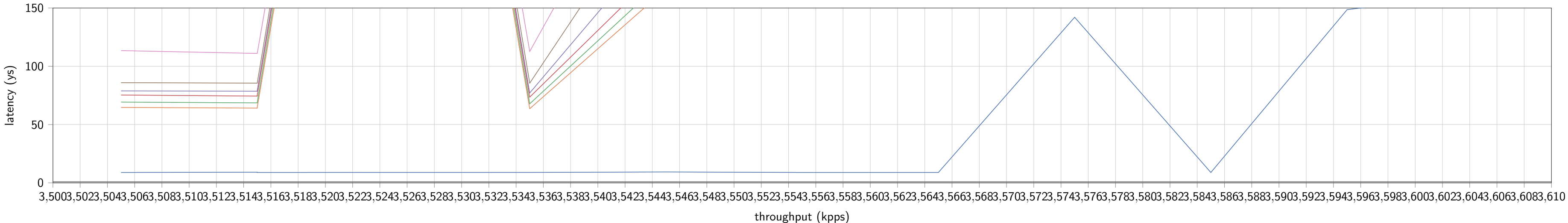


l2_bridging_cnf4_mbit*

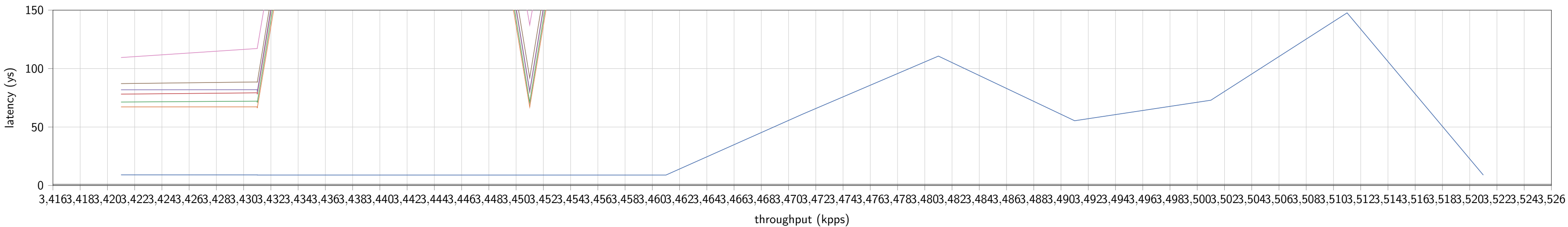




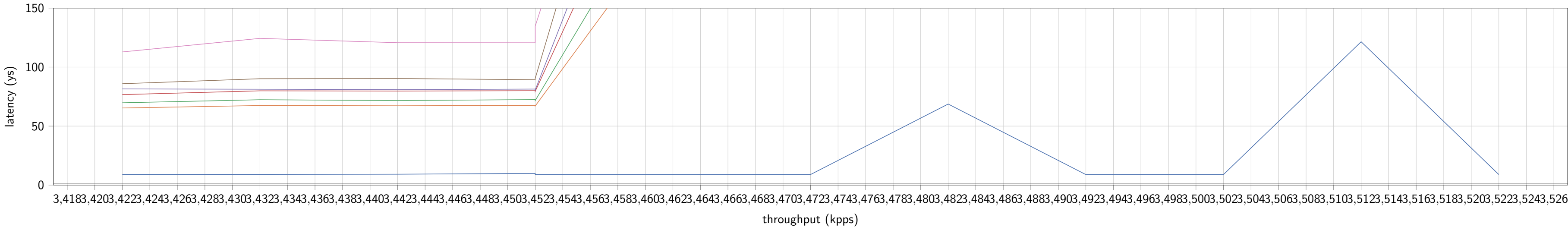
l2_multimac_00000100_mbit*

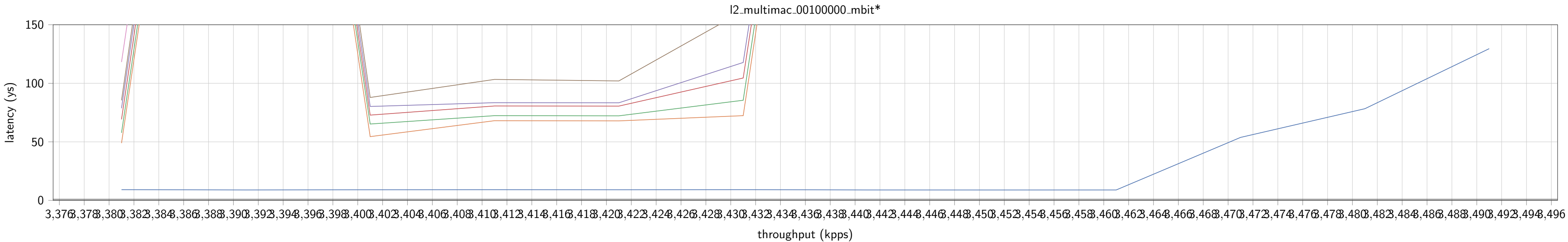


l2_multimac_00001000_mbit*

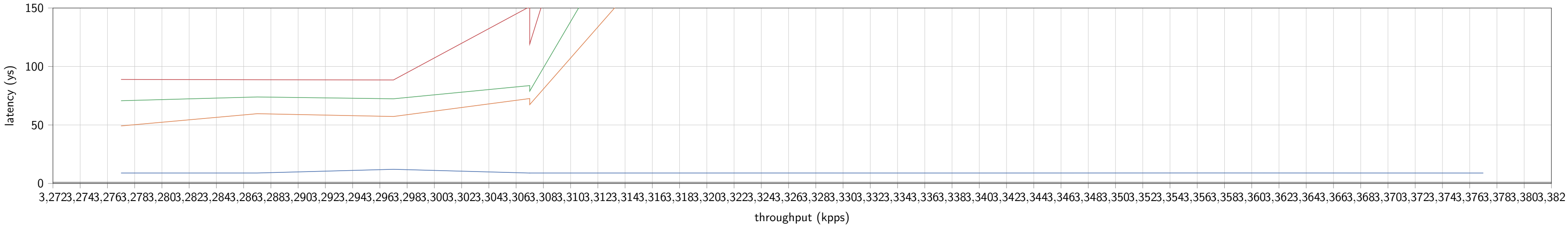


l2_multimac_00010000_mbit*

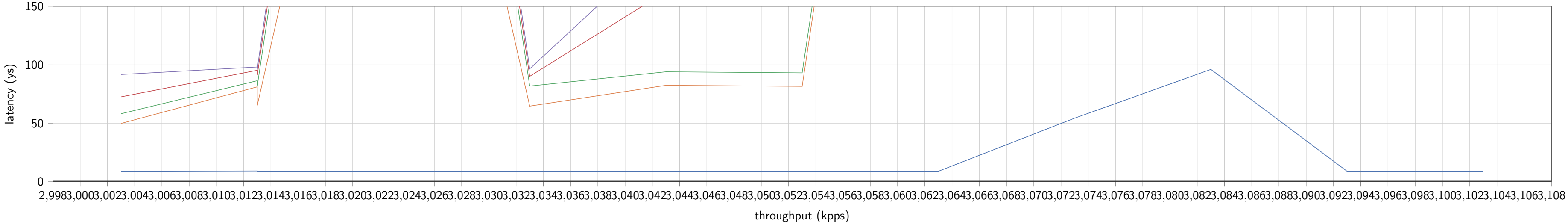


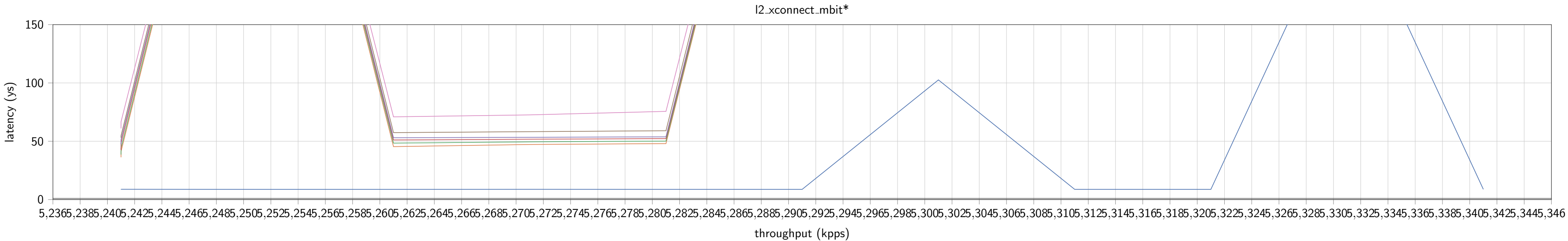


l2_multimac_01000000_mbit*

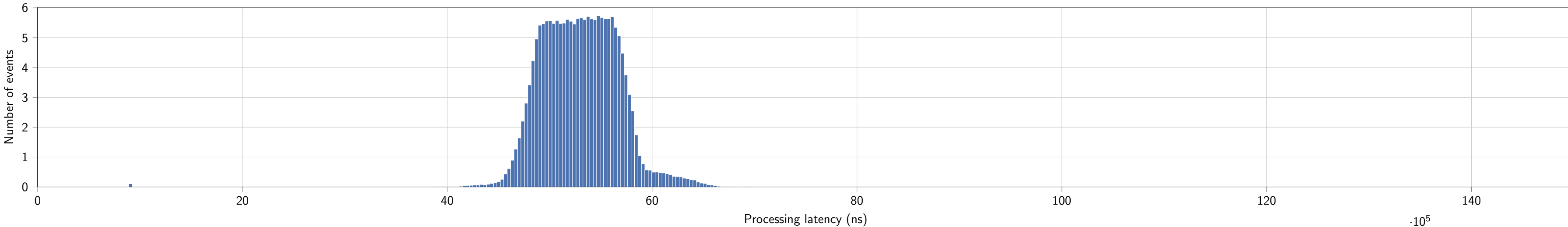


l2_multimac_10000000_mbit*

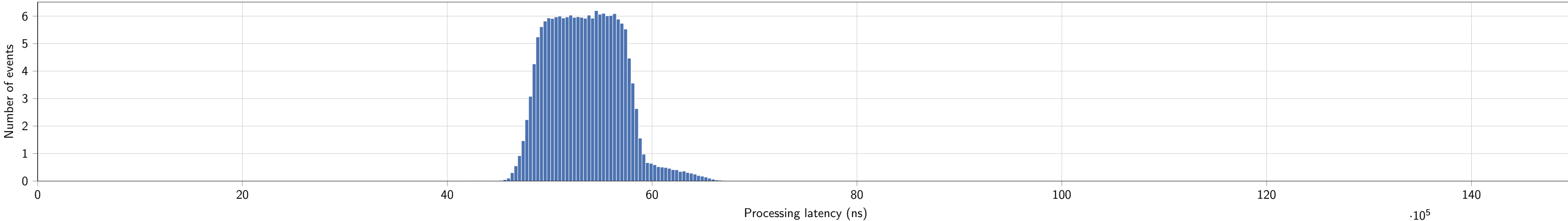




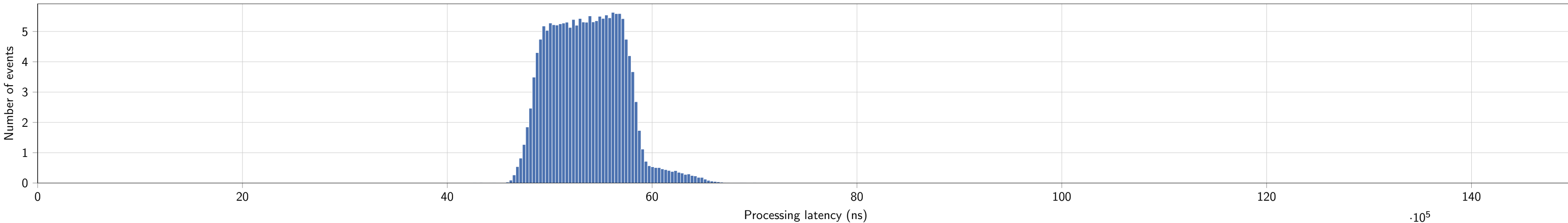
l2_bridging_cnf0_mbit4743hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev

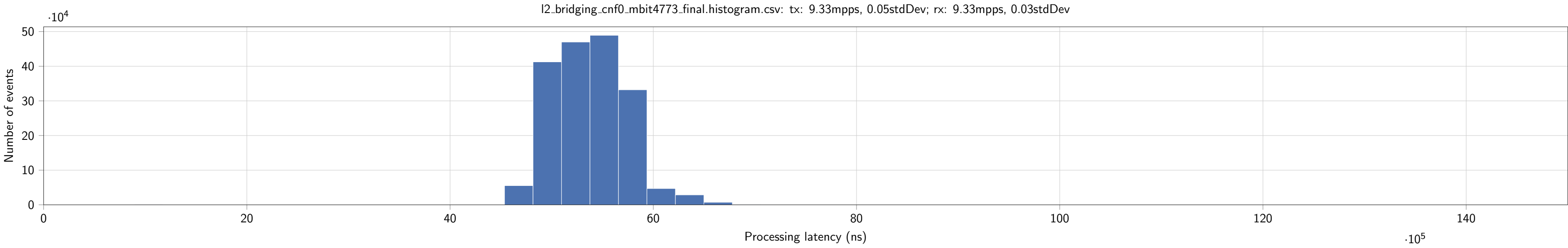


l2_bridging_cnf0_mbit4753hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev

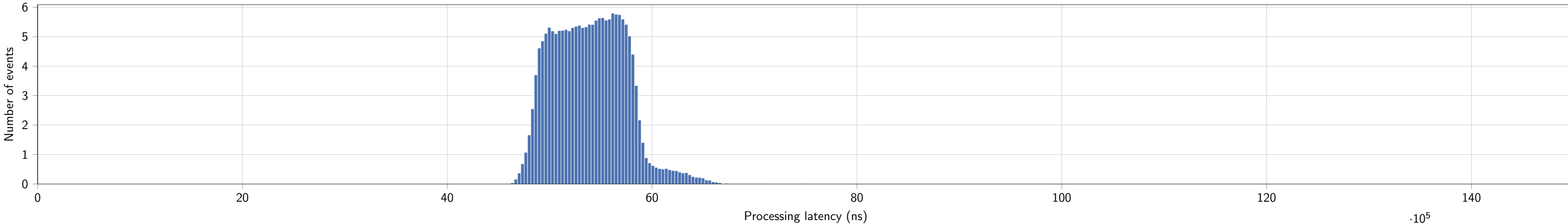


l2_bridging_cnf0_mbit4763hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev

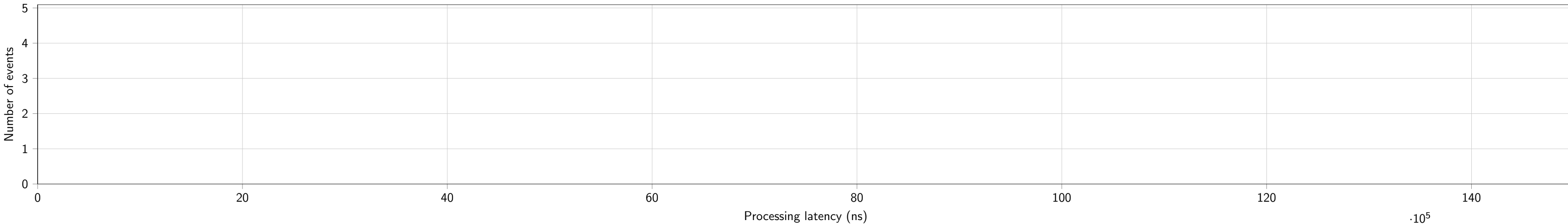




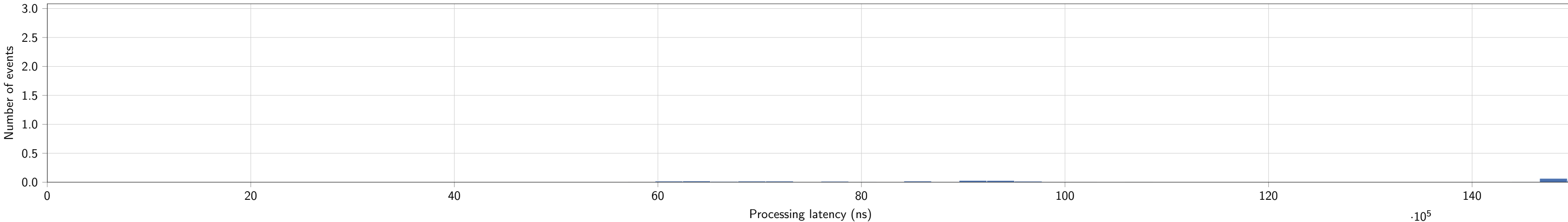
l2_bridging_cnf0_mbit4773hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



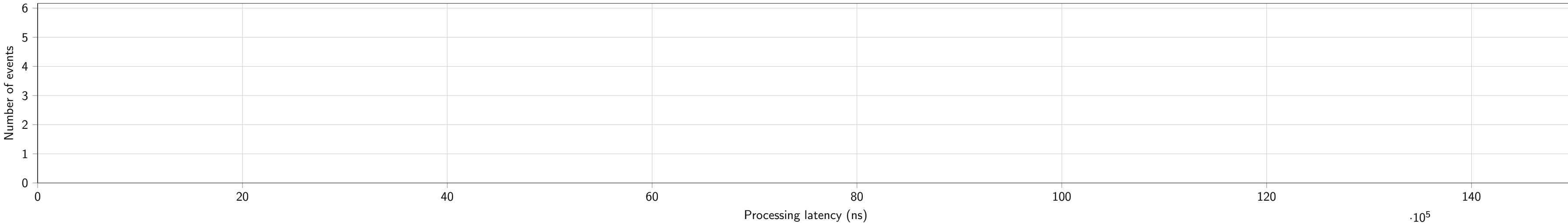
l2_bridging_cnf0_mbit4783hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.23mpps, 0.00stdDev



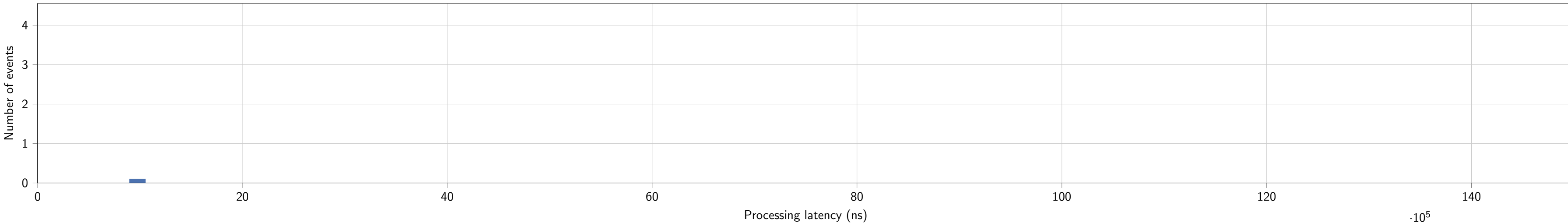
l2_bridging_cnf0_mbit4793hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.40mpps, 0.00stdDev



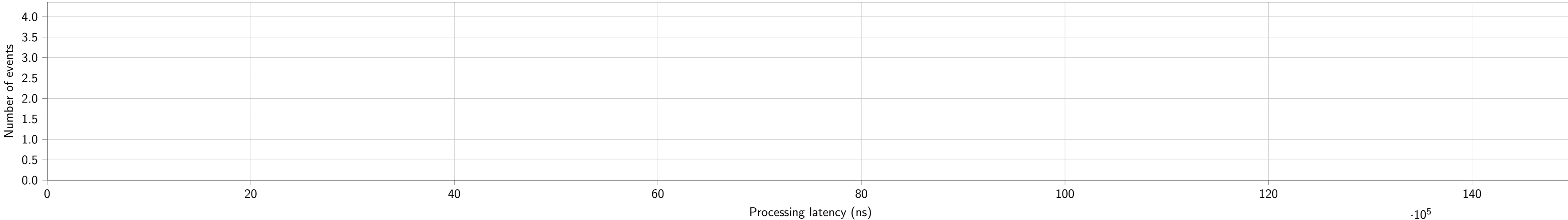
l2_bridging_cnf0_mbit4803hires.histogram.csv: tx: 9.39mpps, 0.04stdDev; rx: 9.36mpps, 0.00stdDev



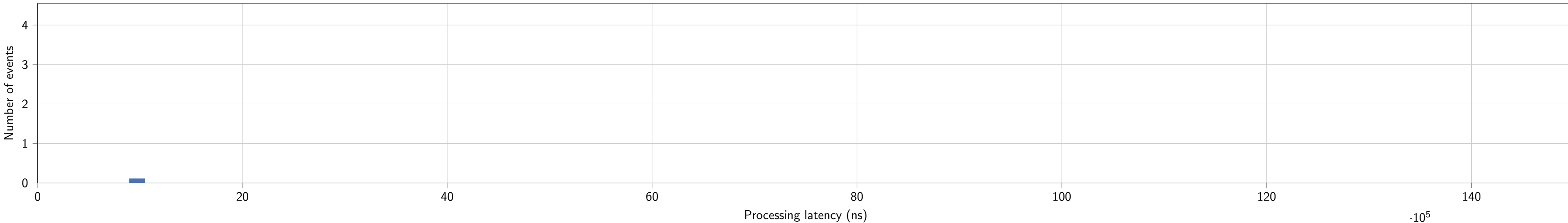
l2_bridging_cnf0_mbit4813hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.40mpps, 0.00stdDev



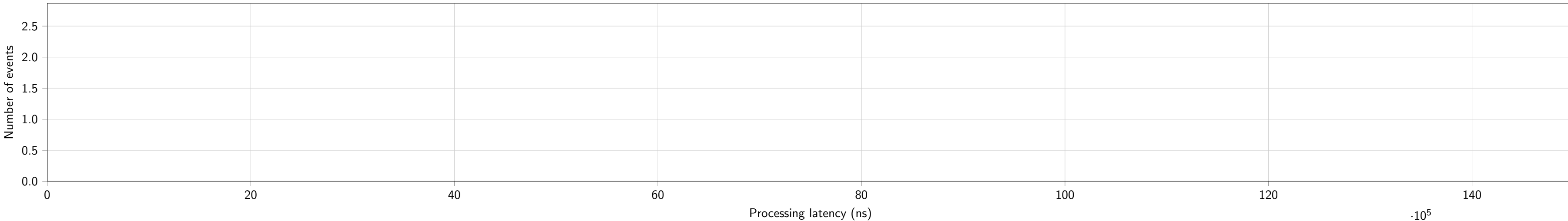
l2_bridging_cnf0_mbit4823hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.35mpps, 0.03stdDev



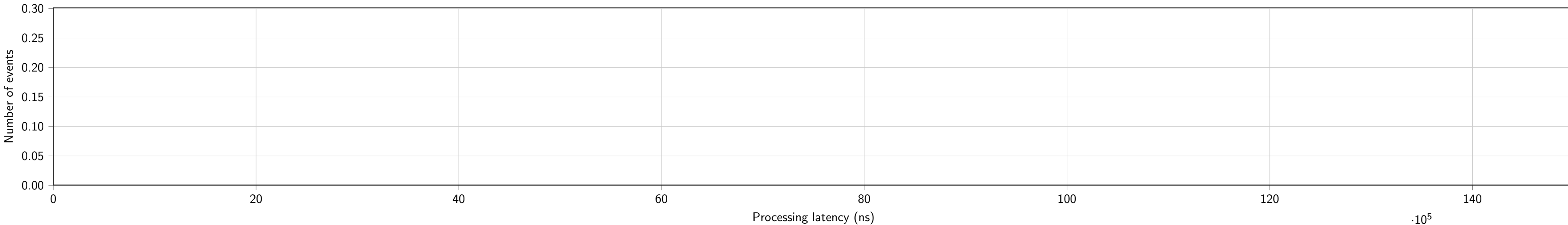
l2_bridging_cnf0_mbit4833hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.37mpps, 0.00stdDev



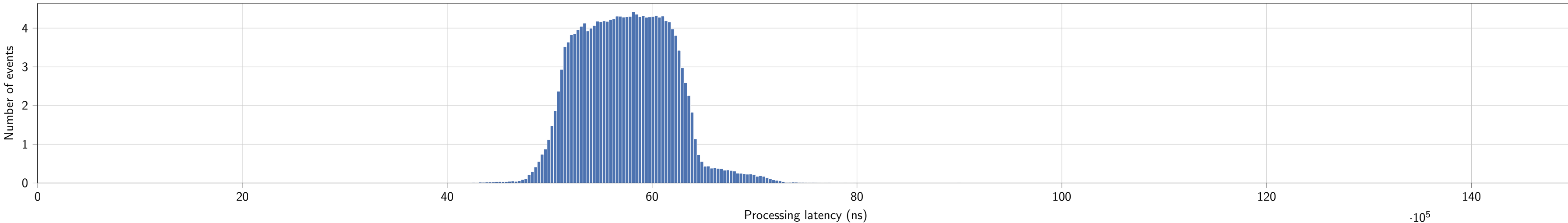
l2_bridging_cnf0_mbit4843hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



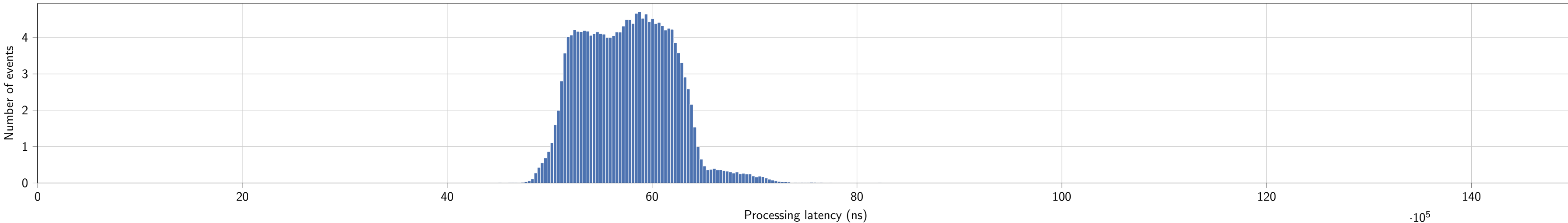
l2_bridging_cnf0_mbit9000.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.36mpps, 0.00stdDev



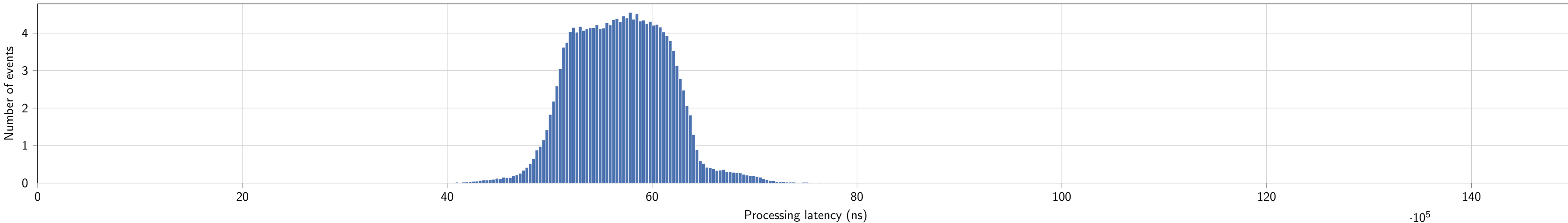
l2_bridging_cnf1_mbit4296hires.histogram.csv: tx: 8.45mpps, 0.04stdDev; rx: 8.45mpps, 0.00stdDev

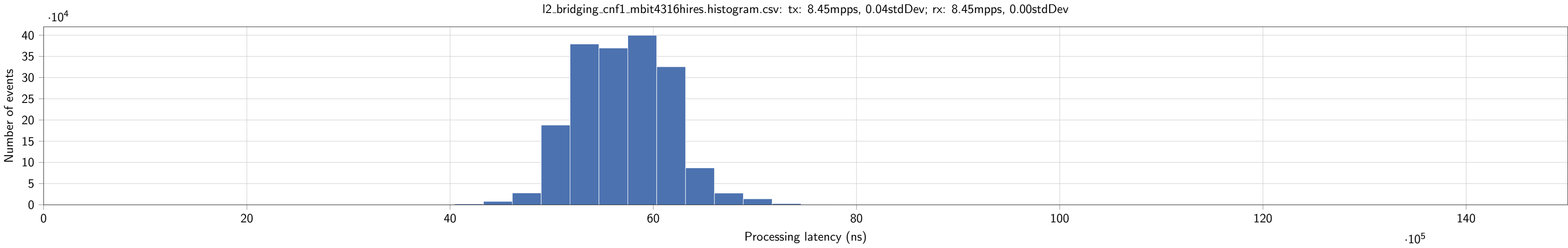


l2_bridging_cnf1_mbit4306hires.histogram.csv: tx: 8.45mpps, 0.04stdDev; rx: 8.45mpps, 0.00stdDev

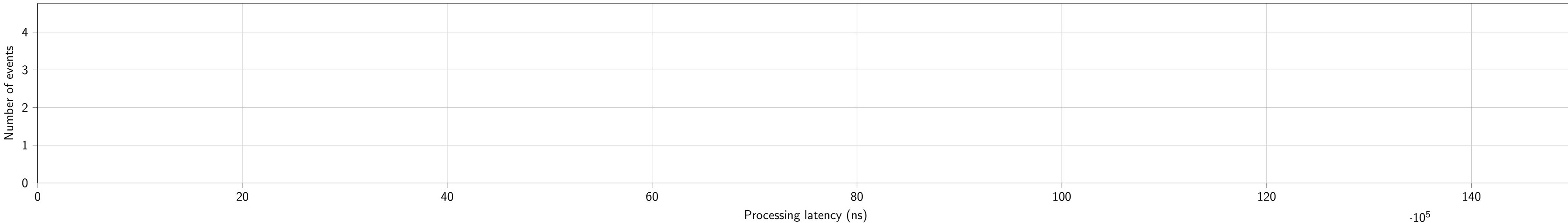


l2_bridging_cnf1_mbit4316_final.histogram.csv: tx: 8.45mpps, 0.04stdDev; rx: 8.45mpps, 0.00stdDev

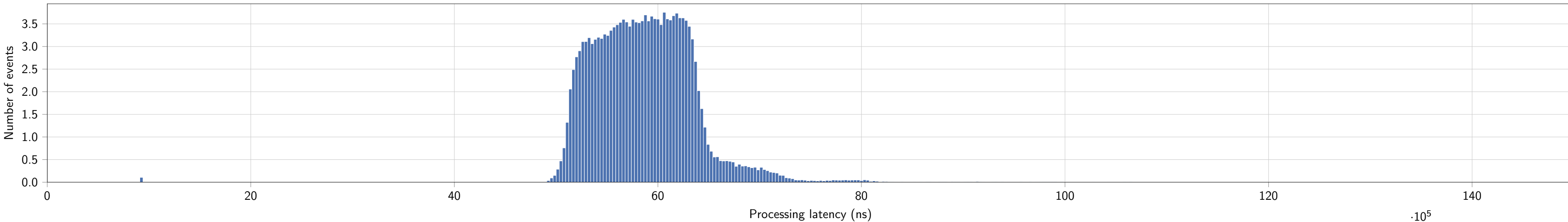




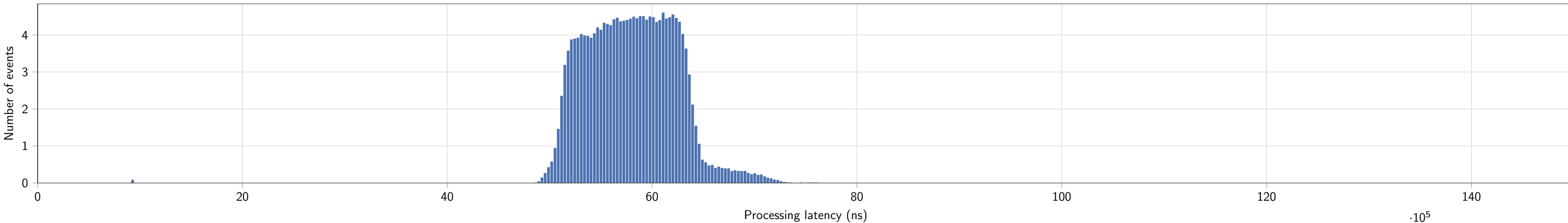
l2_bridging_cnf1_mbit4326hires.histogram.csv: tx: 8.49mpps, 0.04stdDev; rx: 8.43mpps, 0.00stdDev



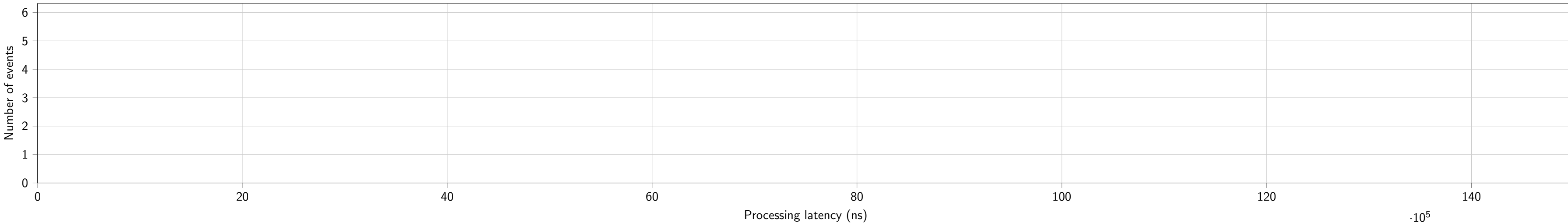
l2_bridging_cnf1_mbit4336hires.histogram.csv: tx: 8.50mpps, 0.04stdDev; rx: 8.51mpps, 0.00stdDev



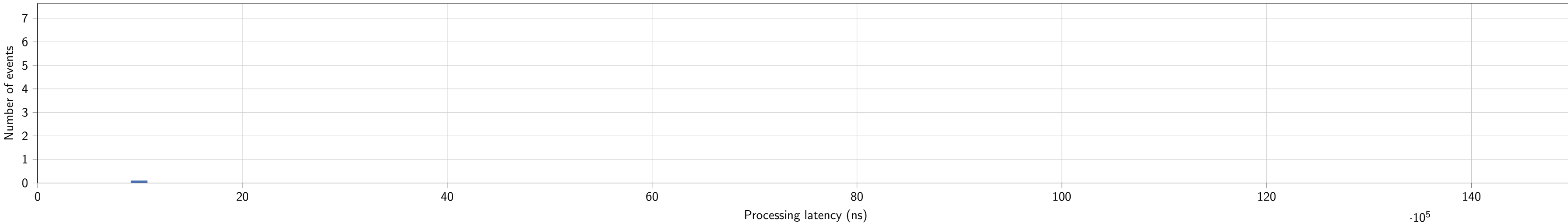
l2_bridging_cnf1_mbit4346hires.histogram.csv: tx: 8.50mpps, 0.04stdDev; rx: 8.51mpps, 0.00stdDev



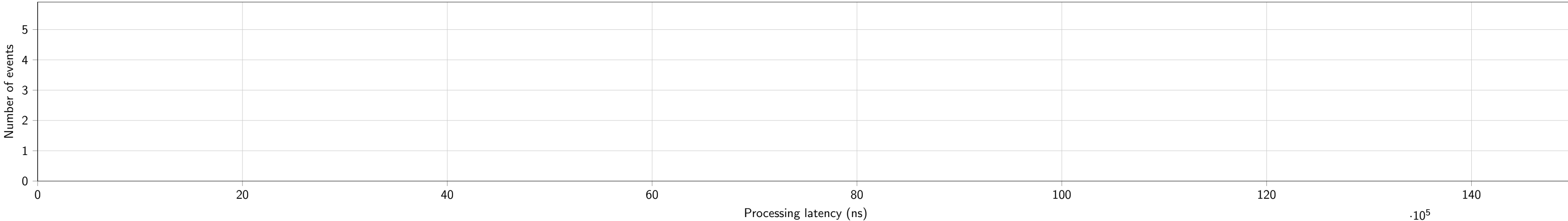
l2_bridging_cnf1_mbit4356hires.histogram.csv: tx: 8.55mpps, 0.04stdDev; rx: 8.52mpps, 0.00stdDev



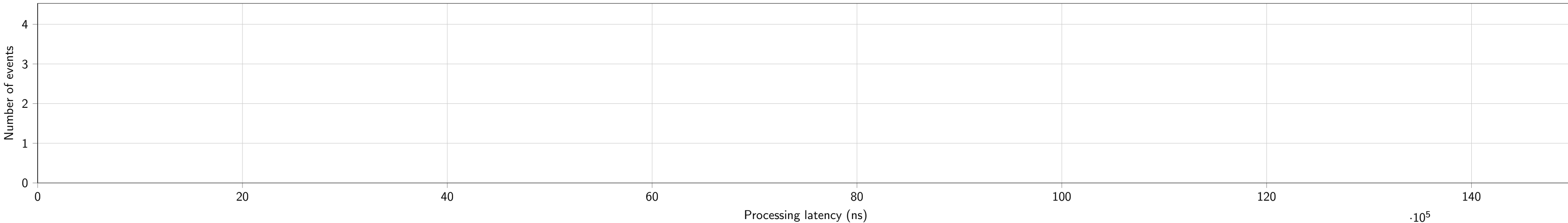
l2_bridging_cnf1_mbit4366hires.histogram.csv: tx: 8.55mpps, 0.04stdDev; rx: 8.47mpps, 0.00stdDev



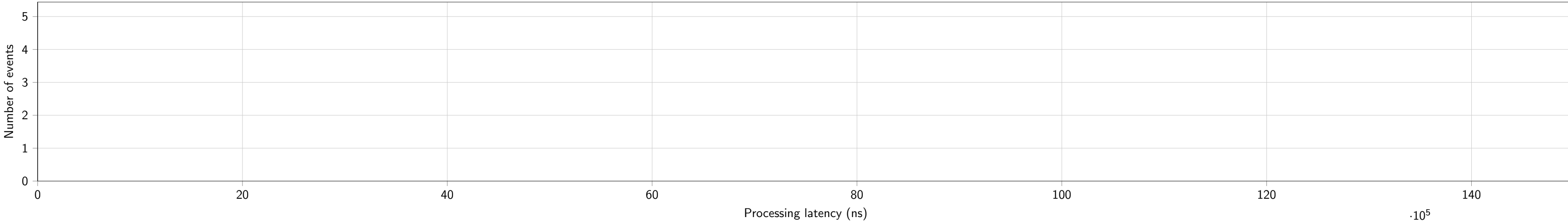
l2_bridging_cnf1_mbit4376hires.histogram.csv: tx: 8.55mpps, 0.04stdDev; rx: 8.53mpps, 0.00stdDev



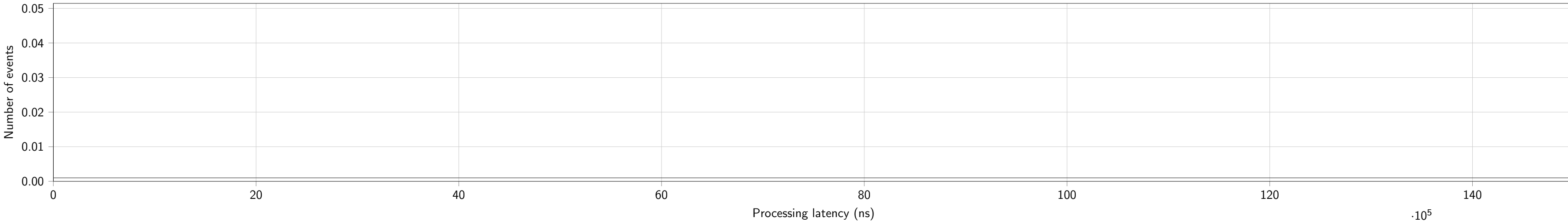
l2_bridging_cnf1_mbit4386hires.histogram.csv: tx: 8.61mpps, 0.05stdDev; rx: 8.51mpps, 0.00stdDev



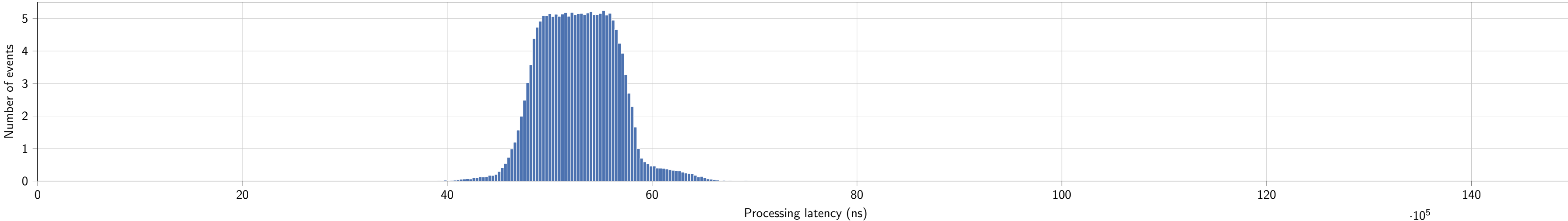
l2_bridging_cnf1_mbit4396hires.histogram.csv: tx: 8.61mpps, 0.04stdDev; rx: 8.53mpps, 0.00stdDev



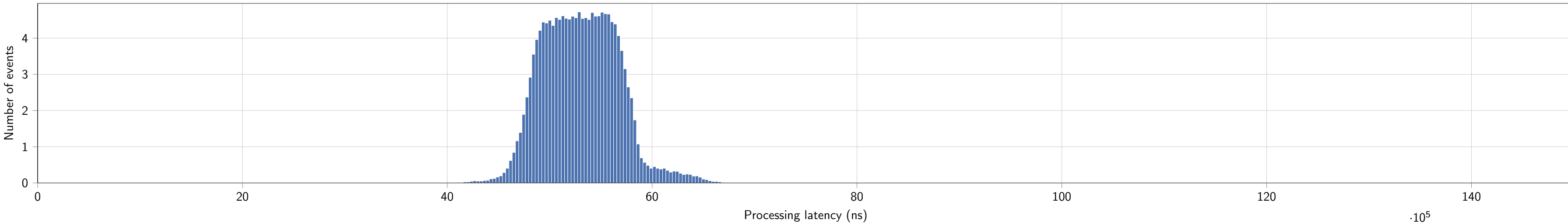
l2_bridging_cnf1_mbit9000.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 8.49mpps, 0.00stdDev



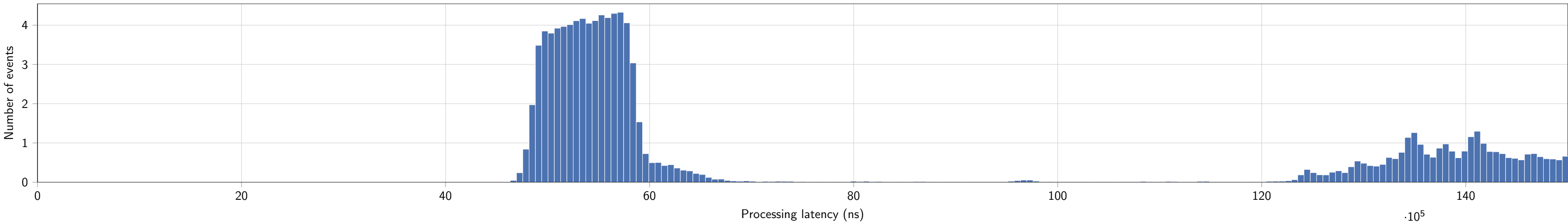
l2_bridging_cnf2_mbit4753hires.histogram.csv: tx: 9.33mpps, 0.04stdDev; rx: 9.34mpps, 0.00stdDev



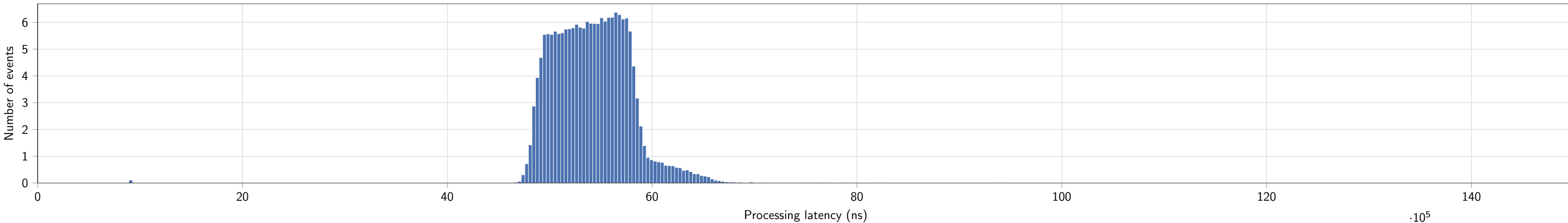
l2_bridging_cnf2_mbit4763hires.histogram.csv: tx: 9.32mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



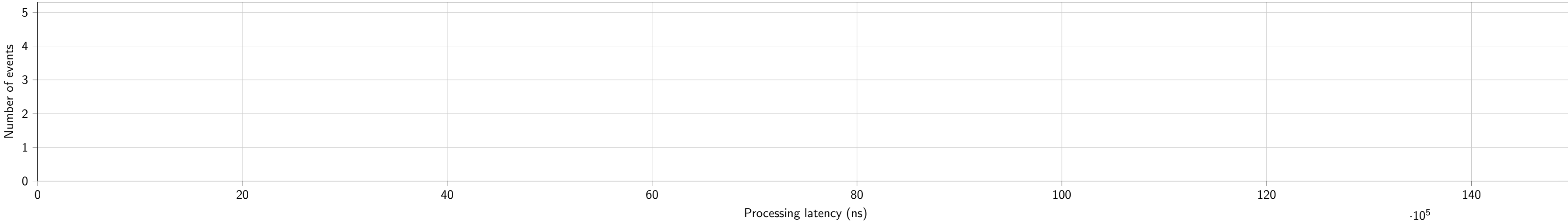
l2_bridging_cnf2_mbit4773hires.histogram.csv: tx: 9.32mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



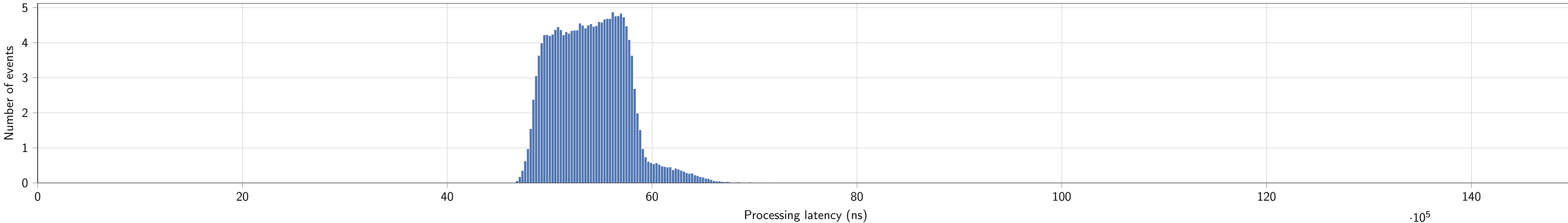
l2_bridging_cnf2_mbit4783hires.histogram.csv: tx: 9.40mpps, 0.05stdDev; rx: 9.41mpps, 0.00stdDev



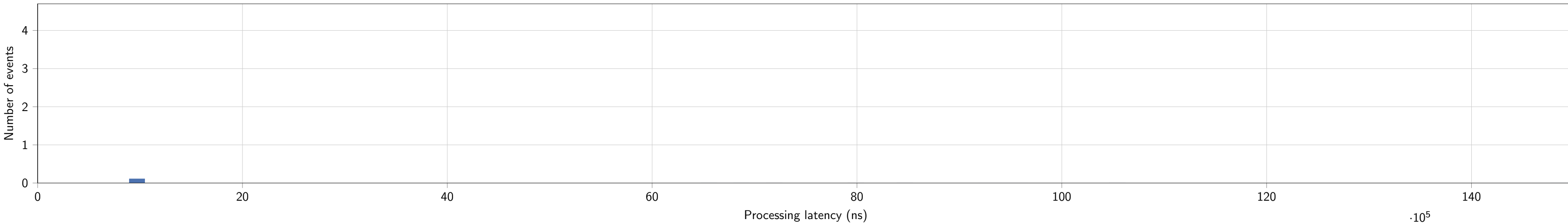
l2_bridging_cnf2_mbit4793_final.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.37mpps, 0.00stdDev



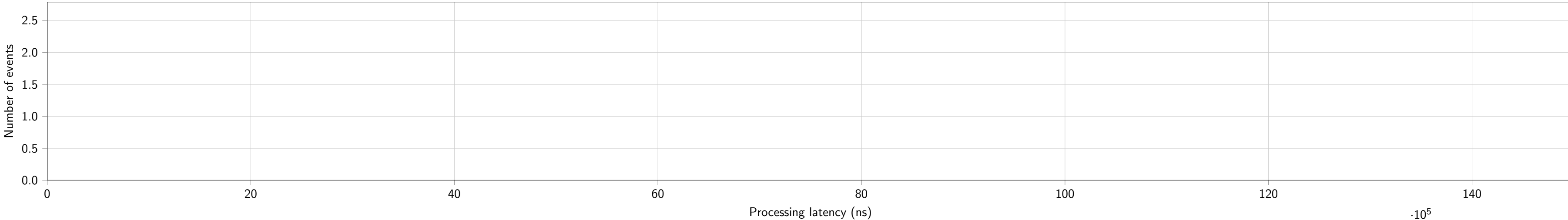
l2_bridging_cnf2_mbit4793hires.histogram.csv: tx: 9.40mpps, 0.05stdDev; rx: 9.41mpps, 0.00stdDev



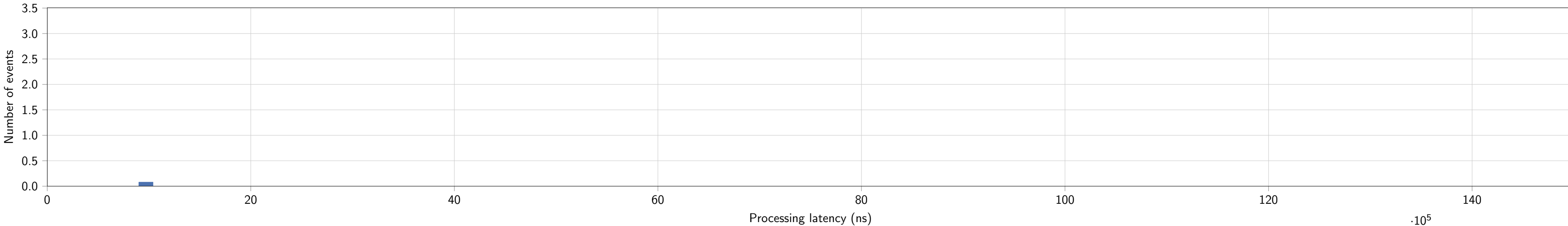
l2_bridging_cnf2_mbit4803hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.38mpps, 0.00stdDev



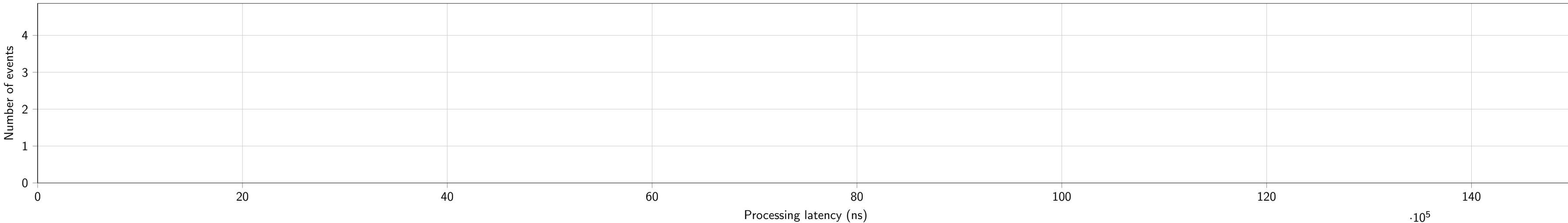
l2_bridging_cnf2_mbit4813hires.histogram.csv: tx: 9.46mpps, 0.04stdDev; rx: 9.36mpps, 0.00stdDev



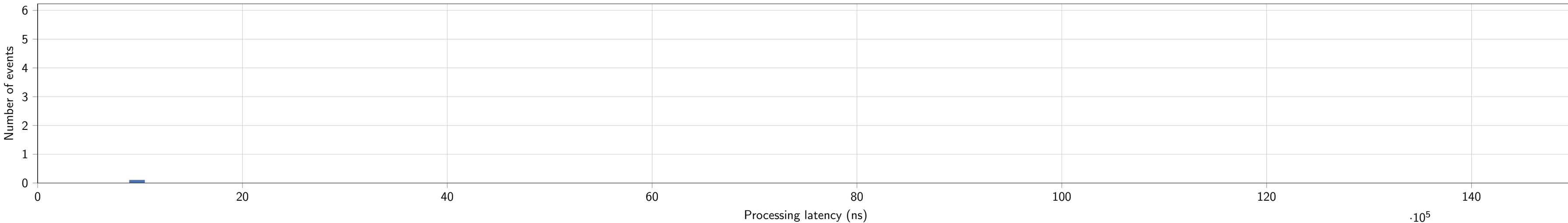
l2_bridging_cnf2_mbit4823hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.36mpps, 0.00stdDev



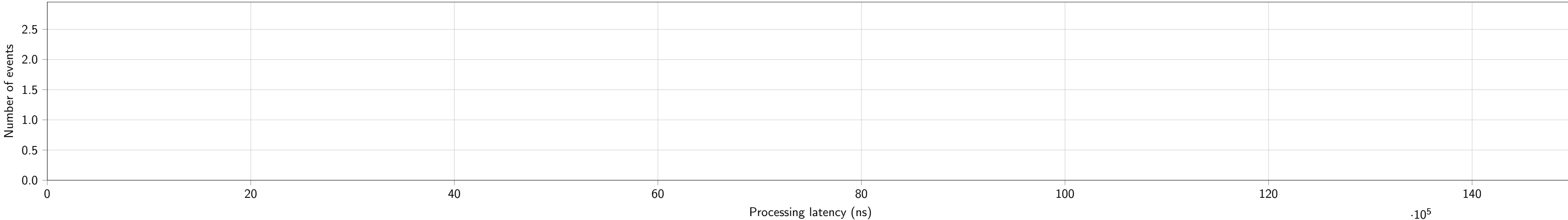
l2_bridging_cnf2_mbit4833hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.39mpps, 0.00stdDev



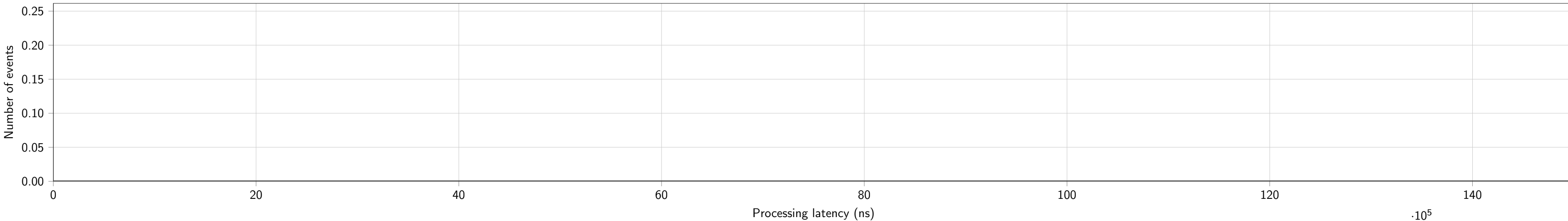
l2_bridging_cnf2_mbit4843hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.41mpps, 0.00stdDev



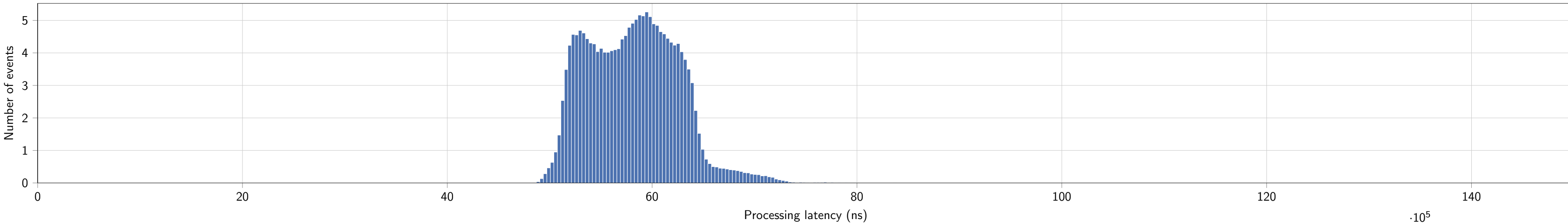
l2_bridging_cnf2_mbit4853hires.histogram.csv: tx: 9.53mpps, 0.05stdDev; rx: 9.40mpps, 0.00stdDev



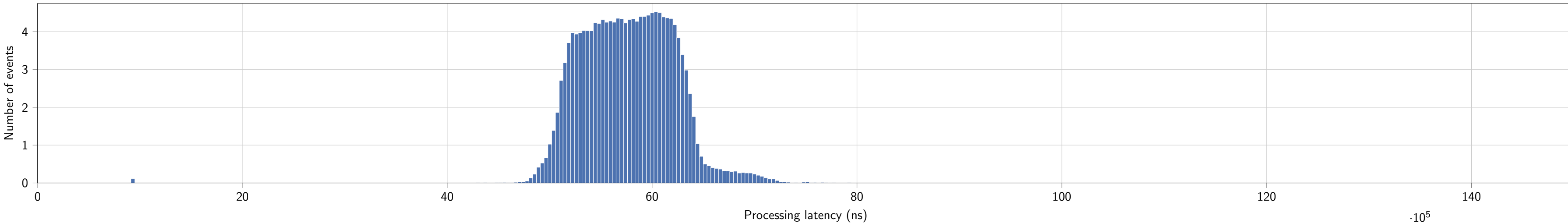
l2_bridging_cnf2_mbit9000.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.38mpps, 0.00stdDev

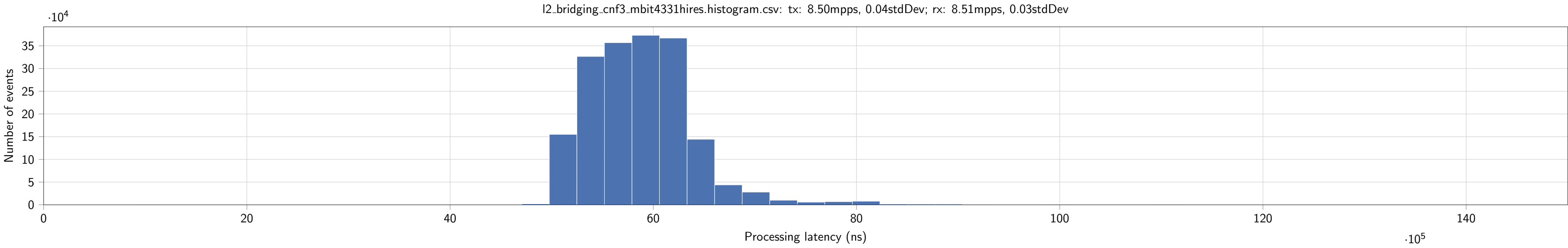


l2_bridging_cnf3_mbit4311hires.histogram.csv: tx: 8.45mpps, 0.04stdDev; rx: 8.46mpps, 0.00stdDev



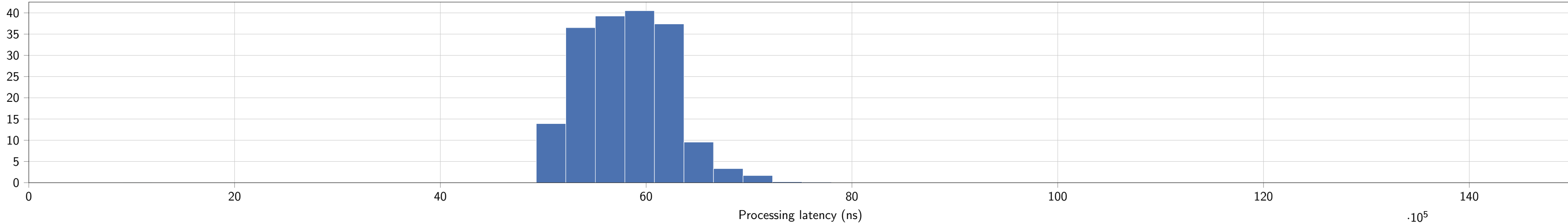
l2_bridging_cnf3_mbit4321hires.histogram.csv: tx: 8.44mpps, 0.05stdDev; rx: 8.46mpps, 0.00stdDev



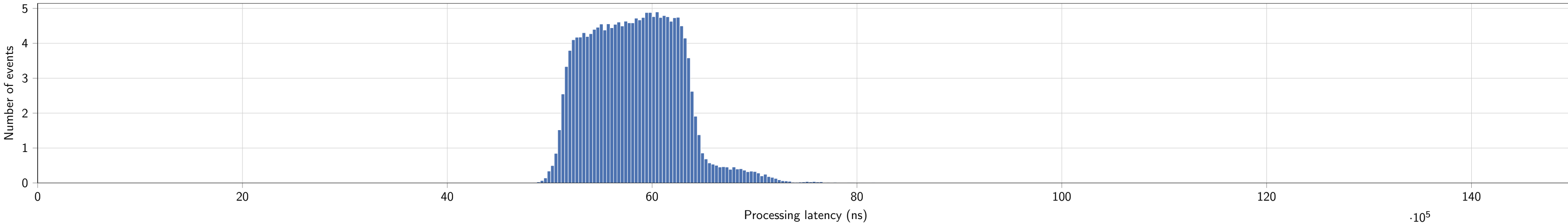


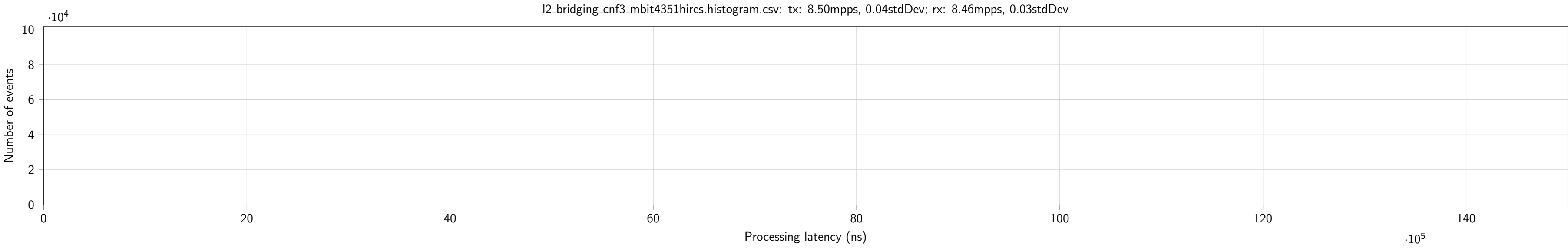
l2_bridging_cnf3_mbit4341_final.histogram.csv: tx: 8.50mpps, 0.04stdDev; rx: 8.51mpps, 0.00stdDev

Number of events

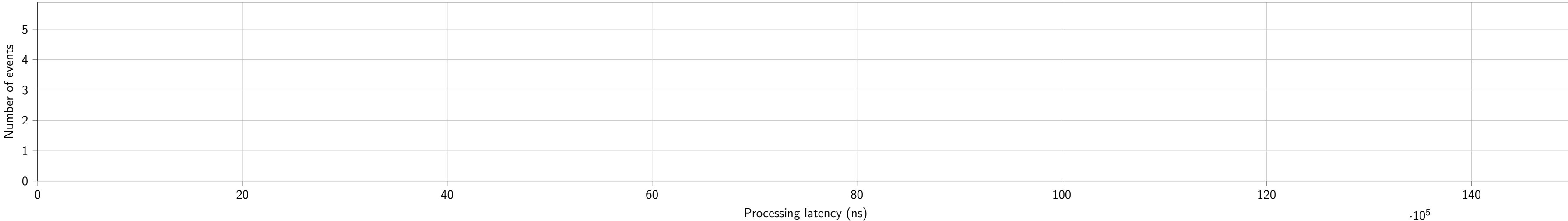


l2_bridging_cnf3_mbit4341hires.histogram.csv: tx: 8.50mpps, 0.04stdDev; rx: 8.51mpps, 0.00stdDev

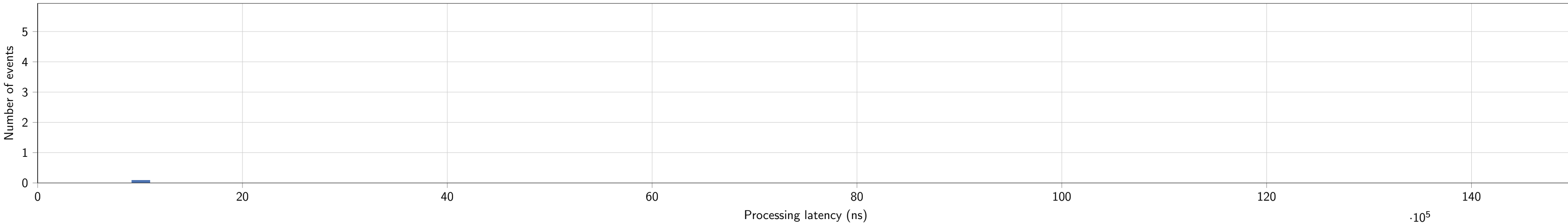


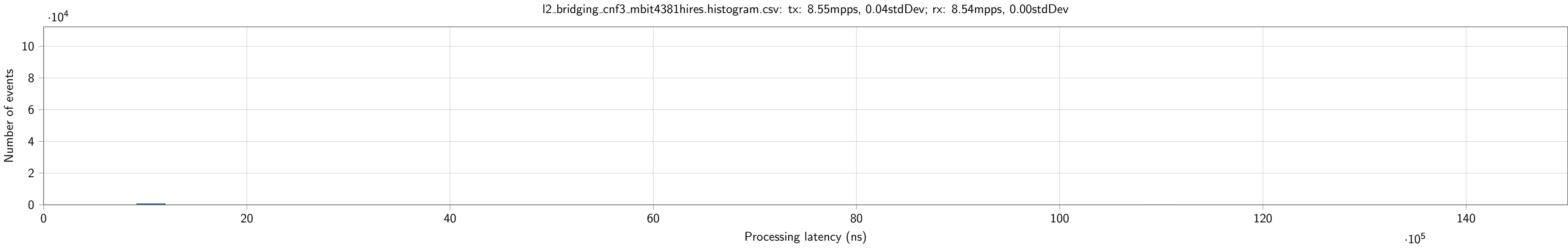


l2_bridging_cnf3_mbit4361hires.histogram.csv: tx: 8.55mpps, 0.04stdDev; rx: 8.54mpps, 0.00stdDev

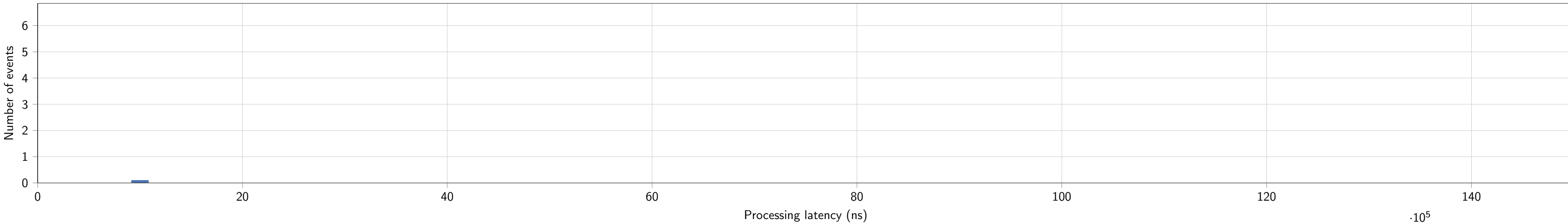


l2_bridging_cnf3_mbit4371hires.histogram.csv: tx: 8.55mpps, 0.04stdDev; rx: 8.39mpps, 0.00stdDev

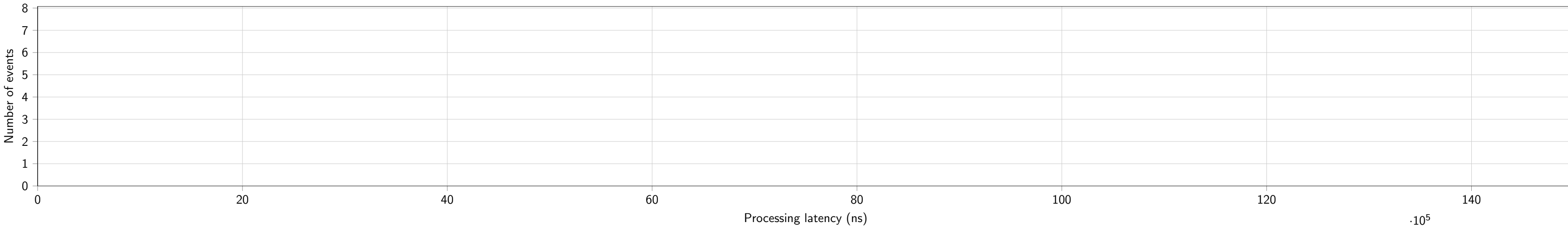




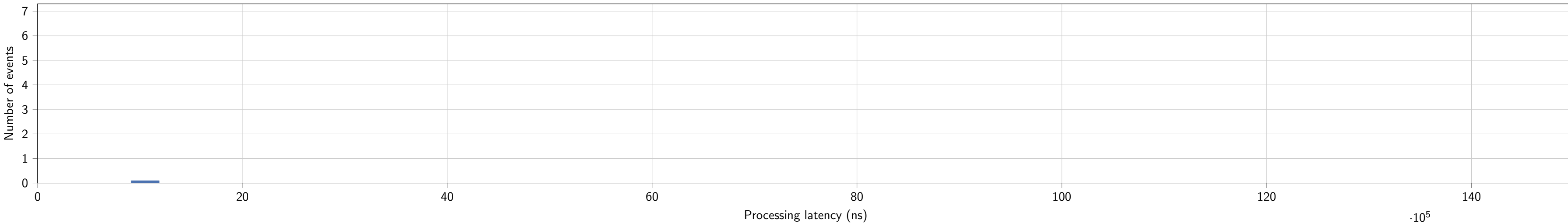
l2_bridging_cnf3_mbit4391hires.histogram.csv: tx: 8.61mpps, 0.04stdDev; rx: 8.55mpps, 0.00stdDev



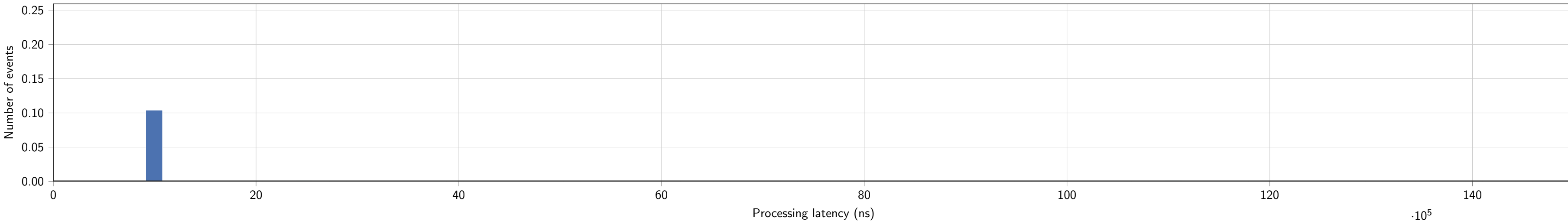
l2_bridging_cnf3_mbit4401hires.histogram.csv: tx: 8.61mpps, 0.04stdDev; rx: 8.54mpps, 0.00stdDev



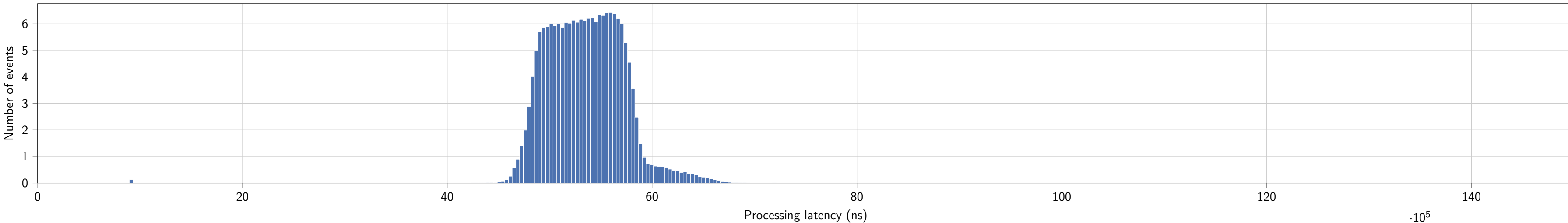
l2_bridging_cnf3_mbit4411hires.histogram.csv: tx: 8.61mpps, 0.04stdDev; rx: 8.52mpps, 0.00stdDev



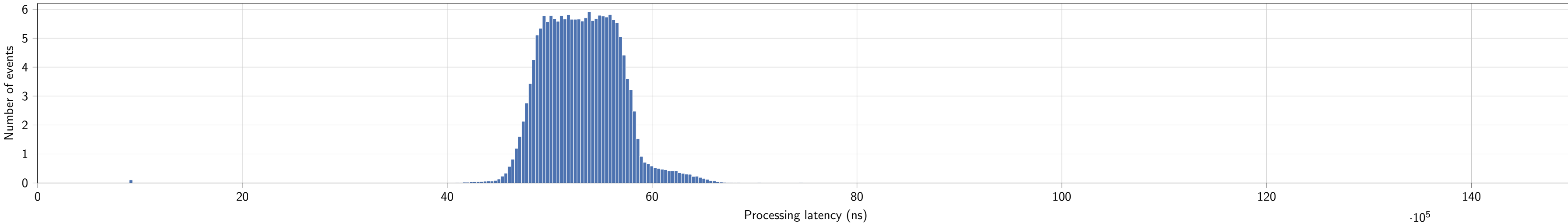
l2_bridging_cnf3_mbit9000.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 8.52mpps, 0.04stdDev

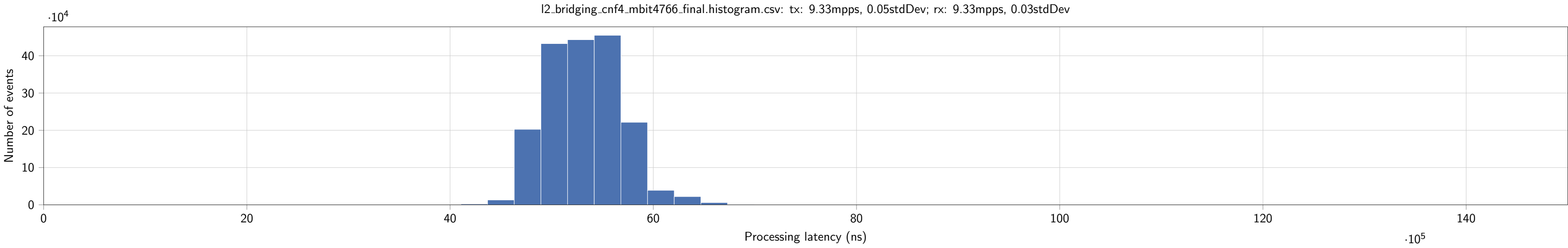


l2_bridging_cnf4_mbit4746hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev

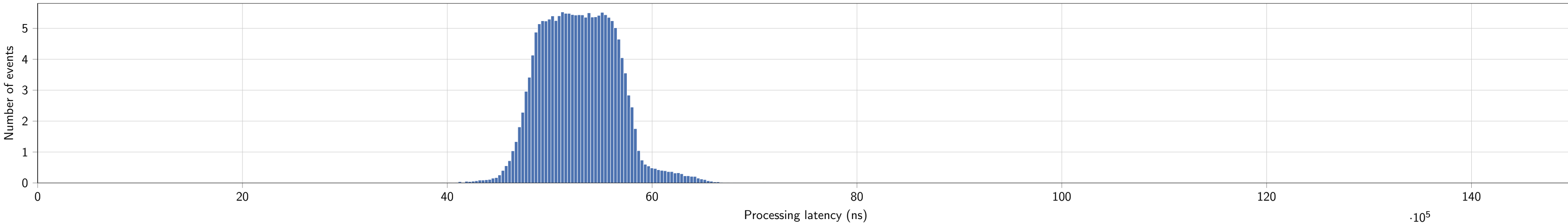


l2_bridging_cnf4_mbit4756hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev

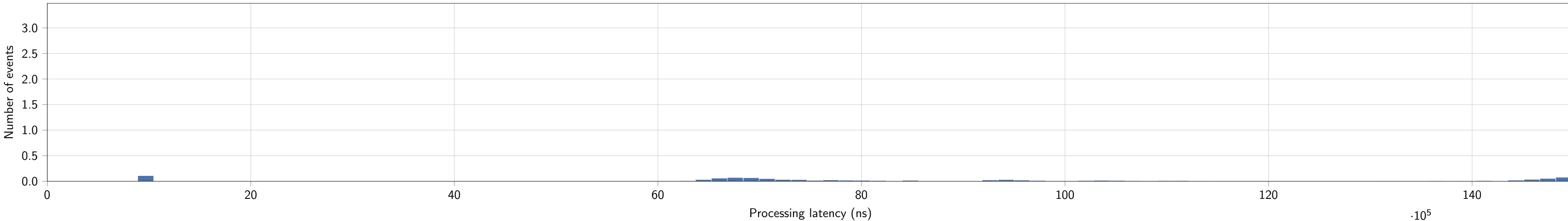




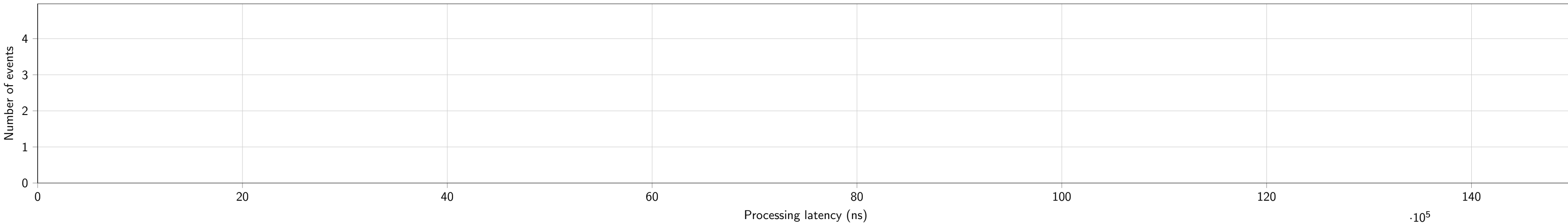
l2_bridging_cnf4_mbit4766hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



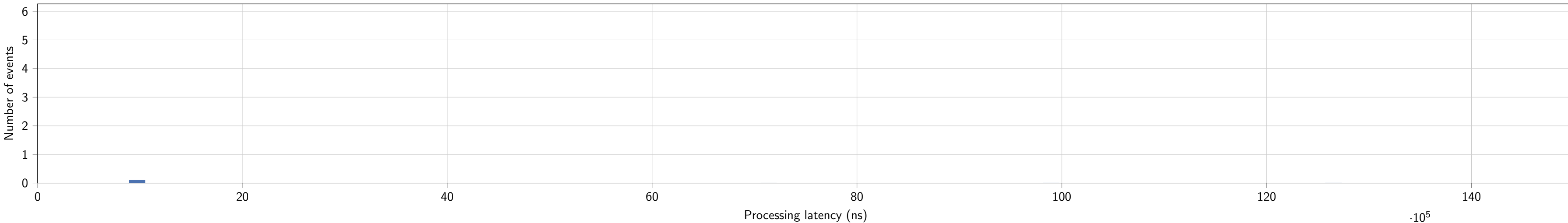
l2_bridging_cnf4_mbit4776hires.histogram.csv: tx: 9.32mpps, 0.05stdDev; rx: 9.33mpps, 0.00stdDev



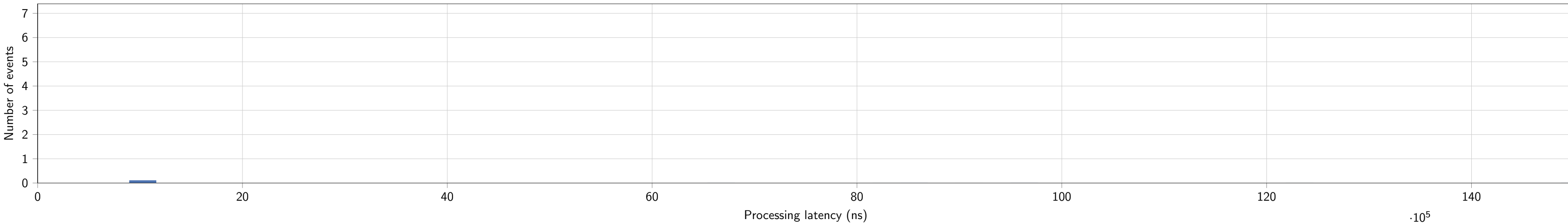
l2_bridging_cnf4_mbit4786hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.38mpps, 0.00stdDev



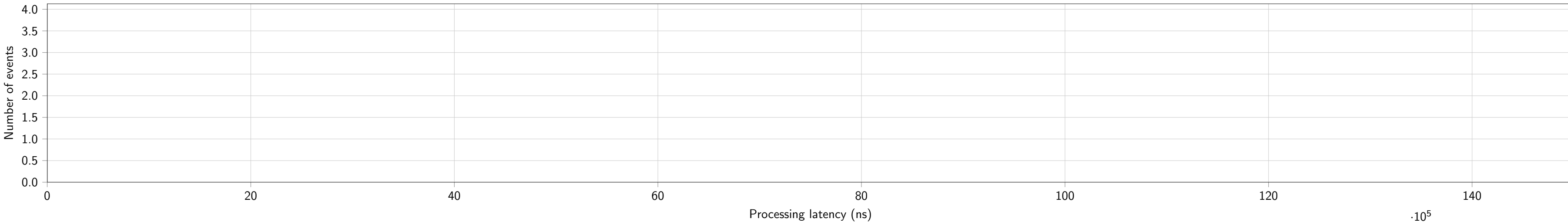
l2_bridging_cnf4_mbit4796hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.37mpps, 0.00stdDev



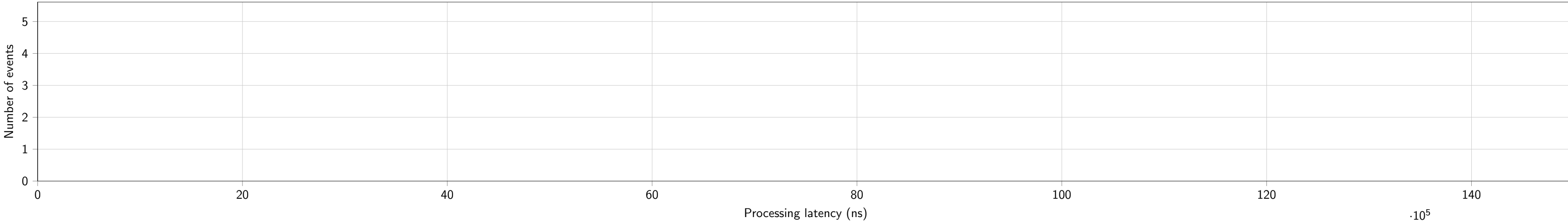
l2_bridging_cnf4_mbit4806hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.37mpps, 0.00stdDev



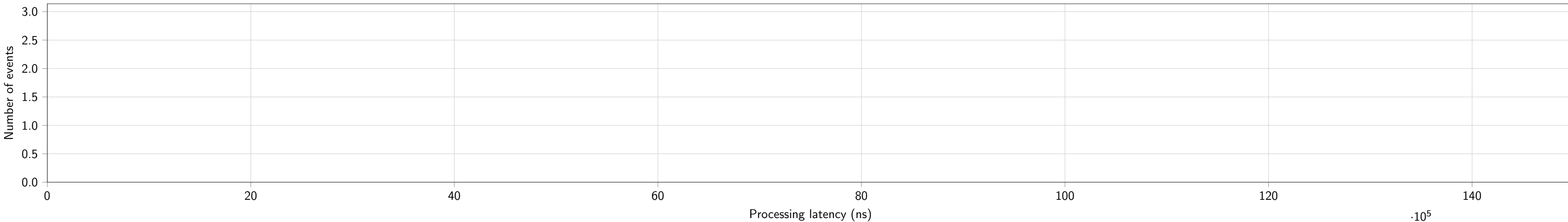
l2_bridging_cnf4_mbit4816hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.39mpps, 0.00stdDev



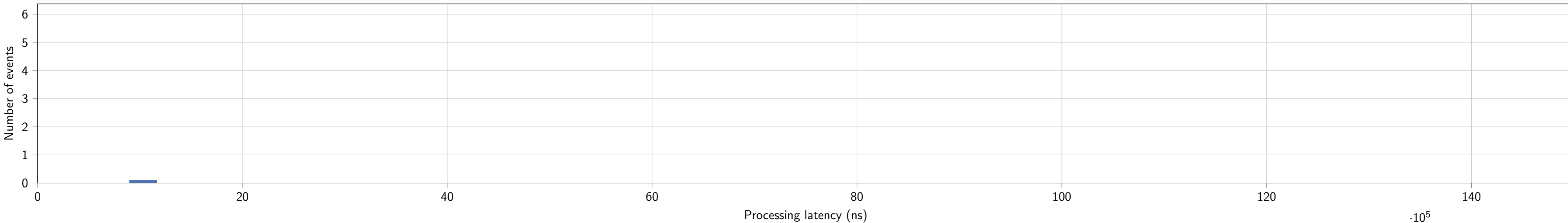
l2_bridging_cnf4_mbit4826hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.40mpps, 0.00stdDev



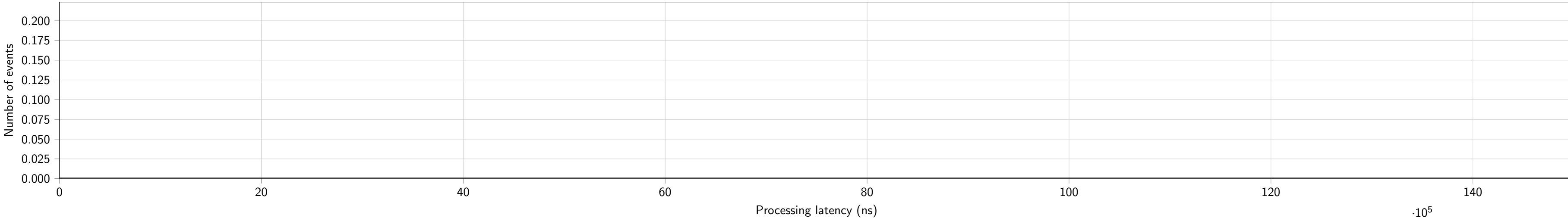
l2_bridging_cnf4_mbit4836hires.histogram.csv: tx: 9.46mpps, 0.04stdDev; rx: 9.28mpps, 0.00stdDev



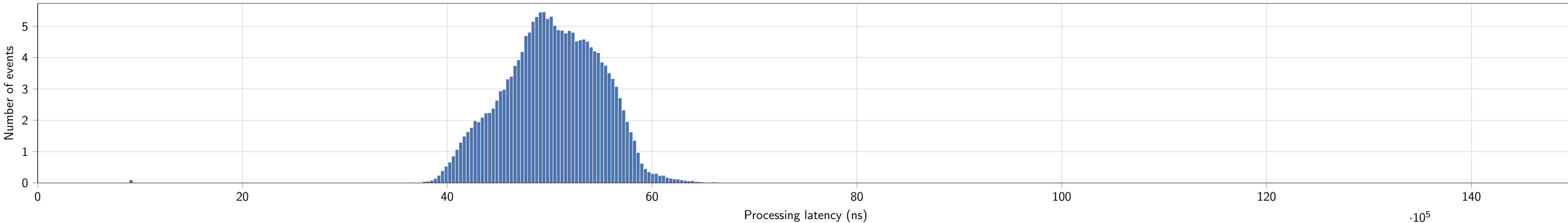
l2_bridging_cnf4_mbit4846hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.37mpps, 0.04stdDev



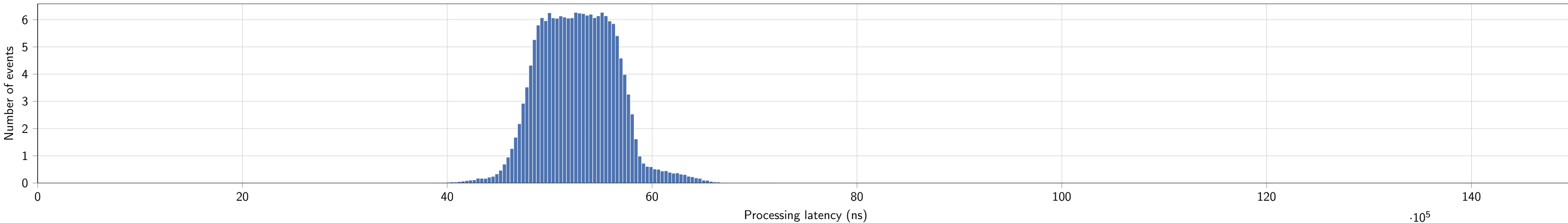
l2_bridging_cnf4_mbit9000.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 9.37mpps, 0.00stdDev



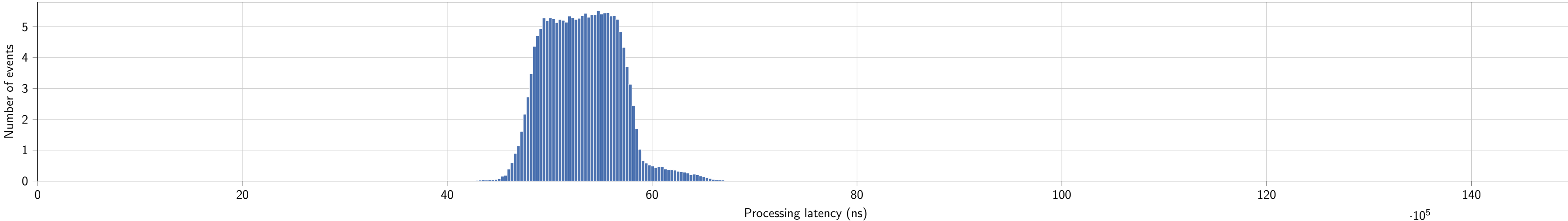
l2_bridging_cnf5_mbit4740hires.histogram.csv: tx: 9.26mpps, 0.04stdDev; rx: 9.27mpps, 0.00stdDev



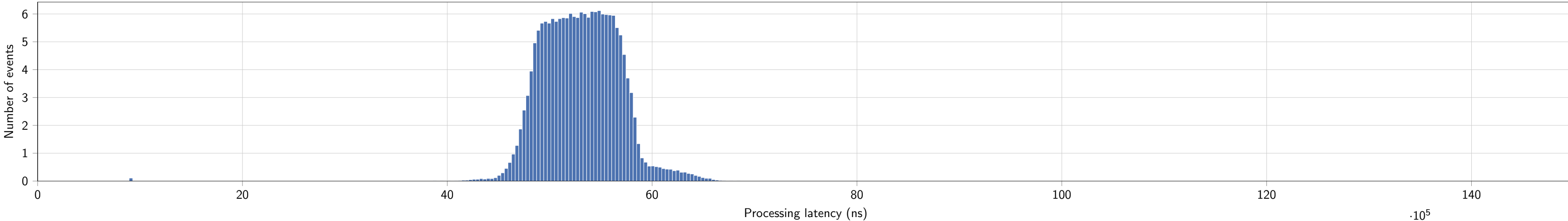
l2_bridging_cnf5_mbit4750hires.histogram.csv: tx: 9.33mpps, 0.04stdDev; rx: 9.34mpps, 0.00stdDev



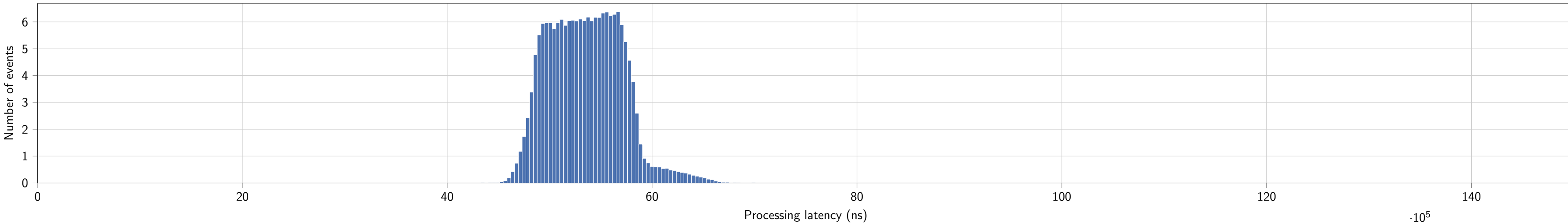
l2_bridging_cnf5_mbit4760hires.histogram.csv: tx: 9.33mpps, 0.04stdDev; rx: 9.34mpps, 0.00stdDev



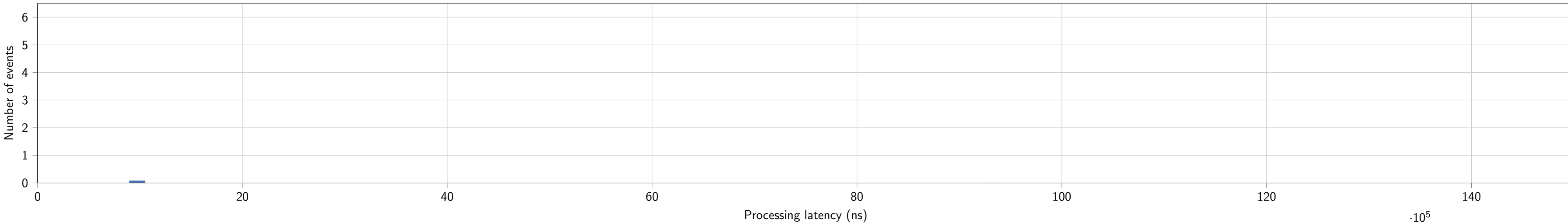
l2_bridging_cnf5_mbit4770_final.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



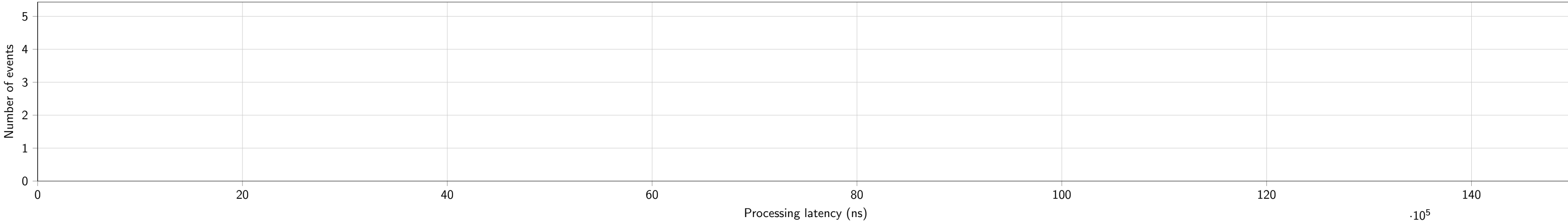
l2_bridging_cnf5_mbit4770hires.histogram.csv: tx: 9.33mpps, 0.05stdDev; rx: 9.34mpps, 0.00stdDev



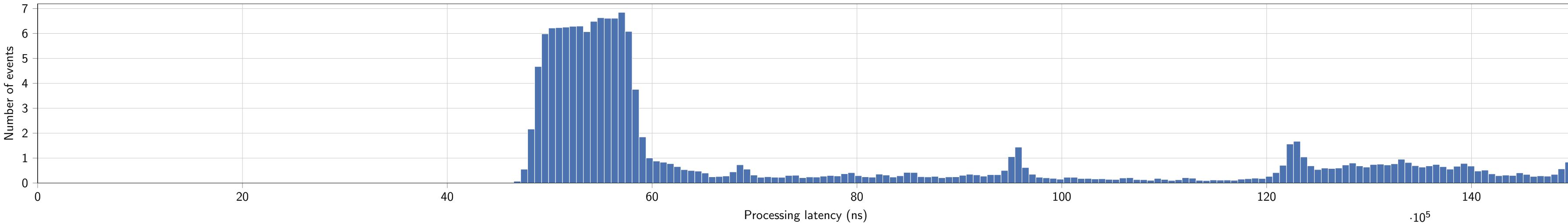
l2_bridging_cnf5_mbit4780hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.37mpps, 0.00stdDev



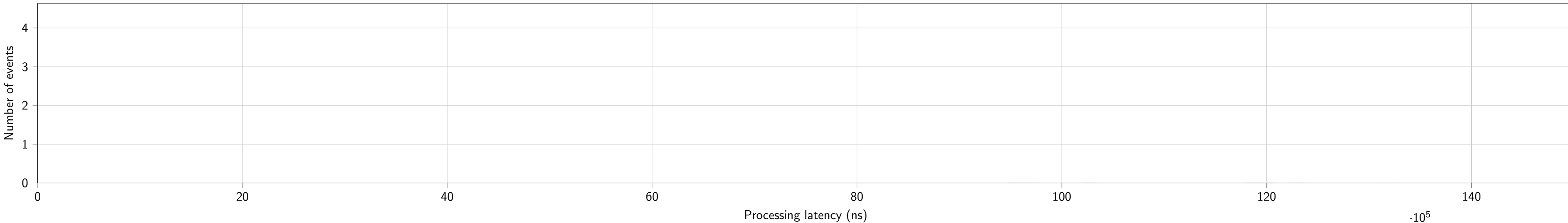
l2_bridging_cnf5_mbit4790hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.28mpps, 0.00stdDev



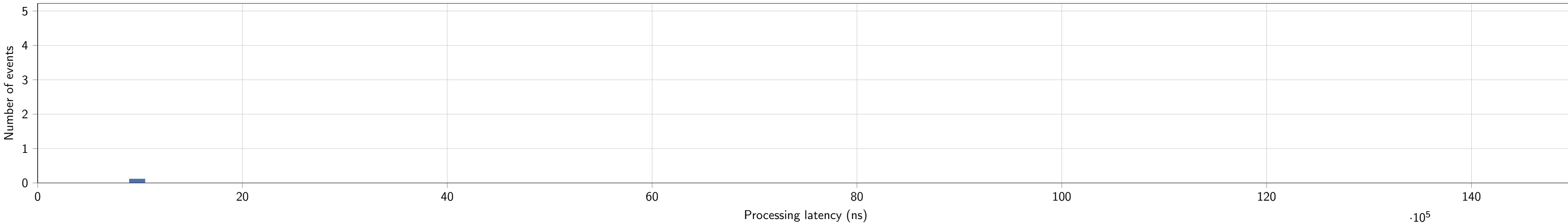
l2_bridging_cnf5_mbit4800hires.histogram.csv: tx: 9.40mpps, 0.04stdDev; rx: 9.41mpps, 0.00stdDev



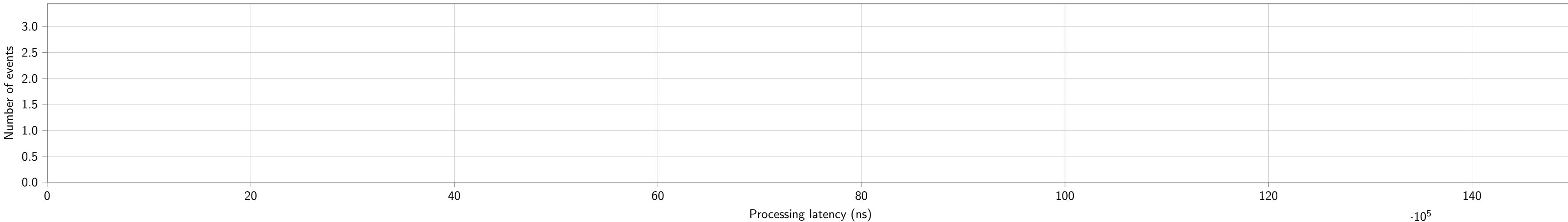
l2_bridging_cnf5_mbit4810hires.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.38mpps, 0.00stdDev



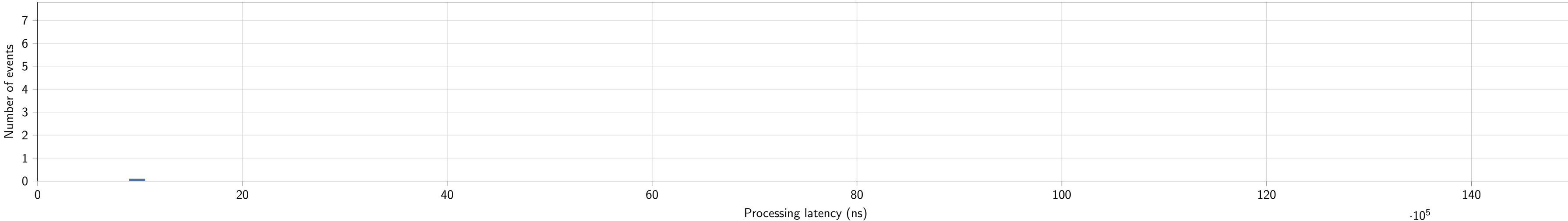
l2_bridging_cnf5_mbit4820hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.38mpps, 0.00stdDev



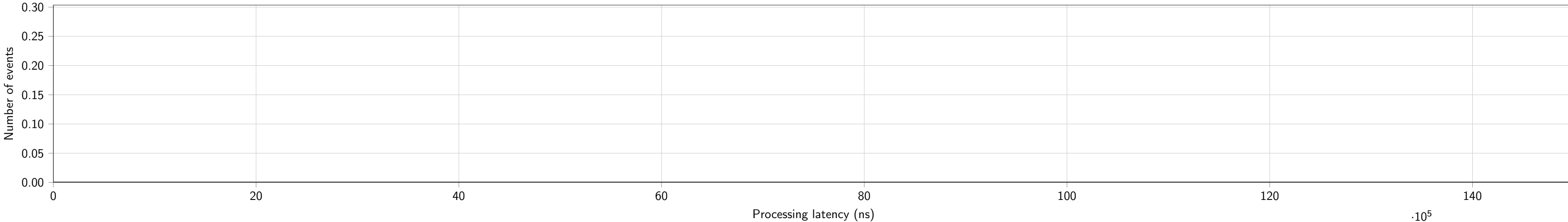
l2_bridging_cnf5_mbit4830hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.38mpps, 0.00stdDev

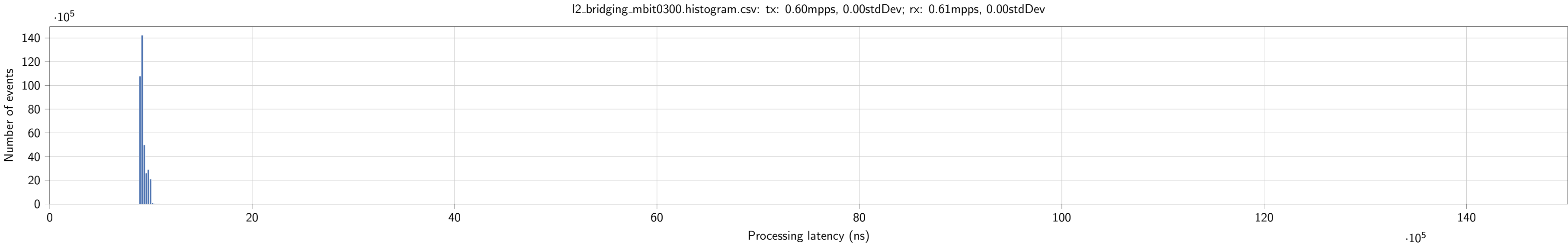


l2_bridging_cnf5_mbit4840hires.histogram.csv: tx: 9.46mpps, 0.05stdDev; rx: 9.25mpps, 0.00stdDev

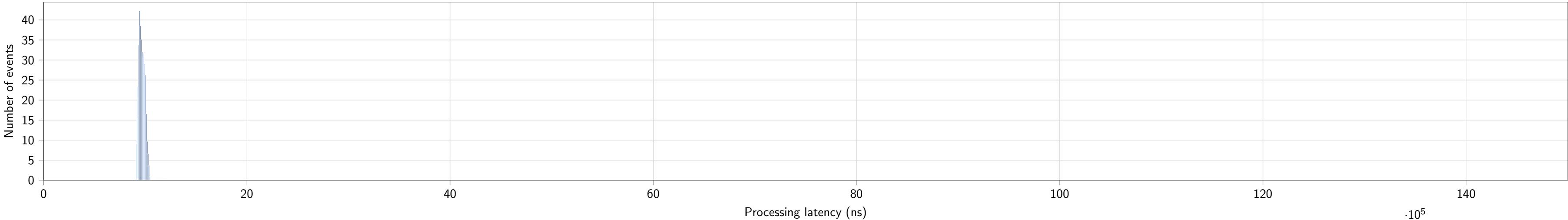


l2_bridging_cnf5_mbit9000.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.36mpps, 0.00stdDev

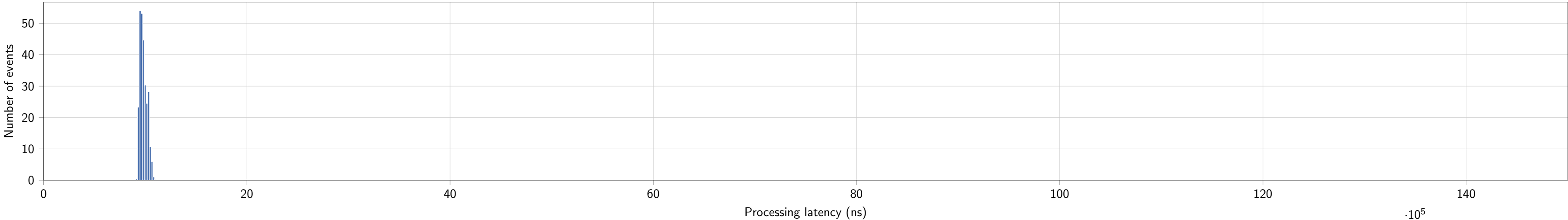




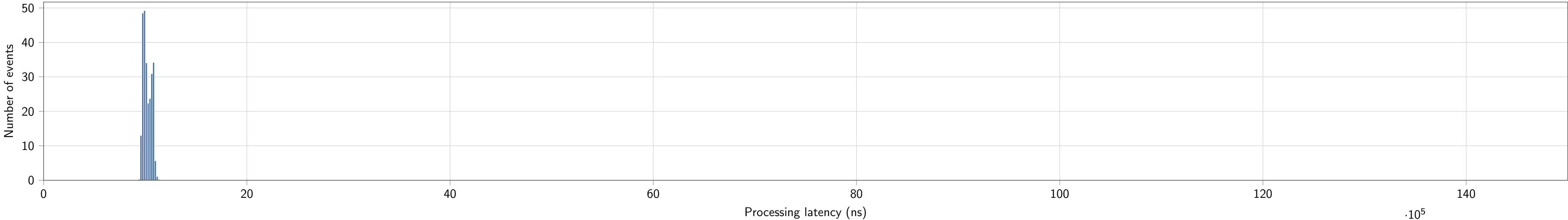
l2_bridging_mbit0600.histogram.csv: tx: 1.19mpps, 0.01stdDev; rx: 1.19mpps, 0.00stdDev

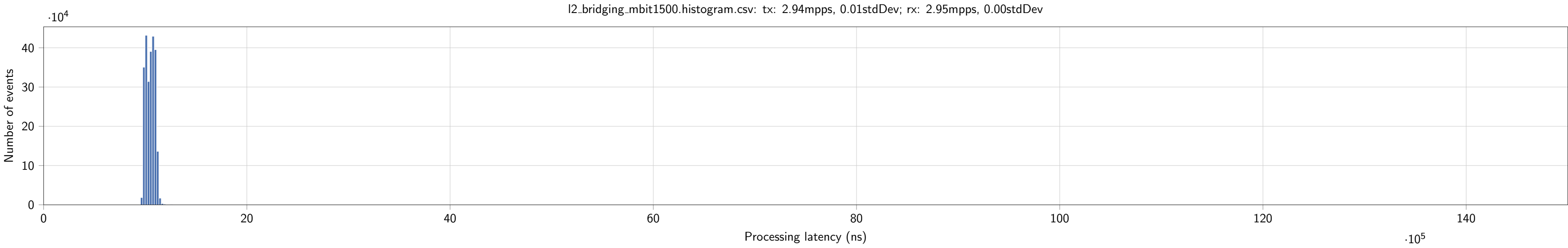


l2_bridging_mbit0900.histogram.csv: tx: 1.77mpps, 0.01stdDev; rx: 1.77mpps, 0.00stdDev

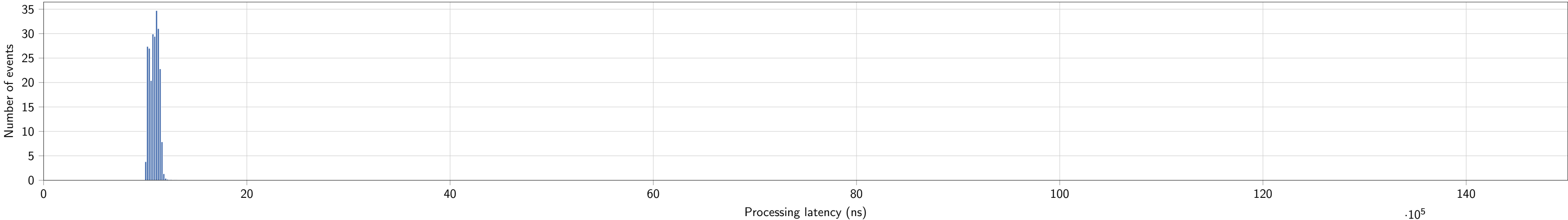


l2_bridging_mbit1200.histogram.csv: tx: 2.36mpps, 0.01stdDev; rx: 2.36mpps, 0.00stdDev

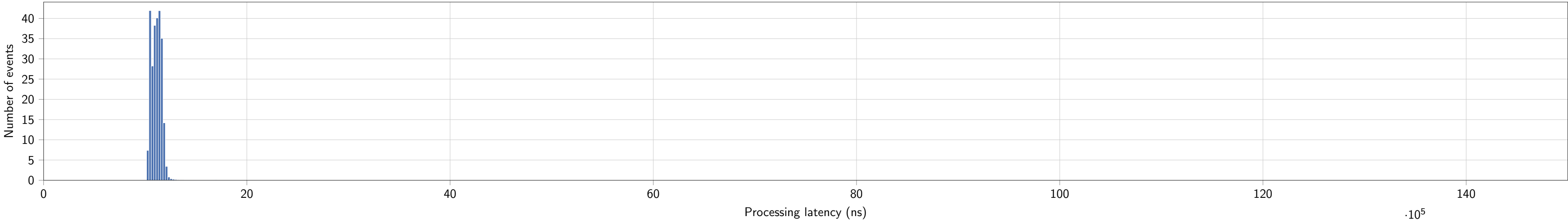




l2_bridging_mbit1800.histogram.csv: tx: 3.53mpps, 0.02stdDev; rx: 3.53mpps, 0.00stdDev



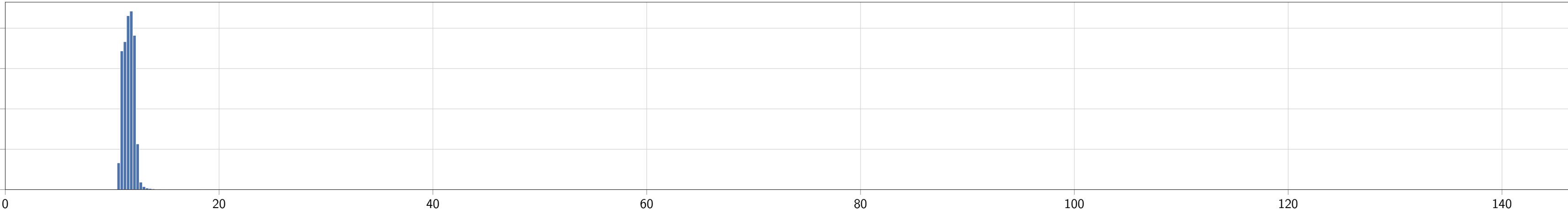
l2_bridging_mbit2100.histogram.csv: tx: 4.12mpps, 0.02stdDev; rx: 4.12mpps, 0.00stdDev



l2_bridging_mbit2400.histogram.csv: tx: 4.71mpps, 0.02stdDev; rx: 4.71mpps, 0.00stdDev

Number of events

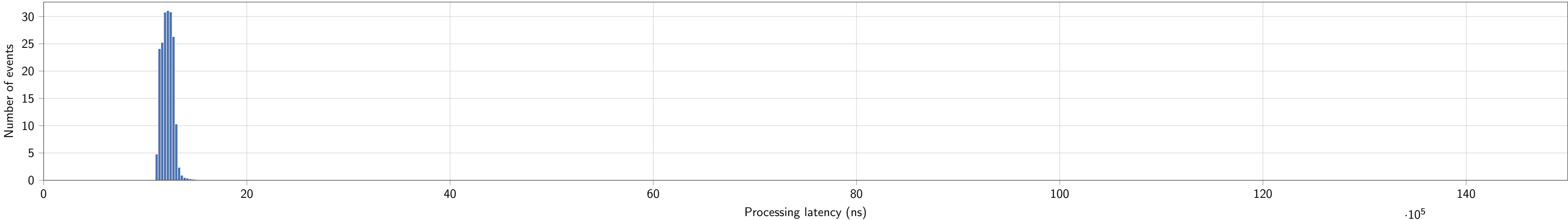
$\cdot 10^4$



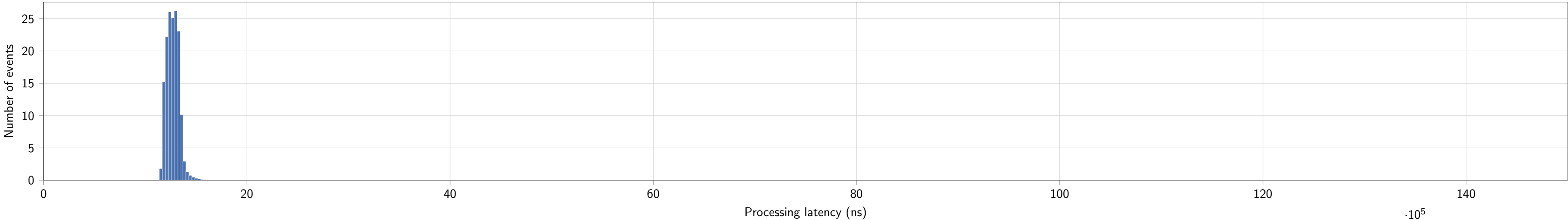
Processing latency (ns)

$\cdot 10^5$

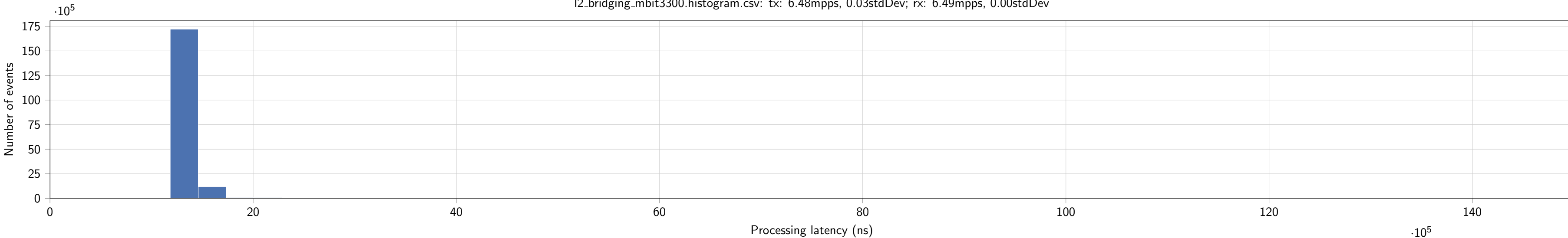
l2_bridging_mbit2700.histogram.csv: tx: 5.28mpps, 0.03stdDev; rx: 5.28mpps, 0.00stdDev



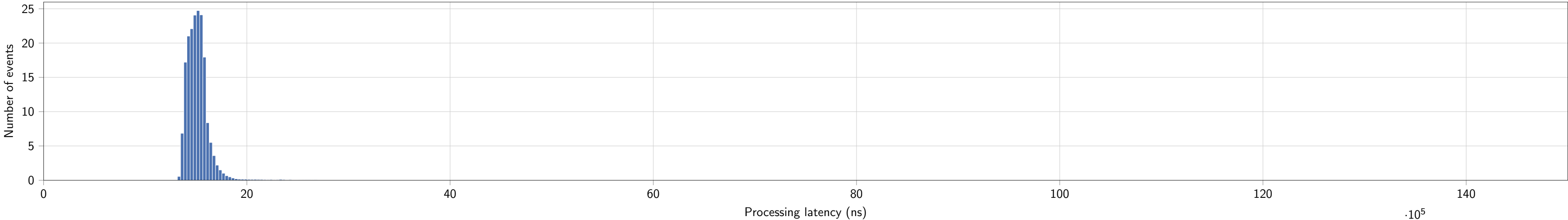
l2_bridging_mbit3000.histogram.csv: tx: 5.87mpps, 0.03stdDev; rx: 5.88mpps, 0.00stdDev



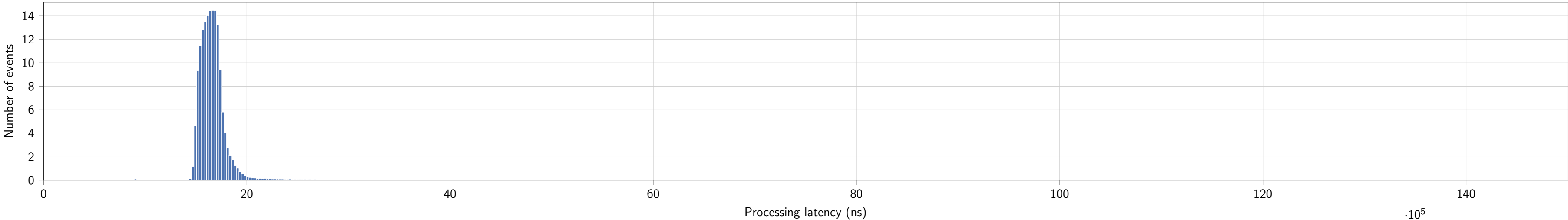
l2_bridging_mbit3300.histogram.csv: tx: 6.48mpps, 0.03stdDev; rx: 6.49mpps, 0.00stdDev

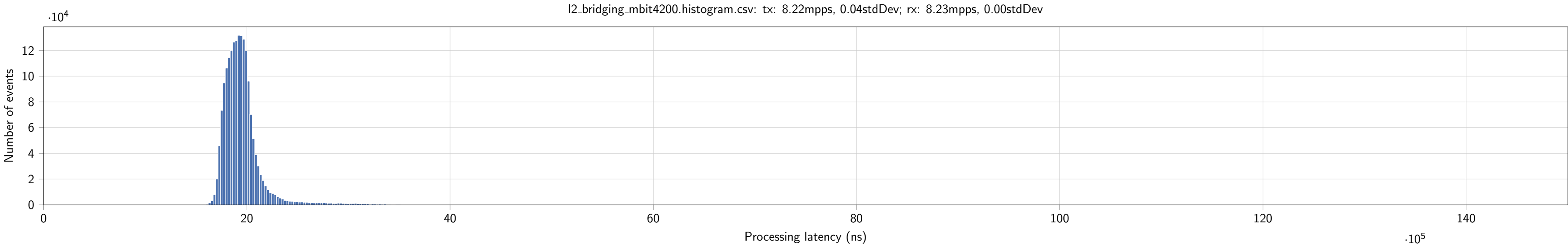


l2_bridging_mbit3600.histogram.csv: tx: 7.06mpps, 0.03stdDev; rx: 7.07mpps, 0.00stdDev

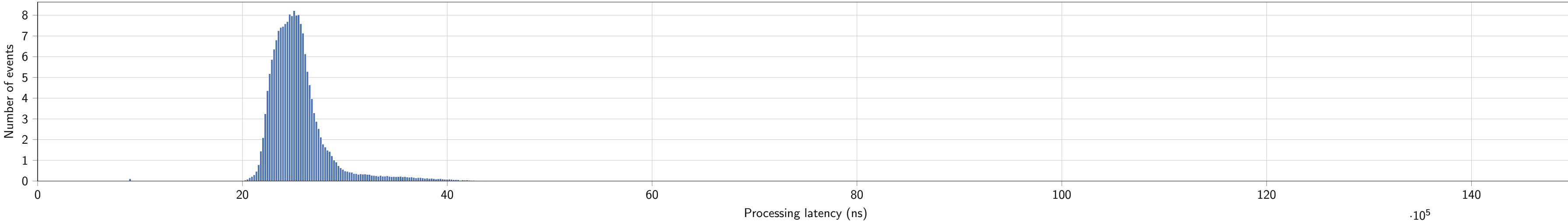


l2_bridging_mbit3900.histogram.csv: tx: 7.62mpps, 0.04stdDev; rx: 7.63mpps, 0.00stdDev

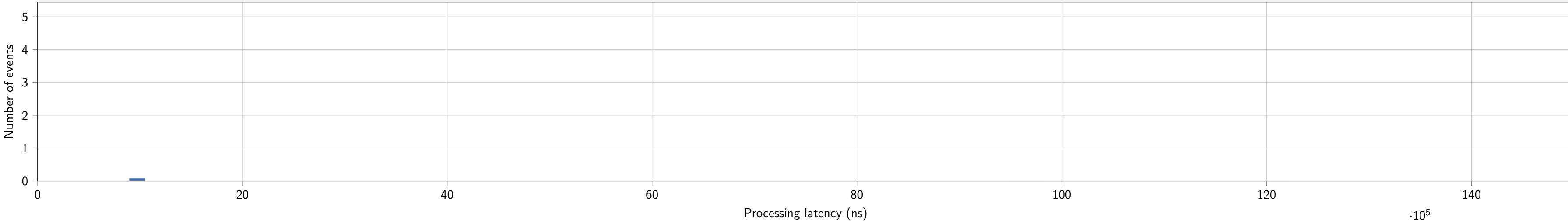




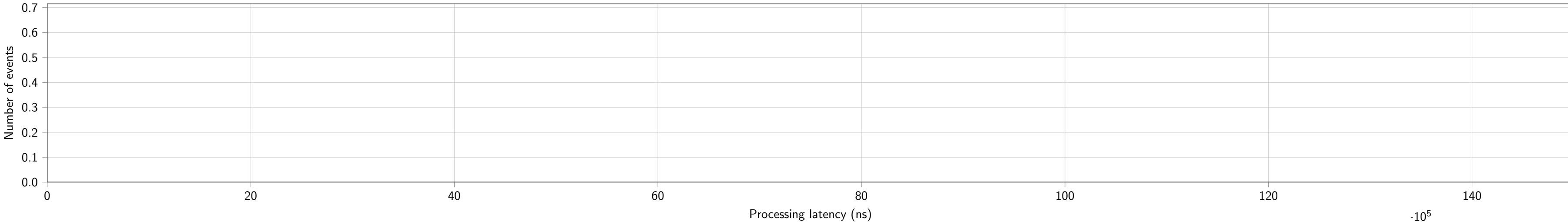
l2_bridging_mbit4500.histogram.csv: tx: 8.80mpps, 0.04stdDev; rx: 8.81mpps, 0.00stdDev



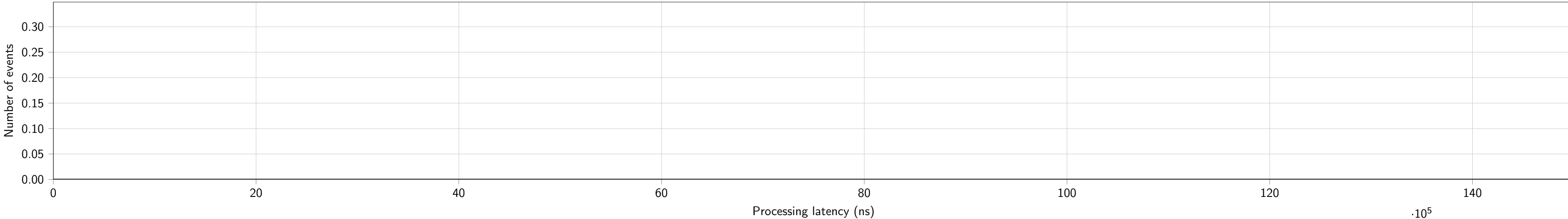
l2_bridging_mbit4800.histogram.csv: tx: 9.39mpps, 0.05stdDev; rx: 9.36mpps, 0.00stdDev



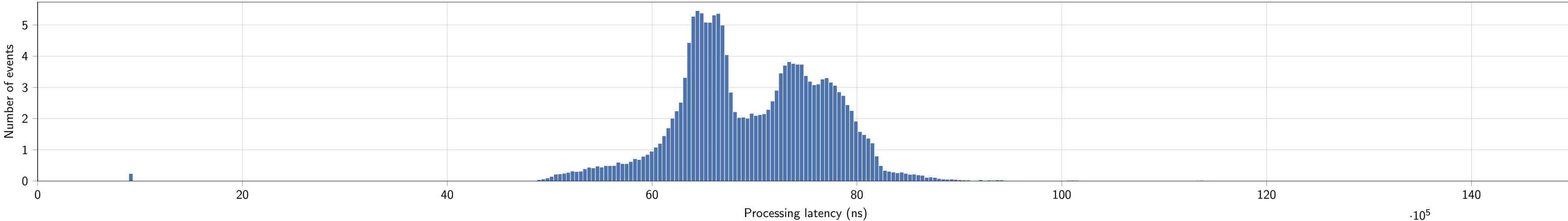
l2_bridging_mbit5100.histogram.csv: tx: 9.99mpps, 0.05stdDev; rx: 9.32mpps, 0.00stdDev



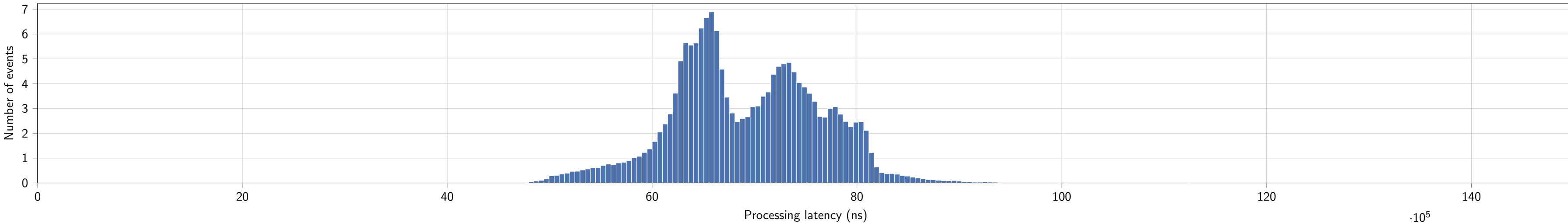
l2_bridging_mbit5400.histogram.csv: tx: 10.58mpps, 0.05stdDev; rx: 9.35mpps, 0.00stdDev



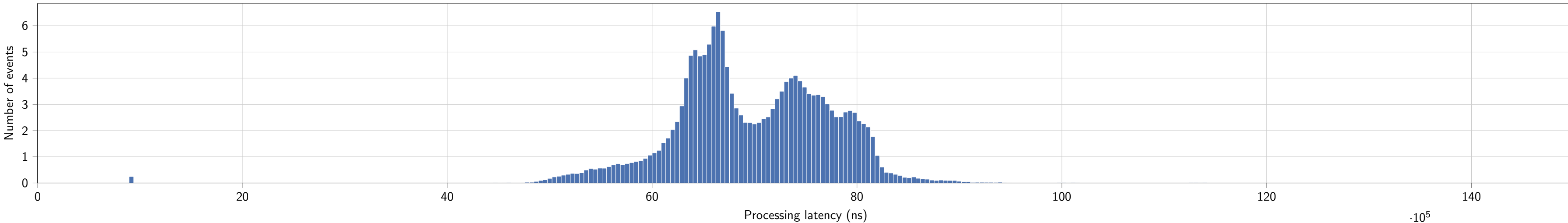
l2_multimac_00000100_mbit3505hires.histogram.csv: tx: 6.87mpps, 0.04stdDev; rx: 6.88mpps, 0.00stdDev



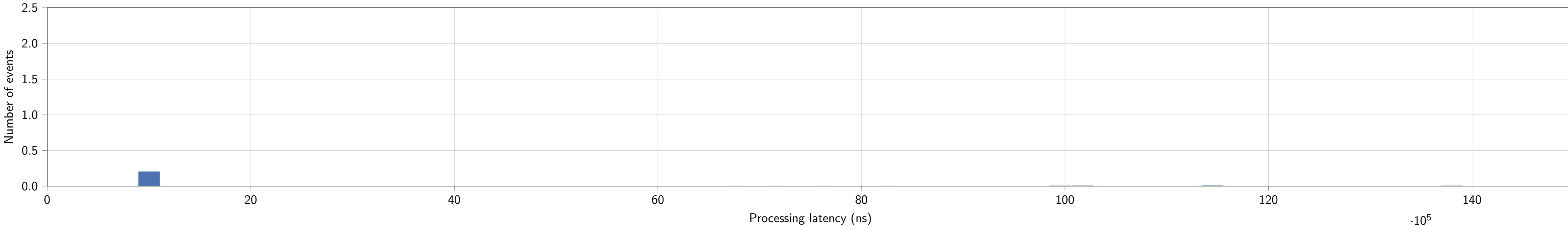
l2_multimac_00000100_mbit3515_final.histogram.csv: tx: 6.87mpps, 0.04stdDev; rx: 6.88mpps, 0.00stdDev



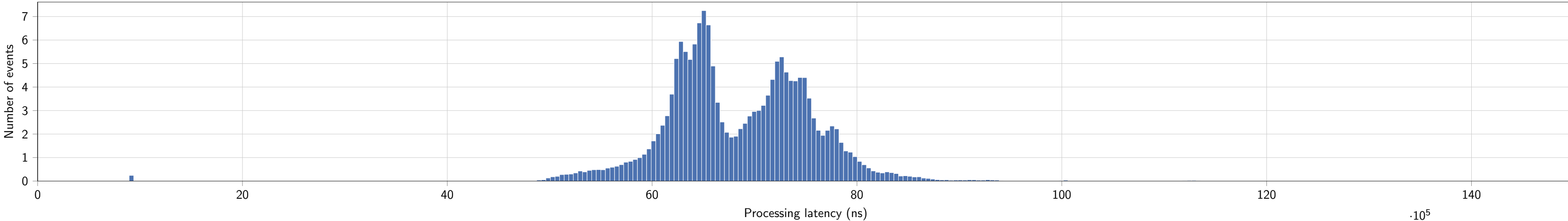
l2_multimac_00000100_mbit3515hires.histogram.csv: tx: 6.87mpps, 0.04stdDev; rx: 6.88mpps, 0.00stdDev



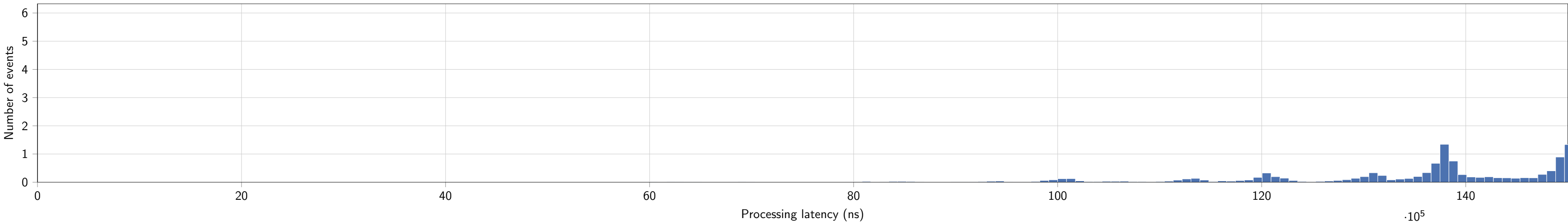
l2_multimac_00000100_mbit3525hires.histogram.csv: tx: 6.90mpps, 0.04stdDev; rx: 6.90mpps, 0.00stdDev



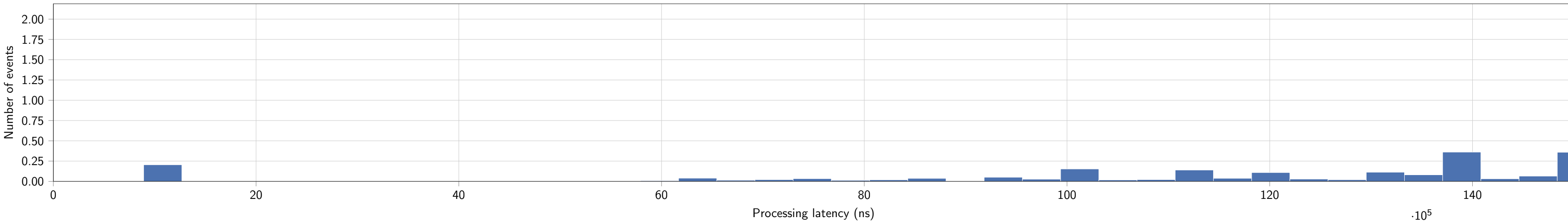
l2_multimac_00000100_mbit3535hires.histogram.csv: tx: 6.90mpps, 0.04stdDev; rx: 6.91mpps, 0.00stdDev



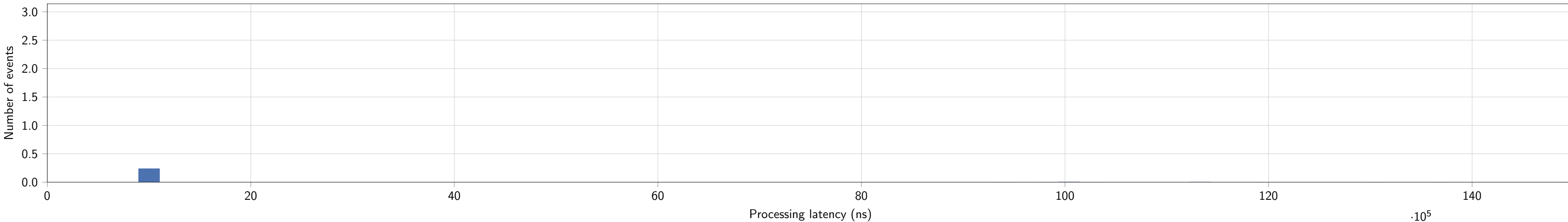
l2_multimac_00000100_mbit3545hires.histogram.csv: tx: 6.94mpps, 0.04stdDev; rx: 6.95mpps, 0.00stdDev



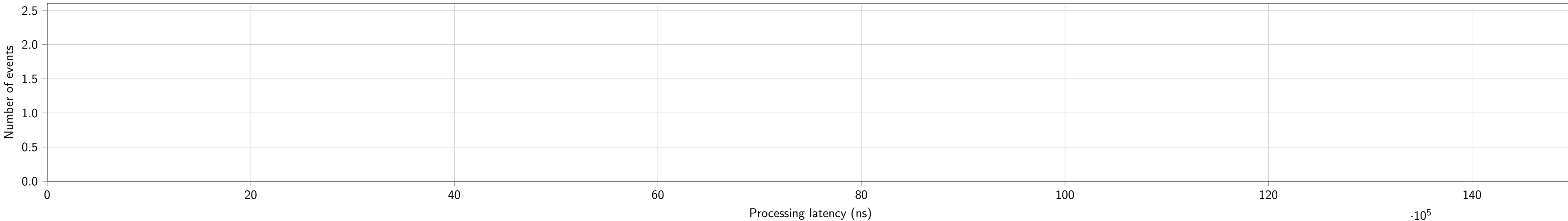
l2_multimac_00000100_mbit3555hires.histogram.csv: tx: 6.94mpps, 0.04stdDev; rx: 6.94mpps, 0.03stdDev



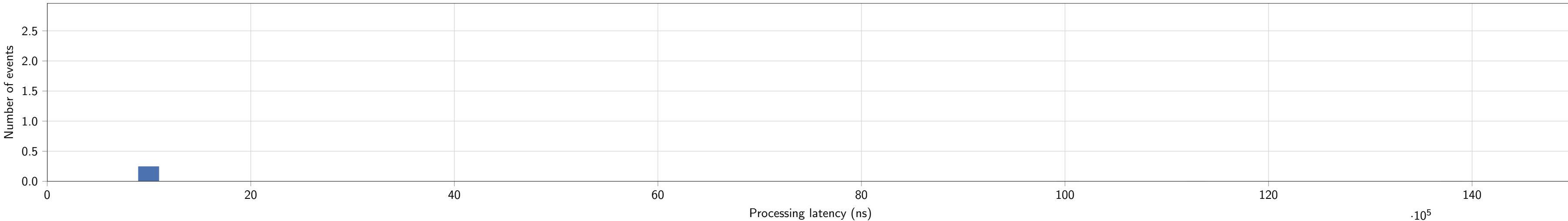
l2_multimac_00000100_mbit3565hires.histogram.csv: tx: 6.97mpps, 0.04stdDev; rx: 6.97mpps, 0.00stdDev



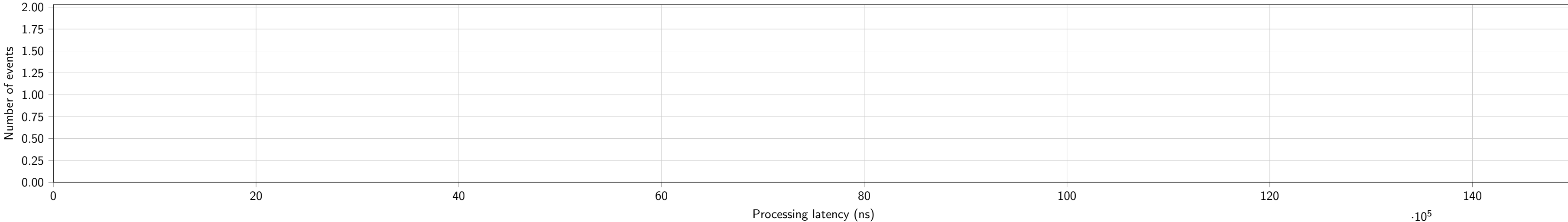
l2_multimac_00000100_mbit3575hires.histogram.csv: tx: 6.97mpps, 0.04stdDev; rx: 6.88mpps, 0.00stdDev



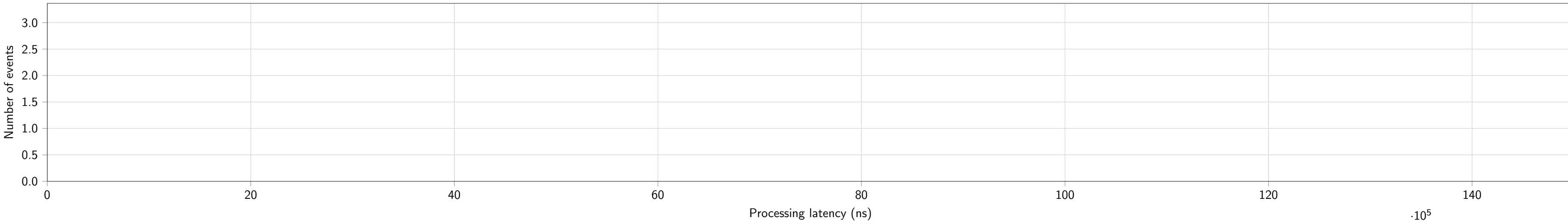
l2_multimac_00000100_mbit3585hires.histogram.csv: tx: 7.01mpps, 0.04stdDev; rx: 6.96mpps, 0.00stdDev



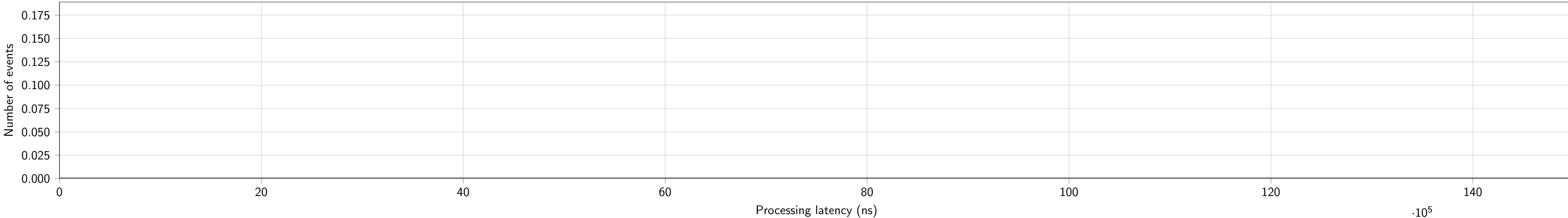
l2_multimac_00000100_mbit3595hires.histogram.csv: tx: 7.01mpps, 0.05stdDev; rx: 6.94mpps, 0.00stdDev



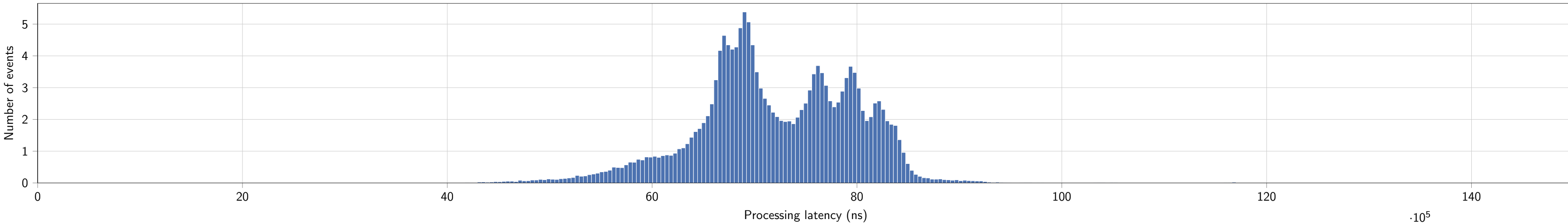
l2_multimac_00000100_mbit3605hires.histogram.csv: tx: 7.05mpps, 0.05stdDev; rx: 6.97mpps, 0.03stdDev



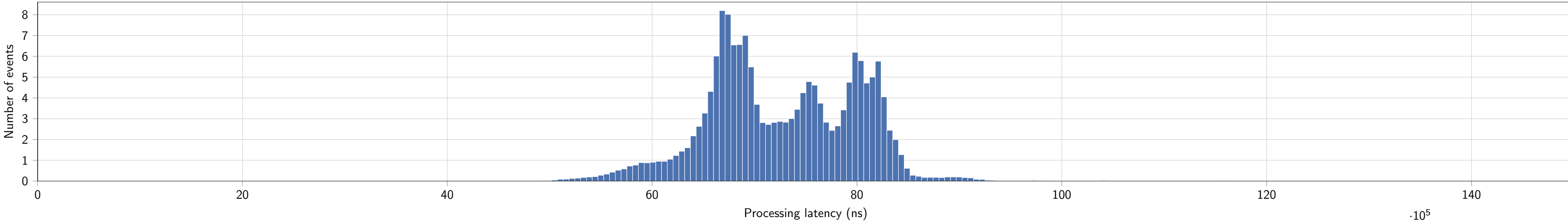
l2_multimac_00000100_mbit9000.histogram.csv: tx: 10.16mpps, 0.06stdDev; rx: 6.94mpps, 0.00stdDev



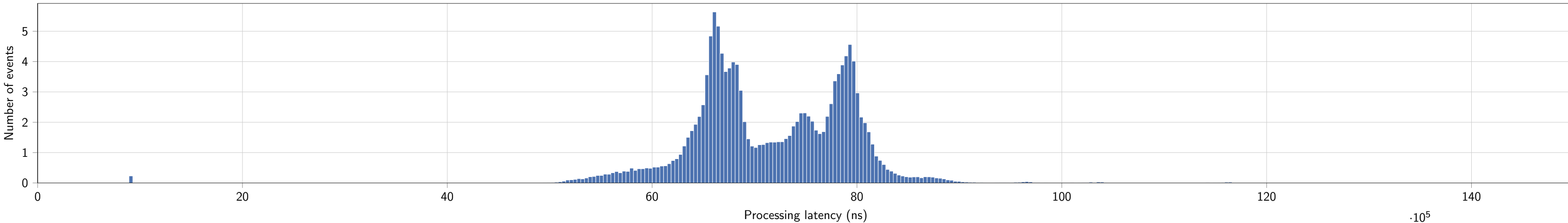
l2_multimac_00001000_mbit3421hires.histogram.csv: tx: 6.68mpps, 0.04stdDev; rx: 6.69mpps, 0.00stdDev



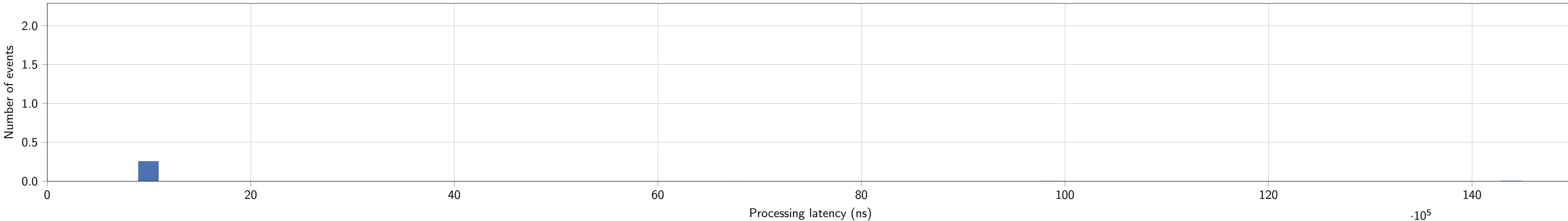
l2_multimac_00001000_mbit3431_final.histogram.csv: tx: 6.72mpps, 0.04stdDev; rx: 6.73mpps, 0.00stdDev



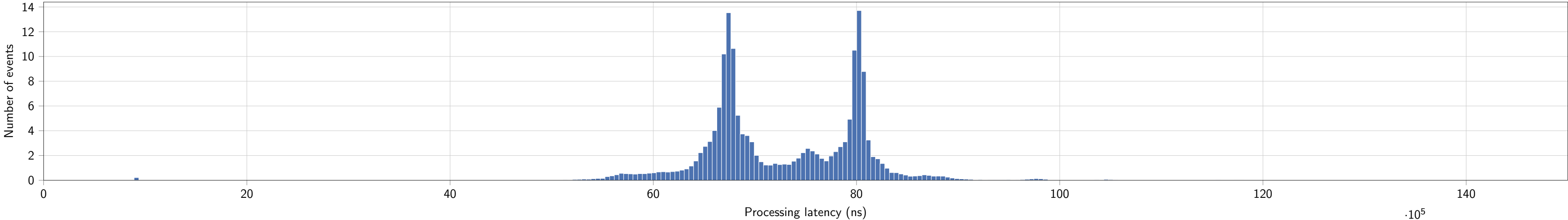
l2_multimac_00001000_mbit3431hires.histogram.csv: tx: 6.72mpps, 0.05stdDev; rx: 6.73mpps, 0.00stdDev



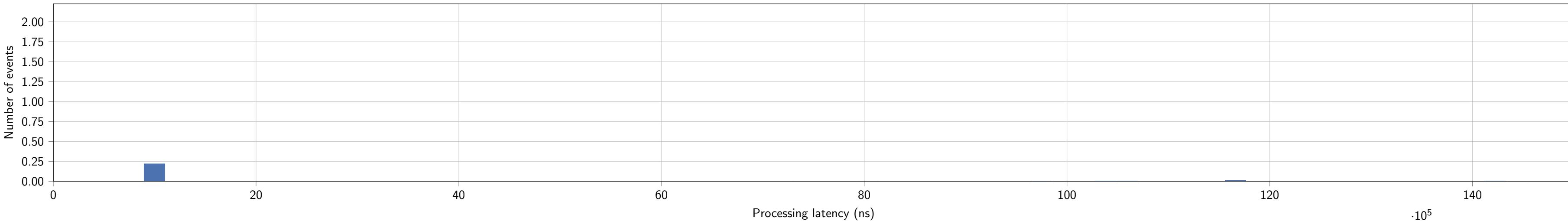
l2_multimac_00001000_mbit3441hires.histogram.csv: tx: 6.75mpps, 0.04stdDev; rx: 6.74mpps, 0.00stdDev



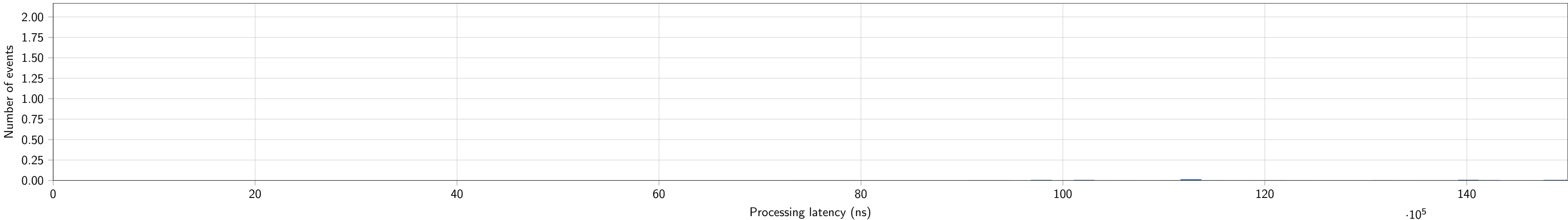
l2_multimac_00001000_mbit3451hires.histogram.csv: tx: 6.76mpps, 0.04stdDev; rx: 6.77mpps, 0.00stdDev



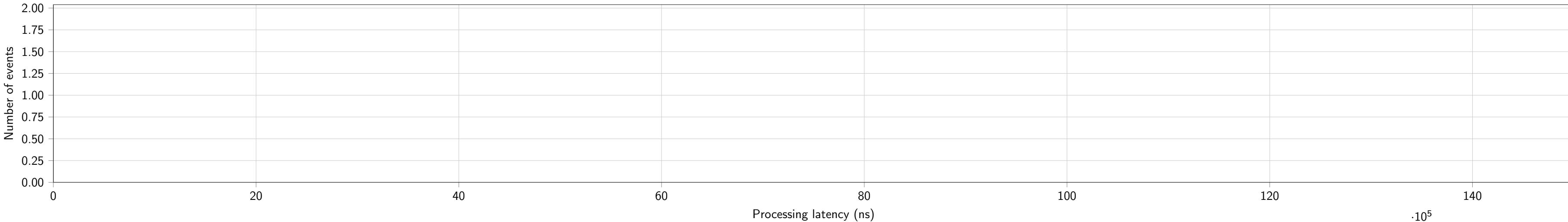
l2_multimac_00001000_mbit3461hires.histogram.csv: tx: 6.78mpps, 0.05stdDev; rx: 6.77mpps, 0.00stdDev



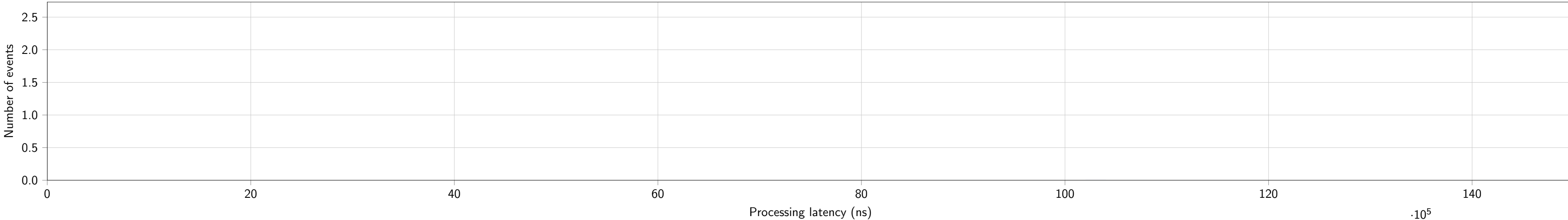
l2_multimac_00001000_mbit3471hires.histogram.csv: tx: 6.78mpps, 0.04stdDev; rx: 6.79mpps, 0.00stdDev



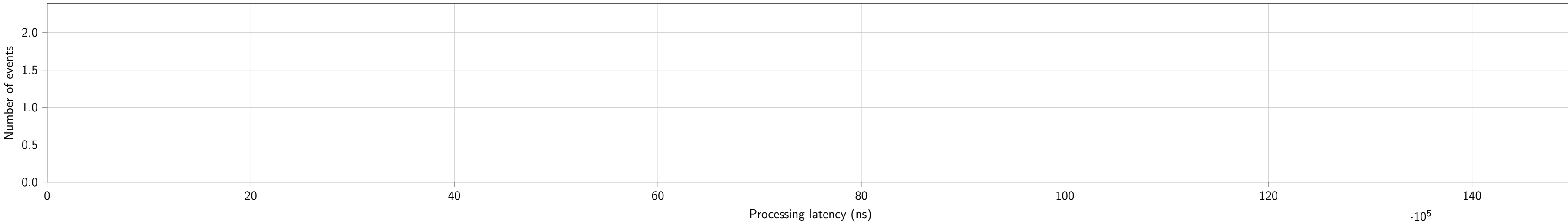
l2_multimac_00001000_mbit3481hires.histogram.csv: tx: 6.82mpps, 0.04stdDev; rx: 6.76mpps, 0.00stdDev



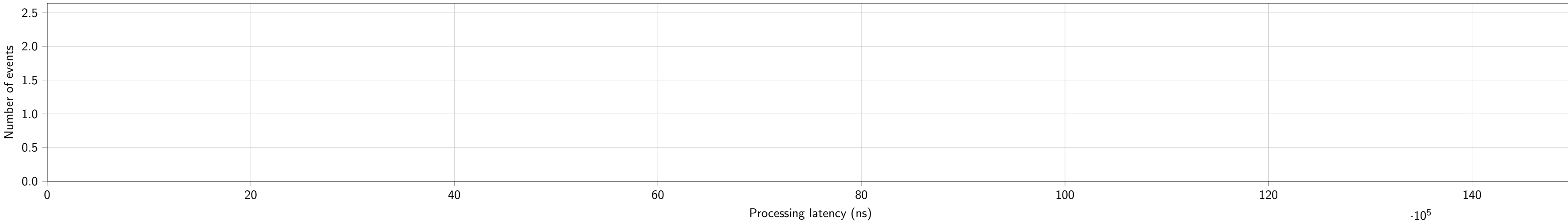
l2_multimac_00001000_mbit3491hires.histogram.csv: tx: 6.82mpps, 0.04stdDev; rx: 6.79mpps, 0.00stdDev



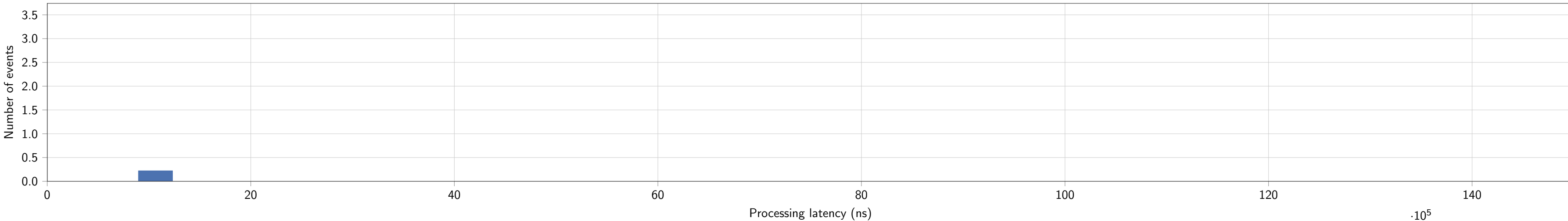
l2_multimac_00001000_mbit3501hires.histogram.csv: tx: 6.86mpps, 0.04stdDev; rx: 6.81mpps, 0.00stdDev



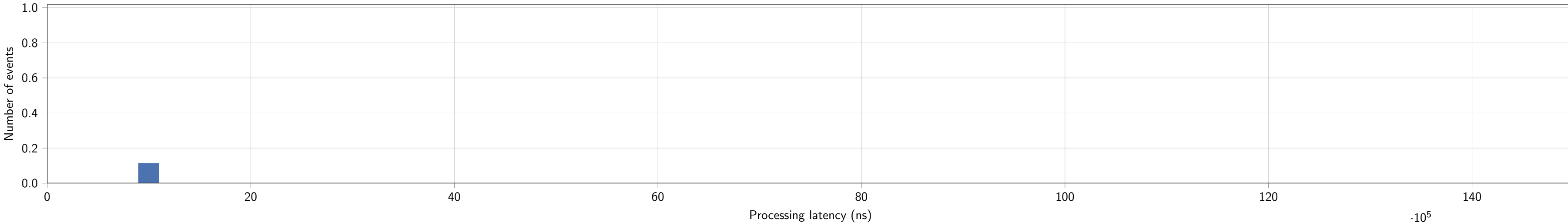
l2_multimac_00001000_mbit3511hires.histogram.csv: tx: 6.86mpps, 0.04stdDev; rx: 6.78mpps, 0.00stdDev



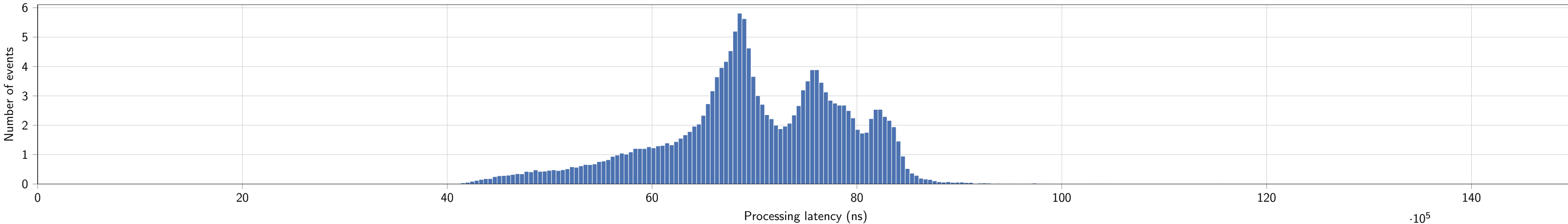
l2_multimac_00001000_mbit3521hires.histogram.csv: tx: 6.90mpps, 0.04stdDev; rx: 6.79mpps, 0.00stdDev



l2_multimac_00001000_mbit9000.histogram.csv: tx: 10.12mpps, 0.05stdDev; rx: 6.78mpps, 0.00stdDev



l2_multimac_00010000_mbit3422hires.histogram.csv: tx: 6.68mpps, 0.04stdDev; rx: 6.69mpps, 0.00stdDev



l2_multimac_00010000_mbit3432hires.histogram.csv: tx: 6.72mpps, 0.04stdDev; rx: 6.73mpps, 0.00stdDev

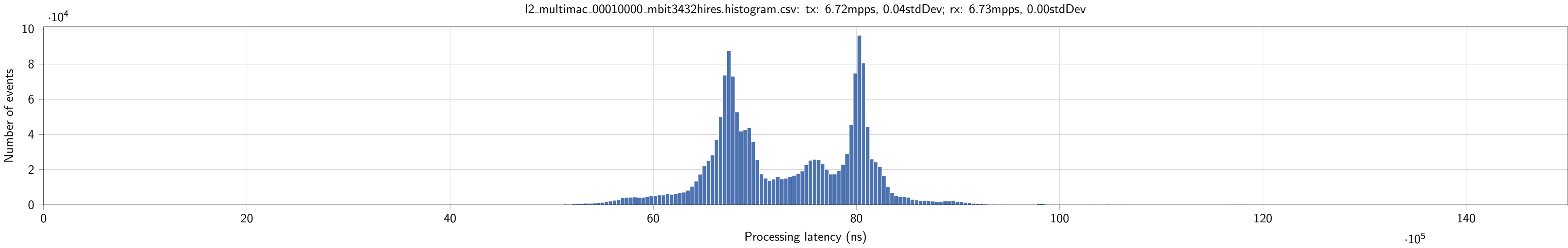
Number of events

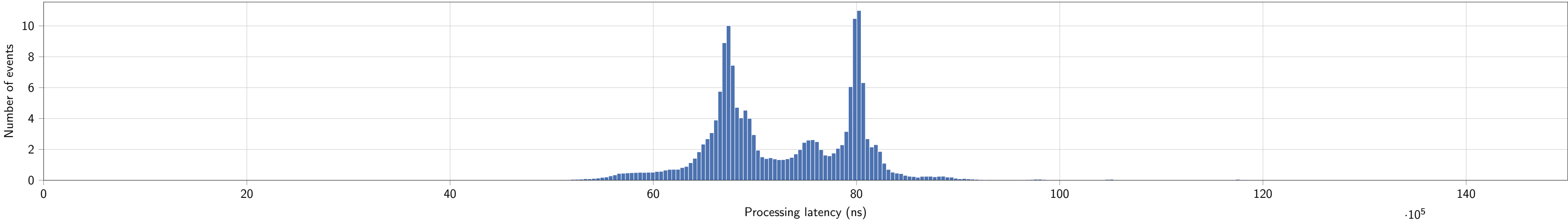
10
8
6
4
2
0

0 20 40 60 80 100 120 140

Processing latency (ns)

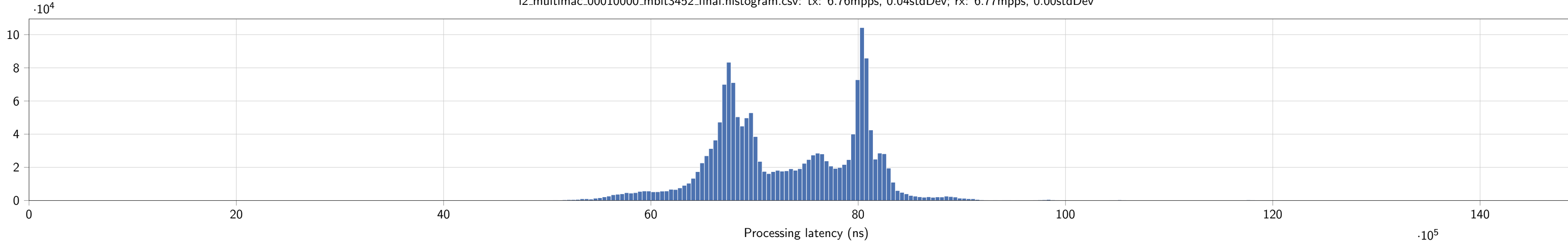
$\cdot 10^5$



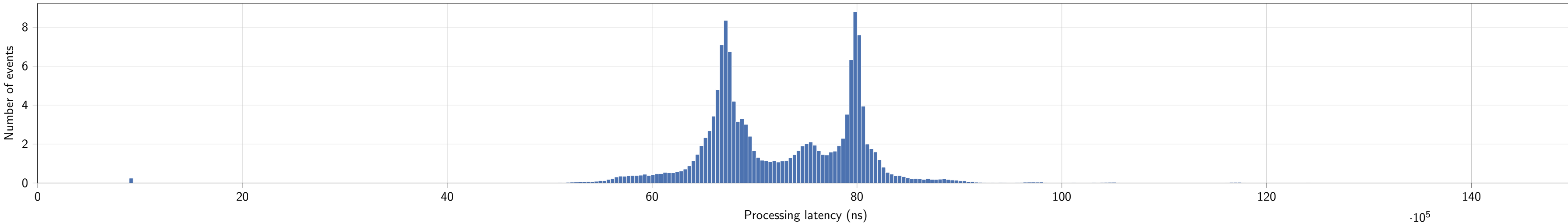


l2_multimac_00010000_mbit3452_final.histogram.csv: tx: 6.76mpps, 0.04stdDev; rx: 6.77mpps, 0.00stdDev

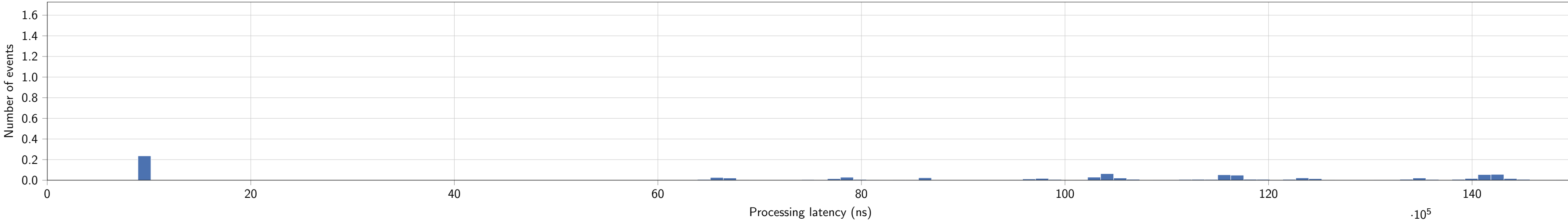
Number of events



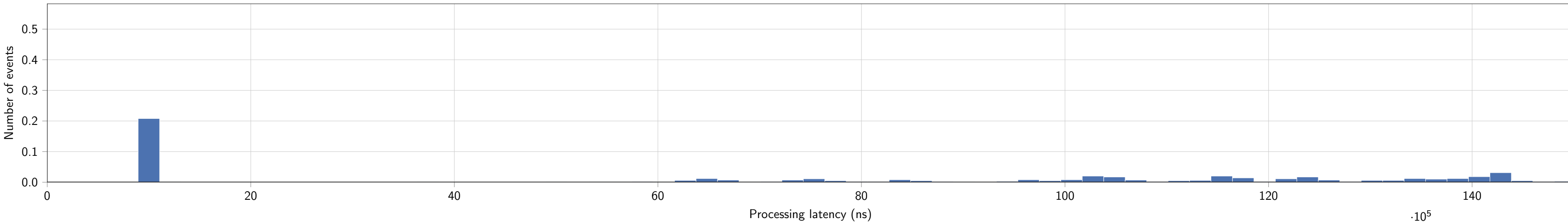
l2_multimac_00010000_mbit3452hires.histogram.csv: tx: 6.75mpps, 0.05stdDev; rx: 6.77mpps, 0.00stdDev



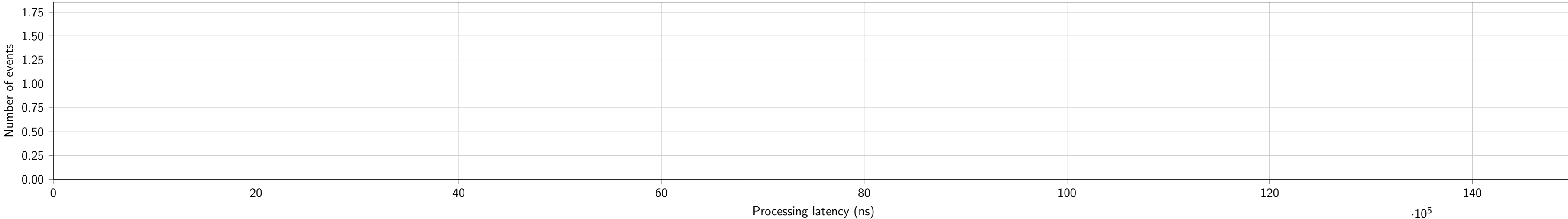
l2_multimac_00010000_mbit3462hires.histogram.csv: tx: 6.79mpps, 0.04stdDev; rx: 6.80mpps, 0.00stdDev



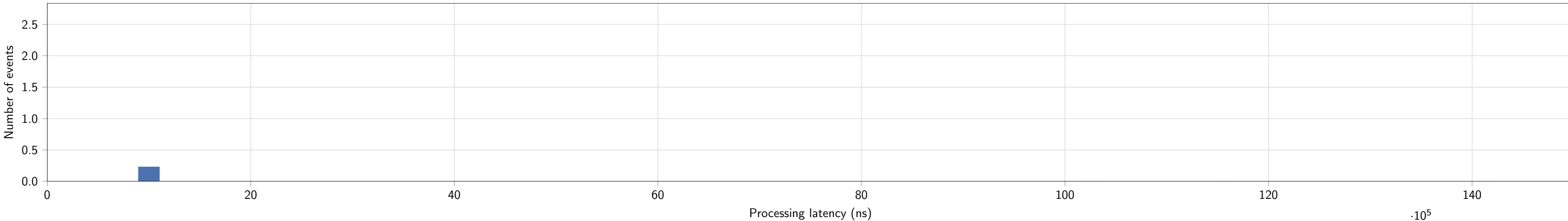
l2_multimac_00010000_mbit3472hires.histogram.csv: tx: 6.79mpps, 0.04stdDev; rx: 6.79mpps, 0.00stdDev



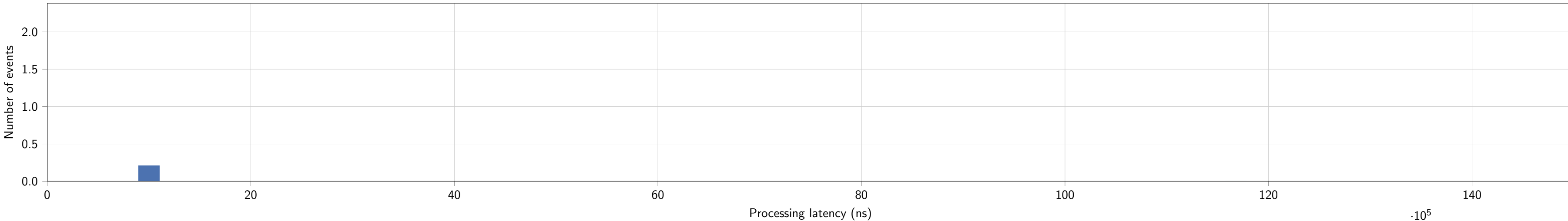
l2_multimac_00010000_mbit3482hires.histogram.csv: tx: 6.82mpps, 0.05stdDev; rx: 6.77mpps, 0.00stdDev



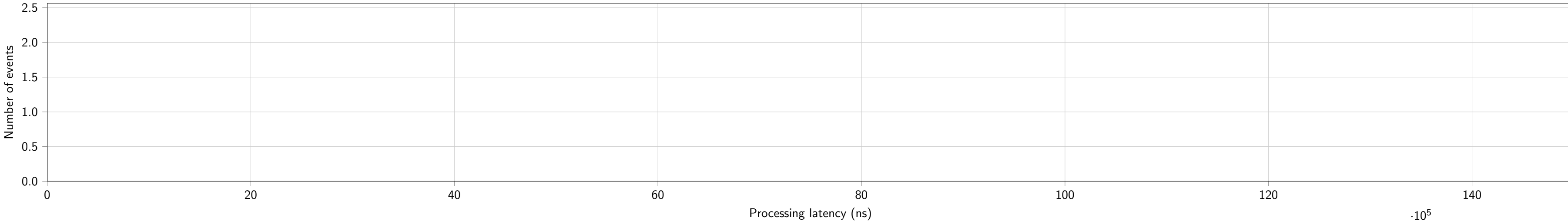
l2_multimac_00010000_mbit3492hires.histogram.csv: tx: 6.82mpps, 0.04stdDev; rx: 6.79mpps, 0.00stdDev



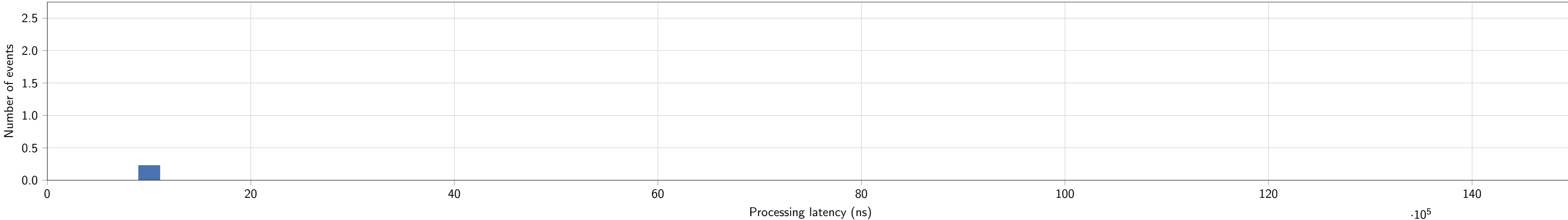
l2_multimac_00010000_mbit3502hires.histogram.csv: tx: 6.86mpps, 0.04stdDev; rx: 6.81mpps, 0.00stdDev



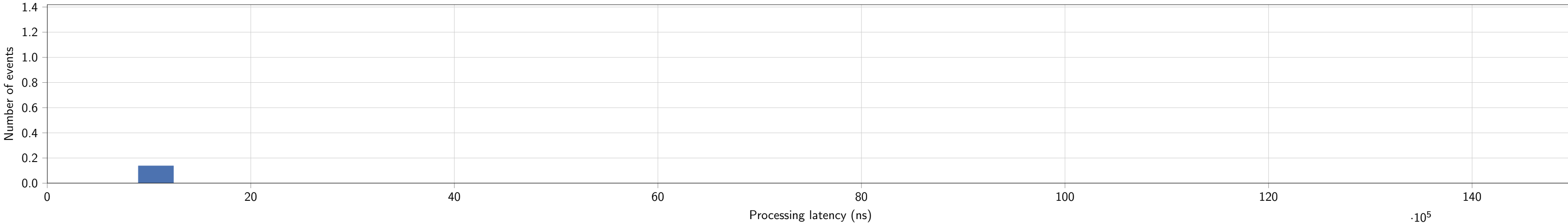
l2_multimac_00010000_mbit3512hires.histogram.csv: tx: 6.86mpps, 0.04stdDev; rx: 6.79mpps, 0.00stdDev



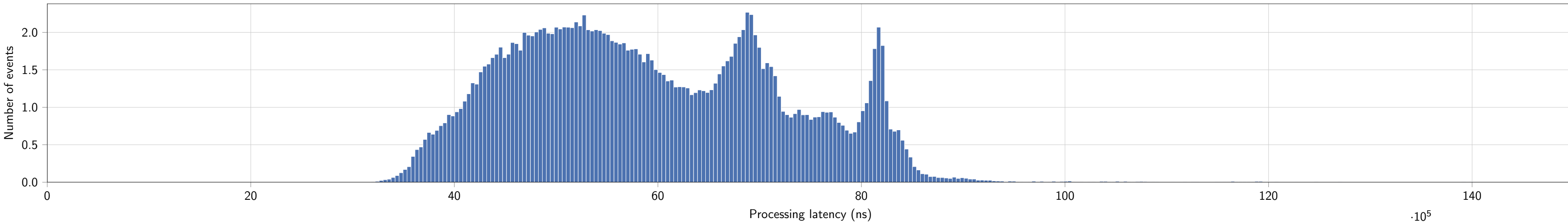
l2_multimac_00010000_mbit3522hires.histogram.csv: tx: 6.90mpps, 0.04stdDev; rx: 6.76mpps, 0.00stdDev



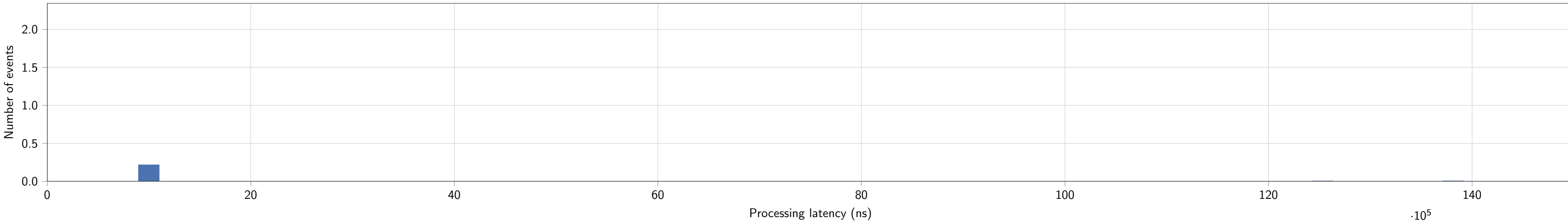
l2_multimac_00010000_mbit9000.histogram.csv: tx: 10.13mpps, 0.05stdDev; rx: 6.78mpps, 0.03stdDev



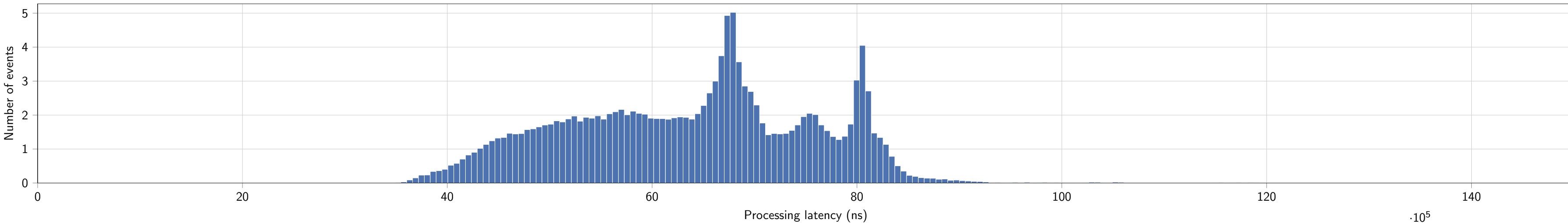
l2_multimac_00100000_mbit3381_final.histogram.csv: tx: 6.61mpps, 0.04stdDev; rx: 6.62mpps, 0.00stdDev



l2_multimac_00100000_mbit3391hires.histogram.csv: tx: 6.64mpps, 0.04stdDev; rx: 6.64mpps, 0.00stdDev



l2_multimac_00100000_mbit3401hires.histogram.csv: tx: 6.65mpps, 0.05stdDev; rx: 6.66mpps, 0.00stdDev



l2_multimac_00100000_mbit3411hires.histogram.csv: tx: 6.68mpps, 0.04stdDev; rx: 6.69mpps, 0.00stdDev

Number of events

$\cdot 10^4$

16
14
12
10
8
6
4
2
0

20

40

60

80

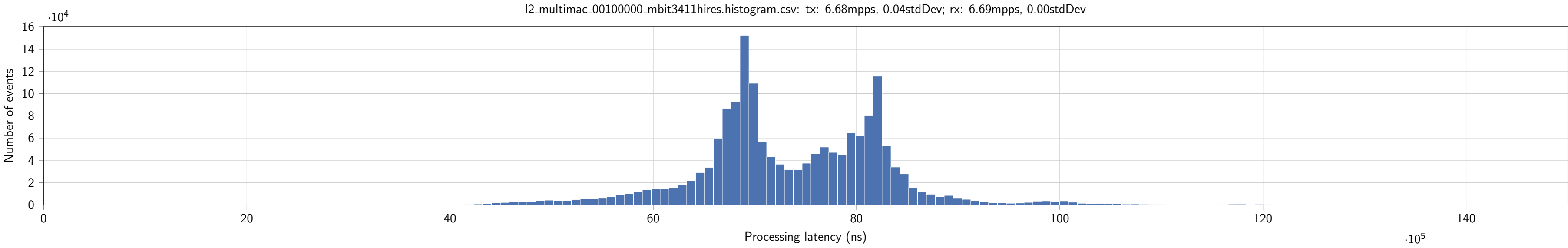
100

120

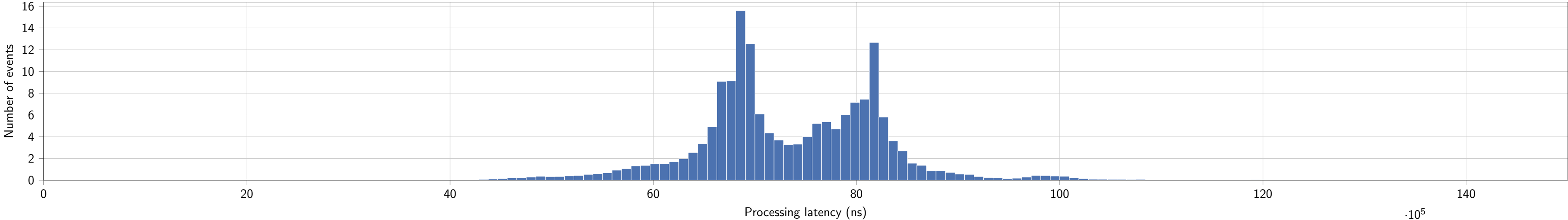
140

Processing latency (ns)

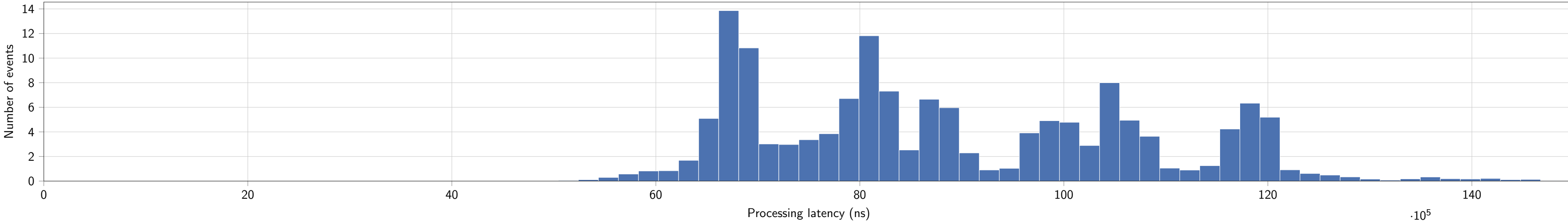
$\cdot 10^5$



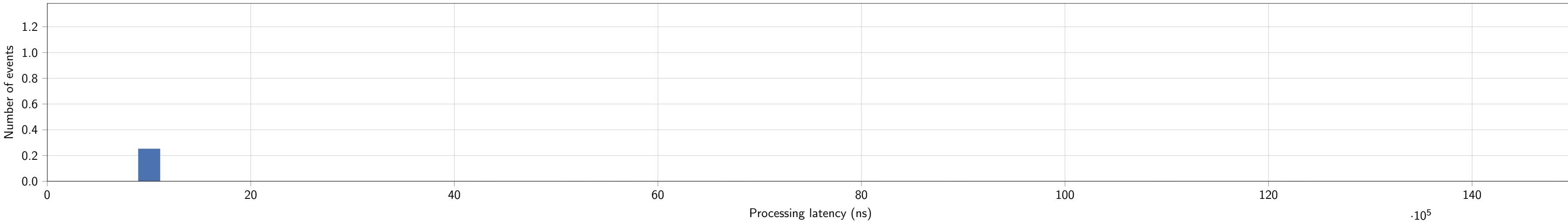
l2_multimac_00100000_mbit3421hires.histogram.csv: tx: 6.68mpps, 0.04stdDev; rx: 6.69mpps, 0.00stdDev



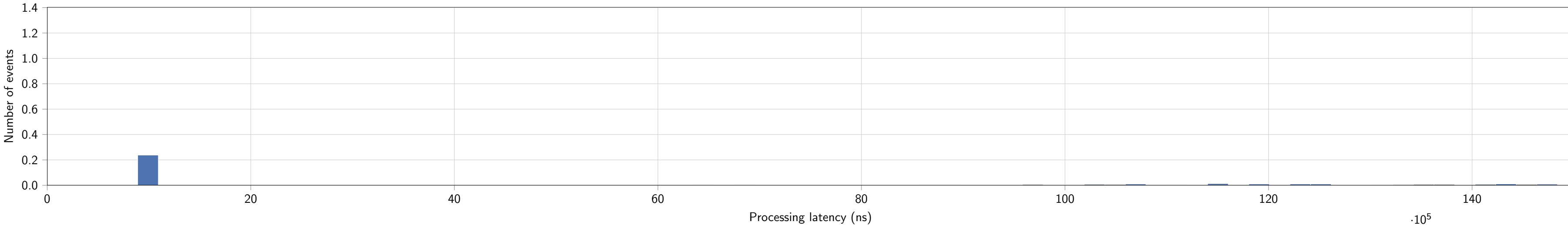
l2_multimac_00100000_mbit3431hires.histogram.csv: tx: 6.72mpps, 0.04stdDev; rx: 6.73mpps, 0.00stdDev



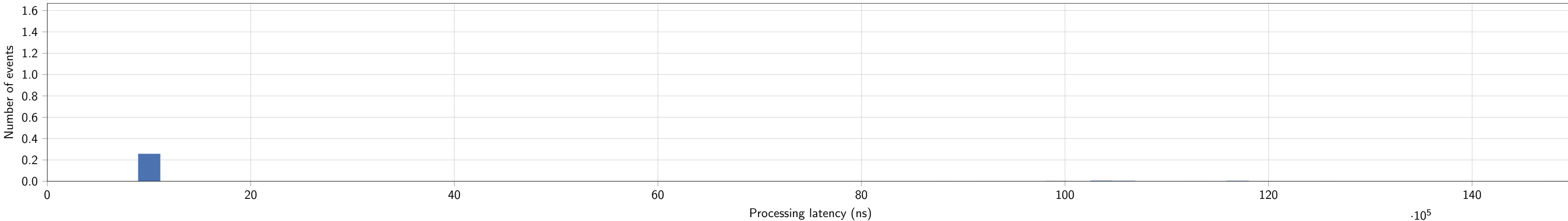
l2_multimac_00100000_mbit3441hires.histogram.csv: tx: 6.75mpps, 0.05stdDev; rx: 6.70mpps, 0.00stdDev



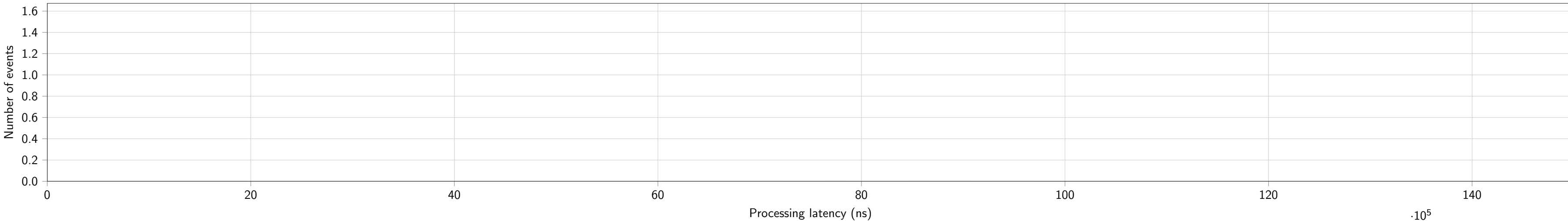
l2_multimac_00100000_mbit3451hires.histogram.csv: tx: 6.75mpps, 0.04stdDev; rx: 6.76mpps, 0.00stdDev



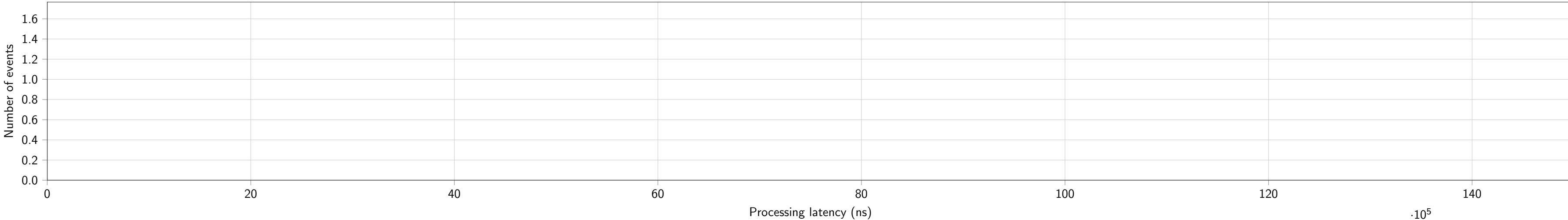
l2_multimac_00100000_mbit3461hires.histogram.csv: tx: 6.78mpps, 0.05stdDev; rx: 6.73mpps, 0.00stdDev



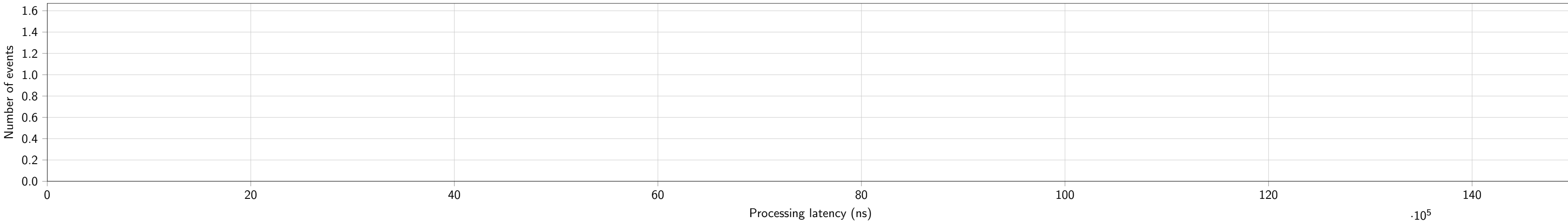
l2_multimac_00100000_mbit3471hires.histogram.csv: tx: 6.78mpps, 0.04stdDev; rx: 6.75mpps, 0.00stdDev



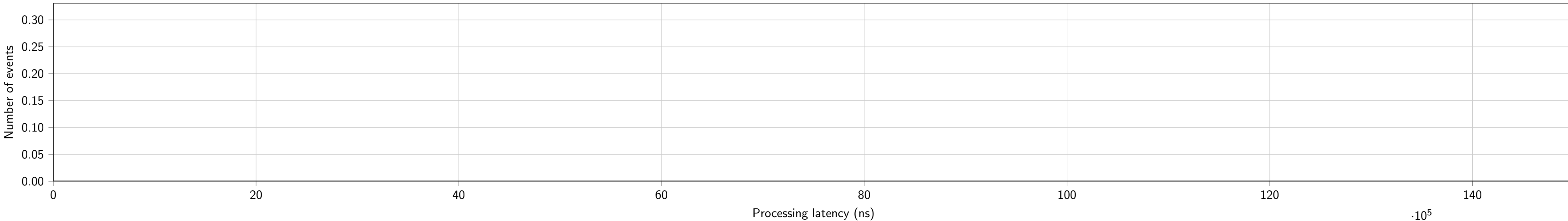
l2_multimac_00100000_mbit3481hires.histogram.csv: tx: 6.82mpps, 0.04stdDev; rx: 6.73mpps, 0.00stdDev



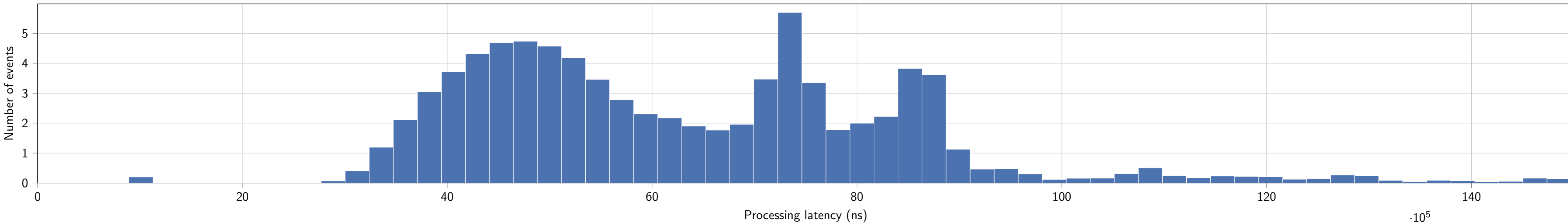
l2_multimac_00100000_mbit3491hires.histogram.csv: tx: 6.82mpps, 0.04stdDev; rx: 6.75mpps, 0.00stdDev



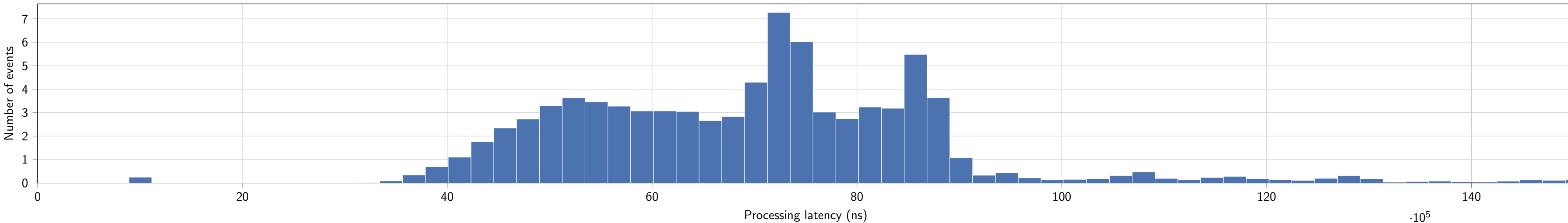
l2_multimac_00100000_mbit9000.histogram.csv: tx: 10.12mpps, 0.05stdDev; rx: 6.72mpps, 0.00stdDev



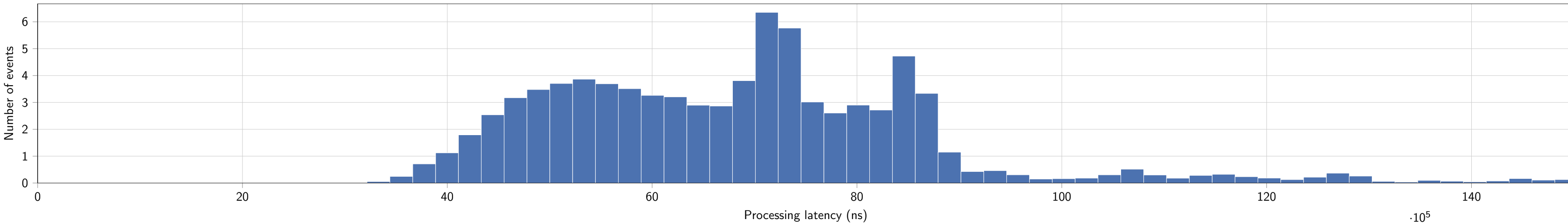
l2_multimac_01000000_mbit3277hires.histogram.csv: tx: 6.41mpps, 0.04stdDev; rx: 6.41mpps, 0.01stdDev



l2_multimac_01000000_mbit3287hires.histogram.csv: tx: 6.44mpps, 0.04stdDev; rx: 6.45mpps, 0.01stdDev



l2_multimac_01000000_mbit3297hires.histogram.csv: tx: 6.44mpps, 0.04stdDev; rx: 6.45mpps, 0.01stdDev



l2_multimac_01000000_mbit3307_final.histogram.csv: tx: 6.47mpps, 0.05stdDev; rx: 6.48mpps, 0.01stdDev

Number of events

10
8
6
4
2
0

$\cdot 10^4$

0

20

40

60

80

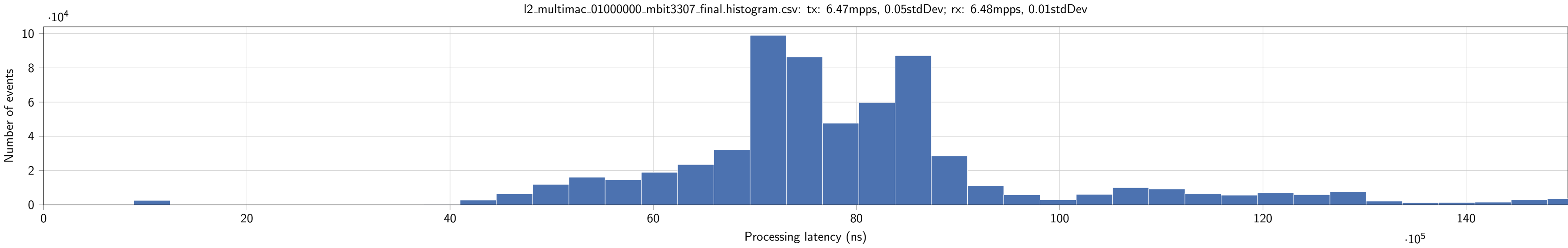
100

120

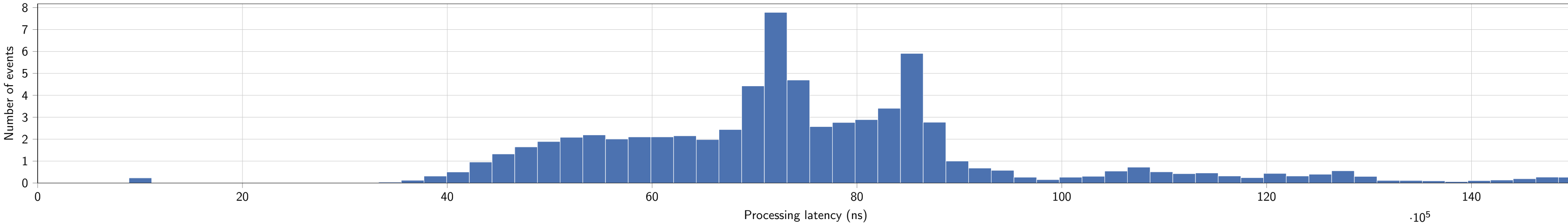
140

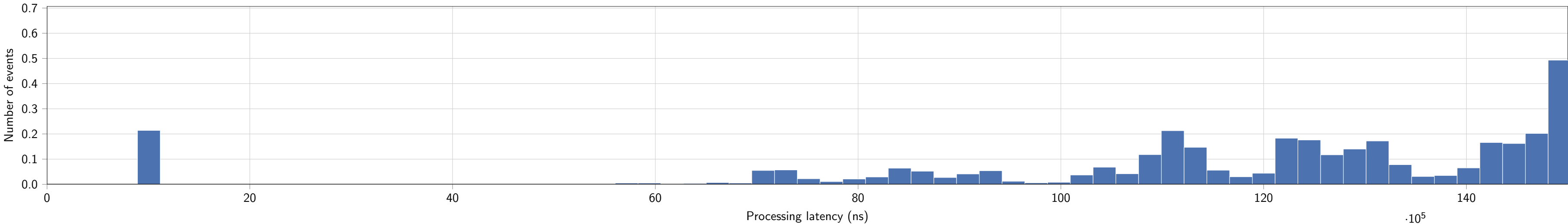
Processing latency (ns)

$\cdot 10^5$

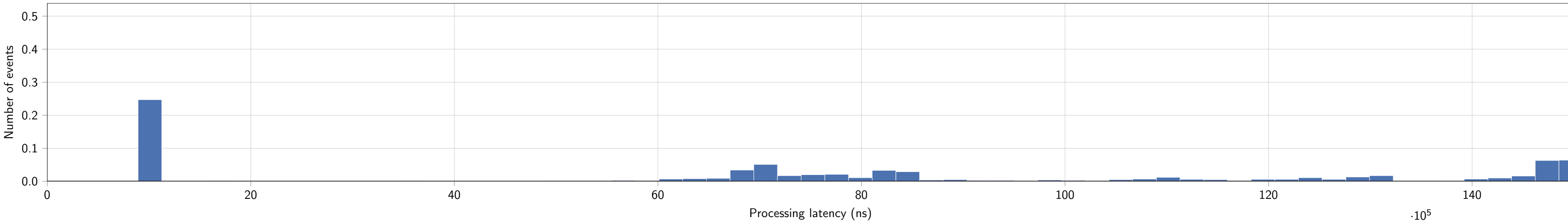


l2_multimac_01000000_mbit3307hires.histogram.csv: tx: 6.47mpps, 0.05stdDev; rx: 6.48mpps, 0.01stdDev

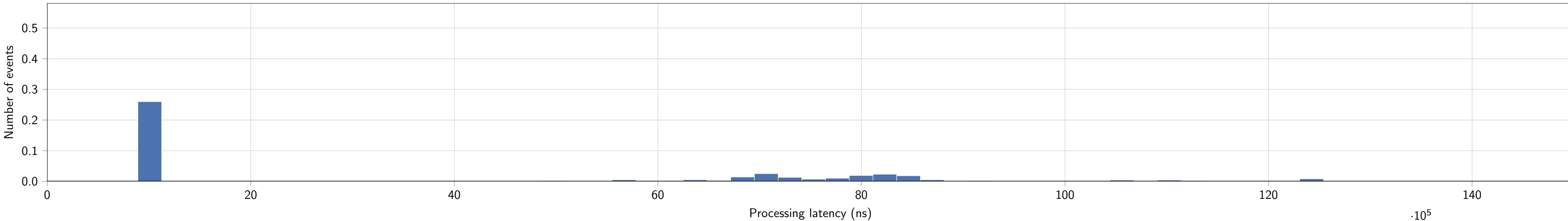




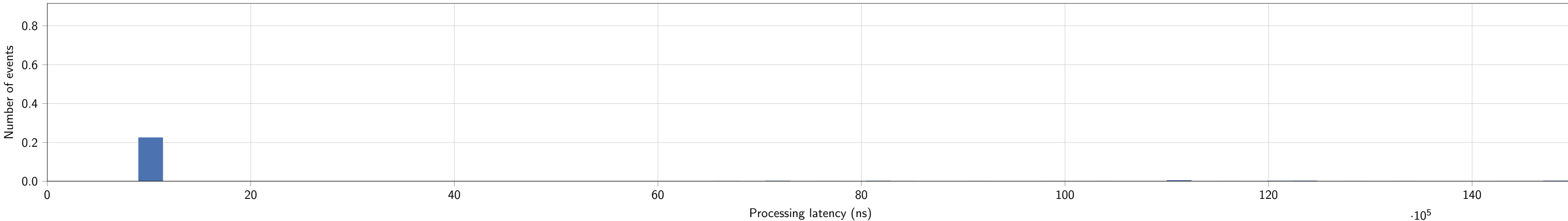
l2_multimac_01000000_mbit3327hires.histogram.csv: tx: 6.50mpps, 0.04stdDev; rx: 6.49mpps, 0.01stdDev



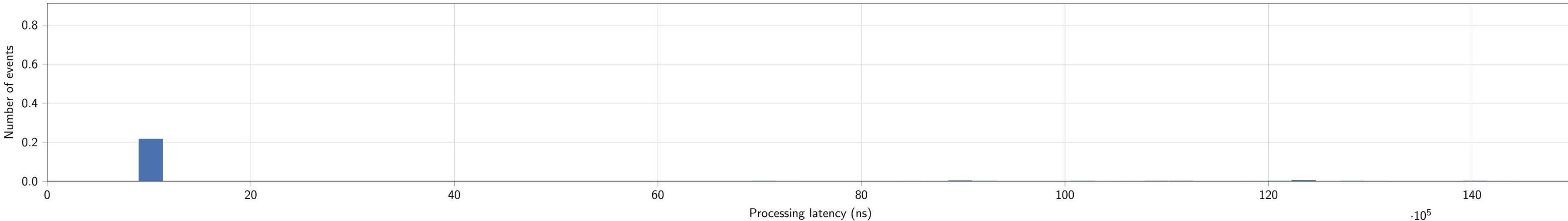
l2_multimac_01000000_mbit3337hires.histogram.csv: tx: 6.54mpps, 0.04stdDev; rx: 6.51mpps, 0.01stdDev



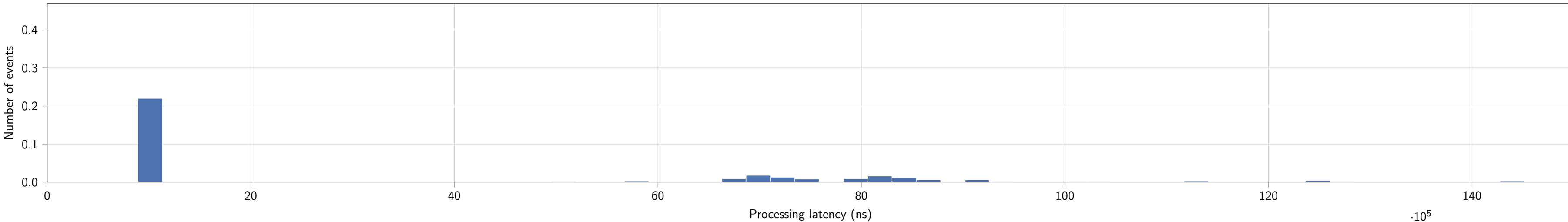
l2_multimac_01000000_mbit3347hires.histogram.csv: tx: 6.53mpps, 0.05stdDev; rx: 6.43mpps, 0.01stdDev



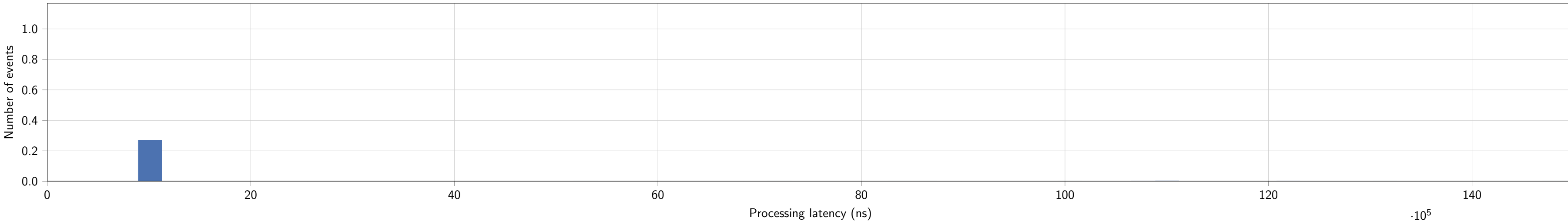
l2_multimac_01000000_mbit3357hires.histogram.csv: tx: 6.57mpps, 0.05stdDev; rx: 6.47mpps, 0.01stdDev



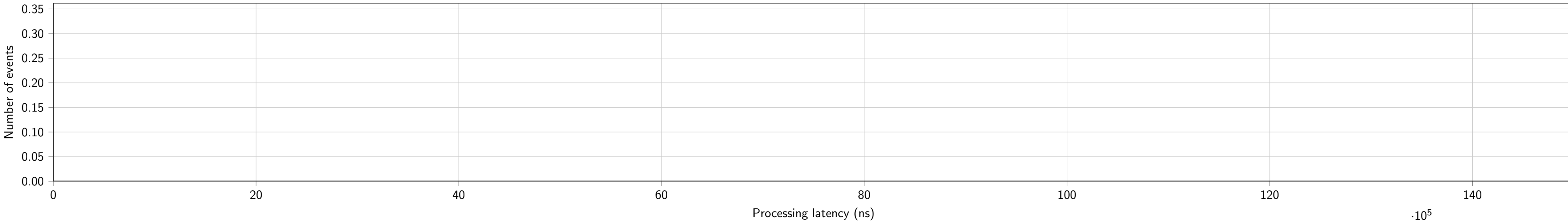
l2_multimac_01000000_mbit3367hires.histogram.csv: tx: 6.57mpps, 0.04stdDev; rx: 6.50mpps, 0.04stdDev



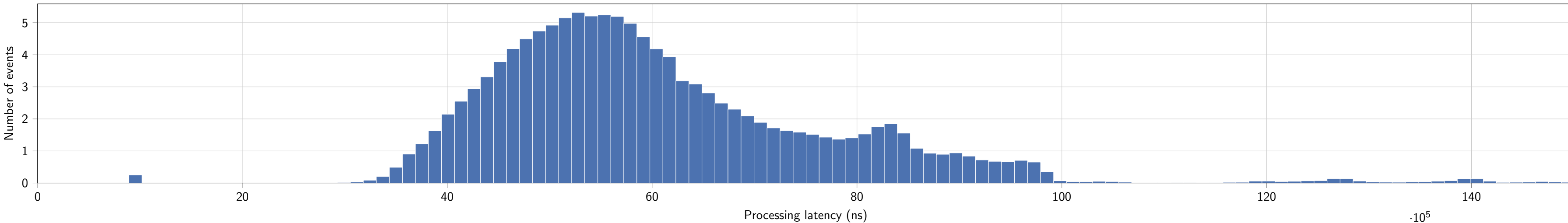
l2_multimac_01000000_mbit3377hires.histogram.csv: tx: 6.61mpps, 0.04stdDev; rx: 6.48mpps, 0.01stdDev

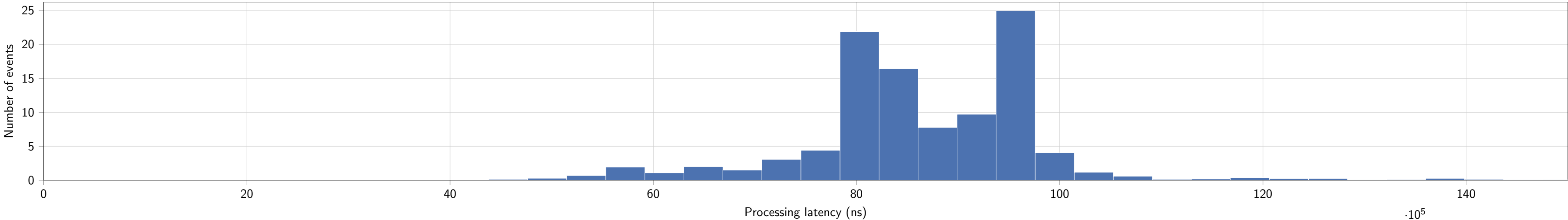


l2_multimac_01000000_mbit9000.histogram.csv: tx: 10.11mpps, 0.05stdDev; rx: 6.50mpps, 0.01stdDev

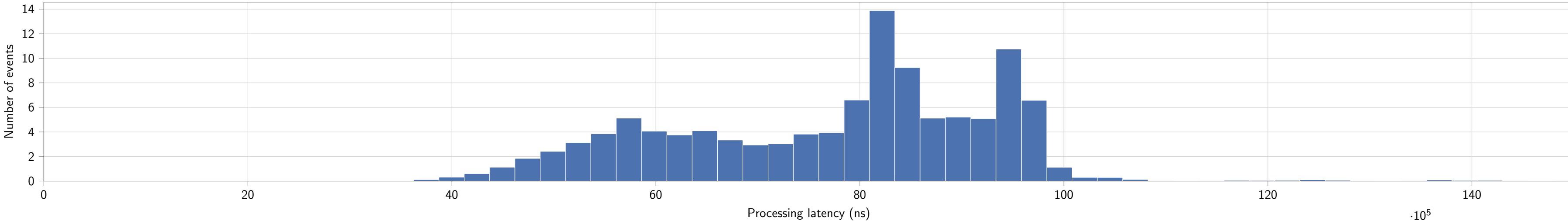


l2_multimac_10000000_mbit3003hires.histogram.csv: tx: 5.87mpps, 0.04stdDev; rx: 5.88mpps, 0.00stdDev

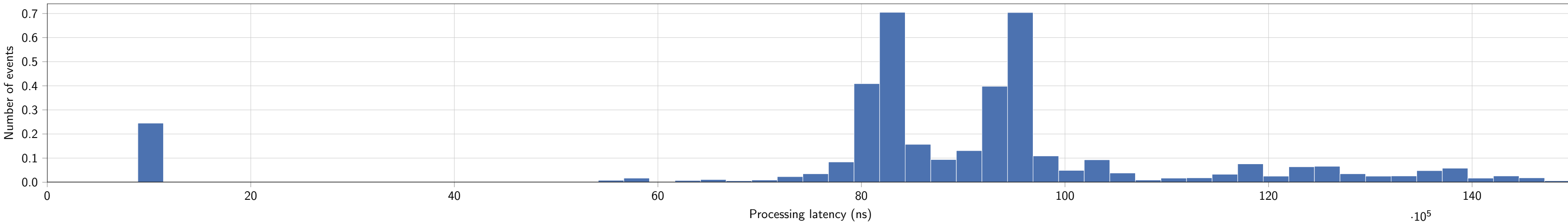




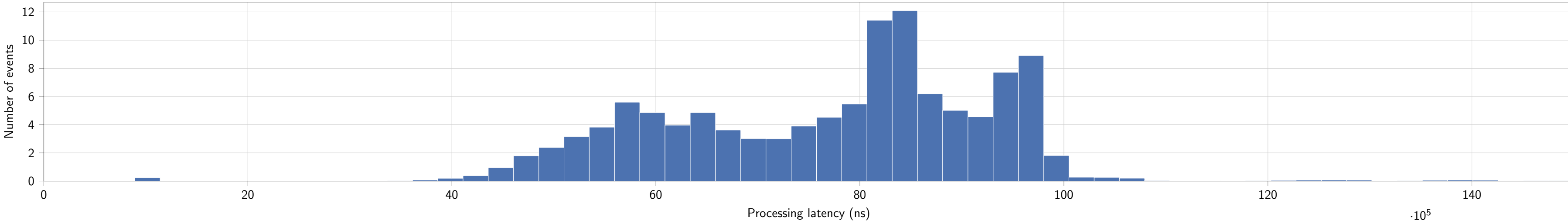
l2_multimac_10000000_mbit3013hires.histogram.csv: tx: 5.89mpps, 0.04stdDev; rx: 5.90mpps, 0.01stdDev



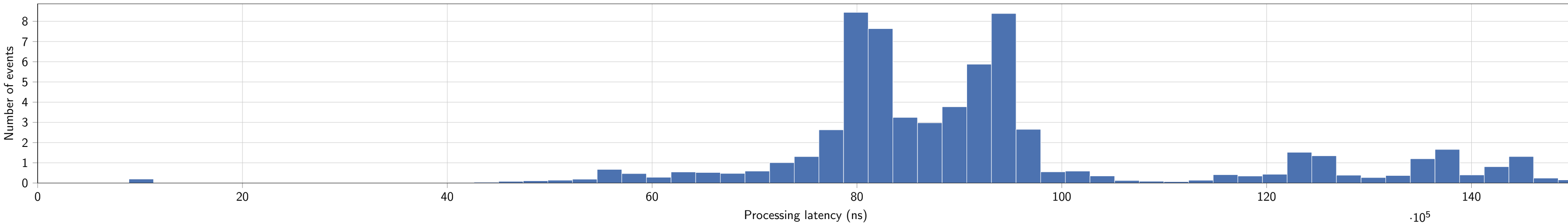
l2_multimac_10000000_mbit3023hires.histogram.csv: tx: 5.92mpps, 0.04stdDev; rx: 5.88mpps, 0.02stdDev



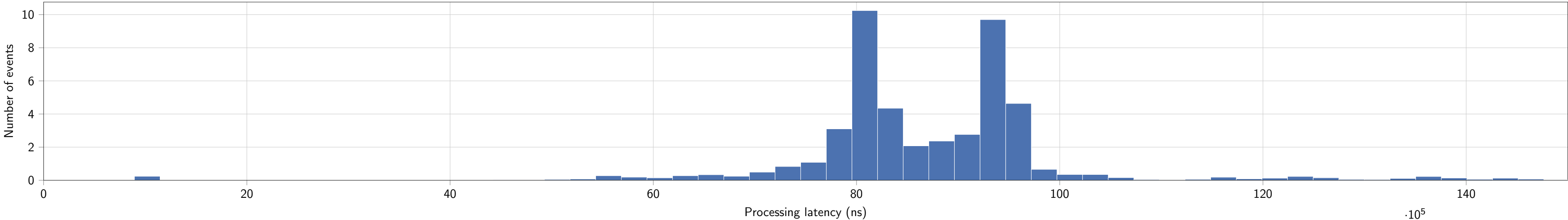
l2_multimac_10000000_mbit3033hires.histogram.csv: tx: 5.92mpps, 0.04stdDev; rx: 5.93mpps, 0.00stdDev



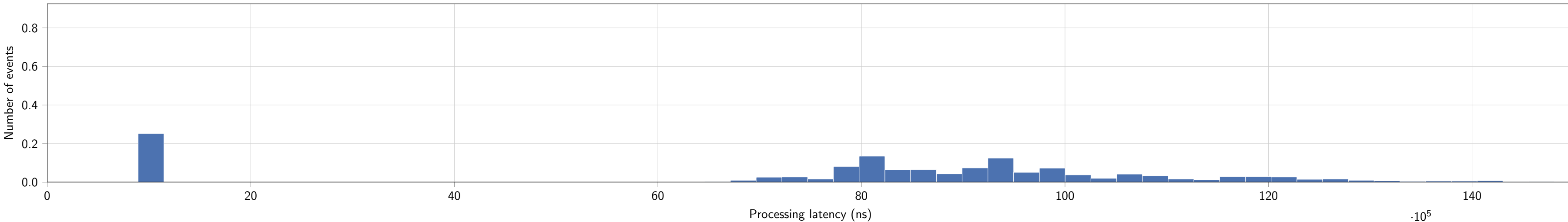
l2_multimac_10000000_mbit3043hires.histogram.csv: tx: 5.95mpps, 0.04stdDev; rx: 5.94mpps, 0.01stdDev



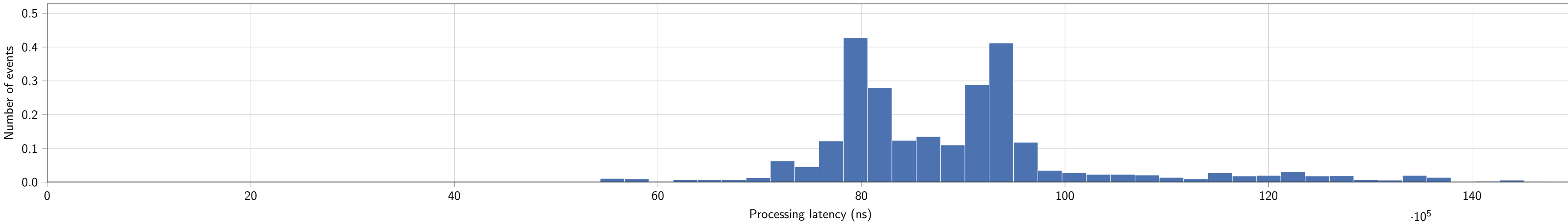
l2_multimac_10000000_mbit3053hires.histogram.csv: tx: 5.97mpps, 0.04stdDev; rx: 5.96mpps, 0.01stdDev



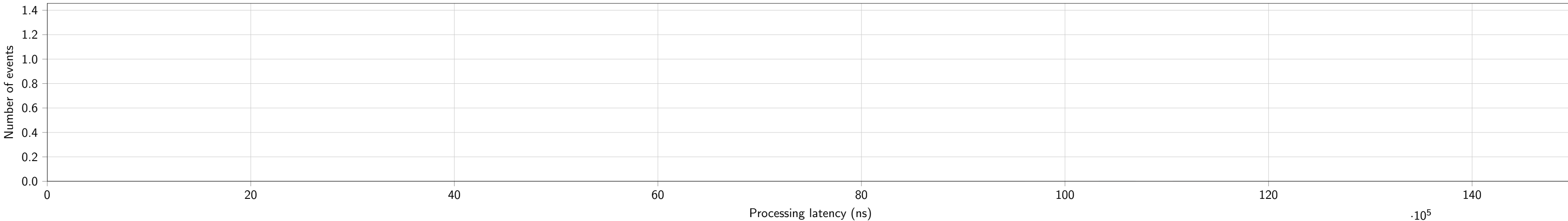
l2_multimac_10000000_mbit3063hires.histogram.csv: tx: 6.00mpps, 0.04stdDev; rx: 5.95mpps, 0.02stdDev



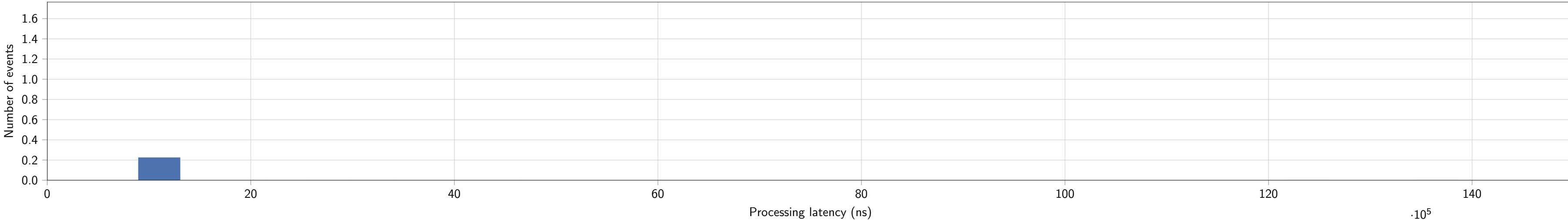
l2_multimac_10000000_mbit3073hires.histogram.csv: tx: 6.00mpps, 0.04stdDev; rx: 5.97mpps, 0.02stdDev



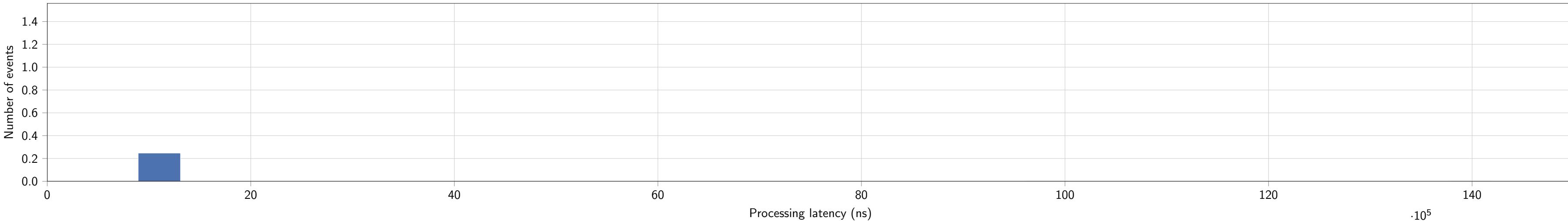
l2_multimac_10000000_mbit3083hires.histogram.csv: tx: 6.03mpps, 0.04stdDev; rx: 5.96mpps, 0.04stdDev



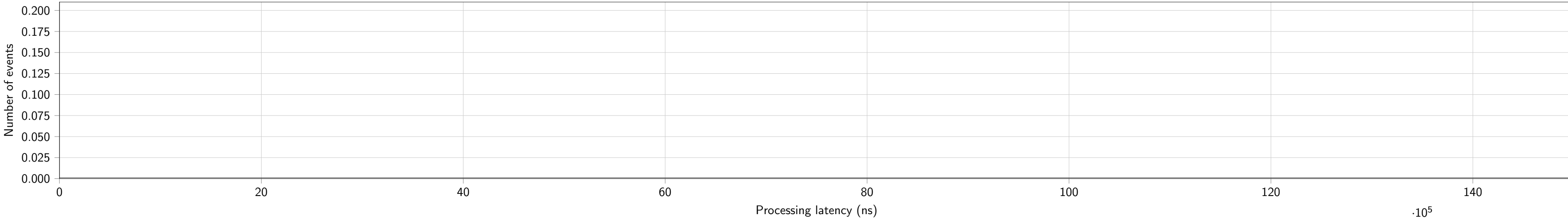
l2_multimac_10000000_mbit3093hires.histogram.csv: tx: 6.06mpps, 0.04stdDev; rx: 5.99mpps, 0.03stdDev



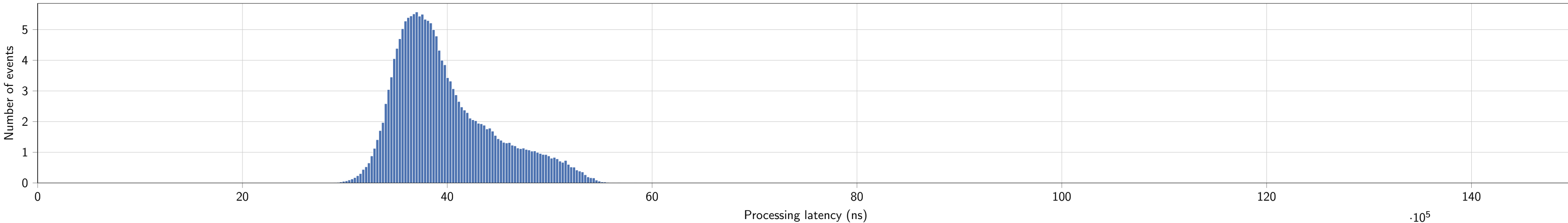
l2_multimac_10000000_mbit3103hires.histogram.csv: tx: 6.06mpps, 0.04stdDev; rx: 5.97mpps, 0.03stdDev



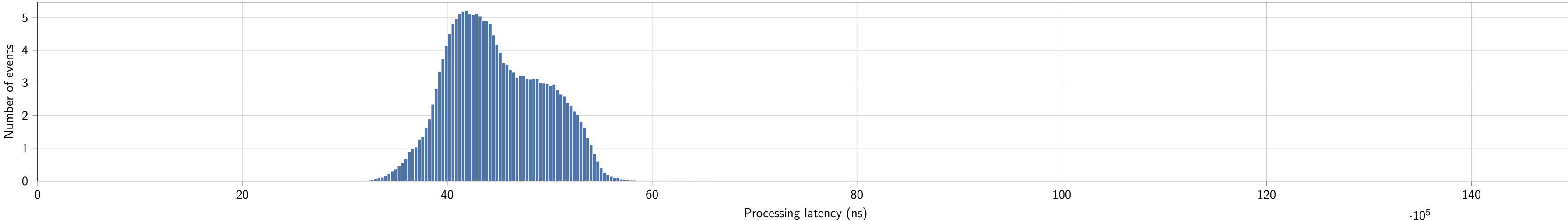
l2_multimac_10000000_mbit9000.histogram.csv: tx: 10.18mpps, 0.05stdDev; rx: 5.96mpps, 0.00stdDev



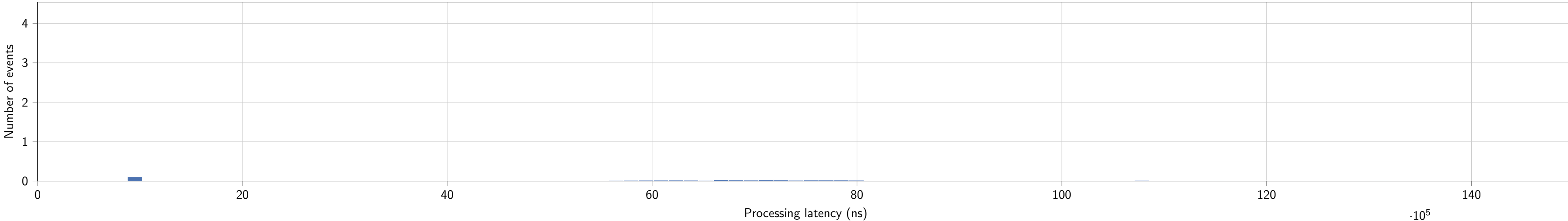
l2_xconnect_mbit5241_final.histogram.csv: tx: 10.24mpps, 0.05stdDev; rx: 10.25mpps, 0.00stdDev



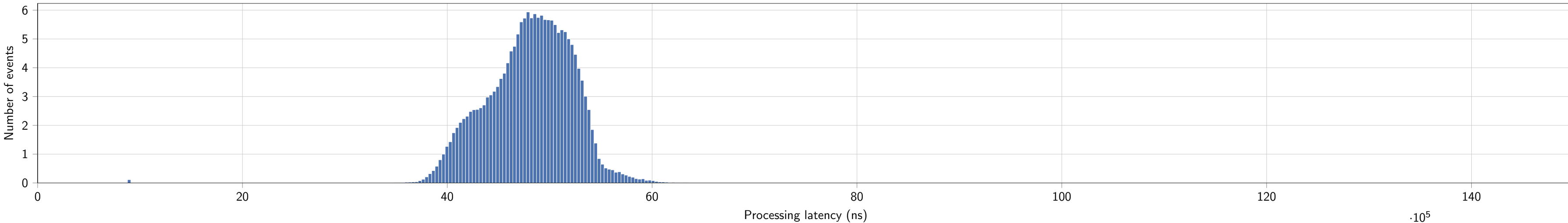
l2_xconnect_mbit5241hires.histogram.csv: tx: 10.24mpps, 0.05stdDev; rx: 10.25mpps, 0.00stdDev



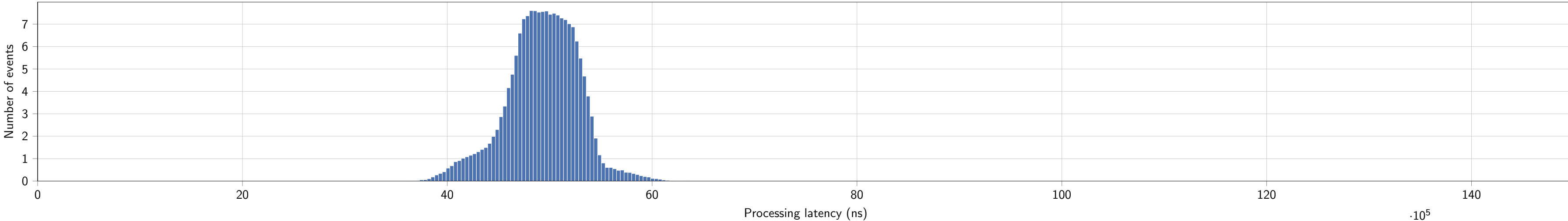
l2_xconnect_mbit5251hires.histogram.csv: tx: 10.32mpps, 0.05stdDev; rx: 10.32mpps, 0.00stdDev



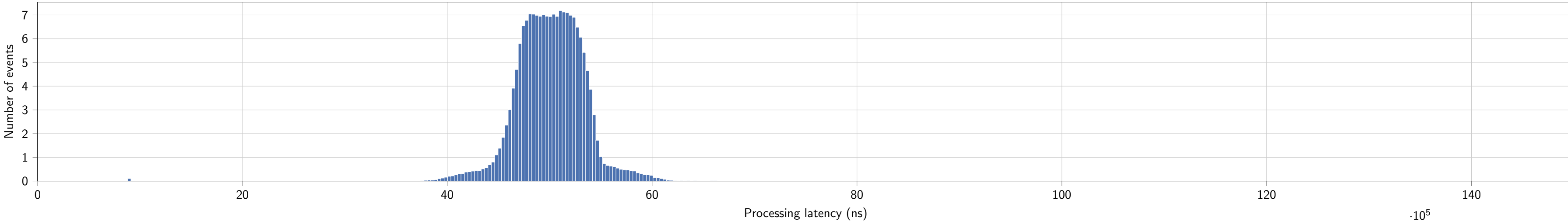
l2_xconnect_mbit5261hires.histogram.csv: tx: 10.33mpps, 0.05stdDev; rx: 10.34mpps, 0.00stdDev



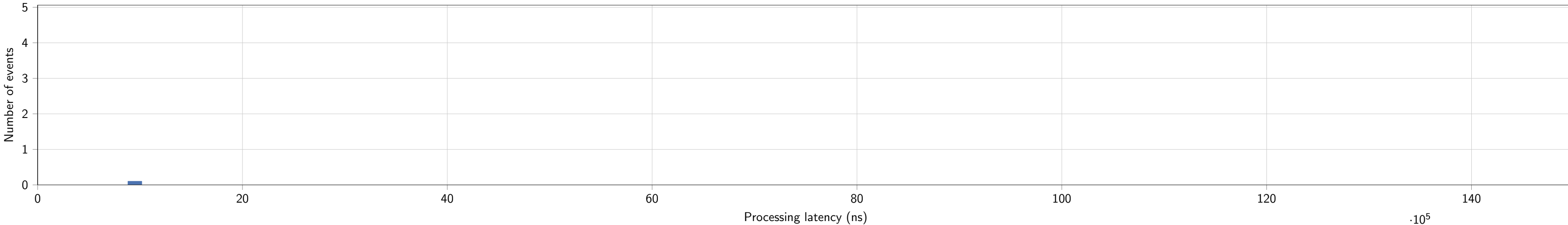
l2_xconnect_mbit5271hires.histogram.csv: tx: 10.33mpps, 0.05stdDev; rx: 10.34mpps, 0.00stdDev



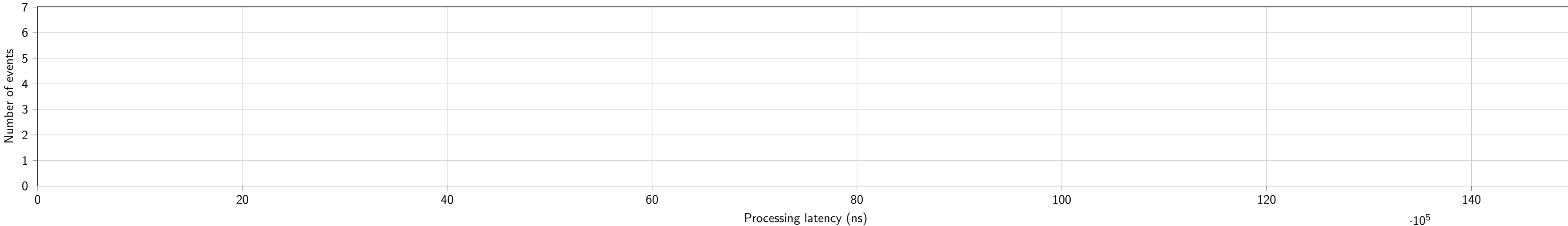
l2_xconnect_mbit5281hires.histogram.csv: tx: 10.33mpps, 0.05stdDev; rx: 10.34mpps, 0.00stdDev



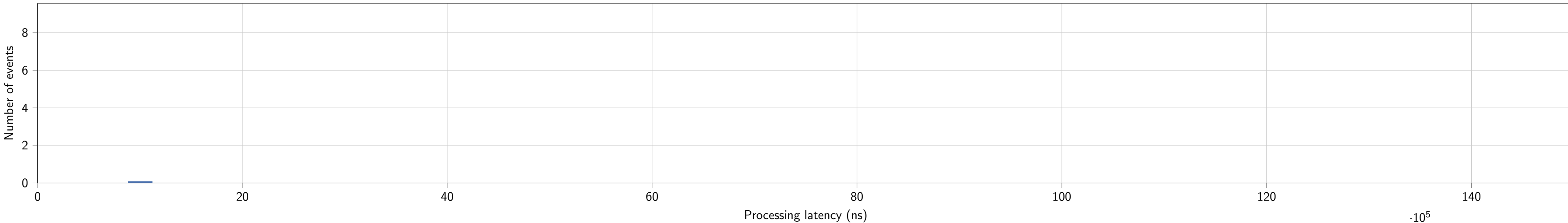
l2_xconnect_mbit5291hires.histogram.csv: tx: 10.41mpps, 0.05stdDev; rx: 10.30mpps, 0.00stdDev



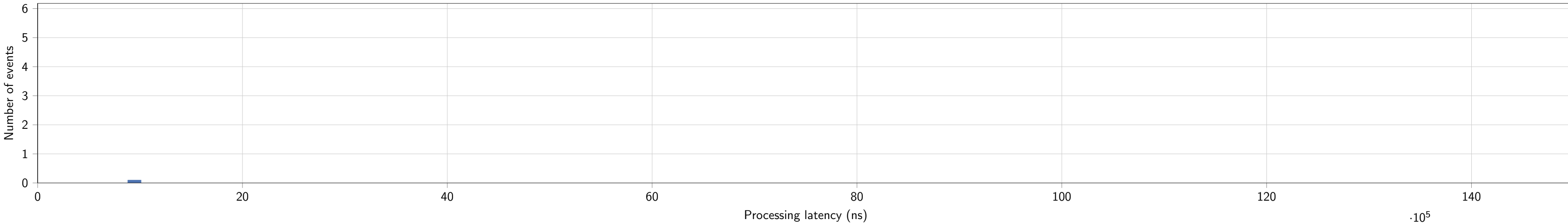
l2_xconnect_mbit5301hires.histogram.csv: tx: 10.41mpps, 0.05stdDev; rx: 10.36mpps, 0.04stdDev



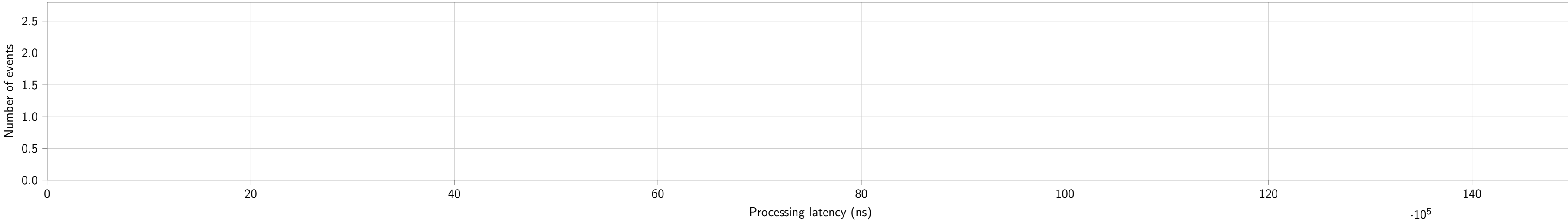
l2_xconnect_mbit5311hires.histogram.csv: tx: 10.41mpps, 0.05stdDev; rx: 10.36mpps, 0.00stdDev



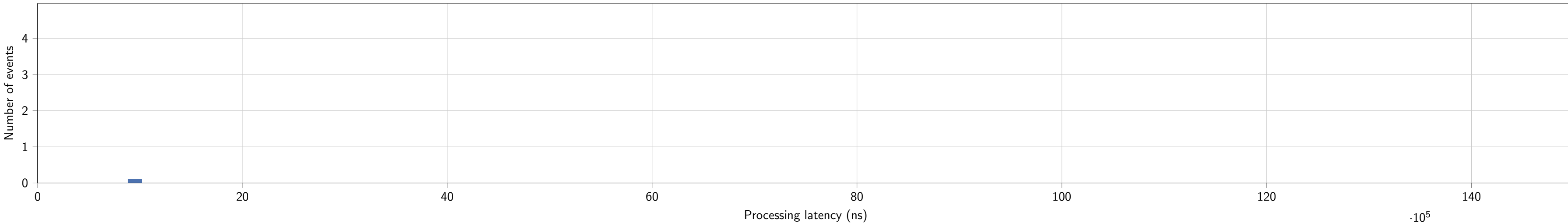
l2_xconnect_mbit5321hires.histogram.csv: tx: 10.41mpps, 0.05stdDev; rx: 10.36mpps, 0.00stdDev



l2_xconnect_mbit5331hires.histogram.csv: tx: 10.41mpps, 0.05stdDev; rx: 10.22mpps, 0.00stdDev



l2_xconnect_mbit5341hires.histogram.csv: tx: 10.49mpps, 0.05stdDev; rx: 10.35mpps, 0.00stdDev



l2_xconnect_mbit9000.histogram.csv: tx: 14.87mpps, 0.07stdDev; rx: 10.33mpps, 0.00stdDev

