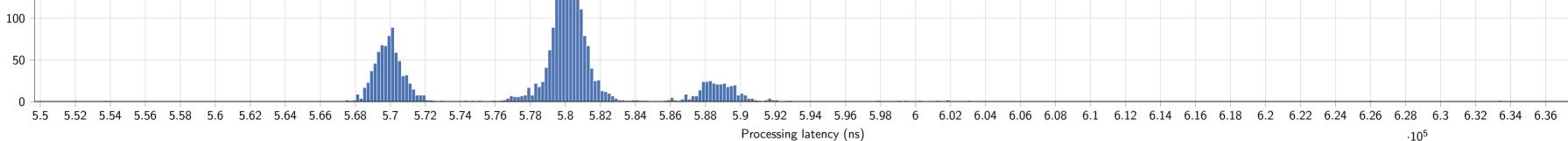
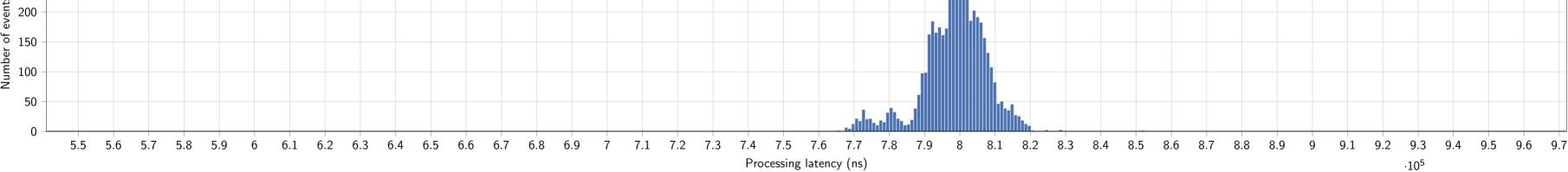
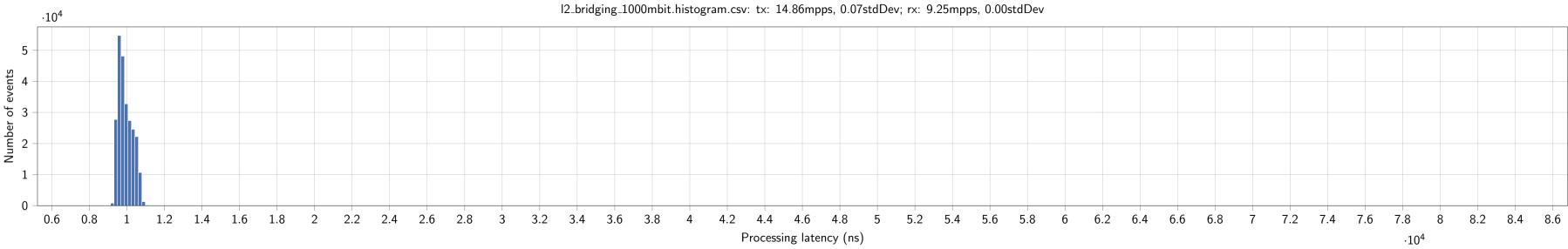


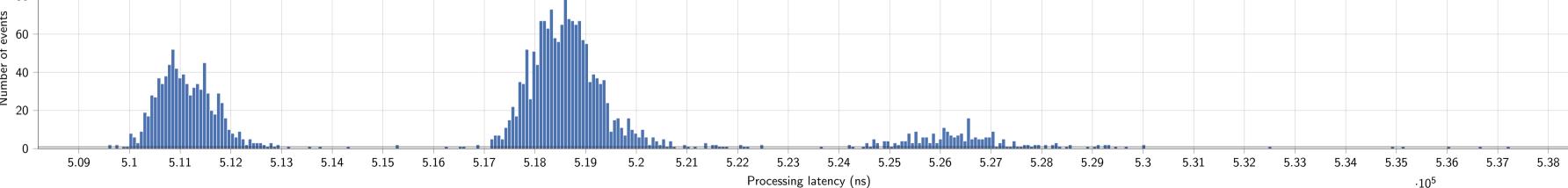
l2_bridging_1_load.histogram.csv: tx: 0.99mpps, 0.00stdDev; rx: 0.99mpps, 0.00stdDev





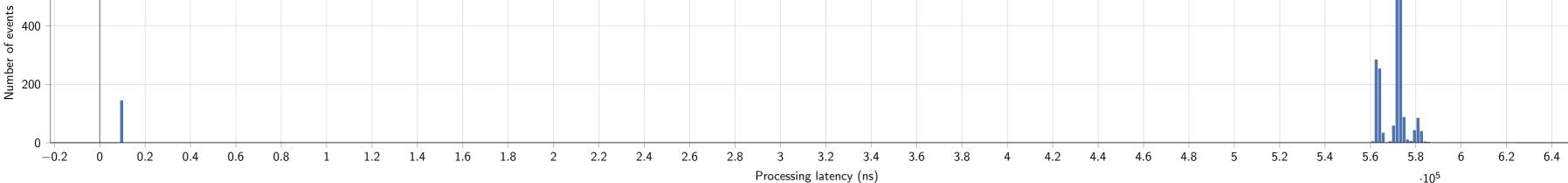


I2_xconnect_load.histogram.csv: tx: 1.96mpps, 0.01stdDev; rx: 1.97mpps, 0.00stdDev



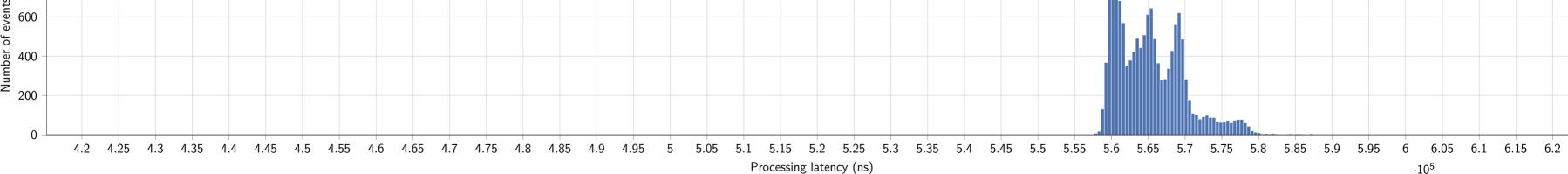


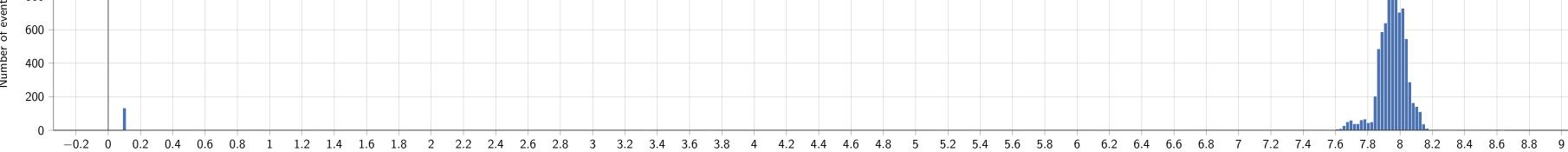
Processing latency (ns)



l2_bridging_6800mbit.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.37mpps, 0.00stdDev

l2_bridging_5000mbit.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.36mpps, 0.00stdDev

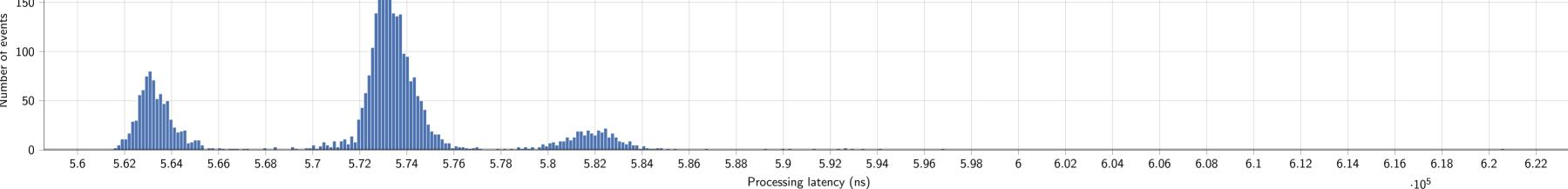




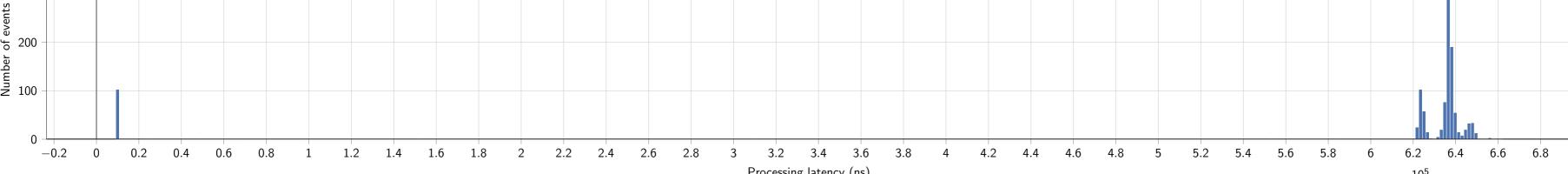
Processing latency (ns)

l2_multimac_10000.histogram.csv: tx: 12.49mpps, 0.06stdDev; rx: 9.35mpps, 0.00stdDev

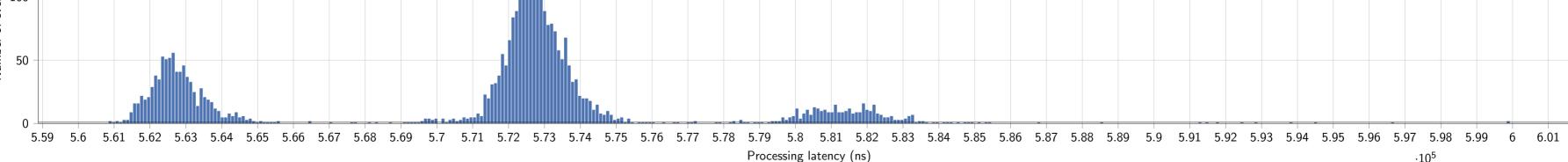
l2_bridging_2_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.37mpps, 0.00stdDev



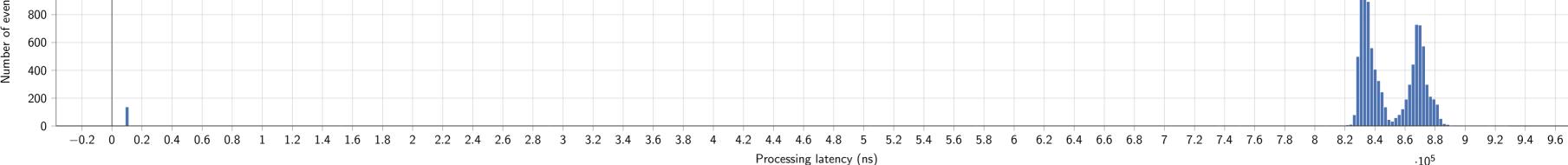
I2_bridging_3_load.histogram.csv: tx: 10.15mpps, 0.05stdDev; rx: 6.72mpps, 0.00stdDev



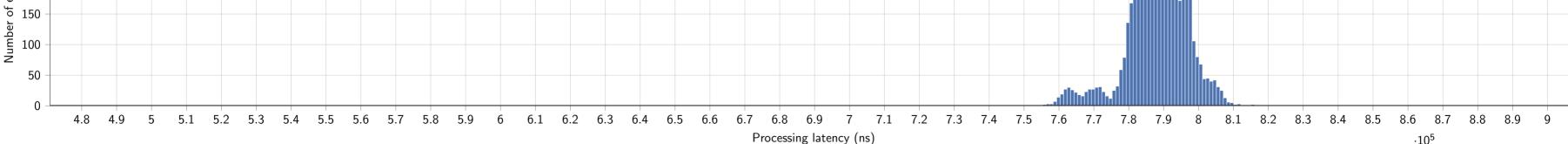
I2_bridging_4_load.histogram.csv: tx: 10.15mpps, 0.05stdDev; rx: 6.51mpps, 0.00stdDev

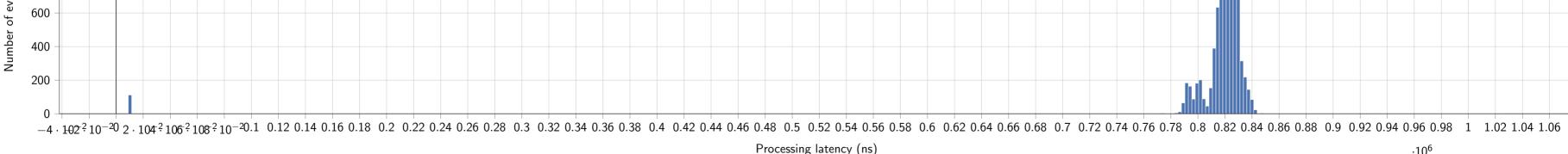


l2_multimac_100.histogram.csv: tx: 10.19mpps, 0.05stdDev; rx: 6.77mpps, 0.00stdDev

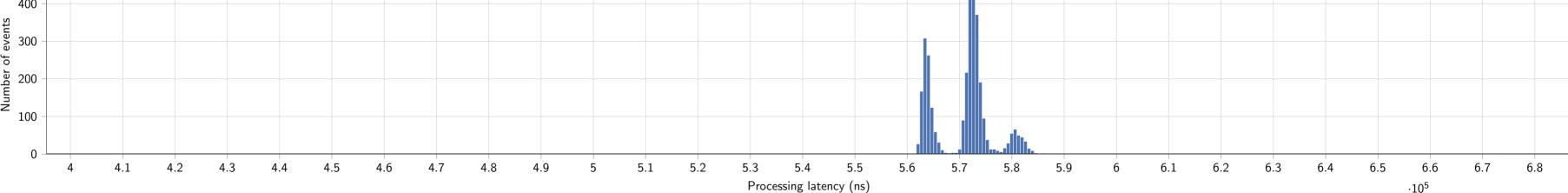


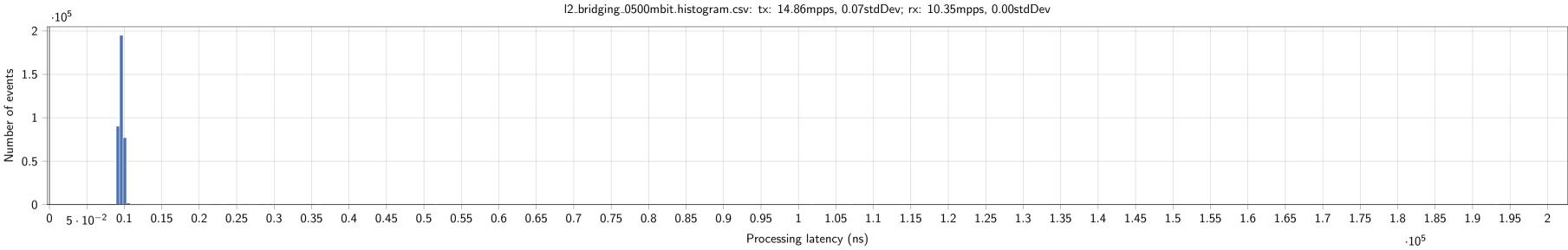
l2_multimac_100000.histogram.csv: tx: 10.16mpps, 0.05stdDev; rx: 6.69mpps, 0.00stdDev

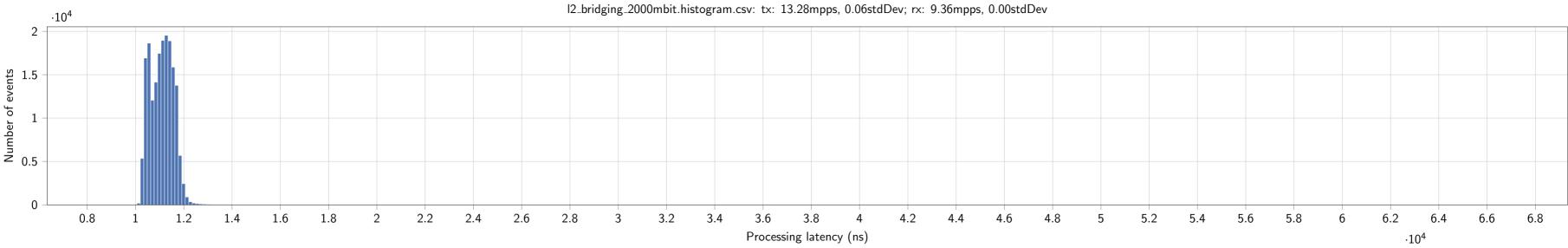




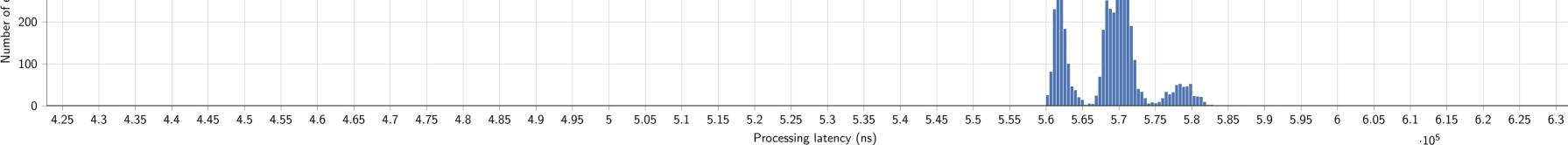
l2_bridging_6600mbit.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 8.42mpps, 0.00stdDev

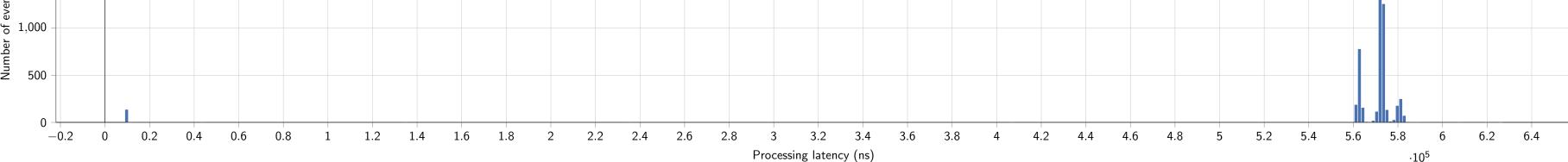






l2_bridging_6000mbit.histogram.csv: tx: 10.14mpps, 0.05stdDev; rx: 6.16mpps, 0.00stdDev





I2_bridging_0_load.histogram.csv: tx: 11.78mpps, 0.06stdDev; rx: 9.37mpps, 0.00stdDev

