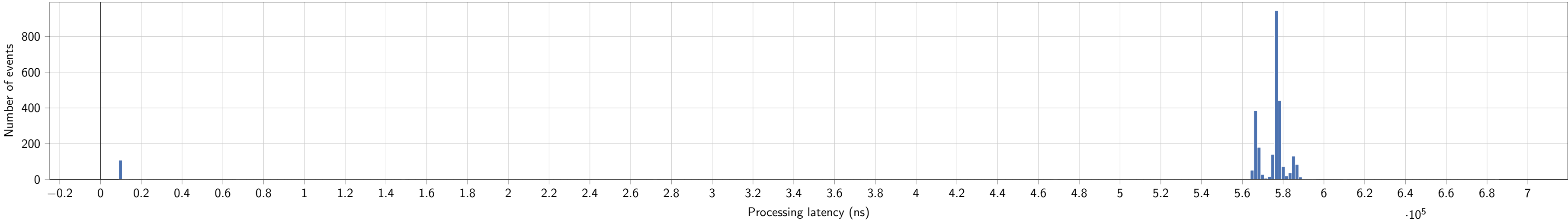
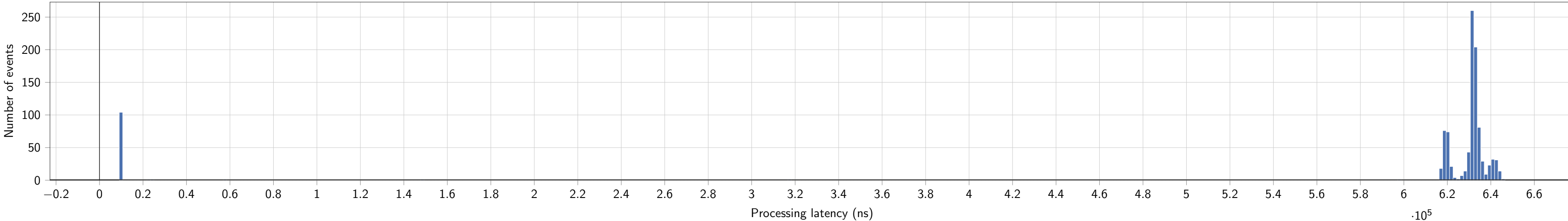


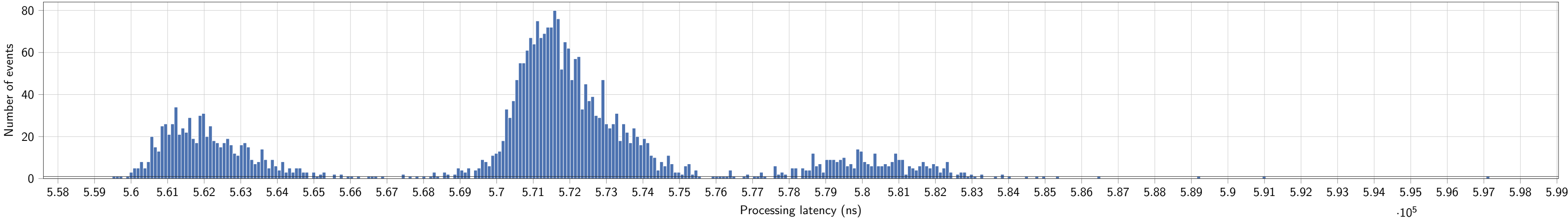
l2\_bridging\_5\_load.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 8.49mpps, 0.00stdDev



l2\_bridging\_1\_load.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 8.50mpps, 0.00stdDev

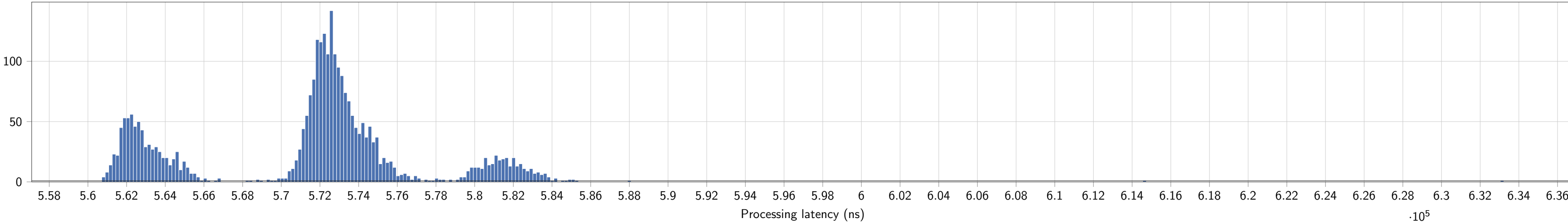


l2\_bridging\_2\_load.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 9.38mpps, 0.00stdDev



l2\_bridging\_0\_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 9.40mpps, 0.00stdDev

Number of events



l2\_bridging\_4\_load.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 9.30mpps, 0.00stdDev

Number of events

1,000

500

0

-0.2

0

0.2

0.4

0.6

0.8

1

1.2

1.4

1.6

1.8

2

2.2

2.4

2.6

2.8

3

3.2

3.4

3.6

3.8

4

4.2

4.4

4.6

4.8

5

5.2

5.4

5.6

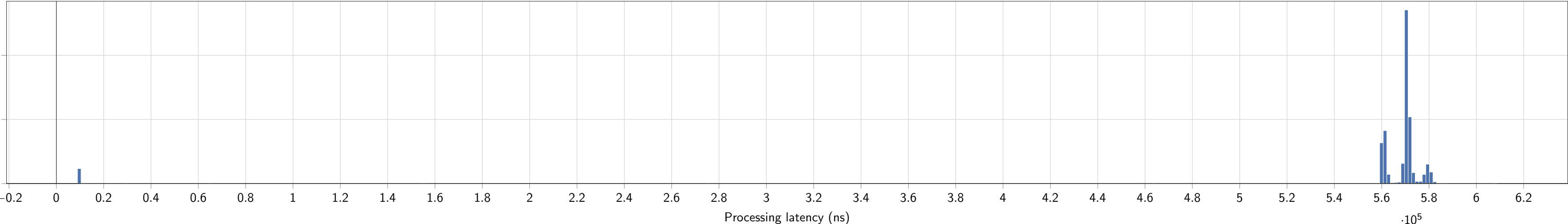
5.8

6

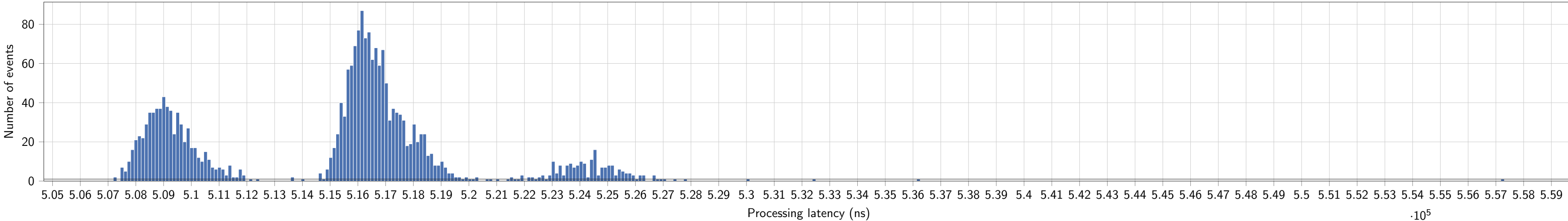
6.2

Processing latency (ns)

$\cdot 10^5$



l2\_xconnect\_load.histogram.csv: tx: 14.86mpps, 0.07stdDev; rx: 10.39mpps, 0.01stdDev



l2\_bridging\_3\_load.histogram.csv: tx: 14.86mpps, 0.08stdDev; rx: 9.37mpps, 0.00stdDev

