Address	Bits						ĺ																									
	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	or operation																									1	Output 2 select					
0x4200 0000	reset_n: set to '1' for operation					n/a							Delay Raw			Input Derivative	Input 1/2			Delay Fine				e/u	D /1-		4 5 6	Biqua Delay Biqua Biqua Biqua	yed ad 3 ad 4	Raw		
0x4300 0000		n/a Disable Output 2 Disable Output 1														DIO P	 - -															
n=2,3,4,5,6,7																																
0x4n00 0008															Biqu	uad (n-1)	- B0														
0x4n10 0000																uad (
0x4n10 0008																uad (
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