

Experiment 6: ARM Assembly - Emulation in KEIL

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1 Introduction:

I am Deepak Charan S and this is my report for the 6th assignment, which I had done on 1st October 2024

2 Objectives

To learn the architecture of ARM processor and the basics of ARM instruction set, in particular the ARM instructions pertaining to computations. To go through example programs and write assembly language programs for the given set of problems:

1. Compute the factorial of a given number using ARM processor through assembly programming.
2. Combine the low four bits of each of four consecutive bytes beginning at LIST into one 16-bit halfword 'A'. Similarly, combine the higher four bits of each of the four consecutive bytes beginning at LIST into one 16-bit halfword 'B'. Combine BA (in that order) and store the result in the 32-bit variable RESULT.
3. Given a 32 bit number, identify whether it is an even or odd, without using division.

3 Equipments/Software Required:

1. KEIL MicroVision 5 IDE for ARM [MDK529.EXE] and ARM7 component, MDK79525.EXE

2. PC Windows OS with the both the files [MDK529.EXE and MDK79525.EXE] loaded

4 Code Used

1. Factorial Computation

```
1      AREA abc, CODE, READONLY
2  init
3      LDR R0, NUM1
4      LDR R1, NUM2
5      LDR R2, NUM3
6      MOV R3, R2
7  loop
8      MUL R3, R2, R0
9      MOV R2, R3
10     SUB R0, R0, #1
11     CMP R1, R0
12     BCC loop
13     B over
14 over
15     B over
16 NUM1 DCW &0000
17     align
18 NUM2 DCW &0000
19     align
20 NUM3 DCW &0001
21     END
```

2. 16-bit addition using a 8-bit processor

```
1      AREA abc, CODE, READONLY ;
2      LDR R0, =LIST
3  init
4      LDR R1, [R0], #1
5      BIC R2, R1, #0xFFFFFFFF0
6      BIC R1, R1, #0xFFFFFFFF0F
7      LDR R3, [R0], #1
8      BIC R4, R3, #0xFFFFFFFF0
9      BIC R3, R3, #0xFFFFFFFF0F
10     LDR R5, [R0], #1
11     BIC R6, R5, #0xFFFFFFFF0
12     BIC R5, R5, #0xFFFFFFFF0F
```

```

13      LDR R7,[R0],#1
14      BIC R8, R7, #0xFFFFFFFF0
15      BIC R7, R7, #0xFFFFFFFF0F
16      LDR R0,NUM
17      MUL R11,R5,R0
18      MUL R5,R6,R0
19      MUL R9,R0,R0
20      MUL R12,R3,R9
21      MUL R3,R4,R9
22      MUL R10,R9,R0
23      MUL R13,R1,R10
24      MUL R1,R2,R10
25      ADD R7, R11, R7
26      ADD R7, R12, R7
27      ADD R7, R13, R7
28      ADD R8, R5, R8
29      ADD R8, R3, R8
30      ADD R8, R1, R8
31      MUL R9,R7,R10
32      ADD R1, R8, R9
33      SWI &11
34  endloop
35      B endloop
36  LIST DCB 0xDB, 0xEE, 0xAE, 0xDF
37      align
38  NUM      DCW &0010
39      END

```

3. Checking Even/Odd

```

1      AREA abc, CODE, READONLY
2  init
3      LDR R0,NUM1
4      BIC R1,R0,#0xFFFFFFFFE
5  over
6      B over
7  NUM1 DCW &FFFF
8      END

```

5 Procedure Followed

To complete this experiment, we took the following steps:

1. Go through the handout to understand the working of ARM processors and assembly.
2. KEIL MicroVision 5 IDE for ARM [MDK529.EXE] and ARM7 component, MDK79525.EXE on our windows PCs.
3. Write the ARM assembly code on Keil to accomplish all the three objectives.
4. Clean existing solutions and build new solution
5. Debug and test them out line by line on Keil and analyse the values in registers and flags.
6. Verify the result with manually calculated answer.

Note: The code for Even-Odd checker was written by me

6 Interpretations of result

Factorial computation, rearranging and manipulation of bits, checking of even/odd of 32-bit binary numbers are demonstrated through emulation of ARM assembly programming.

7 References:

Handouts, Sample Code, Welsh textbook provided in moodle.