

# Experiment 8: ADC / DAC Implementation in ARM based LPC2148 Development Board (through C - Interface)

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November 2024

## **1 Objective**

1. To understand C-interfacing (use C-programming) in an ARM platform
2. To study and implement ADC / DAC in ARM platform

## **2 Equipments/Software Required:**

1. ARK's LPC2148 embedded ARM development board and accessories
2. RS-232 cable
3. Windows PC with Keil microvision 5, Flash magic, and Burn o-mat.
4. USB - serial converter
5. DSO (Digital Storage Oscilloscope)
6. Sample programs for generating digital inputs. (For analog inputs, potentiometer is used)

## **3 General Procedure:**

1. Go through the handout to understand the workings of LPC2148 board.
2. Download KEIL Microvision 5 and Flash Magic on a Windows PC.

3. build a new project in KEIL Microvision 5 with the right configurations and create a new C file in the Source File section
4. Write the proper C code to accomplish all the desired objectives.
5. Burn the code into the board using Flash Magic (By using the .hex file that would have been automatically generated when we built the project)
6. Verify if the code works by checking for multiple test cases.
7. Repeat this procedure for all the tasks

## 4 ADC Tasks:

### 4.1 Convert analog signal from a sensor into a digital signal

#### 4.1.1 Code:

```

1
2 #include <LPC214x.H>                                /* LPC214x
   definitions */
3
4 //#include "delay.h"
5 //#include "led.c"
6
7 unsigned long Read_ADC0(unsigned char);
8 void Init_ADC0(unsigned char);
9 void delay_mSec(int dCnt);
10
11 #ifndef __ADC_H
12 #define __ADC_H
13
14
15 #define CHANNEL_0    0
16 #define CHANNEL_1    1
17 #define CHANNEL_2    2
18 #define CHANNEL_3    3
19 #define CHANNEL_4    4
20 #define CHANNEL_5    5
21 #define CHANNEL_6    6
22 #define CHANNEL_7    7

```

```

23
24
25 /* Crystal frequency,10MHz~25MHz should be the same as
    actual status. */
26 #define Fosc          12000000    /* 12 MHz is the
    operational frequency of o/p dgtlClk */
27 #define ADC_CLK       1000000     /* set to 1Mhz */
28
29 /* A/D Converter 0 (AD0) */
30 #define AD0_BASE_ADDR    0xE0034000
31 #define ADC_INDEX        4
32
33 #define ADC_DONE          0x80000000
34 #define ADC_OVERRUN       0x40000000
35
36
37 #define ADC_FullScale_Volt  3.3 // 3.3V - ADC Referance
    Voltage
38 #define ADC_FullScale_Count 1024 // 2^10 - 10 bit ADC
39 #define LED_IOPIN          IOOPIN
40 #define BIT(x) (1 << x)
41
42 #define LED_D0 (1 << 10)          // P0.10 mapping same as
    in Exp7 switch LED
43 #define LED_D1 (1 << 11)          // P0.11
44 #define LED_D2 (1 << 12)          // P0.12
45 #define LED_D3 (1 << 13)          // P0.13
46
47 #define LED_D4 (1 << 15)          // P0.15
48 #define LED_D5 (1 << 16)          // P0.16
49 #define LED_D6 (1 << 17)          // P0.17
50 #define LED_D7 (1 << 18)          // P0.18
51 #define LED_DATA_MASK      ((unsigned long)((LED_D7
    | LED_D6 | LED_D5 | LED_D4 | LED_D3 | LED_D2 |
    LED_D1 | LED_D0)))
52
53 #define LED1_ON            LED_IOPIN |= (unsigned long)(LED_D0)
    ; // LED1 ON
54 #define LED2_ON            LED_IOPIN |= (unsigned long)(LED_D1)
    ; // LED2 ON
55 #define LED3_ON            LED_IOPIN |= (unsigned long)(LED_D2)
    ; // LED3 ON
56 #define LED4_ON            LED_IOPIN |= (unsigned long)(LED_D3)

```

```

    ;          // LED4 ON
57 #define LED5_ON      LED_IOPIN |= (unsigned long)(LED_D4)
    ;          // LED5 ON
58 #define LED6_ON      LED_IOPIN |= (unsigned long)(LED_D5)
    ;          // LED6 ON
59 #define LED7_ON      LED_IOPIN |= (unsigned long)(LED_D6)
    ;          // LED7 ON
60 #define LED8_ON      LED_IOPIN |= (unsigned long)(LED_D7)
    ;          // LED8 ON
61
62 #define LED1_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D0);          // LED1 ON
63 #define LED2_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D1);          // LED2 ON
64 #define LED3_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D2);          // LED3 ON
65 #define LED4_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D3);          // LED4 ON
66 #define LED5_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D4);          // LED5 ON
67 #define LED6_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D5);          // LED6 ON
68 #define LED7_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D6);          // LED7 ON
69 #define LED8_OFF      LED_IOPIN &= (unsigned long)~(
    LED_D7);          // LED8 ON
70
71 #endif
72 #ifndef LED_DRIVER_OUTPUT_EN
73 #define LED_DRIVER_OUTPUT_EN (1 << 5)    // P0.5
74 #endif
75 //LED definitions
76
77
78
79
80
81 int main (void)
82 {
83
84     unsigned long ADC_val;
85
86     Init_ADC0(CHANNEL_1);

```

```

87 Init_ADC0(CHANNEL_2);
88
89 delay_mSec(100);
90
91 IOODIR |= LED_DATA_MASK;           // GPIO
92     Direction control -> pin is output
93 IOODIR |= LED_DRIVER_OUTPUT_EN;    // GPIO
94     Direction control -> pin is output
95 IOOCLR |= LED_DRIVER_OUTPUT_EN;
96
97 while(1)
98 {
99     //ADC_val = Read_ADC0(CHANNEL_1);
100     ADC_val = Read_ADC0(CHANNEL_2);
101     ADC_val=(ADC_val>>2);
102     delay_mSec(5);
103
104
105     if(ADC_val & BIT(0)){ LED8_ON; }
106     else {LED8_OFF;}
107     if(ADC_val & BIT(1)) {LED7_ON;}
108     else {LED7_OFF;}
109     if(ADC_val & BIT(2)) {LED6_ON;}
110     else {LED6_OFF;}
111     if(ADC_val & BIT(3)) {LED5_ON;}
112     else {LED5_OFF;}
113     if(ADC_val & BIT(4)) {LED4_ON;}
114     else {LED4_OFF;}
115     if(ADC_val & BIT(5)) {LED3_ON;}
116     else {LED3_OFF;}
117     if(ADC_val & BIT(6)){ LED2_ON;}
118     else {LED2_OFF;}
119     if(ADC_val & BIT(7)) {LED1_ON;}
120     else {LED1_OFF;}
121 }
122
123
124
125 //     return 0;
126 }
127

```

```

128 void Init_ADC0(unsigned char channelNum)
129 {
130     if(channelNum == CHANNEL_1)
131         PINSEL1 = (PINSEL1 & ~(3 << 24)) | (1 << 24);
132         // P0.28 -> AD0.1
133
134     if(channelNum == CHANNEL_2)
135         PINSEL1 = (PINSEL1 & ~(3 << 26)) | (1 << 26);
136         // P0.29 -> AD0.2
137
138     if(channelNum == CHANNEL_3)
139         PINSEL1 = (PINSEL1 & ~(3 << 28)) | (1 << 28);
140         // P0.30 -> AD0.3
141
142     ADOCR = ( 0x01 << 1 ) | // SEL
143             =1, select channel 0, 1 to 4 on ADC0
144             (( Fosc / ADC_CLK - 1 ) << 8 ) | //
145             CLKDIV = Fpclk / 1000000 - 1
146             ( 0 << 16 ) | // BURST
147             = 0, no BURST, software controlled
148             ( 0 << 17 ) | // CLKS
149             = 0, 11 clocks/10 bits
150             ( 1 << 21 ) | // PDN =
151             1, normal operation
152             ( 0 << 22 ) | // TEST1
153             :0 = 00
154             ( 0 << 24 ) | // START
155             = 0 A/D conversion stops
156             ( 0 << 27 ); /* EDGE
157             = 0 (CAP/MAT singal falling,trigger A/D
158             conversion) */
159 }
160 unsigned long Read_ADC0( unsigned char channelNum )
161 {
162     unsigned long regVal, ADC_Data;
163
164     /* Clear all SEL bits */
165     ADOCR &= 0xFFFFF00;
166     /* switch channel, start A/D convert */
167     ADOCR |= (1 << 24) | (1 << channelNum);
168 }

```

```

159     /* wait until end of A/D convert */
160     while ( 1 ) {
161
162     //         regVal = *(volatile unsigned long *) (
163         ADO_BASE_ADDR + ADC_INDEX);
164         regVal = ADOGDR;
165
166         if ( regVal & ADC_DONE ){
167             break;
168         }
169
170         /* stop ADC now */
171         ADCR &= 0xF8FFFFFF;
172         /* save data when it's not overru otherwise, return
173         zero */
174         if ( regVal & ADC_OVERRUN ) {
175             return ( 0 );
176         }
177         ADC_Data = ( regVal >> 6 ) & 0x3FF;
178         /* return A/D conversion value */
179         return ( ADC_Data );
180     }
181
182 void delay_mSec(int dCnt)           // pr_note:~dCnt mSec
183 {
184     int j=0,i=0;
185
186     while(dCnt--)
187     {
188         for(j=0;j<1000;j++)
189         {
190             /* At 60Mhz, the below loop introduces
191             delay of 10 us */
192             for(i=0;i<10;i++);
193         }
194     }
195 }

```

#### 4.1.2 Observations:

- Decreasing step size increases number of bits, and also quantization error increases
- Since  $q=10$  for our ADC, Quantization error is 60.2.

Video for ADC is provided [here](#)

## 5 DAC Tasks:

### 5.1 Saw Tooth Wave

#### 5.1.1 Implementation:

- First set up the DAC outputs
- For constructing a Saw Tooth Wave, we write a for loop in an infinite while loop to send values in an increasing order (and ORing it with the DAC\_BIAS) until a certain threshold (and for loop would start from beginning).
- the while loop ensures we continuously observe a Saw tooth Wave

#### 5.1.2 Code:

```
1 //DAC program for LPC2148 SUNTECH KIT
2
3
4 #include "LPC214x.H" /* LPC214x
   definitions */
5
6 #define DAC_BIAS      0x00010000
7 void mydelay(int);
8
9 void DACInit( void )
10 {
11     /* setup the related pin to DAC output */
12     PINSEL1 &= 0xFFF3FFFF;
13     PINSEL1 |= 0x00080000; /* set p0.25 to DAC output
   */
14     return;
15 }
```



```

16
17 int main (void)
18 {
19     DACInit();
20     int cnt=0;
21     //SQUARE WAVE
22     while(1)
23     {
24         cnt=0;
25         while(cnt<=0x3ff)
26         {
27             DACR=(cnt<<6)|DAC_BIAS;
28             cnt=cnt+1;
29             if(cnt==0x03FF)
30             {cnt=0;
31                 mydelay(0xFF);
32             }
33         }*/
34         return 0;
35     }
36
37 void mydelay(int x)
38 {
39     int j,k;
40     for(j=0;j<=x;j++)
41     {
42         for(k=0;k<=0xFF;k++);
43     }
44 }

```

## 5.2 Triangle Wave

### 5.2.1 Implementation:

- Similar to the Saw tooth Wave, we write a reverse for loop (which starts from 0x3FF and goes till 0) after the first for loop to obtain a triangle wave

### 5.2.2 Code:

```

1 //DAC program for LPC2148 SUNTECH KIT
2
3

```

```

4  #include "LPC214x.H"                                /* LPC214x
    definitions */
5
6  #define DAC_BIAS                                0x00010000
7  void mydelay(int);
8
9  void DACInit( void )
10 {
11     /* setup the related pin to DAC output */
12     PINSEL1 &= 0xFFF3FFFF;
13     PINSEL1 |= 0x00080000; /* set p0.25 to DAC output
    */
14     return;
15 }
16
17 int main (void)
18 {
19     DACInit();
20
21
22     while(1){
23         for(int cnt=0; cnt<1000; cnt++){
24             DACR=((cnt)<<6) | DAC_BIAS;
25         }
26
27
28         for(int dnt=1000; dnt>=0; dnt--){
29             DACR=((dnt)<<6) | DAC_BIAS;
30         }
31     }
32 }
33
34     return 0;
35 }
36
37 void mydelay(int x)
38 {
39     int j,k;
40     for(j=0; j<=x; j++)
41     {
42         for(k=0; k<=0xFF; k++);
43     }
44 }

```

## 5.3 Sine Wave

### 5.3.1 Implementation:

- For a sine wave, we use '*math.h*' library to get the sine of the value
- We use a for loop to pass the sine of a number (which increases till a certain limit) and send it to the DACR (we accordingly scale and round off the sine value)

### 5.3.2 Code:

```
1  #include "LPC214x.h"
2  #include <math.h>
3  #define DAC_BIAS 0x00010000
4
5  // configure the DAC
6  void DACInit(void)
7  {
8      PINSEL1 &= 0xFFF3FFFF;
9      PINSEL1 |= 0x00080000;
10 }
11 // main routine
12
13
14 void delay_ms( int j)
15 {
16     int x,i;
17     for(i=0;i<j;i++)
18     {
19         for(x=0; x<6000; x++);    /* loop to generate 1
20                                     milisecond delay with Cclk = 60MHz */
21     }
22 }
23 int main (void)
24 {
25
26     int i=0,value=0;
27     DACInit();
28     while(1)
29     {
30
31         while(i!=32)
```

```

32 {
33     value = (int)((sin((2*3.141/32)*i)+1)*500);
34
35
36 DACR = ( (1<<16) | (value<<6) );
37 delay_ms(1);
38 i++;
39 }
40
41     i=0;
42
43 }
44 }

```

## 5.4 Photos

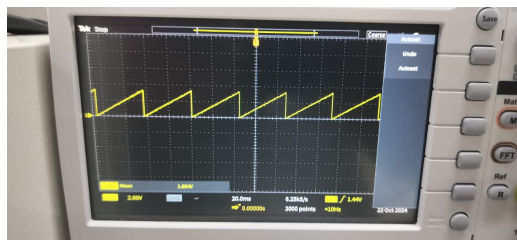


Figure 1: Saw Tooth Wave

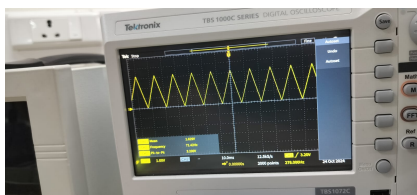


Figure 2: Triangle Wave

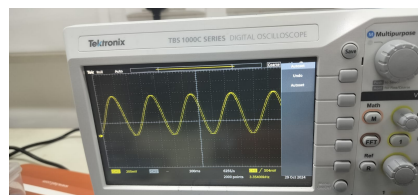


Figure 3: Sine Wave

*Note: I constructed the Sine Wave*

## 6 References:

1. Handouts and the Code provided in Moodle