High Performance Computer Architectures Practical Course
- Exercise 4 -

Tutorium 1

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Figure 1: Output

```
1  TStopwatch timerSIMD;
2  for( int ii = 0; ii < NIter; ii++ )
3      for( int i = 0; i < N; i++ ) {
4          for( int j = 0; j < N; j++ ) {
5              fvec &aVec = reinterpret_cast<fvec&>(a[i][
                   j]);
6              fvec &cVec = reinterpret_cast<fvec&>(
                   c_simd[i][j]);
7              cVec = f(aVec);
8          }
9      }
10 timerSIMD.Stop();
```
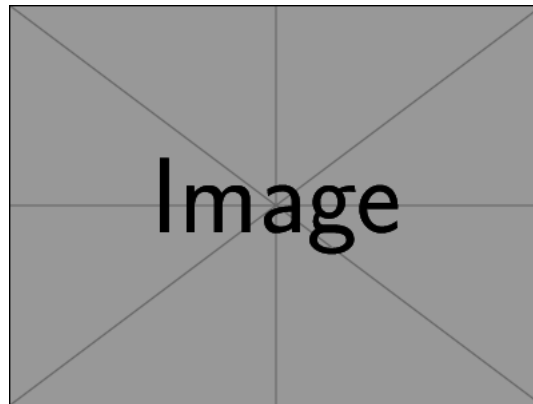
File 1: Matrix.cpp