$\mathbf{SAMODYA} \underset{\mathtt{San \ Jose, \ CA \cdot hello@ransara.xyz \cdot https://s.ransara.xyz}}{\mathsf{ABEYSIRIWARDANE}}$

Work

Test Engineer I	Jabil Circuit, San Jose, CA	Spring 2019 - Present
☐ Design and develop a tab completer framerove technicians' debug workflow a	amework for the Python REPL to use in b nd operator on-boarding process	uilding completers that
$\hfill\Box$ Build a Continuous Delivery pipeline f	or internal tools to get faster iterations	
$\hfill\Box$ Training in Design For Testing in the α	context of electronics mass manufacturing	
Test Engineering Intern	Jabil Circuit, San Jose, CA	Summer 2018
$\hfill\Box$ End to end development of a tool for l	ive analysis of internal test and debugging	data
$\hfill\Box$ Design and develop a failure resistant	testcell grid data aggregator, backend and	a simple frontend
\square For high impact in workcell efficiency,	selected for competition: Jabil - Design Be	est Practices (ongoing)
☐ Experience with: Golang, Python, MyS	SQL, JS/React	
Gradudate Research Assistant	Computer Science, Purdue	Spring 2017–Spring 2018
☐ Worked on: 3-way merging data struct	tures research project	
☐ Workshop paper: Mergeable Types, M	L Workshop, 2017	
\square Experience with: OCaml, Git internals	s, Database Consistency	
Teaching Assistant	Computer Engineering, Purdue	Spring 2015
\square Course: Computer Design and Prototy	yping (ECE437)	
☐ Assist students in benchmarking, analy pipelined MIPS processor	yzing performance and taking design decisi	ons to develop a multicore
\square Experience with: Verilog, Python, Log	ic synthesis targeting FPGA	
Student Software Developer	Krannert Computing, Purdue	Summer 2012–Fall 2016
$\hfill \square$ Worked with a team on: Backend and	frontend for web and desktop applications	
\square CV Parser, Job Queue, Calendar Appl	ication, Course Content aggregator	
\Box Mentor new students on coding standa	ards, best practices and version control	
\square Experience with: C#, MSSQL, Regex,	${\rm XML/XSLT,\ HTML/CSS/JS,\ MVC,\ TFS}$	
EDUCATION		
MSc Computer Science	Purdue, West Lafayette, IN	Spring 2016– <i>Fall 2018</i>
☐ Courses: Distributed systems, Advance Language Theory, Algorithm design are	ed database systems (Relational and non-rad analysis	elational), Programming
BSc Computer Engineering	Purdue, West Lafayette, IN	Fall 2011–Spring 2015
	lesign and prototyping, Signals and system vanced C, Intro to AI, Intro to Infosec, Con	
NOTABLE PROJECTS		
Mergeable Types ocaml	https://github.c	om/sransara/mtypes-lib
·	ta structures with 3-way merge operations	
Distributed Key-Value Store java	https://github.com/sra	
	ons, $2PL/2PC$ concurrency control, Strong	
Micro Language Compiler java		/sransara/microbe-lang
	eration, Control flow graph analysis and a V	
Multicore MIPS processor verilog		sransara/aww-processor
	S instruction set architecture optimized for	r synthesis
OTHER		
\square Recognized in hall of fame as reward re	ecipient for disclosure of XSS bugs in Gmai	l: 2012 July, 2012 October
☐ Awarded bronze medal at Internationa	l Junior science olimpiad, Baku, Azerbaija	n: 2009