



P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 30	0.020 at V _{GS} = - 10 V	- 9.1		
	0.035 at V _{GS} = - 4.5 V	- 6.9		

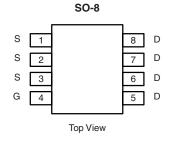
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Advanced High Cell Density Process
- Compliant to RoHS Directive 2002/95/EC

Pb-free RoHS COMPLIANT HALOGEN Available

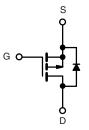
APPLICATIONS

- Load Switches
- · Battery Switch



Ordering Information: Si4435BDY-T1-E3 (Lead (Pb)-free)

Si4435BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 30		٧
Gate-Source Voltage		V _{GS}	± 20		
O D . O (T 150.00)3	T _A = 25 °C	- I _D	- 9.1	- 7	A
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 7.3	- 5.6	
Pulsed Drain Current		I _{DM}	- 50		Α
Continuous Diode Current (Diode Conduction) ^a	Diode Current (Diode Conduction) ^a		- 2.1	- 1.25	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	2.5	1.5	W
	T _A = 70 °C		1.6	0.9	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manian una lumation ta Austrianti	t ≤ 10 s	- R _{thJA}	40	50	°C/W
Maximum Junction-to-Ambient ^a	Steady State		70	85	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	18	22	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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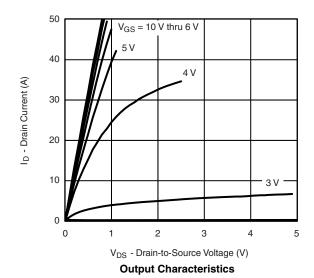
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 1	μΑ	
					- 5		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 40			Α	
Drain-Source On-State Resistance ^a	В	V _{GS} = - 10 V, I _D = - 9.1 A		0.015	0.020	Ω	
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 6.9 A		0.025	0.035		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 9.1 A		24		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 2.1 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			33	70		
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -9.1 \text{ A}$		5.8		nC	
Gate-Drain Charge	Q_{gd}			8.6			
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		15	25		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, $V_{GEN}=$ - 10 V, $R_g=$ 6 Ω		110	170	ns	
Fall Time	t _f			70	110		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.1 A, dl/dt = 100 A/μs		60	90		

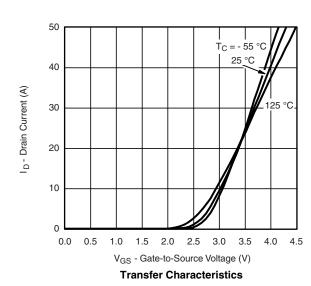
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

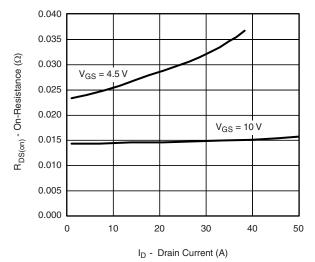




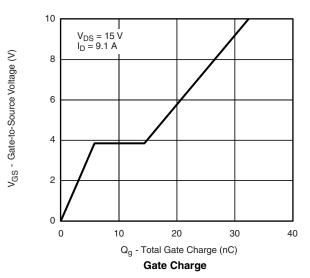


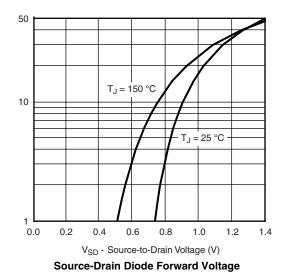


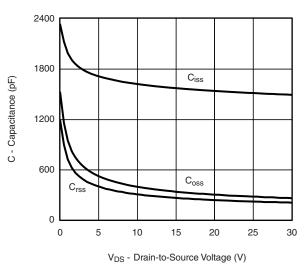
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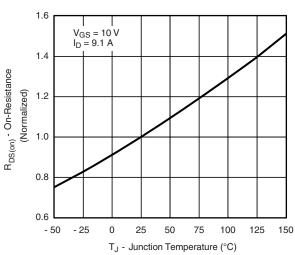
On-Resistance vs. Drain Current



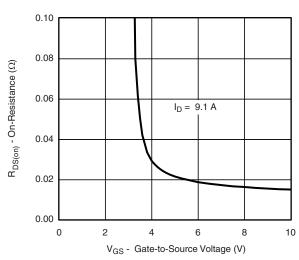




Capacitance



On-Resistance vs. Junction Temperature



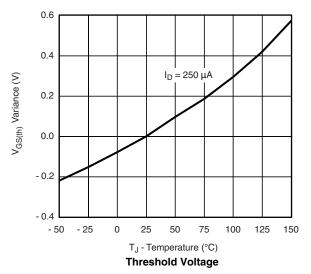
On-Resistance vs. Gate-to-Source Voltage

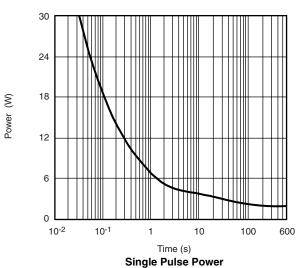
Is - Source Current (A)

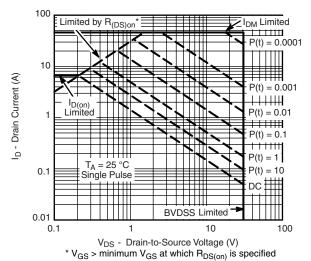
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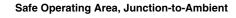
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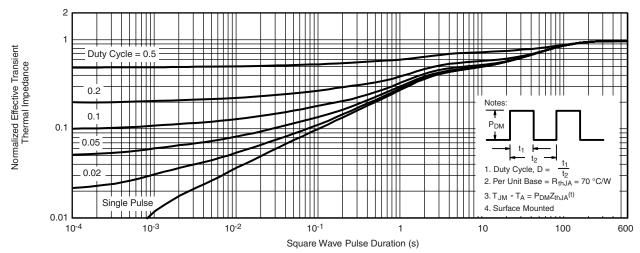
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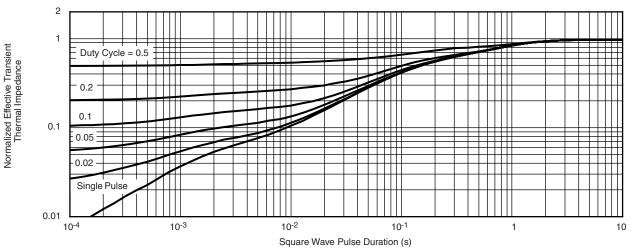




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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