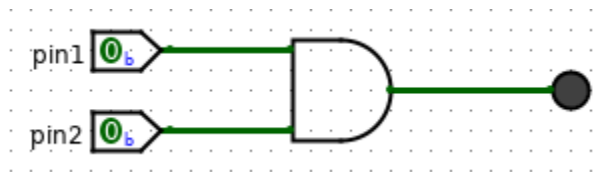


Computer Systems Week 1 Lab

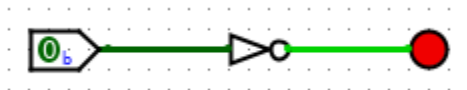
Daniel Coady (102084174)

07/08/2019

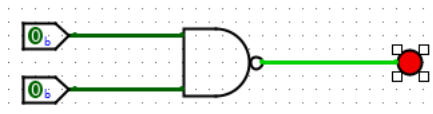
AND Gate		
pin1	pin2	output
0	0	0
1	0	0
0	1	0
1	1	1



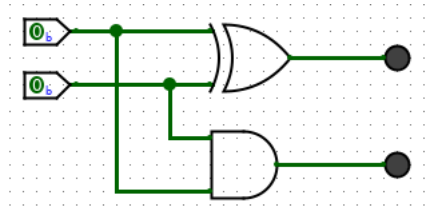
NOT Gate	
pin1	output
0	1
1	0



NAND Gate		
pin1	pin2	output
0	0	1
1	0	1
0	1	1
1	1	0



Half Adder			
pin1	pin2	output	carry bit
0	0	0	0
1	0	1	0
0	1	1	0
1	1	0	1



Full Adder				
pin1	pin2	pin3	output1	output2
0	0	0	0	0
1	0	0	1	0
0	1	0	1	0
0	0	1	1	0
1	1	0	0	1
1	0	1	0	1
0	1	1	0	1
1	1	1	1	1

