



Pooja Rani

Curriculum Vitae

Education

- 2018 – now **Ph.D in Computer Science**, *University of Berne*, Switzerland.
- 2015 – 2017 **Masters of Engineering in Software System**, *Birla Institute of Technology and Science-Pilani*, Rajasthan, India, *CGPA – 8.53*.
- 2011 – 2014 **Bachelor of Engineering in Computer Science**, *The Institution of Engineers*, India, *CGPA – 8.33*.
- 2008 – 2011 **Diploma in Computer Science and Engineering**, *Kalpana Chawla Government Polytechnic for Women*, Ambala, Haryana, India, *Percentage – 83*.

Technical Skill

- Programming Language C/C++, Java, HTML, CSS, Unified Modelling Language(UML), Node js, Pharo
- Database MySQL, Hive, Oracle
- Development Tools Eclipse, Net-Beans, IntelliJ, Django
- Testing Tools Selenium, TestNG, Postman
- Version Controlling Git
- Data Science Apache Hadoop, Apache Hive, IBM SPSS
- Design Tools Rational Rose, Lucid chart, Visual Paradigm
- Electives Cloud Computing, Object Oriented Analysis and Design, Artificial Intelligence, Software Architecture, Software Testing, Software for Embedded System, Real Time Systems, Data Warehousing.

Work Experience

People Interactive Group pvt ltd.

July,2017 – **Software Developer**, *Technology Team*, Mumbai, India..

Dec, 2017 Responsibilities:

- To handle payment module of website
- Helped in changing underlying architecture of product and migrating code to node js loopback framework
- Learned delivering code without production bug and maintaining code standards.

TECHNOLOGY: Loopback, Lodash, Lumen, Node JS, Php, MySQL, Linux, Apache Server, JIRA

VMware Software India pvt ltd.

Jan,2017 – **Member of Technical Staff, Project Intern**, *Cloud EUC-Research and Development*, Bangalore, India, VMware Identity and access Management(VIDM) product focus on developing digital workspace where a user can get all enterprise applications at a single portal.

Jun,2017

Responsibilities:

- Helped in developing a framework for automating identity access management flow.
- Developed regression test suite and implemented to execute on continous integration model
- Worked on automating testing of cloud environment Integration of VIDM product with AirWatch(MDM) to keep enterprise data seperate and available on one portal.

TECHNOLOGY: Spring framework, Selenium, TestNG, Postman, Java, vSphere, Mac, Jenkins

Samsung Research Institute

Jul,2011 – **Software Tester**, *Mobile Evaluation Team of India-Quality Assurance*, Noida, India,
July,2014 Team focus on testing of mobile phones before launch to market and managing test plans executing across all India centers.

Responsibilities:

- Created test plan, test templates, resource planning, organized review meetings.
- Understanding of various testing phases in software development lifecycle(SDLC).
- Acted as a Product Owner, managing a team of 20 testers and all regional testers (73 members) by assigning tasks, making deadlines and reports.

TECHNOLOGY: PLM for bug tracking, XML, Agilent 8990

Projects

Automatic conversion of a UML Diagram into test cases

Jan,2016 – **Research Project**, This project aimed at minimizing development cost by designing
Dec,2016 test cases in design phase using UML diagrams. It took sequence diagram as input then sequence diagram is traversed, conditional predicates are selected and converted to control flow to find test path scenarios.

TECHNOLOGY: Visual Paradigm, Net-Beans, Swing, Java, Grappa library, SAX Parser

Answer Summarization System

Oct,2016 – Developed an application where machine is able to present summarized answer for a question.
Nov,2016 It requires preprocessing, calculating relevance of a sentence and question and retrieving answer from multiple documents.

TECHNOLOGY: Natural lanaguge toolkit for python, python, Scikitn-learn and numpy library

Implantable Heart Device for Heart Rate Correction-Pacemaker

- Nov,2016 – Developed real time system which helps in controlling abnormal heart rhythms. System uses sensor as shared resource and implemented using MUTEX. Whenever heart rate is low than defined range, electric pulse are generated using sporadic job.

TECHNOLOGY: Micro COS2 , C

Academic Conference Management System

- Jan,2016 – Designed a web application to organize academic conferences where author can submit their paper, reviewer can review paper. Implemented using software engineering processes, performed functional and non-functional requirements engineering, identifying suitable software architecture style and design patterns.

TECHNOLOGY: Apache server tomcat, Eclipse, JSP, Servlets, CSS, HTML, JDBC, Oracle Database

Smart Attendance using RFID System

- Apr,2016 – Designed an application for automatic attendance using RFID system. RFID card reader read RFID card and process attendance of students. If students are lacking minimum attendance criteria then warning is sent via SMS and Email. Teachers can also view attendance of their lecture and analyse.

TECHNOLOGY: Raspberry Pi, Django, python, SQLite, Rational Rose

Security enhancement of containers using Seccomp2

- Oct,2015 – Project targeted at analyzing security requirements of computational programs and providing secure application using sandboxing mechanism in linux kernel. Implemented using Seccomp2 by customizing LXC with writing Seccomp policy.

TECHNOLOGY: Linux container, LXC library, Seccomp2, C

Course content delivery using Apache Mesos scheduler

- Sep,2015 – Implemented scheduler for course content delivery system like student can request for course content(pdf,ppt etc). Collecting and processing user request, matching task configuration with available resource configuration and then providing content.

TECHNOLOGY: Apache Mesos Scheduler, Java, Eclipse

Relation between documents using Map-Reduce for file system

- Aug,2015 – Developed an algorithm to traverse files from HDFS in a multi-node cluster, filtering stop keywords and finding relation between documents.

TECHNOLOGY: Apache Hadoop, Java, Eclipse

Position of Responsibility

- Feb,2018 – **Teaching Assistant, University of Berne.**

Jun, 2018 Programming language: Explaining various concepts of object-oriented programming languages and designing exercises.

- Aug,2015 – **Teaching Assistant, BITS Pilani.**

- Dec,2016
- Computer Architecture: Explaining various concepts of Verilog and Computer Architecture during lab hours, redesigning lab experiments.
 - Computer Programming: Explaining concepts of C Language, organizing various quiz and test timely, providing solution and handling all queries during lab hours.
 - Software Testing: Designing questions and answers, checking answer sheets and handling queries related to subject.

- Mar,2013 – **Review Member, Samsung Research Institute.**

Jul,2014 Designing new test cases, analysing market defects and updating test cases.

Aug,2010 – **Talent Show Event Co-ordinator, KCGPW, Ambala.**
Jul,2011 Planning various events, promoting events, designing articles and conducting events.

Achievements and Awards

- 2018 Best presentation award at SATTOSE, 2018
- 2014 Qualified GATE 2015 - Research fellowship from MHRD(Ministry of Human Research Development), India.
- 2014 Certification for cracking foundation level exam organised by International Software Testing Qualification Boards(ISTQB)
- 2012 Employee of the Year – Recieved youngest employee of the year for excellence and team work.
- 2012 Secured 2nd rank in annual evaluation at Samsung.
- 2011 Batch Topper – Secured 1st Rank in Diploma college.
- 2010 Runners-up position in state inter college youth festival dance competition.
- 2007 Silver Medal – Won Silver Medal in state level Taekwondo Championship, Haryana.
- 2003 Runners-up position in state level volleyball competition.

Interests

- Dance
- Cooking
- Taekwondo
- Reading

Personal Details

Date of Birth – 20-01-1993
Nationality – Indian
Language – English, Hindi

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References

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