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Functional safety packages
STM32 MCUs and MPUs
STM8 MCUs

“If only

I could speed up the design time of safety-certified systems

This is where we come in

Free safety packages for STM32 and STM8 with an ecosystem of ST Authorized Partners

Achieve functional safety certification with ST MCUs and MPUs

With its **Functional Safety Packages** based on robust built-in MCU/MPU safety features, ST provides a comprehensive set of certified software libraries and documentation for manufacturers to significantly reduce the development efforts, time and cost to achieve functional safety standard certifications.

- **SIL Functional Safety Package**
  for industrial IEC 61508 (STM32)
- **ASIL Functional Safety Package**
  for automotive ISO 26262 (STM8A)
- **Class B Functional Safety Package**
  for household electrical appliances
  IEC 60335-1/60730-1 (STM32 & STM8)

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STM32 built-in safety features

- Dual watchdogs: Independent watchdog and system window watchdog
- Backup clock circuitry with clock security system (CSS)
- Supply monitoring (POR, BOR, PVD)
- I/O function locking
- PWM critical register protections with write-once registers (except on STM32L0/L1)
- Memory protection unit (MPU) with 8 or 16 regions to ensure data integrity from invalid behavior (except on STM32F0)
- Built-in safety features in Cortex-M cores (dual stack pointer, fault exceptions, debug module)

Other features	F0	F1	G0	F3	G4	F2/F4	F7	H7	L0/L1	L4/L4+	L5	U5	WB	WL	MP1
Nb of Hardware CRC unit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	2
Programmable polynomial in CRC unit	(1)		●		●		●	●	(1)	●	●	●	●	●	●
Multiple Flash memory protection levels	●		●	●	●	●	●	●	●	●	●	●	●	●	
PWM stop on core lockup	●		●	●	●			●		●	●	●	●	●	●
Parity bit for SRAM memory (1bit/byte)	●		●	●	●					●	●		●	●	
ECC (SECDED) for SRAM								●				●			
ECC (SECDED) for Flash memory			●		●			●		●	●	●	●	●	

(1) Depending on part number

SIL Functional Safety Package

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SIL functional safety package for STM32

Reduce time and cost to build STM32-based systems certified to IEC 61508 industrial safety standard

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SIL Functional Safety Package for STM32

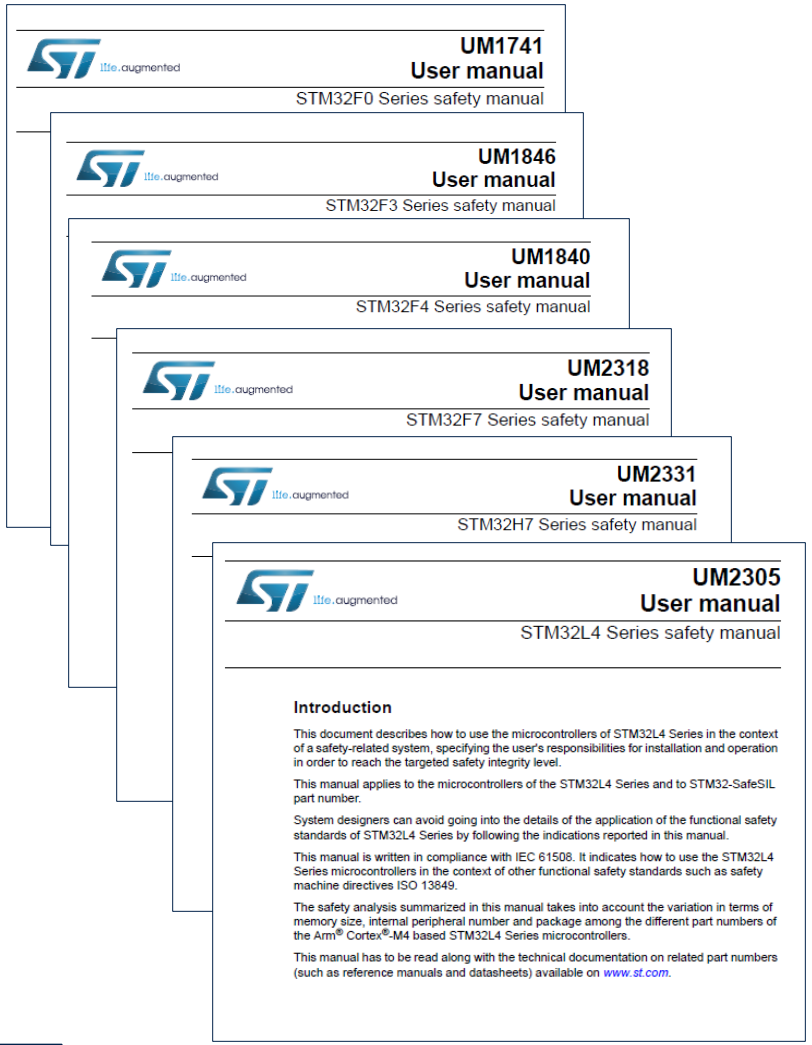
without Package

ST provides a complete, certified offering to

- Lower project costs
- Reduce design complexity
- Ease SIL certification assessment

with Package

SIL functional safety for STM32 safety documentation



Safety manuals: detailed list of safety requirements (conditions of use) and examples to guide STM32 users to achieve safety integrity level certification in compliance with IEC 61508.

Available at STM32 series level for free download on www.st.com/x-cube-stl

FMEA: detailed list of MCU/MPU failure modes and related mitigation measures adopted

FMEDA: static snapshot reporting IEC 61508 failure rates, computed at both MCU/MPU and basic function detail levels.

Available on demand at STM32 series level (*)(**) on www.st.com/x-cube-stl

(*) submitted to NDA
(**) FMEDA snapshot is generated for a specific set of part numbers

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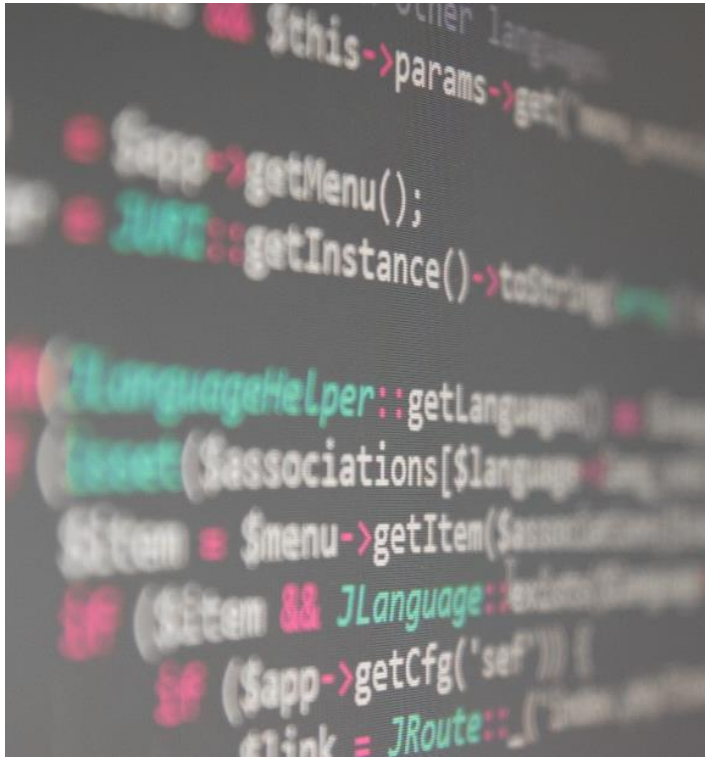
SIL functional safety package for STM32 X-CUBE-STL self-test libraries



- Software-based diagnostic suite designed to detect random hardware failures in safety-critical STM32 core components (CPU + SRAM + Flash memory)
- Diagnostic coverage verified by state-of-the-art ST proprietary fault injection methodology
- Application independent: can be potentially used in any end customer application
- Compiler independent: delivered as object code
- Certified by TÜV Rheinland [1]
- IEC 61508 SC3 compliant
- Provided with safety manual and user guide

Available on demand at STM32 series level[2]
www.st.com/x-cube-stl

(1) The original certificate and the updated list of certificated software versions can be downloaded from TÜV Rheinland websites: www.fsproducts.com, www.certipedia.com
(2) submitted to NDA

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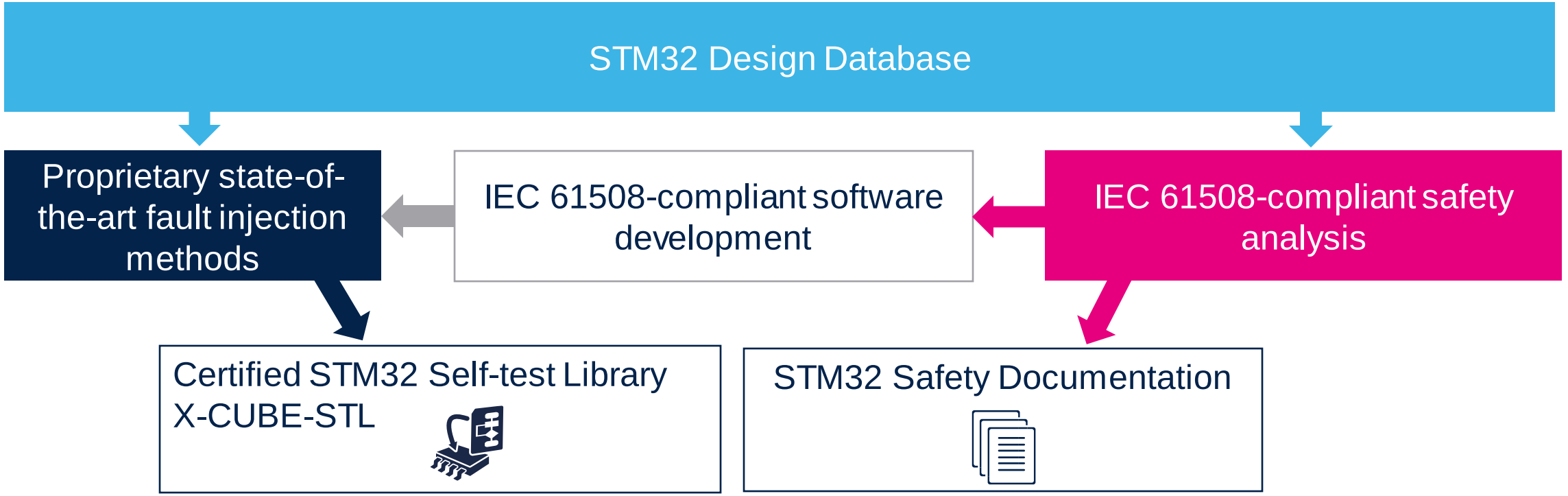
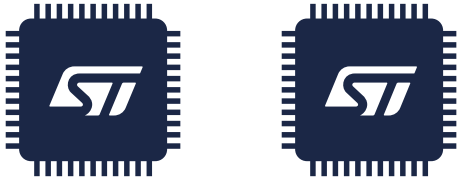
ST functional safety methodology

ST builds functional safety solutions for its STM32 Arm® Cortex®-M microcontroller family, including detailed and accurate safety analyses supported by verification activities based on state-of-the-art fault injection methods.



Achieve SIL2/SIL3 with STM32

SIL2	**Achievable with single STM32** (1oo1 architecture) 
SIL3	**Achievable with two STM32** (1oo2 architecture) 

1oo1: 1 out of 1 MCU (no redundancy)

1oo2 : 1 out of 2 MCUs (1 redundant system)

STM32 Safety Concepts

- **UM1741** STM32F0 Series safety manual
- **UM1814** STM32F1 Series safety manual
- **UM1845** STM32F2 Series safety manual
- **UM1846** STM32F3 Series safety manual
- **UM1840** STM32F4 Series safety manual
- **UM2318** STM32F7 Series safety manual
- **UM2455** STM32G0 Series safety manual
- **UM2454** STM32G4 Series safety manual
- **UM2840** STM32H7 dual-core safety manual
- **UM2331** STM32H7 single-core safety manual
- **UM2037** STM32L0 Series safety manual
- **UM1813** STM32L1 Series safety manual
- **UM2305** STM32L4 and STM32L4+ Series safety manual
- **UM2752** STM32L5 Series safety manual
- **UM2714** STM32MP1 Series safety manual
- **UM2875** STM32U5 Series safety manual
- **UM2814** STM32WL5x dual-core safety manual

STM32 MCU single Cortex-M core

Refer to STM32F0, F1, F2, F3, F4, F7, H7 single core, G0, G4, L0, L1, L4/L4+, L5, U5 safety manuals for details
TÜV Rheinland single core certificate

New

STM32 MCU dual Cortex-M core

Refer to STM32H7 dual-core and STM32WL5x dual-core safety manuals for details
TÜV Rheinland dual core certificate

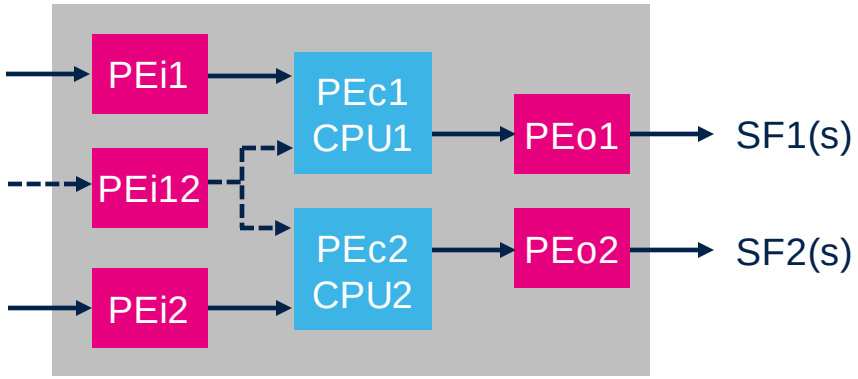
New

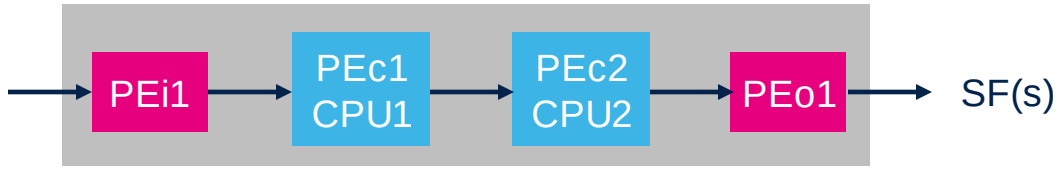
STM32MP1 MPU dual Cortex-A7 and Cortex-M4

Refer to STM32MP1 safety manual for details
TÜV Rheinland dual core certificate

STM32 MCU dual Cortex-M core Safety Concept

2 possible schemes for acquisition, execution and transfer of result





Individual scheme
Each CPU implement a specific safety function, no collaboration

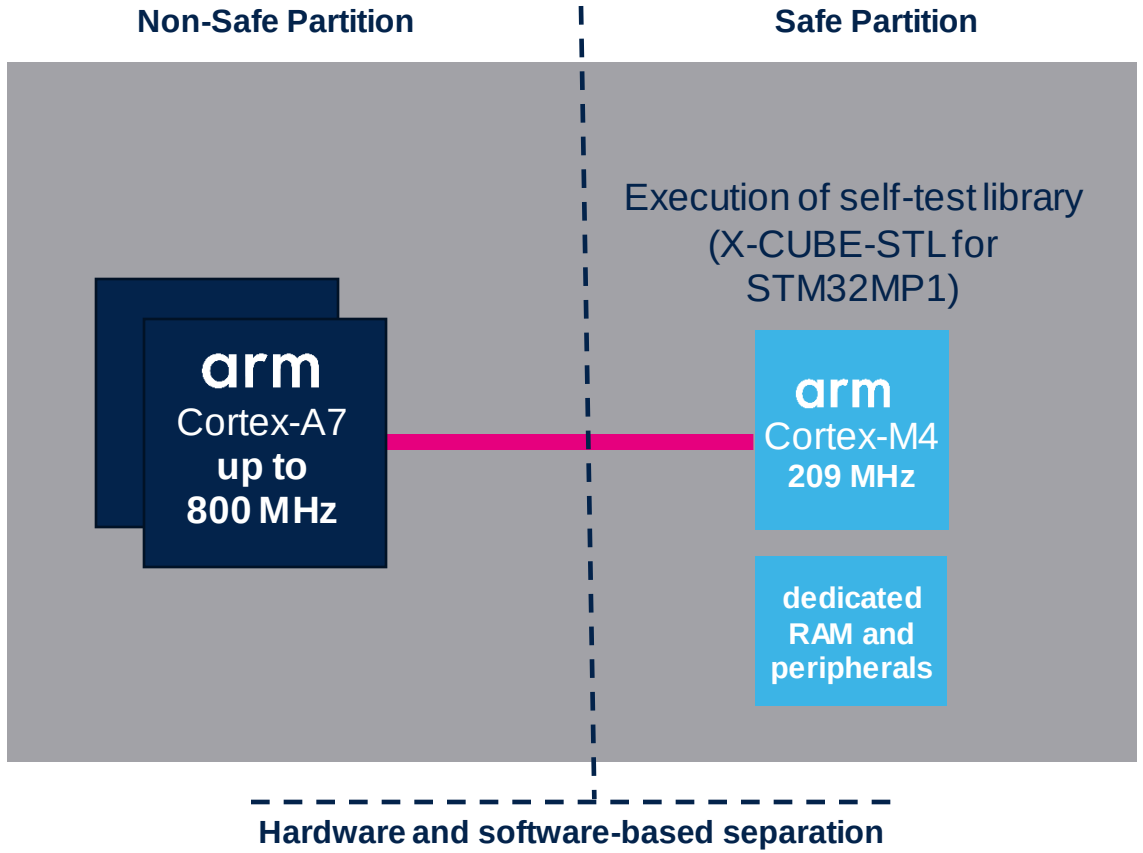
Collaborative scheme
The 2 CPUs collaborate for the implementation of the same safety function

PEi = input processing element
PEc = computation processing element
PEo = input processing element
SF(s) = on or multiple safety Functions

More details in UM2840 STM32H7 dual-core safety manual and UM2814 STM32WL5x dual-core safety manual

STM32MP1 MPU dual Cortex-A7 and Cortex-M4 Safety Concept

Safety function implementation confined in Cortex-M4 real-time side



The coexistence with non-safety related software on Cortex-A7 (e.g. Linux) is possible

More details in UM2714 STM32MP1 Series safety manual

ASIL Functional Safety Package

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STM8A-SafeASIL Functional Safety Package

Reduce time and cost to build STM8A-based systems certified to ISO 26262 automotive functional safety standard

ASIL A/B

Customer Development

+

Specification for Self-Test Library

Safety documentation

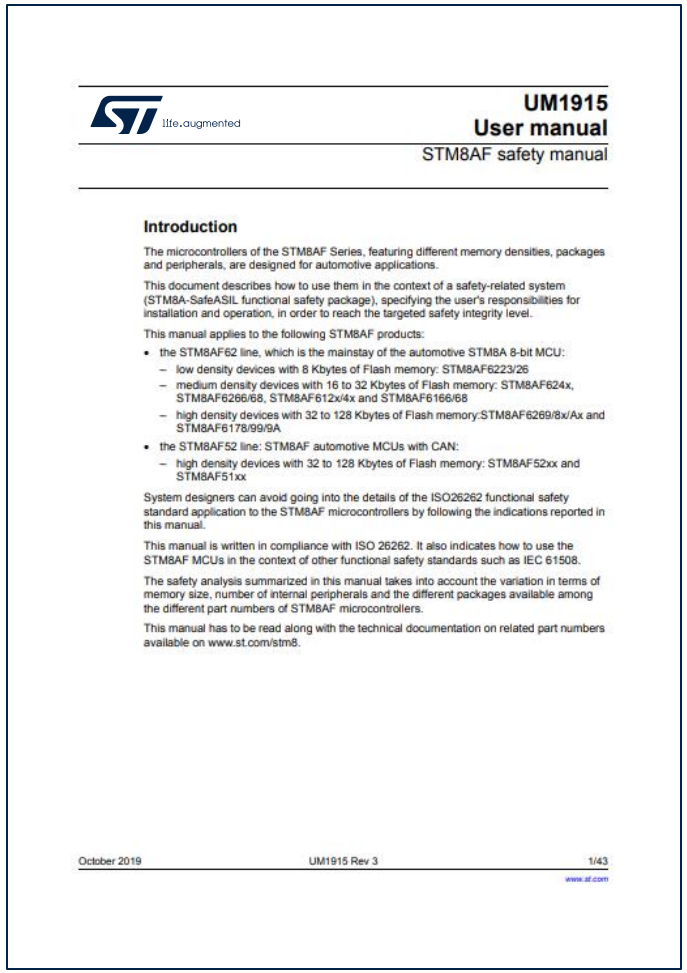
MCU Safety Features

Product Portfolio

STM8A

ST Quality foundations

STM8A-SafeASIL safety documentation



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UM1915
User manual
STM8AF safety manual

Introduction

The microcontrollers of the STM8AF Series, featuring different memory densities, packages and peripherals, are designed for automotive applications.

This document describes how to use them in the context of a safety-related system (STM8A-SafeASIL functional safety package), specifying the user's responsibilities for installation and operation, in order to reach the targeted safety integrity level.

This manual applies to the following STM8AF products:

- the STM8AF62 line, which is the mainstay of the automotive STM8A 8-bit MCU:
  - low density devices with 8 Kbytes of Flash memory: STM8AF6223/26
  - medium density devices with 16 to 32 Kbytes of Flash memory: STM8AF624x, STM8AF6266/68, STM8AF612x/4x and STM8AF6166/68
  - high density devices with 32 to 128 Kbytes of Flash memory:STM8AF6269/8x/Ax and STM8AF6178/99/9A
- the STM8AF52 line: STM8AF automotive MCUs with CAN:
  - high density devices with 32 to 128 Kbytes of Flash memory: STM8AF52xx and STM8AF51xx

System designers can avoid going into the details of the ISO26262 functional safety standard application to the STM8AF microcontrollers by following the indications reported in this manual.

This manual is written in compliance with ISO 26262. It also indicates how to use the STM8AF MCUs in the context of other functional safety standards such as IEC 61508.

The safety analysis summarized in this manual takes into account the variation in terms of memory size, number of internal peripherals and the different packages available among the different part numbers of STM8AF microcontrollers.

This manual has to be read along with the technical documentation on related part numbers available on www.st.com/stm8.

October 2019 UM1915 Rev 3 1/43

Safety manual: Detailed list of safety requirements and examples to support STM8AF and STM8AL use in applications that need to fulfill functional safety requirements as defined by automotive safety integrity level ASIL B of ISO 26262.

Available for STM8AF and STM8AL series for free download on www.st.com/stm8safety

FMEA: detailed list of MCU failure modes and related mitigation measures adopted
FMEDA: static snapshot reporting ISO 26262 failure rates, computed at both MCU / basic function detail levels.

Available on demand for STM8AF and STM8AL (*)
Ask your local ST contact.

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(*) submitted to NDA

STM8A-SafeASIL specification for self-test library

AN5482

full list of detailed safety requirements enabling STM8AF and STM8AL users to realize, in the framework of their ISO26262-compliant software development process, the software Self-test Library required by STM8AF or STM8AL Safety Manual to support application up to ASIL B.

The quality of the specification document allows its direct use in a development process compliant to ISO26262-6 requirements.

The specification includes the evidences and rationales behind the generation of the safety requirements for the completeness of end-user safety case.

Application independent: can be used in potentially any end-user application.

on demand for STM8AF and STM8AL series(*)
Ask your local ST contact

(*) submitted to NDA

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CLASS B Functional Safety Package

ClassB functional safety package for STM32 and STM8 MCUs

Reduce time and cost to build STM32 & STM8 based systems certified to IEC 60335-1 and 60730-1 household electrical appliance safety standards.

- **Certified** ST self-test libraries
- **Optimized** code based on STM32CubeHAL
- **Safety manuals** (guidelines and examples)
- For STM32: Support of IAR™ EWARM, Keil® MDK-ARM, and STM32CubeIDE
- **Worldwide standards coverage** (IEC, UL, and CSA)

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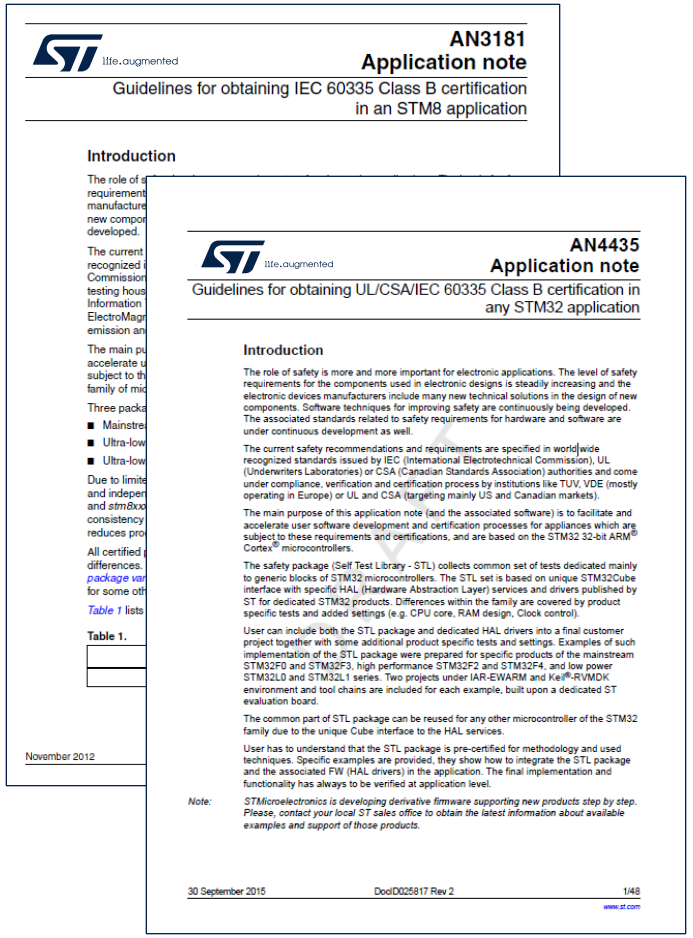
ClassB functional safety package for STM32 and STM8 MCUs

Package name	X-CUBE-CLASSB	STM8-SafeClassB
STM32 Series covered	**V2.2.0 -** STM32F0, F1, F3, F2, F4, F7, STM32L0, L1, L4 **V2.3.0** - STM32G0, G4, WB, H7 single core **V2.4.0** - STM32L5 **V3.0.0, 3.0.1** - STM32H7 dual core	STM8AF STM8AL STM8L STM8S
Self-test libraries based on	**STM32CubeHAL**	Optimized direct access to STM8 registers
Supported development environments	**IAR Embedded Workbench®, ARM KEIL®, STM32CubeIDE**	**IAR Embedded Workbench®, Cosmic®**
Certification	**UL@2016-2021**	**UL & VDE@2018**
IEC 60335-1 and 60730-1 international standards coverage	IEC, UL and CSA	
Safety manual (guidelines)	AN4435	AN3181

ClassB safety manuals



AN3181
Application note
Guidelines for obtaining IEC 60335 Class B certification in an STM8 application

Introduction

AN4435
Application note
Guidelines for obtaining UL/CSA/IEC 60335 Class B certification in any STM32 application

Introduction

The role of safety is more and more important for electronic applications. The level of safety requirements for the components used in electronic designs is steadily increasing and the electronic devices manufacturers include many new technical solutions in the design of new components. Software techniques for improving safety are continuously being developed. The associated standards related to safety requirements for hardware and software are under continuous development as well.

The current safety recommendations and requirements are specified in world|wide recognized standards issued by IEC (International Electrotechnical Commission), UL (Underwriters Laboratories) or CSA (Canadian Standards Association) authorities and come under compliance, verification and certification process by institutions like TUV, VDE (mostly operating in Europe) or UL and CSA (targeting mainly US and Canadian markets).

The main purpose of this application note (and the associated software) is to facilitate and accelerate user software development and certification processes for appliances which are subject to these requirements and certifications, and are based on the STM32 32-bit ARM® Cortex® microcontrollers.

The safety package (Self Test Library - STL) collects common set of tests dedicated mainly to generic blocks of STM32 microcontrollers. The STL set is based on unique STM32Cube interface with specific HAL (Hardware Abstraction Layer) services and drivers published by ST for dedicated STM32 products. Differences within the family are covered by product specific tests and added settings (e.g. CPU core, RAM design, Clock control).

User can include both the STL package and dedicated HAL drivers into a final customer project together with some additional product specific tests and settings. Examples of such implementation of the STL package were prepared for specific products of the mainstream STM32F0 and STM32F3, high performance STM32F2 and STM32F4, and low power STM32L0 and STM32L1 series. Two projects under IAR-EWARM and Keil®-RVMDK environment and tool chains are included for each example, built upon a dedicated ST evaluation board.

The common part of STL package can be reused for any other microcontroller of the STM32 family due to the unique Cube interface to the HAL services.

User has to understand that the STL package is pre-certified for methodology and used techniques. Specific examples are provided, they show how to integrate the STL package and the associated FW (HAL drivers) in the application. The final implementation and functionality has always to be verified at application level.

Note: STMicroelectronics is developing derivative firmware supporting new products step by step. Please, contact your local ST sales office to obtain the latest information about available examples and support of those products.

30 September 2015 DocID025817 Rev 2 1/48

Guidelines and examples
for STM32 and STM8 users
to achieve Class B certification
in compliance with IEC 60335-1 and 60730-1.

Functional Safety Packages summary

Functional Safety Packages for STM32 & STM8 MCUs

	SIL Ready	ASIL Ready	ClassB Ready	
MCU support	STM32	STM8A	STM32	STM8
Achievable safety standards	IEC 61508	ISO 26262	IEC, UL, CSA 60335-1 60730-1	
Certification	TÜVRheinland CERTIFIED		UL CERTIFIED	DVE, UL CERTIFIED
Package content	• Safety Documentation • Self-Test Libraries	• Safety Documentation • Self-Test Library specification	• Safety Documentation • Self-Test Libraries	• Safety Documentation • Self-Test Libraries
Package name	**X-CUBE-STL**	**STM8A-SafeASIL**	**X-CUBE-CLASSB**	**STM8-SafeCLASSB**

Functional Safety Ecosystem

Get support from ST authorized partners

Reduce your project time and cost

Safety Requirements → HW & SW Design → Validation → Certification

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Partner Program

Functional Safety expertise

Functional safety authorized partners

Embedded Software

- arm KEIL
- Embedded Office
- Microsoft
- SCIOPTA
- SEGGER It simply works!
- WITTENSTEIN

Software Development Tools

- arm KEIL
- IAR SYSTEMS

Engineering, consulting, development or design services

- Embedded Office
- embeX Your embedded experts
- hitex EMBEDDED TOOLS & SOLUTIONS
- innotec
- MESCO
- NewTec

Training

- innotec
- MESCO
- NewTec

Arm Compiler for Functional Safety

Qualified toolchain for safety development

Safety Standards:

- ✓ IEC 61508 (Industrial) – SIL 3
- ✓ ISO 26262 (Automotive) – ASIL D
- ✓ EN 50128 (Railways) – SIL 4
- ✓ IEC 62304 (Medical) – CLASS C

*At any Safety Integrity Level

Safety-Ready

Safety Qualified Toolchain

Simplifies Tool Justification

- ❖ TUV Certificate by TUV SUD
- ❖ Qualification Kit
  - ❖ Safety Manual
  - ❖ Defect Report

Comprehensive safety documentatio

Arm Compiler For Functional Safety

Licensed as 'Standalone' or via Arm IDE Toolkits:

- ❑ Arm Development Studio
  - ❑ Gold/Platinum Edition
- ❑ Keil MDK-Professional

arm DEVELOPMENT STUDIO

arm KEIL

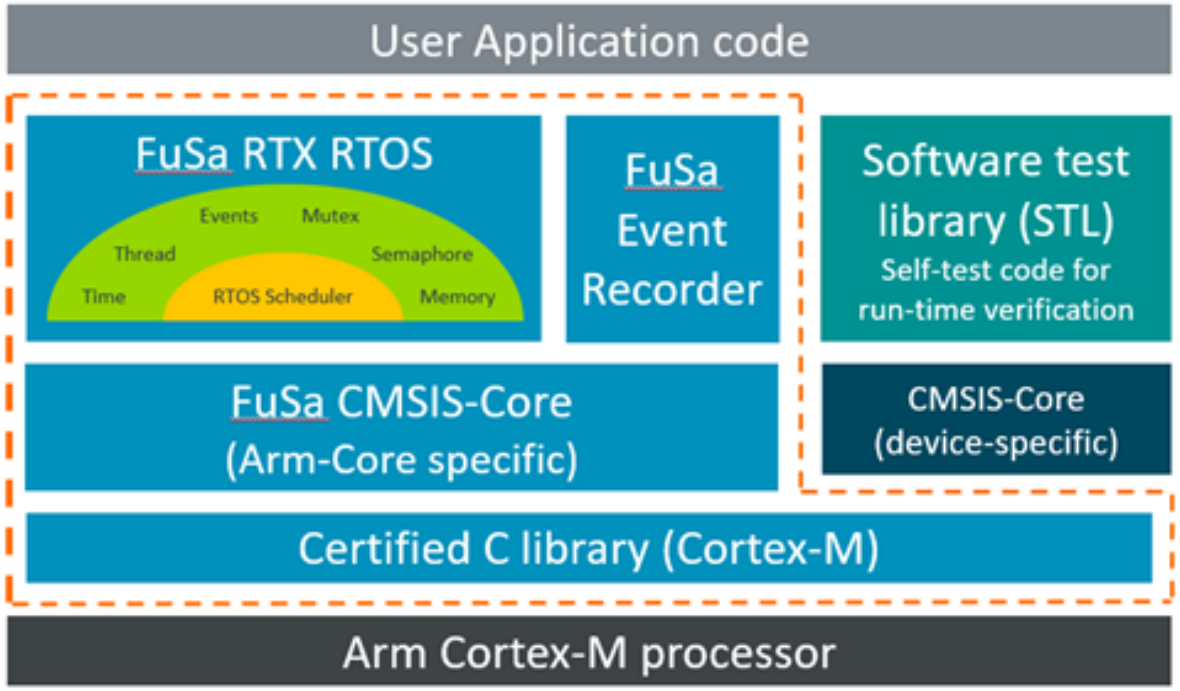
Baseline toolchain for Arm Safety Software development:

- ➢ Certified C Library
- ➢ Arm FuSa Run-Time System
- ➢ Arm Software-Test Libraries

Certified software components

Arm FuSa RTS: Run-Time System for Functional Safety

Software components certified for safety-critical applications



Covered safety standards:

- Automotive: ISO 26262, ASIL D
- Industrial: IEC 61508, SIL 3
- Railways: EN 50128, SIL 4
- Medical: IEC 62304, Class C

Supported processors:

- Cortex-M0/M0+
- Cortex-M3
- Cortex-M4
- Cortex-M7

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Embedded Office

5 Steps to Your Safety Platform

Long-term Maintenance

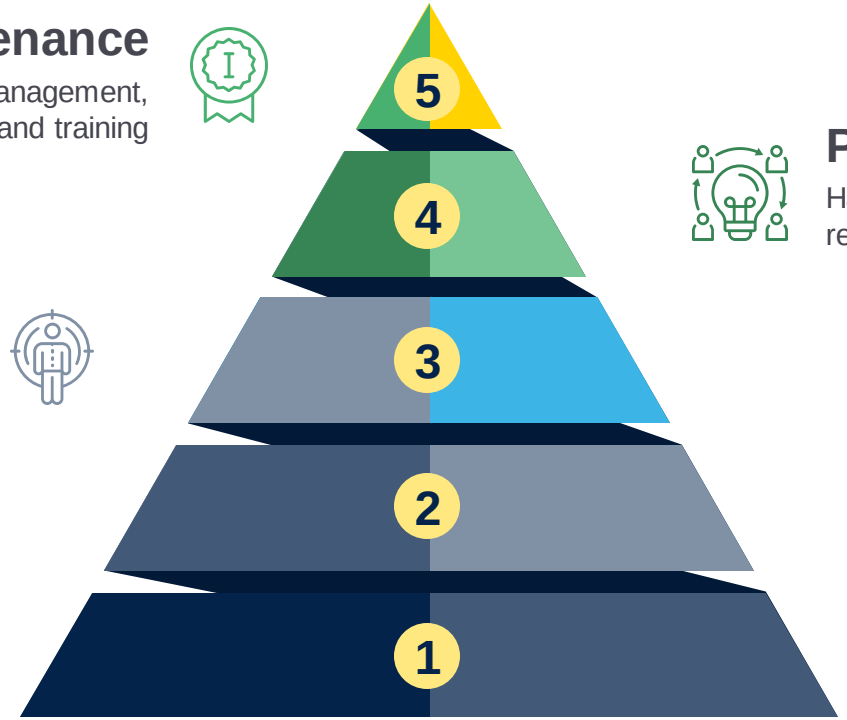
Active functional safety management, workshops and training

Pre-Certification

Harmonize safety manuals, certify remaining parts, assessment with authority

Setup Safety Platform

Integrate software components and realize missing parts



Select Software

ST Microcontroller & Embedded Office products or whatever the system needs

Safety Concept

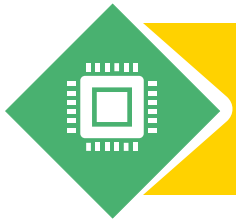
Analyze system needs and provide a safety concept

Embedded Office

5 Steps to Your Safety Platform

Safety & Cyber Security Engineers

TÜV Rheinland certified engineers

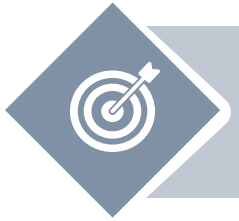


300+ Successful Customer Projects

Aerospace, Industrial, Automotive, Rail, Medical

70+ Satisfied Customers Worldwide

Products, Development Services, Mentoring



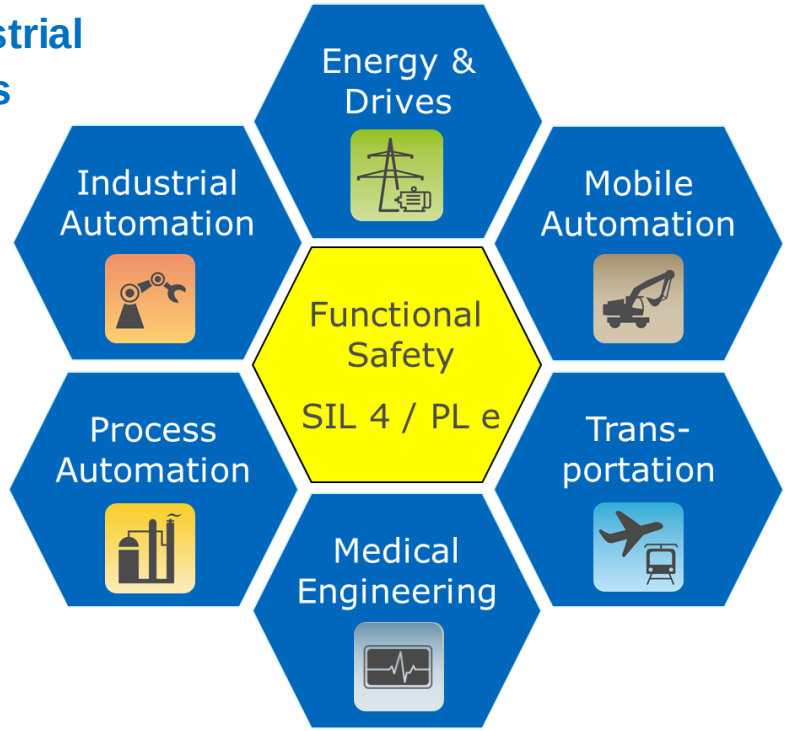
Certified Software Components

Safety RTOS, Safety AddOns, HW Selftests

Development of Turn-Key Certified Products

- System Engineering
- Software
- Hardware
- Mechanics
- Certification
- Production
- Prod. Life Cycle Management

Main Industrial Sectors



More than 150 Experts - 20 years of experience

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Recognized company in functional safety worldwide

- TÜV Rheinland awarded the first Functional Safety Management (FSM) certificate with the **highest maturity level** (5) to embeX

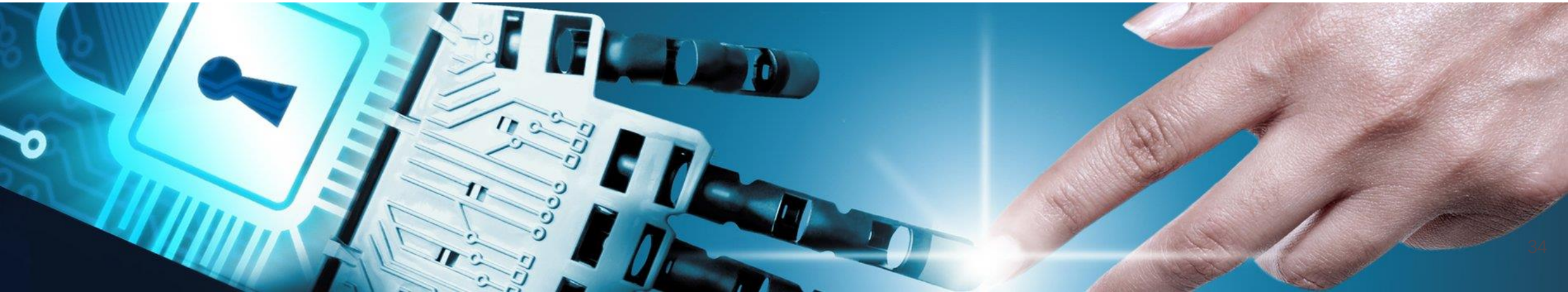
- Offering
  - **Development of certified turnkey safety products and subsystems**
  - **Transfer** of development processes and know-how to customers
  - **Consulting**

Cyber security is an essential prerequisite for safety

Thus, embeX offers:

- Risk Analysis
- Consultancy
- Developments achieving SIL 3 (IEC 61508) and SL 4 (IEC 62443)
- Verification including pen tests and fuzzing

Further information:
https://www.embex-engineering.com/en/competencies-technologies/safety-security/



Consulting & Engineering

Expertise out of our Customer projects

DC/DC converters

Safety integration & certification

Implementing security requirements

Emulator for special micro controllers

IoT implementation and integration

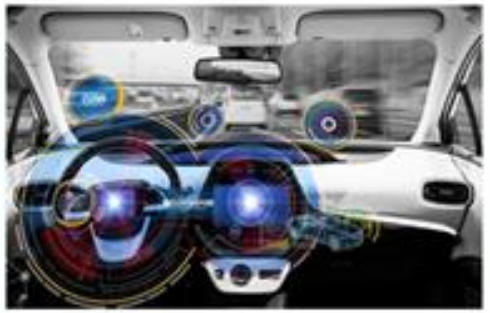
eDrive development

Functional Safety process consulting

Battery management

ECUs for powertrain & combustion engine






IAR Embedded Workbench for safety-critical applications

World leading embedded development tools

- ✓ More than 30 years of experience as a compiler vendor
- ✓ More than 1 million embedded devices built with our tools
- ✓ More than 150,000 users worldwide

The build chains are certified by TÜV SÜD as compliant with the international umbrella standards and the certification **validates the quality** of IAR Systems' entire development processes, as well as the delivered software.

Certified toolchain

- A special functional safety edition of IAR Embedded Workbench

Simplified validation

- Functional Safety certificate from TÜV SÜD
- Safety report from TÜV SÜD
- Safety guide

Guaranteed support through the product life cycle

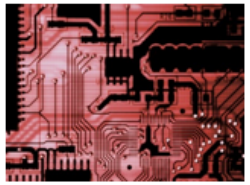
- Prioritized support
- Validated service packs
- Regular reports of known problems

Validated according to:
IEC 61508
ISO 26262
EN 50128, EN 50657
IEC 62304

Arm Functional Safety Partner
Software & Tools

Available for Arm and STM8

Our obsession is SafeWare Engineering!



- Hard and Software (IEC61508)
- Machinery (ISO13849, IEC62061)
- Factory automation (IEC61131-6, IEC61800-5-2)
- Railway Technology (IEC 50126, IEC 50128, IEC 50129)
- Process industry (IEC 61511)
- Nuclear, Wind and Solar Energy
- Automotive Systems (ISO26262)
- Farming Machines (EN16590, ISO25119)

- Consulting
- Training
- Development Support
- Project Implementation
- Standardization, Approval and Certification
- Safety Management
- Specifications and Mathematical Methods

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WWW.INNOTECSAFETY.COM

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GERMANY

+49 (5422) 9811-350

Our range of services: Factory & Process Automation

Tailor-made Development Solutions

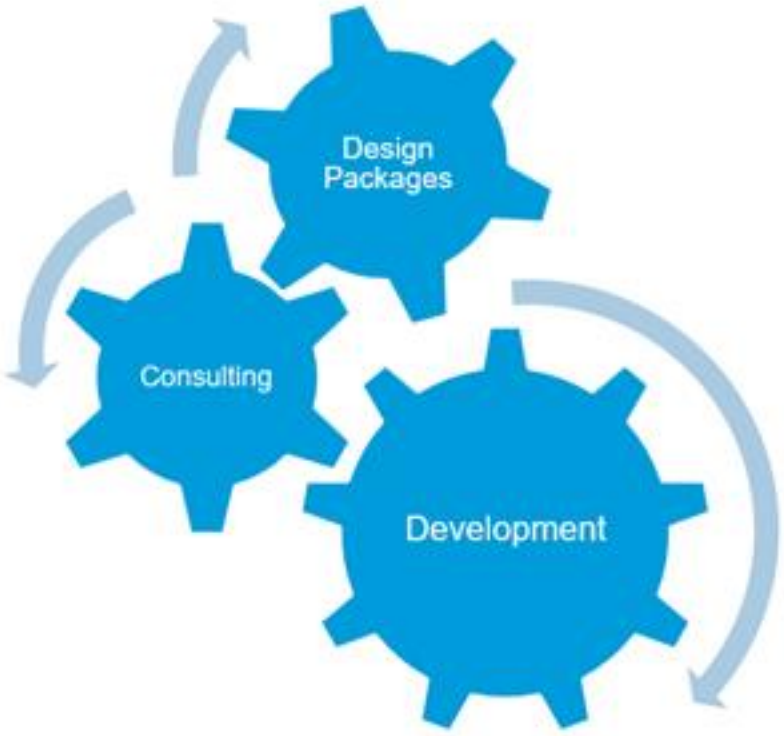
Customized hardware and software development with flexible use of design packages.

Directly applicable DESIGN PACKAGES

Proven circuits and software components for rapid implementation of your development project.

Development Consulting

Development accompanying consulting and coaching in the areas of functional safety, explosion-proof and industrial communication.



Our offering: Your success is our driving force

Consulting

- Technology Consulting
- Functional Safety Management
- Explosion-proof trainings
- Industrial Communication
- Support in the creation of Requirements

Concept – Architecture

- Creation of the Functional Safety Concept
- Creation of the Explosion-proof Concept
- System Architecture
- Quality Assurance Measures

Development – Design / Implementation / Prototyping

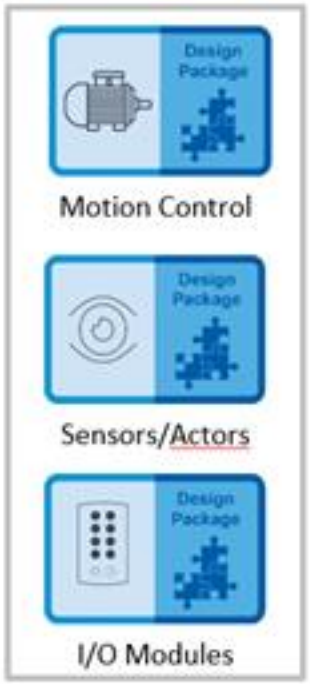
- Hardware Development
- Software Development
- Safety Development
- PCB Layout
- Prototyping
- Type Testing
- Integration Test
- Use of existing Safety Design Packages
- Support of product launching into production

Certification

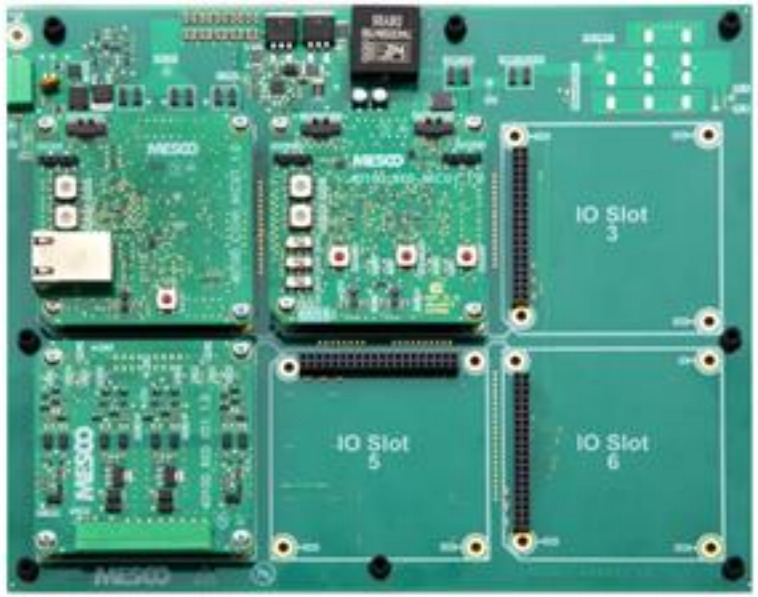
- Comprehensive Support of the Certification

MESCO Safety Design Packages

Build-up with a base board & expansion boards



Design Packages based on **ST solutions**



Built up with a main board & expansion boards as a reference design, our Design Packages simplify and accelerate the development in both safety- and non-safety-related environments.

Expansion boards

Azure RTOS Functional Safety

Azure RTOS ThreadX & ThreadX SMP

A high-performance real-time operating system

Azure RTOS NetX and NetX Duo

A TCP/IP IPv4/IPv6 embedded network stack that includes cloud connectivity and IPsec and TLS/DTLS security protocols

Azure RTOS FileX

An embedded FAT file system that offers optional fault tolerant features

Azure RTOS GUIX Studio and GUIX

A complete design environment and run-time to create and maintain 2D graphical user interfaces

Azure RTOS USBX

A USB stack that provides host, device, and on-the-go support

Azure RTOS Functional Safety

- ThreadX, FileX, GUIX, NetX Duo, USBX pre-certified by TUV to IEC 61508 SIL 4, IEC 62304 Class C, ISO 26262 ASIL D, EN 50128 SW-SIL 4, UL 1998, UL/IEC 60730, UL/IEC 60335
- Azure RTOS pre-certification covers generic C code
- Same source code whether or not certification is needed
- Pre-certification artifacts are licensed separately

NTSafetySolutions

Training & Consulting

- Varied range of seminars for functional safety in practice
- Safety workshops for individual customers

Products, e.g.

- SafeFlex – Reference platform for safety development
- NTSafeDriveMonitor – Safety module for monitoring of drives
- NTBMS – Safety reference platform for Battery Management Systems

Expert services to do with all aspects of product development

- Safety management assessment
- Safety risk assessment
- Safety requirement analysis
- Licensing strategy
- Safety planning
- Safety concept
- Concept examination
- Functional safety management

Managed Services in Product Lifecycle

- Safety system development
- Safety engineering
- Safety software development
- Safety hardware development
- Integration, verification & validation
- Documentation & traceability

NTSafeFlex STM32

Reduce cost and time-to-market of your safety application development with NTSafeFlex STM32 evaluation board and Safety Software Library

- The board is based on two STM32G070 with additional Software Library for functional safety solutions up to SIL 3 and PLe, Cat4.
- Typical applications: safety control logic, motor supervision, general safety applications with low performance standards, etc.

SCIOPTA RTOS

SAFE	SCIOPTA RTOS is designed with safety in mind.
CERTIFIED	SCIOPTA RTOS is certified according following standards: IEC61508 (SIL 3), EN50128/129 (SIL 3/4) and ISO26262 (ASIL D).
MIGRATION NON SAFE – SAFE	SCIOPTA RTOS' certified API does not differ from the non-certified version. All system call are certified.
FAST	SCIOPTA RTOS is tailored to the specific CPU exploiting all its features to provide short latencies, small overhead and determnistic execution.
SMALL	SCIOPTA RTOS is designed to be compact and still offering a wide range of system calls to enable almost any kind of application
DYNAMIC	SCIOPTA RTOS can be used in a complete dynamic manner so that the application can react on upcomming needs.
SCHEDULING	SCIOPTA RTOS uses pre-emptive scheduling based on priorities and round-robin scheduling with optional time slice.
EASY TO USE	SCIOPTA RTOS hides many of the burden other RTOSs put on the developer. A set of six system calls is sufficient for 80% of an application
FUTURE PROOF	SCIOPTA RTOS's asynchronous direct message passing fits perfect future challenges like many-core SoCs or distributed systems.
USE CASES	SCIOPTA RTOS is successfully used in different areas like Automotive, Defense, Rail Way, Medical, Industrial Automation and Consumer Electronics.

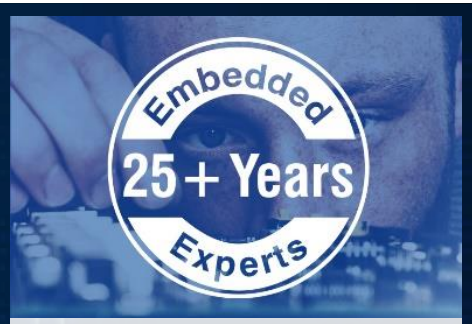
SEGGER Microcontroller

embOS-Safe

- Medical
- Industrial
- Home Appliances
- Transportation
- Automotive
- and more ..

Deployed and proven in several billion devices

embOS is deployed in several billion devices and is a proven choice for embedded products.
It has been deployed in all kinds of applications, such as home appliances, IoT, transportation, industrial, medical or automotive.



More than 27 years of continuous development

SEGGER started to offer embOS in the early 90s as a product and has continued to develop the RTOS and add device support until today, It has become the core for SEGGER's own products as well as a multitude of customer products.

Easy transition from standard to certified

While any application benefits from a reliable operating environment, in some cases, prove in form of certification is required.
In markets where certification might become a requirement, embOS is the ideal choice, as it uses the same code base as embOS-Safe making a later conversion as easy as possible.

embOS features

- Guarantees 100% deterministic real-time operation
- Highest performance with lowest use of memory
- Powerful and easy to use API
- Kernel awareness plugins available
- Zero interrupt latency
- Cycle Precise System Time
- MadeForSTM32

SEGGER Microcontroller

embOS-Safe

embOS is labelled MadeForSTM32

Safety with Certificate

TÜV Süd has verified the embOS development process and confirms, that embOS-Safe is ideally suited as fundamental component for safety products. embOS-Safe is certified for functional safety according to IEC 61508 SIL 3 and IEC 62304 Class C.

Consistent interface

The Application Programming Interface (API) is unchanged in relation to embOS. Therefore existing software parts can be (re-)used easily. This helps to use embOS-Safe in existing applications.

Certification Kit

The embOS-Safe certification kit includes all necessary documents, including the comprehensive embOS Safety Manual.

One-Stop-Solution

The certified RTOS embOS-Safe is also available for SEGGER's IDE Embedded Studio, offering a one-stop-solution. Naturally, embOS-Safe is fully suited for usage with SEGGER's extensive portfolio of outstanding middleware, debug probes and production tools, too.

WITTENSTEIN high integrity systems

SAFERTOS®: Safety Critical RTOS

100% success rate certifying with TÜV SÜD across Industry sectors:

Industrial	**IEC 61508**
Automotive	**ISO 26262**
Medical	**IEC 62304/FDA 510K**
Railway	**EN 50128**

SAFE**RTOS**® is a pre-certified safety Real Time Operating System (RTOS) for embedded processors. It delivers superior performance and dependability, whilst utilizing minimal resources.

SAFE**RTOS** is a safety critical upgrade to FreeRTOS:

- Based on the FreeRTOS functional model
- Rebuilt to comply with **SIL 3 requirements**
- No open source code

SAFE**RTOS can be found in:**

- Dialysis machines
- Prostheses
- Control systems found on trains
- Safety critical servo controllers
- Industrial control systems and many more

life.augmented

WITTENSTEIN high integrity systems

SAFERTOS Support for ST

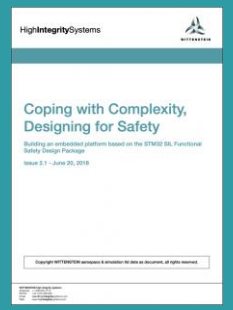
SAFERTOS Supported Platforms	
STM32F3, STM32F4, STM32L4	ARM Cortex-M4
STM32F2, STM32F1, STM32L1, STM32W	ARM Cortex-M3
STM32F0	ARM Cortex-M0
STM32F7, H7	ARM Cortex-M7
STM32H7 Dual Core	ARM Cortex-M7 & ARM Cortex-M4

SAFE**RTOS** supports:

- X-CUBE-STL;
- STM32Cube embedded software;
- STM32 SIL Functional Safety Package;
- Secure boot.

SAFE**RTOS** Demos for ST are available:

- 30-days evaluation packages with full source code on request. Download Demos here.



Free White Paper:
Based on the X-CUBE-STL Functional Safety Package.
Free to Download

WITTENSTEIN high integrity systems

WITTENSTEIN high integrity systems standard offer

WITTENSTEIN high integrity systems (WHIS) are **safety RTOS specialists,** part of The WITTENSTEIN Group. WHIS specialize **high integrity and safety critical** embedded systems design.

SAFE**RTOS**® Source Code

Design Assurance Pack

Middleware

Safety Components

Tools

Training & Support

- Royalty Free, Perpetual Licensing
- 12 Months Free Support & Maintenance
- Smooth path to certification

WHIS also offer Board Support Packages, Training Courses and more…

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Our technology starts with You

Find out more at www.st.com/functionalsafety

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