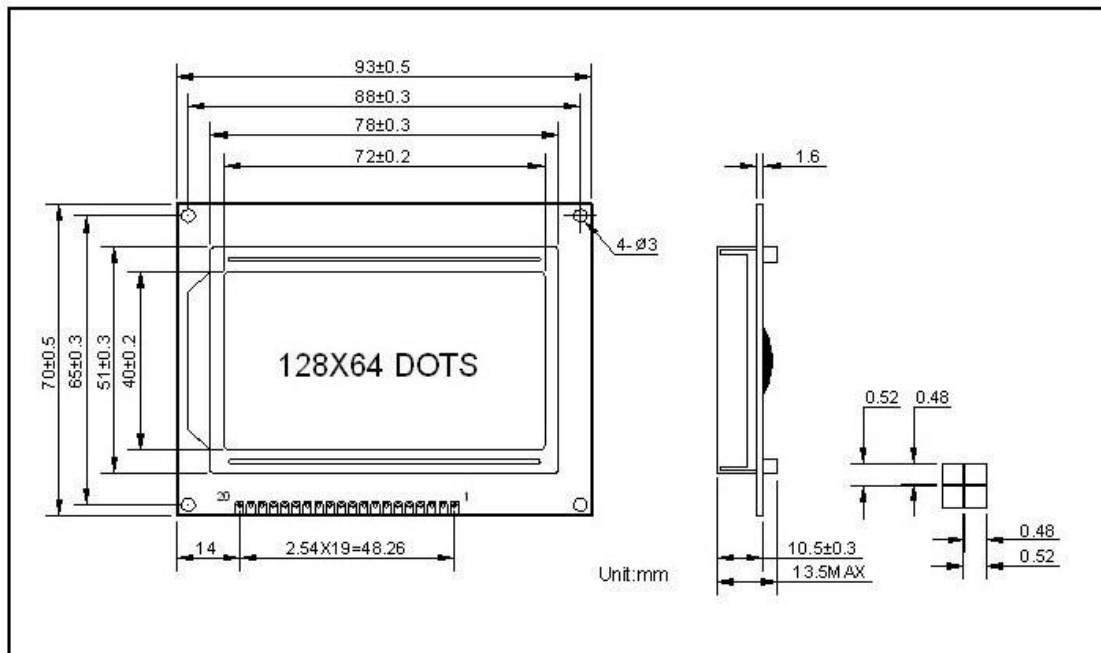


Porta Lógica Ltda



| Items | size | unit |
|----------------|-------------|------|
| Module volume | 93 X 70 X13 | mm |
| View area | 72 X40 | mm |
| Point distance | 0.52 X 0.52 | mm |
| Point size | 0.48 X0.48 | mm |

Parallel mode interface definition:

| Pin number | definition | level | Function specification |
|------------|------------|-------|--|
| 1 | VSS | 0V | Power supply negative |
| 2 | VDD | +5V | Power supply positive |
| 3 | V0 | - | Contrast control |
| 4 | RS | H/L | RS=H:shows data RS=L:controls instruction |
| 5 | R/W | H/L | R/W+H,E=H,data was read DB7-DB0 R/w=L,E=H=L:the data of DB7-DB0 was written to IR or DR |
| 6 | E | H/L | Enable pin |
| 7-14 | DB0-DB7 | H/L | Data line |
| 15 | PSB | H/L | PSB=H,parallel mode,PSB=L,serial mode |
| 16 | NC | - | Empty pin |
| 17 | /RESET | H/L | Reset terminal,low level valid |
| 18 | Voult | - | Module drives voltage output terminal |
| 19 | A | +5V | Backlight power supply positive |
| 20 | K | 0V | Backlight power supply negative |

Serial module interface definition:

| Pin number | definition | level | Function specification |
|------------|------------|-------|--|
| 1 | VSS | 0V | Power supply negative |
| 2 | VDD | +5V | Power supply positive |
| 3 | V0 | - | Contrast control |
| 4 | CS | H/L | Chip selection terminal,high level valid |
| 5 | SID | H/L | Serial data input terminal |
| 6 | CLK | H/L | Serial synchronous clock |
| 15 | PSB | H/L | PSB=H,parallel mode,PSB=L,serial mode |
| 17 | /RESET | H/L | Reset terminal,low level valid |
| 19 | A | +5V | Backlight power supply positive |
| 20 | K | 0V | Backlight power supply negative |