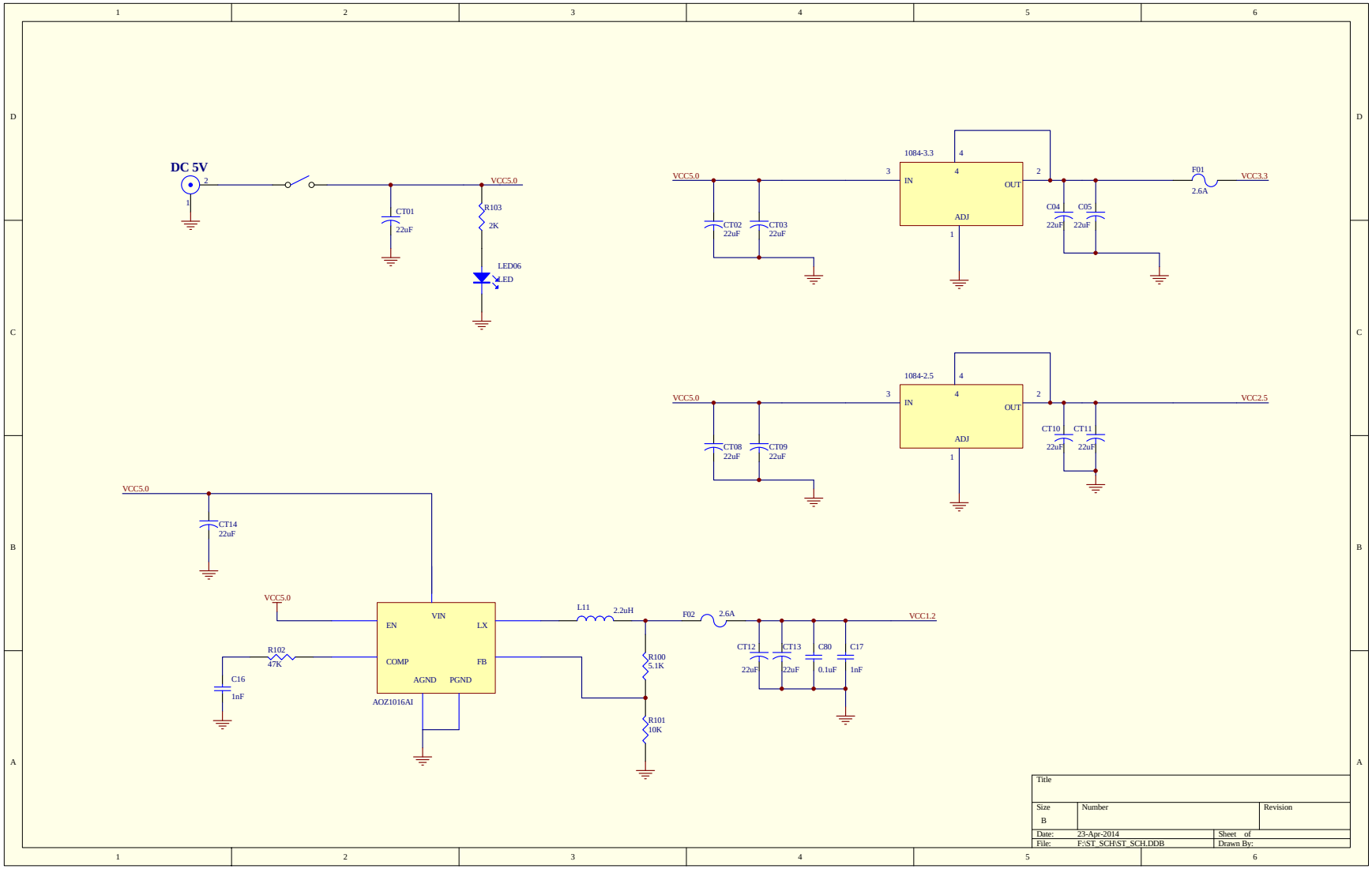
DC 5V
2
1
VCC5.0
CT01
22uF
R103
2K
LED06
LED
1084-3.3
VCC5.0
IN
OUT
ADJ
CT02
22uF
CT03
22uF
C04
22uF
C05
22uF
F01
2.6A
VCC3.3
1084-2.5
VCC5.0
IN
OUT
ADJ
CT08
22uF
CT09
22uF
CT10
22uF
CT11
22uF
VCC2.5
VCC5.0
CT14
22uF
VCC5.0
VIN
EN
LX
COMP
FB
AGND
PGND
AOZ1016AI
R102
47K
C16
1nF
L11
2.2uH
F02
2.6A
VCC1.2
R100
5.1K
R101
10K
CT12
22uF
CT13
22uF
C80
0.1uF
C17
1nF
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Size
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Date:
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F:\ST_SCH\ST_SCH.DDB
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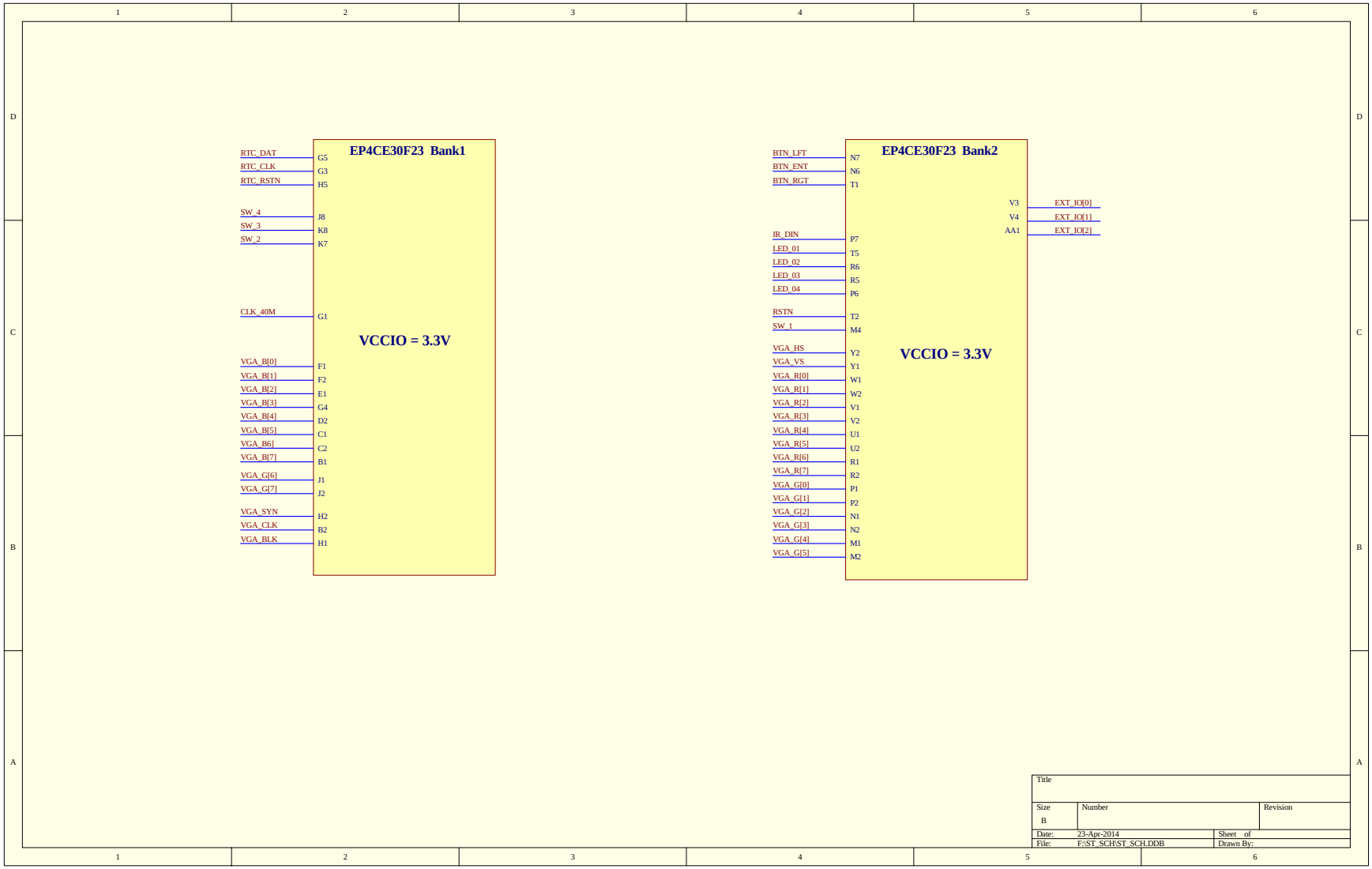
EP4CE30F23 Bank1
VCCIO = 3.3V
RTC_DAT
RTC_CLK
RTC_RSTN
SW_4
SW_3
SW_2
CLK_40M
VGA_B[0]
VGA_B[1]
VGA_B[2]
VGA_B[3]
VGA_B[4]
VGA_B[5]
VGA_B6]
VGA_B[7]
VGA_G[6]
VGA_G[7]
VGA_SYN
VGA_CLK
VGA_BLK
G5
G3
H5
J8
K8
K7
G1
F1
F2
E1
G4
D2
C1
C2
B1
J1
J2
H2
B2
H1
EP4CE30F23 Bank2
VCCIO = 3.3V
BTN_LFT
BTN_ENT
BTN_RGT
IR_DIN
LED_01
LED_02
LED_03
LED_04
RSTN
SW_1
VGA_HS
VGA_VS
VGA_R[0]
VGA_R[1]
VGA_R[2]
VGA_R[3]
VGA_R[4]
VGA_R[5]
VGA_R[6]
VGA_R[7]
VGA_G[0]
VGA_G[1]
VGA_G[2]
VGA_G[3]
VGA_G[4]
VGA_G[5]
N7
N6
T1
P7
T5
R6
R5
P6
T2
M4
Y2
Y1
W1
W2
V1
V2
U1
U2
R1
R2
P1
P2
N1
N2
M1
M2
V3
V4
AA1
EXT_IO[0]
EXT_IO[1]
EXT_IO[2]
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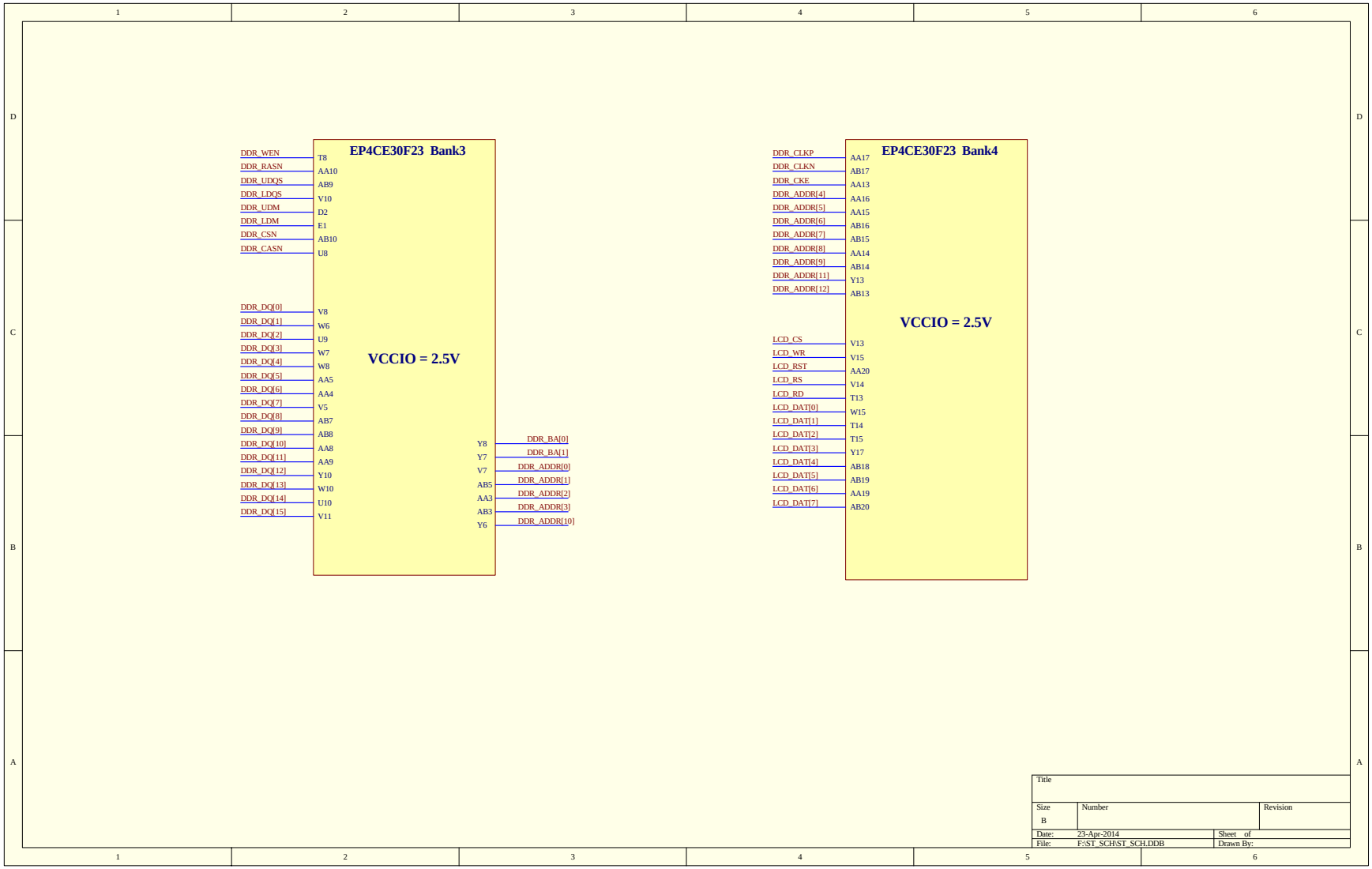

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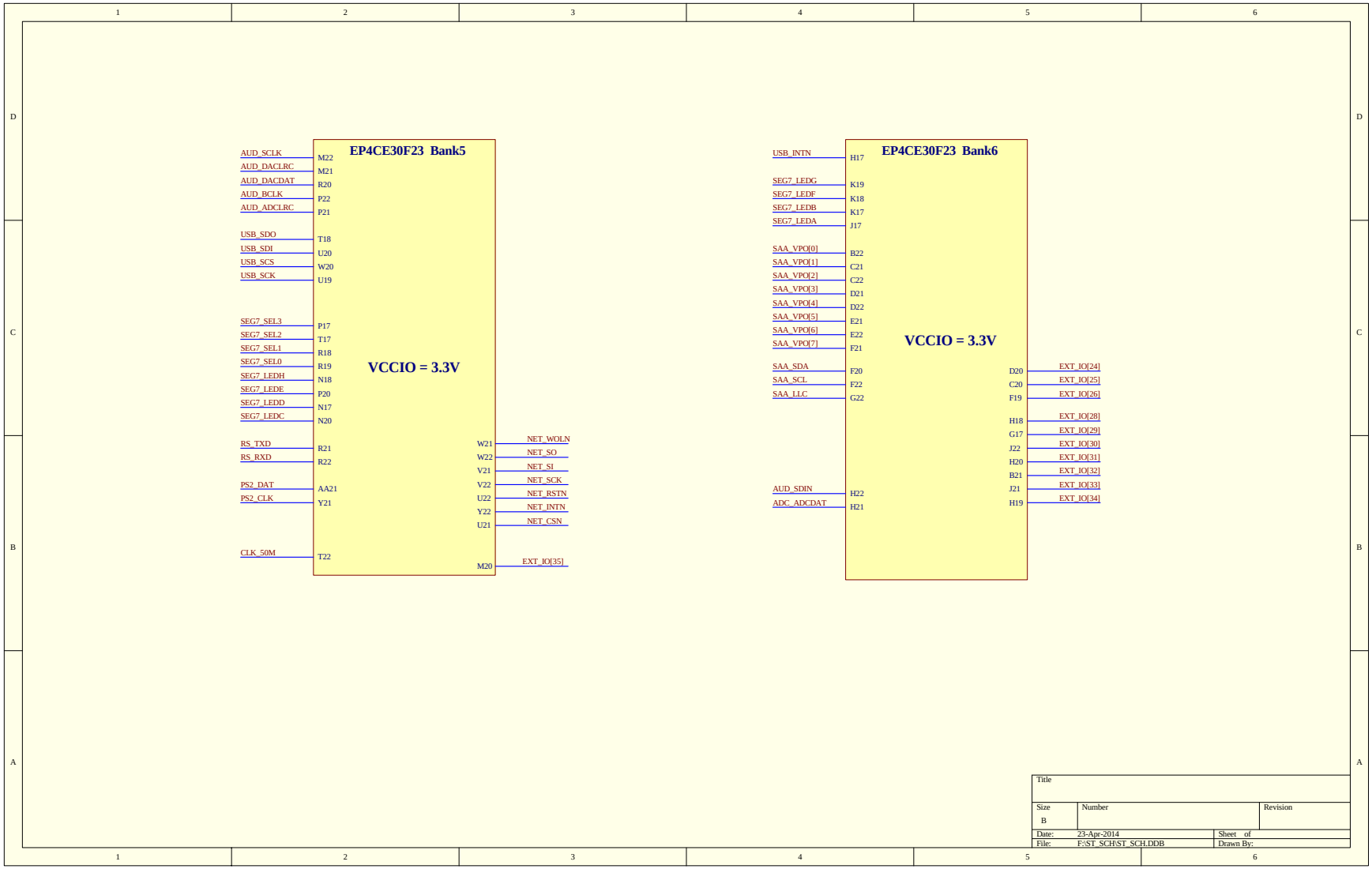
EP4CE30F23 Bank5
VCCIO = 3.3V
AUD_SCLK
AUD_DACLRC
AUD_DACDAT
AUD_BCLK
AUD_ADCLRC
USB_SDO
USB_SDI
USB_SCS
USB_SCK
SEG7_SEL3
SEG7_SEL2
SEG7_SEL1
SEG7_SEL0
SEG7_LEDH
SEG7_LEDE
SEG7_LEDD
SEG7_LEDC
RS_TXD
RS_RXD
PS2_DAT
PS2_CLK
CLK_50M
M22
M21
R20
P22
P21
T18
U20
W20
U19
P17
T17
R18
R19
N18
P20
N17
N20
R21
R22
AA21
Y21
T22
W21
W22
V21
V22
U22
Y22
U21
M20
NET_WOLN
NET_SO
NET_SI
NET_SCK
NET_RSTN
NET_INTN
NET_CSN
EXT_IO[35]
EP4CE30F23 Bank6
VCCIO = 3.3V
USB_INTN
SEG7_LEDG
SEG7_LEDF
SEG7_LEDB
SEG7_LEDA
SAA_VPO[0]
SAA_VPO[1]
SAA_VPO[2]
SAA_VPO[3]
SAA_VPO[4]
SAA_VPO[5]
SAA_VPO[6]
SAA_VPO[7]
SAA_SDA
SAA_SCL
SAA_LLC
AUD_SDIN
ADC_ADCDAT
H17
K19
K18
K17
J17
B22
C21
C22
D21
D22
E21
E22
F21
F20
F22
G22
H22
H21
D20
C20
F19
H18
G17
J22
H20
B21
J21
H19
EXT_IO[24]
EXT_IO[25]
EXT_IO[26]
EXT_IO[28]
EXT_IO[29]
EXT_IO[30]
EXT_IO[31]
EXT_IO[32]
EXT_IO[33]
EXT_IO[34]
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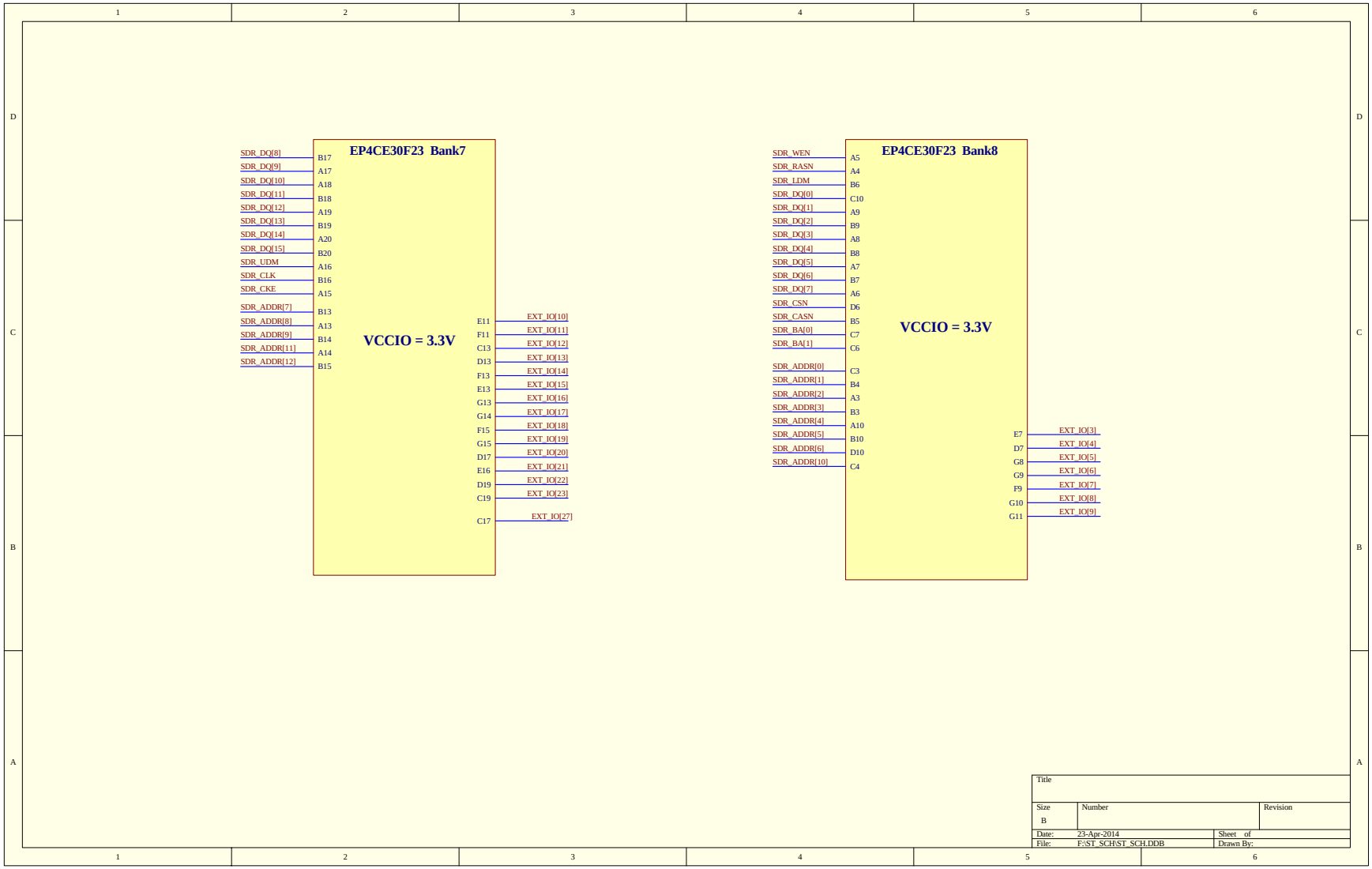
EP4CE30F23 Bank7
VCCIO = 3.3V
SDR_DQ[8] B17
SDR_DQ[9] A17
SDR_DQ[10] A18
SDR_DQ[11] B18
SDR_DQ[12] A19
SDR_DQ[13] B19
SDR_DQ[14] A20
SDR_DQ[15] B20
SDR_UDM A16
SDR_CLK B16
SDR_CKE A15
SDR_ADDR[7] B13
SDR_ADDR[8] A13
SDR_ADDR[9] B14
SDR_ADDR[11] A14
SDR_ADDR[12] B15
E11 EXT_IO[10]
F11 EXT_IO[11]
C13 EXT_IO[12]
D13 EXT_IO[13]
F13 EXT_IO[14]
E13 EXT_IO[15]
G13 EXT_IO[16]
G14 EXT_IO[17]
F15 EXT_IO[18]
G15 EXT_IO[19]
D17 EXT_IO[20]
E16 EXT_IO[21]
D19 EXT_IO[22]
C19 EXT_IO[23]
C17 EXT_IO[27]
EP4CE30F23 Bank8
VCCIO = 3.3V
SDR_WEN A5
SDR_RASN A4
SDR_LDM B6
SDR_DQ[0] C10
SDR_DQ[1] A9
SDR_DQ[2] B9
SDR_DQ[3] A8
SDR_DQ[4] B8
SDR_DQ[5] A7
SDR_DQ[6] B7
SDR_DQ[7] A6
SDR_CSN D6
SDR_CASN B5
SDR_BA[0] C7
SDR_BA[1] C6
SDR_ADDR[0] C3
SDR_ADDR[1] B4
SDR_ADDR[2] A3
SDR_ADDR[3] B3
SDR_ADDR[4] A10
SDR_ADDR[5] B10
SDR_ADDR[6] D10
SDR_ADDR[10] C4
E7 EXT_IO[3]
D7 EXT_IO[4]
G8 EXT_IO[5]
G9 EXT_IO[6]
F9 EXT_IO[7]
G10 EXT_IO[8]
G11 EXT_IO[9]
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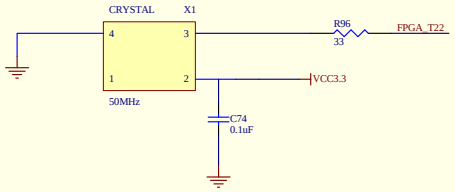
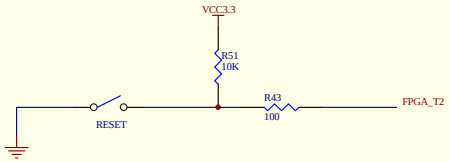
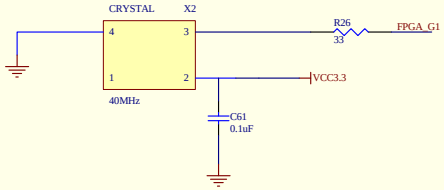
RESET _ CLK _ OTH







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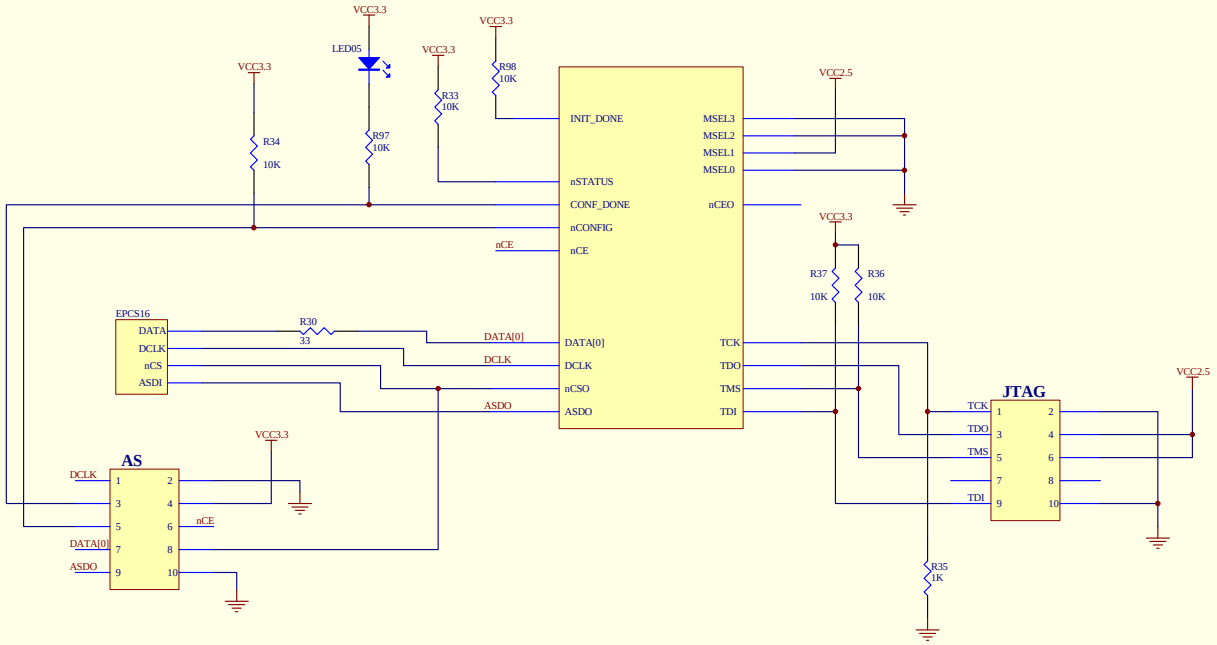

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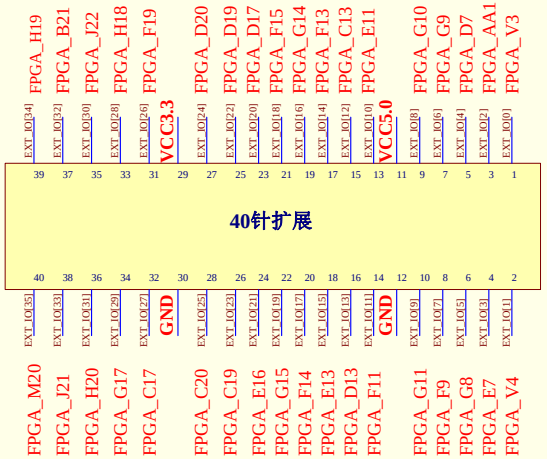

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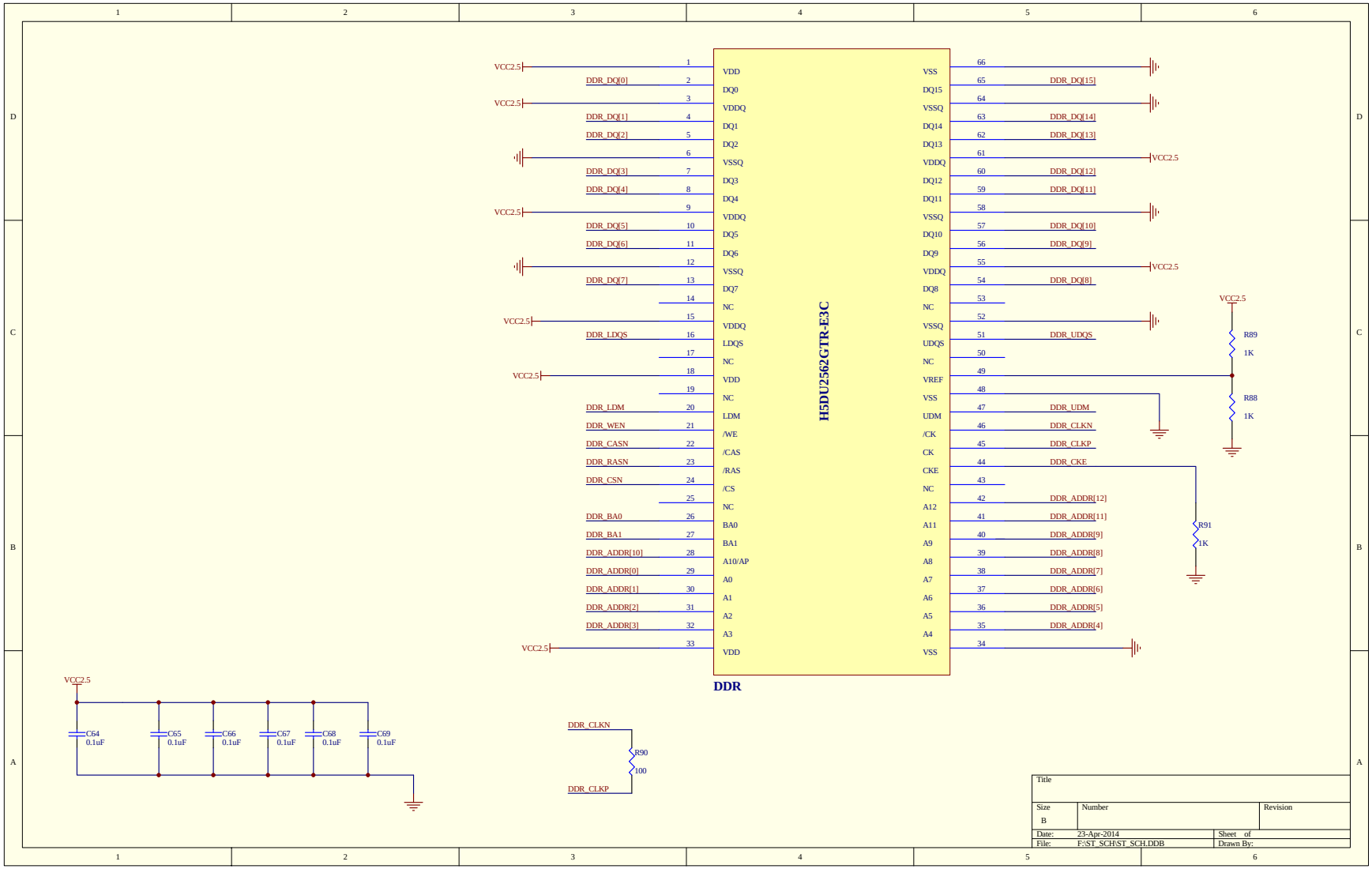
H5DU2562GTR-E3C
DDR
VCC2.5
DDR_DQ[0]
DDR_DQ[1]
DDR_DQ[2]
DDR_DQ[3]
DDR_DQ[4]
DDR_DQ[5]
DDR_DQ[6]
DDR_DQ[7]
DDR_LDQS
DDR_LDM
DDR_WEN
DDR_CASN
DDR_RASN
DDR_CSN
DDR_BA0
DDR_BA1
DDR_ADDR[10]
DDR_ADDR[0]
DDR_ADDR[1]
DDR_ADDR[2]
DDR_ADDR[3]
VDD
DQ0
VDDQ
DQ1
DQ2
VSSQ
DQ3
DQ4
VDDQ
DQ5
DQ6
VSSQ
DQ7
NC
VDDQ
LDQS
NC
VDD
NC
LDM
/WE
/CAS
/RAS
/CS
NC
BA0
BA1
A10/AP
A0
A1
A2
A3
VDD
VSS
DQ15
VSSQ
DQ14
DQ13
VDDQ
DQ12
DQ11
VSSQ
DQ10
DQ9
VDDQ
DQ8
NC
VSSQ
UDQS
NC
VREF
VSS
UDM
/CK
CK
CKE
NC
A12
A11
A9
A8
A7
A6
A5
A4
VSS
DDR_DQ[15]
DDR_DQ[14]
DDR_DQ[13]
DDR_DQ[12]
DDR_DQ[11]
DDR_DQ[10]
DDR_DQ[9]
DDR_DQ[8]
DDR_UDQS
DDR_UDM
DDR_CLKN
DDR_CLKP
DDR_CKE
DDR_ADDR[12]
DDR_ADDR[11]
DDR_ADDR[9]
DDR_ADDR[8]
DDR_ADDR[7]
DDR_ADDR[6]
DDR_ADDR[5]
DDR_ADDR[4]
R89
1K
R88
1K
R91
1K
C64
0.1uF
C65
0.1uF
C66
0.1uF
C67
0.1uF
C68
0.1uF
C69
0.1uF
R90
100
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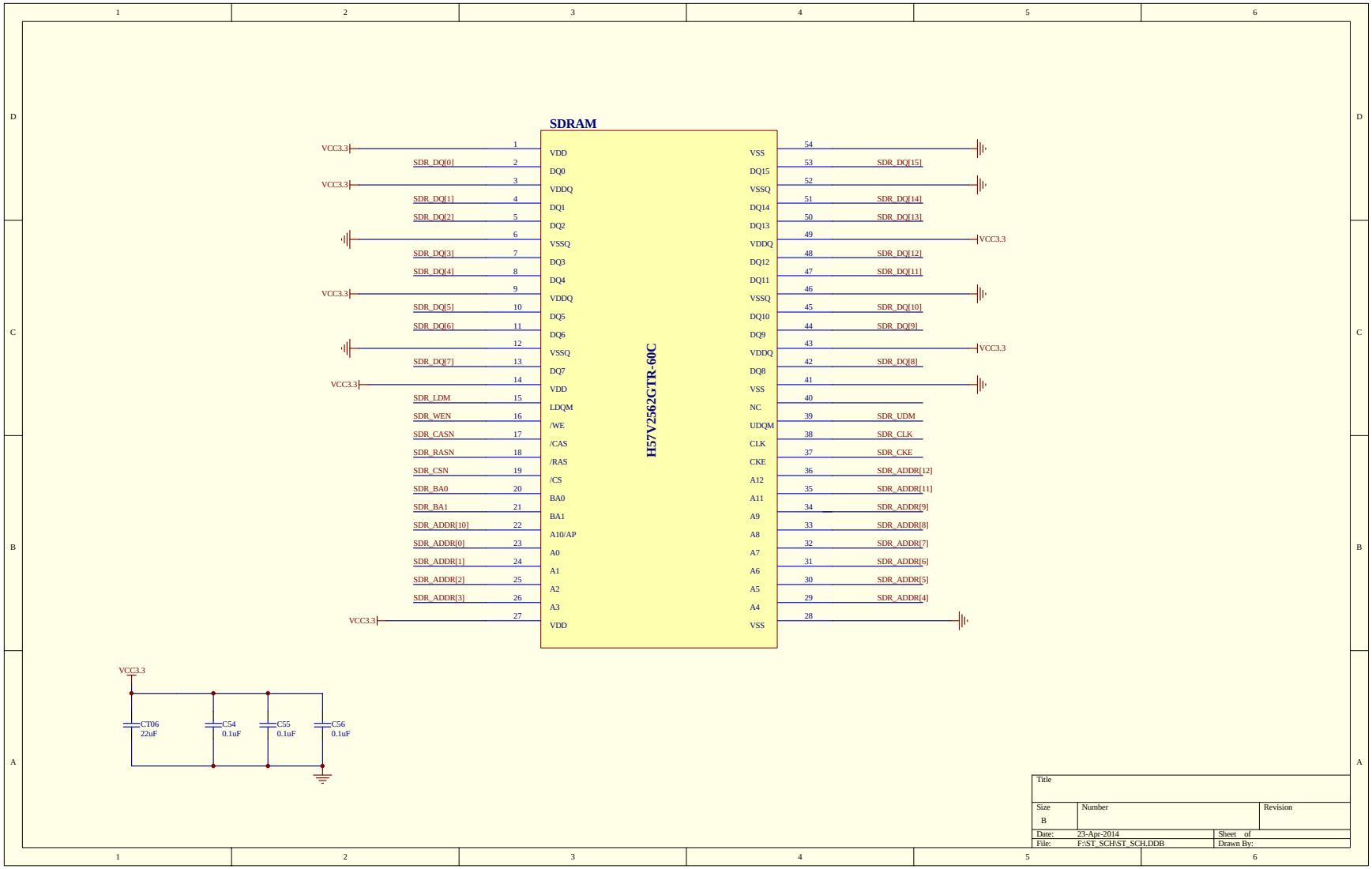
SDRAM
H57V2562GTR-60C
1 VDD
2 DQ0
3 VDDQ
4 DQ1
5 DQ2
6 VSSQ
7 DQ3
8 DQ4
9 VDDQ
10 DQ5
11 DQ6
12 VSSQ
13 DQ7
14 VDD
15 LDQM
16 /WE
17 /CAS
18 /RAS
19 /CS
20 BA0
21 BA1
22 A10/AP
23 A0
24 A1
25 A2
26 A3
27 VDD
54 VSS
53 DQ15
52 VSSQ
51 DQ14
50 DQ13
49 VDDQ
48 DQ12
47 DQ11
46 VSSQ
45 DQ10
44 DQ9
43 VDDQ
42 DQ8
41 VSS
40 NC
39 UDQM
38 CLK
37 CKE
36 A12
35 A11
34 A9
33 A8
32 A7
31 A6
30 A5
29 A4
28 VSS
VCC3.3
SDR_DQ[0]
SDR_DQ[1]
SDR_DQ[2]
SDR_DQ[3]
SDR_DQ[4]
SDR_DQ[5]
SDR_DQ[6]
SDR_DQ[7]
SDR_LDM
SDR_WEN
SDR_CASN
SDR_RASN
SDR_CSN
SDR_BA0
SDR_BA1
SDR_ADDR[10]
SDR_ADDR[0]
SDR_ADDR[1]
SDR_ADDR[2]
SDR_ADDR[3]
SDR_DQ[15]
SDR_DQ[14]
SDR_DQ[13]
SDR_DQ[12]
SDR_DQ[11]
SDR_DQ[10]
SDR_DQ[9]
SDR_DQ[8]
SDR_UDM
SDR_CLK
SDR_CKE
SDR_ADDR[12]
SDR_ADDR[11]
SDR_ADDR[9]
SDR_ADDR[8]
SDR_ADDR[7]
SDR_ADDR[6]
SDR_ADDR[5]
SDR_ADDR[4]
VCC3.3
CT06 22uF
C54 0.1uF
C55 0.1uF
C56 0.1uF
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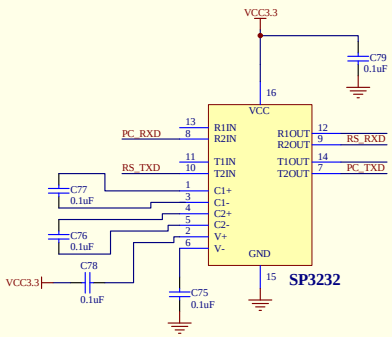

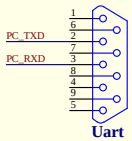

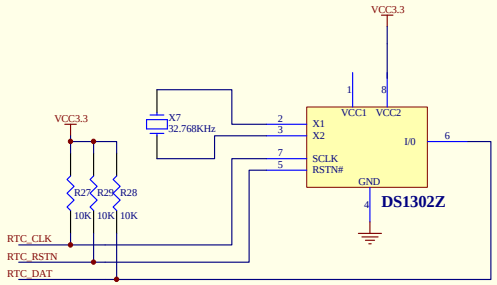

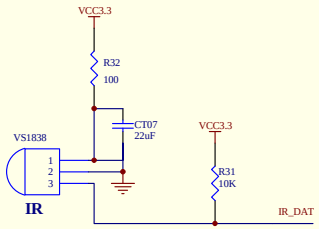

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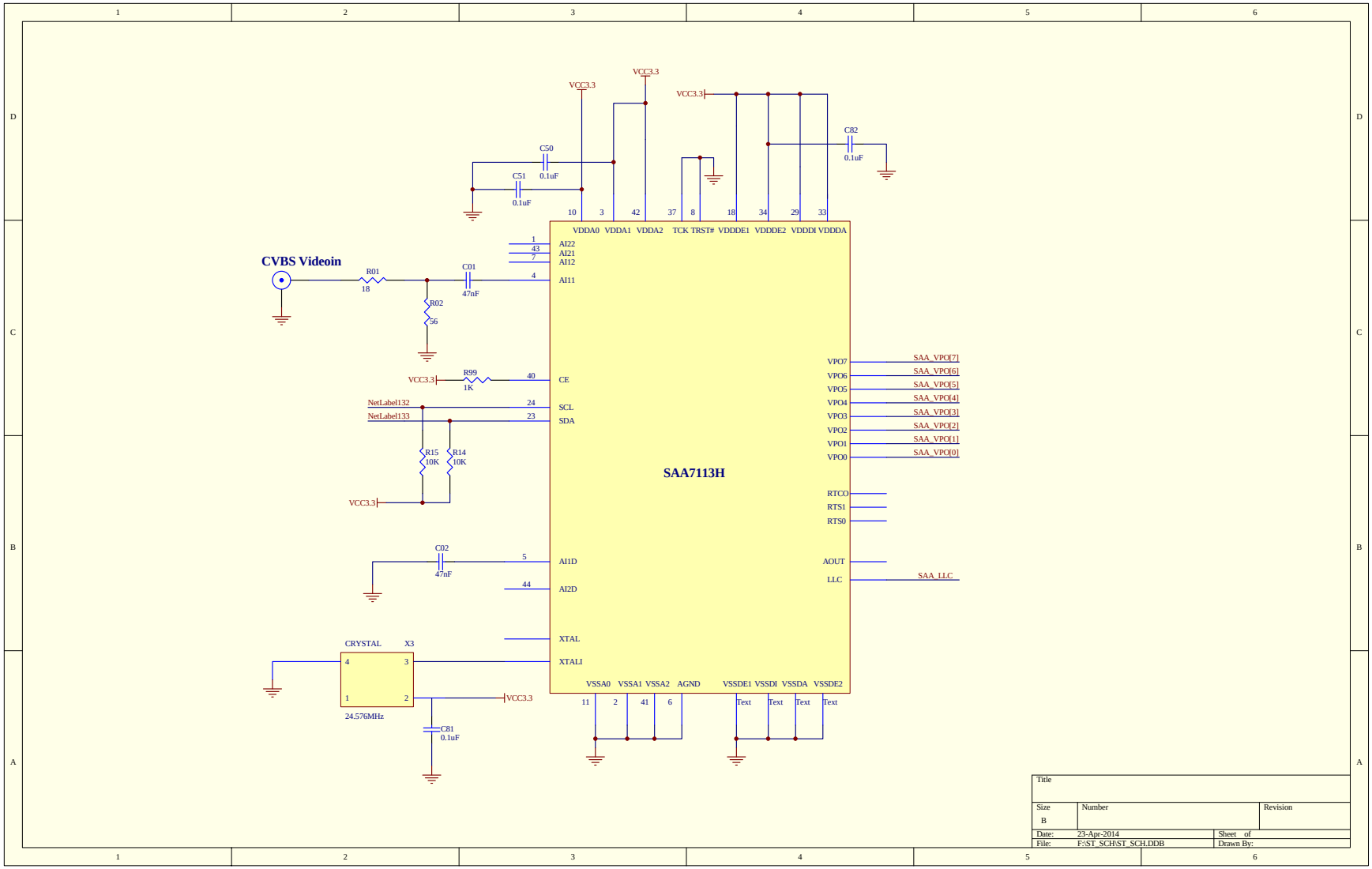
VCC3.3
VCC3.3
VCC3.3
C50
0.1uF
C51
0.1uF
C82
0.1uF
10 3 42 37 8 18 34 29 33
VDDA0 VDDA1 VDDA2 TCK TRST# VDDE1 VDDE2 VDDDI VDDDA
CVBS Videoin
R01
18
C01
47nF
R02
56
1
43
7
4
AI22
AI21
AI12
AI11
VCC3.3
R99
1K
40
CE
NetLabel132
NetLabel133
24
23
SCL
SDA
R15
10K
R14
10K
VCC3.3
SAA7113H
VPO7
VPO6
VPO5
VPO4
VPO3
VPO2
VPO1
VPO0
SAA_VPO[7]
SAA_VPO[6]
SAA_VPO[5]
SAA_VPO[4]
SAA_VPO[3]
SAA_VPO[2]
SAA_VPO[1]
SAA_VPO[0]
RTCO
RTS1
RTS0
C02
47nF
5
AI1D
44
AI2D
AOUT
LLC
SAA_LLC
CRYSTAL
X3
4
3
1
2
24.576MHz
XTAL
XTALI
VCC3.3
C81
0.1uF
VSSA0 VSSA1 VSSA2 AGND VSSDE1 VSSDI VSSDA VSSDE2
11 2 41 6 Text Text Text Text
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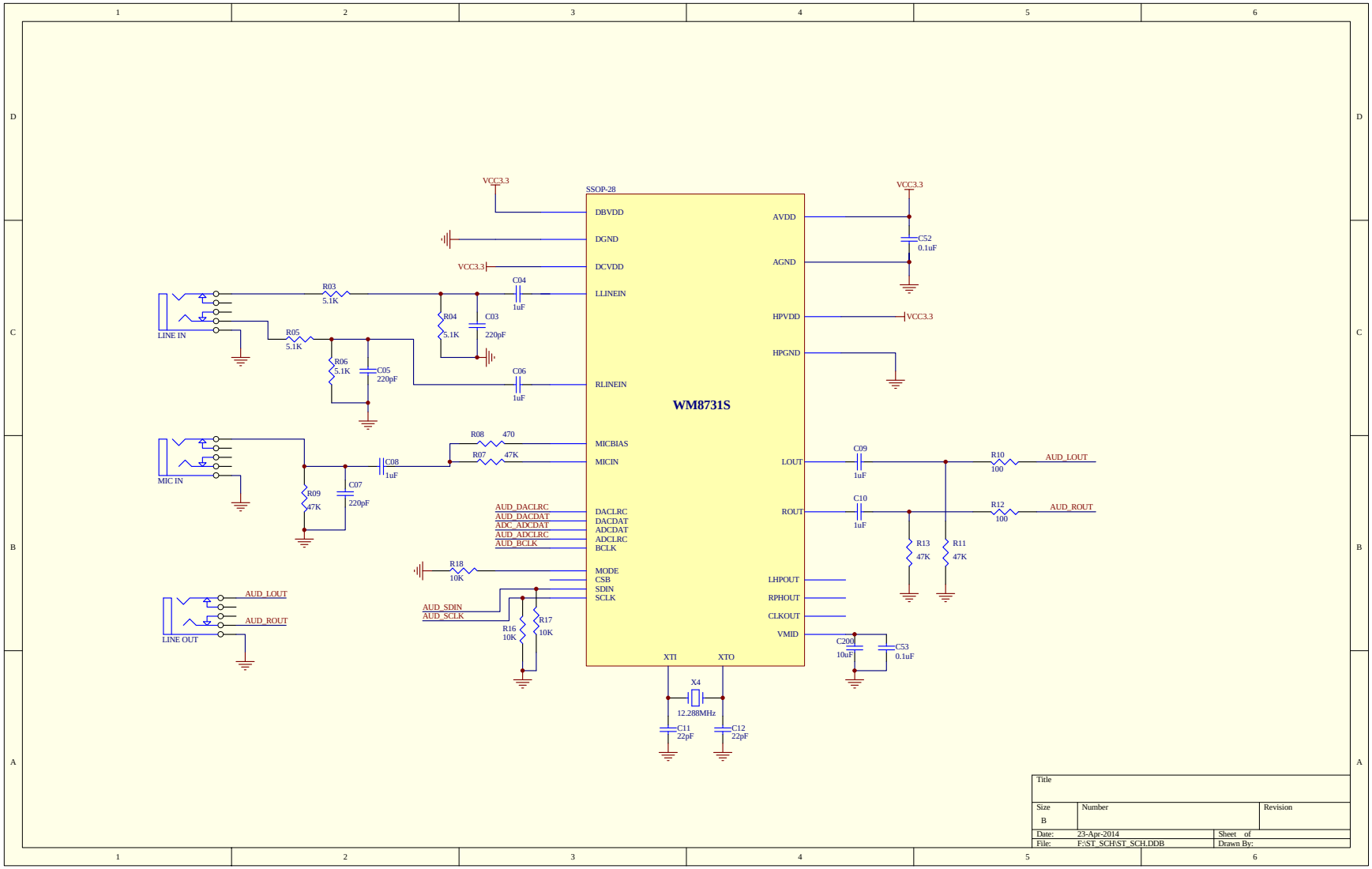
SSOP-28
WM8731S
DBVDD
DGND
DCVDD
LLINEIN
RLINEIN
MICBIAS
MICIN
DACLRC
DACDAT
ADCDAT
ADCLRC
BCLK
MODE
CSB
SDIN
SCLK
XTI
XTO
AVDD
AGND
HPVDD
HPGND
LOUT
ROUT
LHPOUT
RPHOUT
CLKOUT
VMID
VCC3.3
R03 5.1K
R04 5.1K
R05 5.1K
R06 5.1K
C04 1uF
C03 220pF
C05 220pF
C06 1uF
LINE IN
R08 470
R07 47K
C08 1uF
C07 220pF
R09 47K
MIC IN
AUD_DACLRC
AUD_DACDAT
ADC_ADCDAT
AUD_ADCLRC
AUD_BCLK
R18 10K
AUD_SDIN
AUD_SCLK
R16 10K
R17 10K
AUD_LOUT
AUD_ROUT
LINE OUT
C52 0.1uF
C09 1uF
C10 1uF
R10 100
R12 100
R13 47K
R11 47K
C200 10uF
C53 0.1uF
X4
12.288MHz
C11 22pF
C12 22pF
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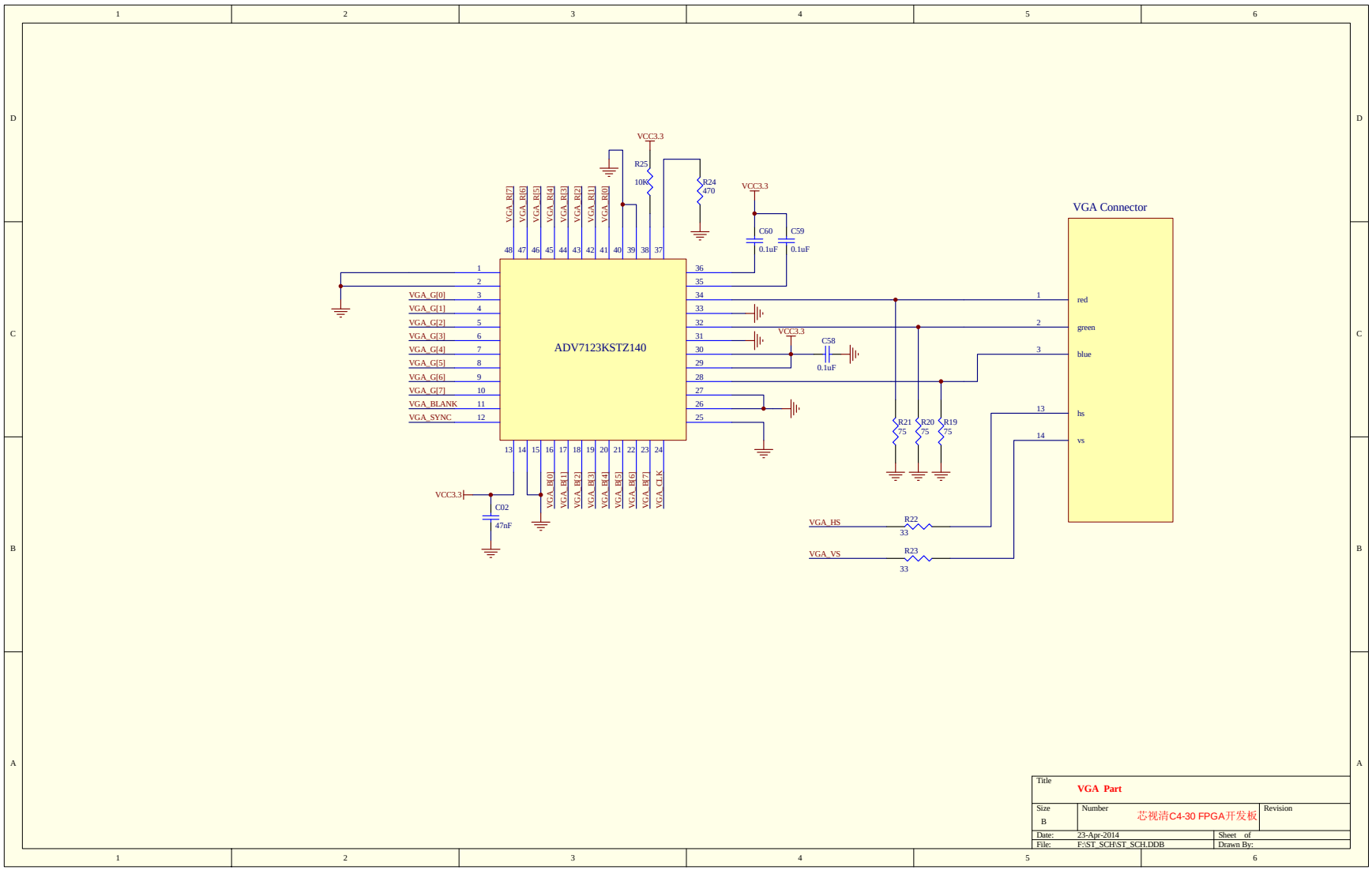

Title	**VGA Part**	
Size: B	Number: 芯视清C4-30 FPGA开发板	Revision:
Date: 23-Apr-2014	Sheet of	
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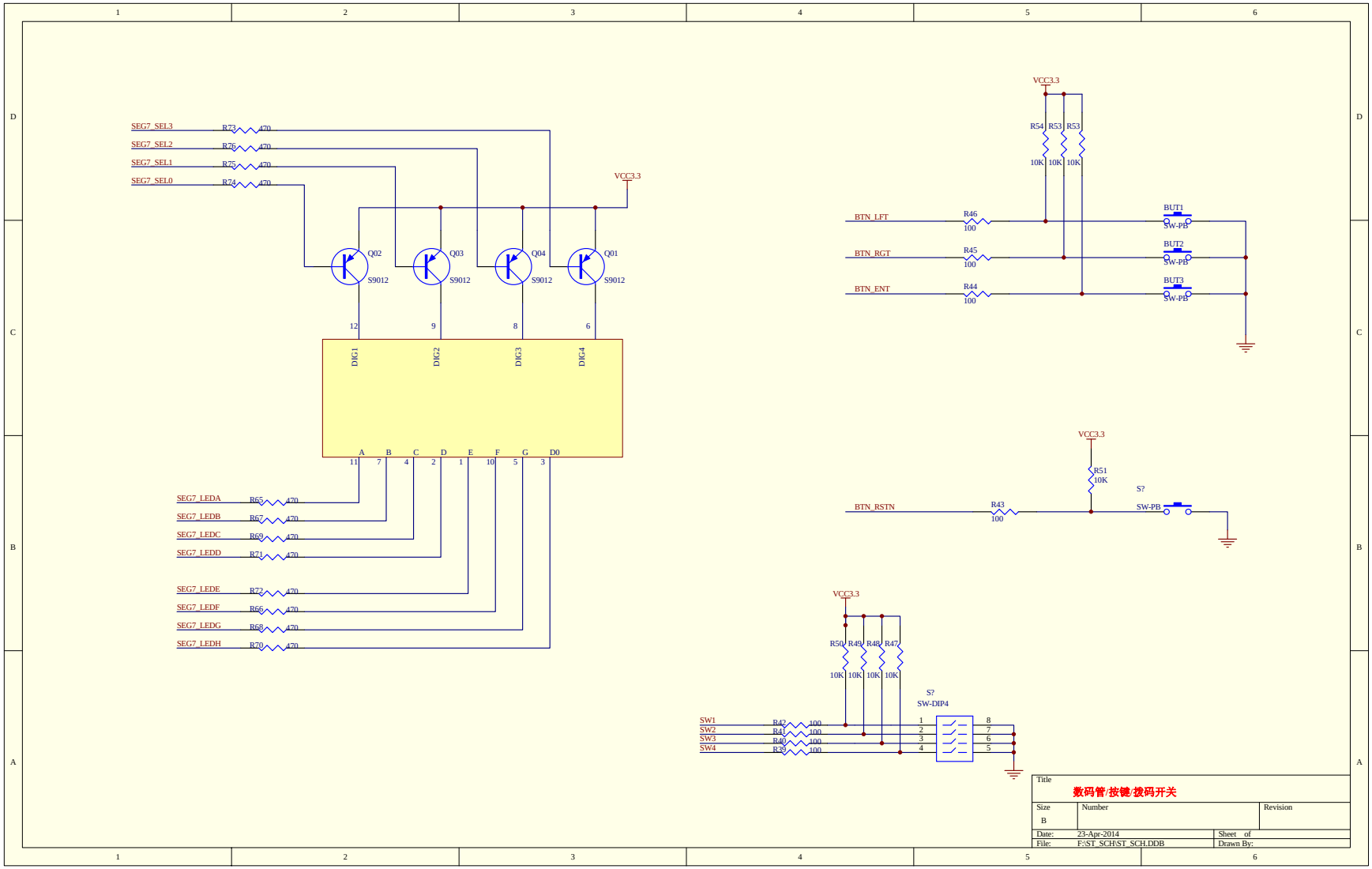
SEG7_SEL3
SEG7_SEL2
SEG7_SEL1
SEG7_SEL0
R73 470
R76 470
R75 470
R74 470
VCC3.3
Q02 S9012
Q03 S9012
Q04 S9012
Q01 S9012
12
9
8
6
DIG1
DIG2
DIG3
DIG4
A B C D E F G D0
11 7 4 2 1 10 5 3
SEG7_LEDA R65 470
SEG7_LEDB R67 470
SEG7_LEDC R69 470
SEG7_LEDD R71 470
SEG7_LEDE R72 470
SEG7_LEDF R66 470
SEG7_LEDG R68 470
SEG7_LEDH R70 470
VCC3.3
R54 R53 R53
10K 10K 10K
BTN_LFT
BTN_RGT
BTN_ENT
R46 100
R45 100
R44 100
BUT1 SW-PB
BUT2 SW-PB
BUT3 SW-PB
VCC3.3
R51 10K
BTN_RSTN
R43 100
S? SW-PB
VCC3.3
R50 R49 R48 R47
10K 10K 10K 10K
S? SW-DIP4
SW1
SW2
SW3
SW4
R42 100
R41 100
R40 100
R39 100
1 2 3 4
8 7 6 5
Title
数码管/按键/拨码开关
Size B
Number
Revision
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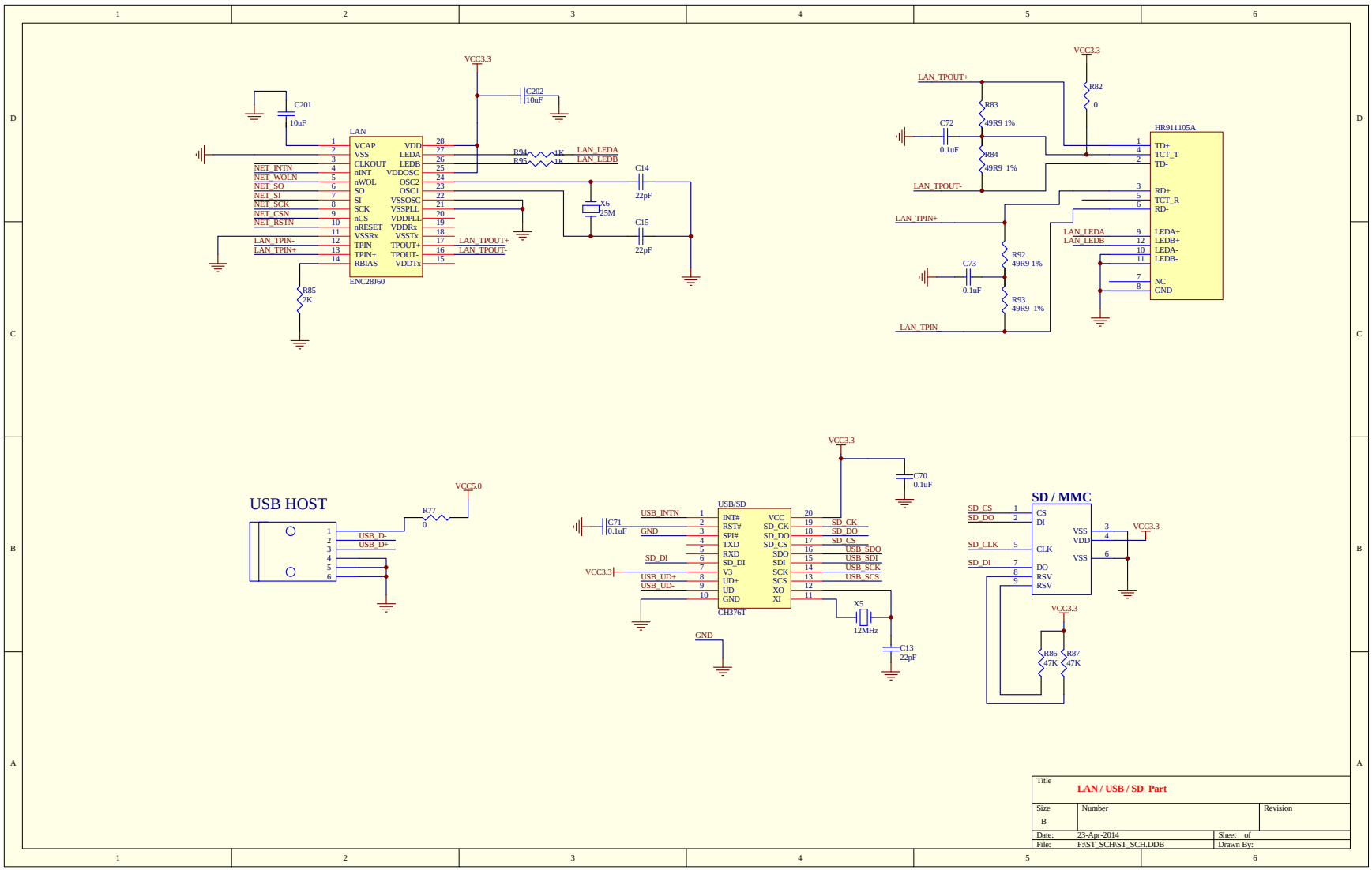
VCC3.3
C201
10uF
C202
10uF
LAN
ENC28J60
VCAP
VSS
CLKOUT
nINT
nWOL
SO
SI
SCK
nCS
nRESET
VSSRx
TPIN-
TPIN+
RBIAS
VDD
LEDA
LEDB
VDDOSC
OSC2
OSC1
VSSOSC
VSSPLL
VDDPLL
VDDRx
VSSTx
TPOUT+
TPOUT-
VDDTx
NET_INTN
NET_WOLN
NET_SO
NET_SI
NET_SCK
NET_CSN
NET_RSTN
LAN_TPIN-
LAN_TPIN+
LAN_TPOUT+
LAN_TPOUT-
R94
R95
1K
LAN_LEDA
LAN_LEDB
C14
22pF
X6
25M
C15
22pF
R85
2K
VCC3.3
LAN_TPOUT+
R83
49R9 1%
C72
0.1uF
R84
49R9 1%
LAN_TPOUT-
R82
0
HR911105A
TD+
TCT_T
TD-
RD+
TCT_R
RD-
LEDA+
LEDB+
LEDA-
LEDB-
NC
GND
LAN_TPIN+
R92
49R9 1%
C73
0.1uF
R93
49R9 1%
LAN_TPIN-
USB HOST
VCC5.0
R77
0
USB_D-
USB_D+
USB/SD
CH376T
INT#
RST#
SPI#
TXD
RXD
SD_DI
V3
UD+
UD-
GND
VCC
SD_CK
SD_DO
SD_CS
SDO
SDI
SCK
SCS
XO
XI
USB_INTN
C71
0.1uF
GND
SD_DI
VCC3.3
USB_UD+
USB_UD-
USB_SDO
USB_SDI
USB_SCK
USB_SCS
X5
12MHz
C13
22pF
GND
VCC3.3
C70
0.1uF
SD / MMC
CS
DI
CLK
DO
RSV
VSS
VDD
SD_CS
SD_DO
SD_CLK
SD_DI
VCC3.3
R86
47K
R87
47K
Title
LAN / USB / SD Part
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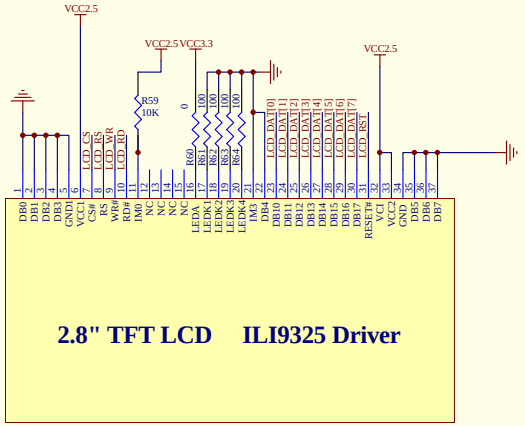

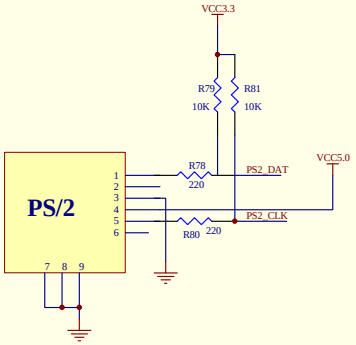

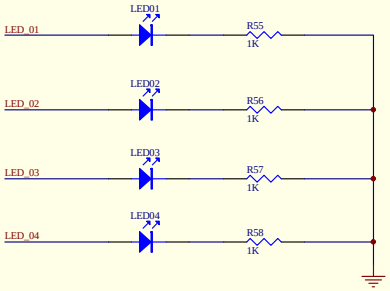

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Size: B	Number	Revision
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