

# Circuit Truth Table

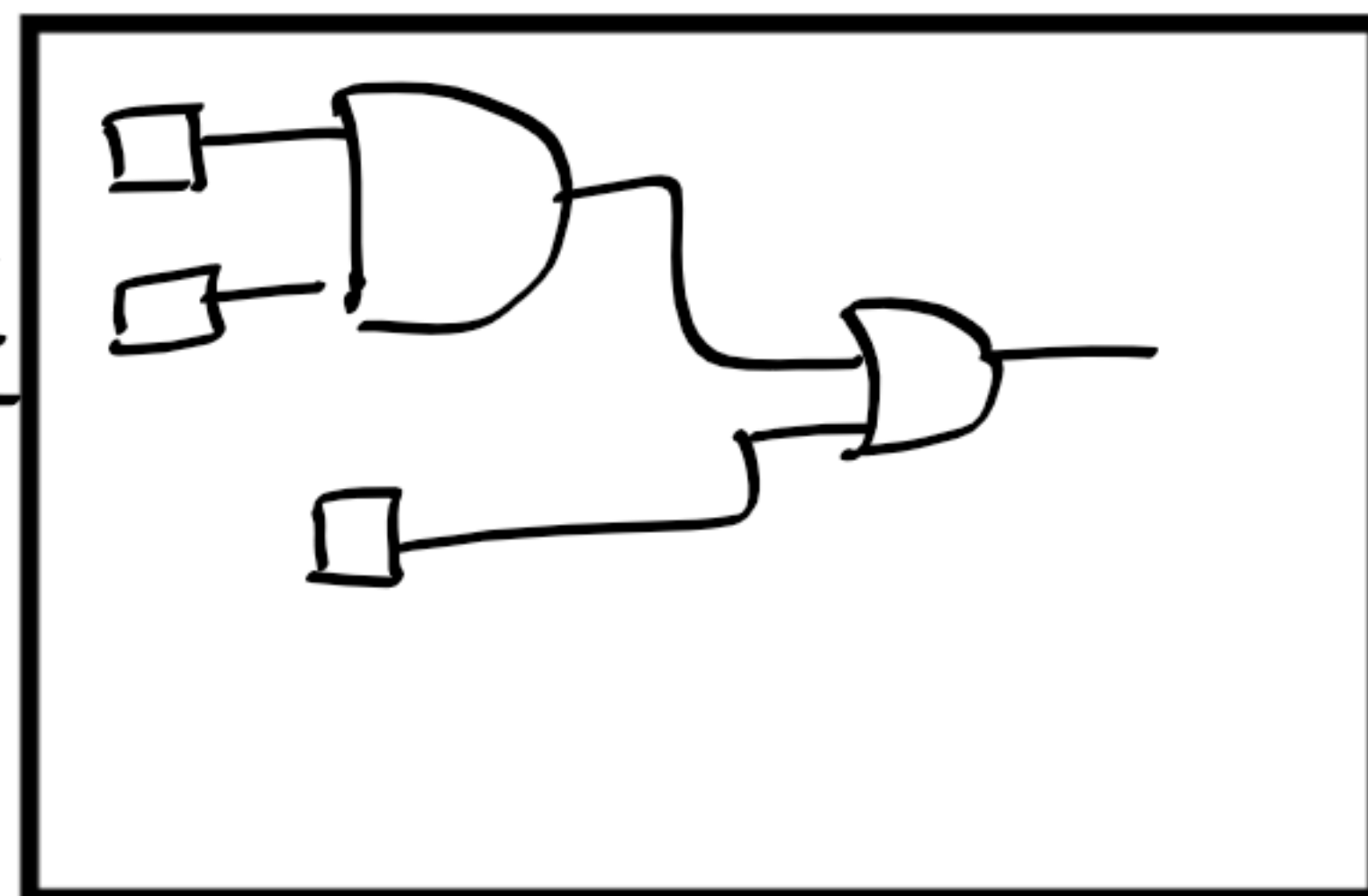
**Test!!**

$I_1$	$I_2$	$I_3$	$I_1 \wedge I_2$	$(I_1 \wedge I_2) \wedge I_3$
T	T	T	T	T
T	T	F	T	F
T	F	T	F	F
T	F	F	F	F
F	T	T	F	F
F	T	F	F	F
F	F	T	F	F
F	F	F	F	F

$I_1$	$I_2$	$I_3$	$O$
T	T	T	T
T	F	T	T
T	F	F	F
F	F	T	F

# Expected Results

# Circuit Map



> 3 passed cases, 1 failed case.

# Dialog Box