Truth Table

I_1	I_2	I_3	I_4	$\overline{(I_1 \vee I_2)}$
T	T	T	T	T
T	T	T	F	T
T	T	F	T	T
T	T	F	F	T
T	F	T	T	T
T	F	T	F	T
T	F	F	T	T
T	F	F	F	T
F	T	T	T	T
F	T	T	F	T
F	T	F	T	T

Boolean Expression

Circuit Map