

Circuit Truth Table

~~~~~  
~~~~~  
~~~~~  
~~~~~  
~~~~~  
~~~~~

Test!!

I_1	I_2	I_3	$I_1 \wedge I_2$	$(I_1 \wedge I_2)$
T	T	T	T	T
T	T	F	T	T
T	F	T	F	T
T	F	F	F	F
F	T	T	F	T
F	T	F	F	F
F	F	T	F	T
F	F	F	F	F

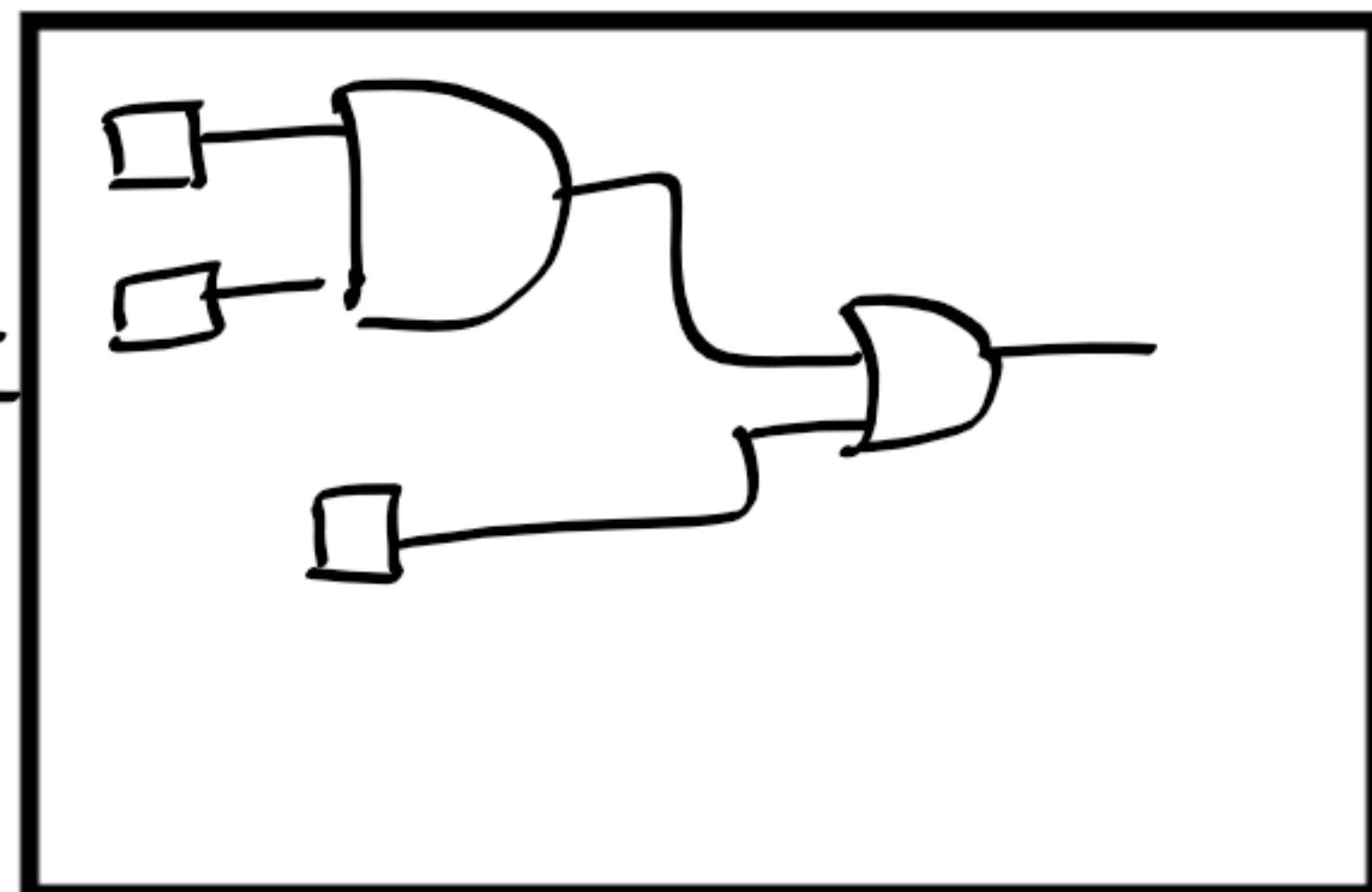
Circuit

Expected Results

~~~~~  
~~~~~  
~~~~~

Circuit Map

~~~~~  
~~~~~  
~~~~~



>

Dialog Box

~~~~~  
~~~~~  
~~~~~