# COMP 8551 Advanced Games Programment Impect Exam Help Techniques //powcoder.com Add WeChat powcoder

Borna Noureddin, Ph.D.

British Columbia Institute of Technology

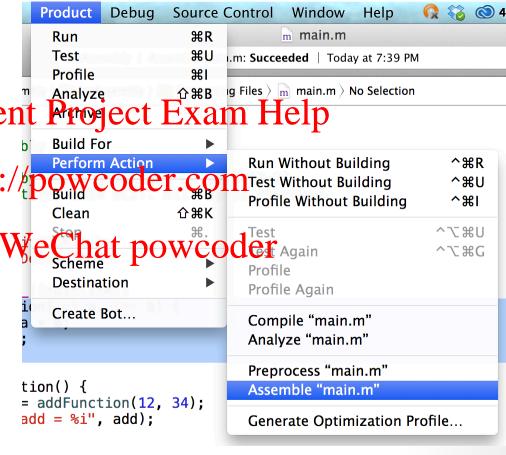
Assembly Language: ARM/NEON

• ARM = RISC architecture Assignment

• Can use https://p

XCode to see Add We assembly code

Can embed in C code



#### Registers

- r0 r3: parameters passed to a function Assignment Project Exam Help
   r4 r11: function's local variables
- r12: scratch register powcoder.com
- r13: stack pointed WeChat powcoder
- r14: link register (address of next instruction to execute when returning from the current function)
- r15: program counter (address of currently executing instruction)

#### Registers

- w0 w3: parameters passed to a function Assignment Project Exam Help
   w4 w11: function's local variables
- w12: scratch register powcoder.com
- w13: stack poiAttel. WeChat powcoder
- w14: link register (address of next instruction to execute when returning from the current function)
- w15: program counter (address of currently executing instruction)

```
int addFunction(int a, int b) {
   int c = a + b;
   return c;
}
   Assignment Project Exam Help
```

https://powcoder.com

Add WeChat powcoder What does this simple function look like in Assembler?

```
; @addFunction
addFunction:
Lfunc begin0:
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:13:0
          sp, sp, #16
     sub
     str wo, Assignment Project Exam Help
     str w1, [sp, #8]
/Users/borna/Documents/dev/jos/armneon/armneon/main.m:14:13 ldr w0, [sp, #12PS://powcoder.com
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:14:17
ldr w1, [sp, #\dd WeChat powcoder
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:14:15
                w0, w0, w1
     add
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:14:9
     str w0, [sp, #4]
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:15:12
     ldr w0, [sp, #4]
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:15:5
                                      : =16
     add
           sp, sp, #16
     ret
```

```
addFunction:
                                            ; @addFunction
Lfunc begin0:
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:13:0
     sub
          sp, sp, #16
     str wo, Assignment Project Exam Help
     str w1, [sp, #8]
/Users/borna/Documents/dev/jos/armneon/armneon/main.m:14:13
ldr w0, [sp, #12] PS://powcoder.com
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:14:17
ldr w1, [sp, #\d WeChat powcoder
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:14:15
                w0, w0, w1
     add
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:14:9
     str w0, [sp, #4]
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:15:12
     ldr w0, [sp, #4]
/Users/borna/Documents/dev/ios/armneon/armneon/main.m:15:5
     add
           sp, sp, #16
                                      : =16
     ret
```

```
0x1000a2768 <+6 Ssignment Project Exam Help
                                                         =0 \times 10
0x1000a276c <+4>:
                     str
                            w0, [sp, #0xc]
                     https://powooderedom
0x1000a2770 <+8>:
0x1000a2774 <+12>: ldr
                            w0, [sp, #0xc]
0x1000a2778 <+16>: ldr<sub>1</sub>
0x1000a277c <+20>:
                            w0, [sp, \#0x4]
0x1000a2780 < +24>: str
0x1000a2784 <+28>: ldr
                            w0, [sp, \#0x4]
                            sp, sp, #0x10
0 \times 1000a2788 < +32 > : add
                                                           =0 \times 10
0x1000a278c < +36>: ret
```

What about calling the function?

Add WeChat powcoder

```
attribute ((noinline))
int add_two_int(int x, int y) {
                    Assignment Project Exam, Help, 16
   int ret;
                                                    str w0, [sp, #12]
                https://powcoder.stom, [sp, #8] "add %w[ret], %w[x], %w[y]" [sp, #12
   asm volatile (
                                                    ldr w1, [sp, #8]
                // outputs Add WeChat powgoderwo, wo, w1
                : [ret]"=r"(ret)
                                                    : InlineAsm End
                                                    str w0, [sp, #4]
                // inputs
                                                    ldr w0, [sp, #4]
                                                    add sp, sp, #16
                : [x]"r"(x), [y]"r"(y)
                                                    ret
                );
   return ret;
```

# XCode / ARM / NEON example

**NEON** for vectorization:

```
Assignment Project Exam Help
<a href="http://www.crickettechnology.com/blog/?p">http://www.crickettechnology.com/blog/?p</a>
<a href="https://powcoder.com">https://powcoder.com</a>
<a href="https://powcoder.com">=691</a>
```

Add WeChat powcoder

#### Additional Reading

http://www.crickettechnology.com/blog/?p=691

http://www.raywenderlich.com/37181/ios-assembly-tutorial Assignment Project Exam Help

http://www.computernolitalgia.n/particles/desemblyhtm

http://en.wikipedia.org/whddsseneschatgpowcodent\_usage

https://software.intel.com/en-us/articles/optimizing-the-rendering-pipeline-of-animated-models-using-the-intel-streaming-simd-extensions

http://en.wikipedia.org/wiki/SIMD



