

School of Computer Science and Statistics
ECTS Module Descriptor

Academic Year	2018-19			
Module Code	CS1021			
Module Title	Introduction to Computing I			
Pre-requisites	None			
ECTS	5			
Chief Examiner	Dr Jeremy Jones			
Teaching Staff	Dr Jeremy Jones			
Delivery	Lecture hours	Lab hours (per student)	Tutorial hours (per student)	Total
	22	10	10	42
	Comments: Attendance at all lectures, labs and tutorials is compulsory.			
Aims	<p>This module provides students with an introduction to the basic structure and operation of a microprocessor system focussing on the central processing unit (CPU), memory and the execution of assembly language programmes.</p> <p>Students are taught concepts that are fundamental to the study of computer science such as binary arithmetic, the architecture of a computer system and how a computer executes programmes</p> <p>Students are shown the relationship between high-level programming language constructs – simple assignments, arithmetic expressions, conditional and iterative statements – and the realisation of these constructs as sequences of assembly language instructions.</p> <p>Students develop their problem solving and programming skills by designing and implementing solutions to programming problems, first in the form of high-level pseudo-code or flowcharts and then as documented and tested assembly language programs.</p>			
Learning Outcomes	<p>When students have successfully completed this module will be able to:</p> <ul style="list-style-type: none"> • describe the basic characteristics, structure and operation of a computer system • represent and interpret basic information in binary form (integers, text, ...) • translate simple high-level programming language constructs into their assembly language equivalents • design, construct, document and test small-scale assembly language programs to solve simple problems • determine the cost of executing instructions and the efficiency of simple algorithms • make use of appropriate documentation and reference material 			

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Syllabus	<p>Specific topics addressed in this module include:</p> <ul style="list-style-type: none"> • number systems, memory and data representation • basic computer architecture (CPU, memory, registers, fetch-decode-execute loop) • assembly language and machine code • binary arithmetic and bit-wise operations • program flow control using branch instructions • memory accesses (using load and store instructions) 										
Assessment	<table> <tr> <td>Coursework</td><td>40%</td></tr> <tr> <td>Mid-term test</td><td>20%</td></tr> <tr> <td>End of term Practical Exam</td><td>40%</td></tr> <tr> <td></td><td><hr/></td></tr> <tr> <td></td><td>100%</td></tr> </table> <p>Coursework consists of 4 short “pair-programming” assignments and 2 larger individual assignments (although this may be subject to change).</p> <p>The mid-term test is a one hour written test held in the week after study week.</p> <p>The end of term practical exam will be a 1.5 hr practical exam held during the end of semester examination period in the Computer Science Labs (if it is not possible to schedule the practical exam, the fall back will be a 2 hr written exam).</p> <p>Supplemental assessment is by examination ONLY (100%). Students repeating ‘off-books’ (OBA) are also assessed by examination ONLY (100%) in all examination sessions.</p>	Coursework	40%	Mid-term test	20%	End of term Practical Exam	40%		<hr/>		100%
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End of term Practical Exam	40%										
	<hr/>										
	100%										
	<p>There is no required text for this module, the notes and labs should be self-explanatory. Suggested complementary texts are:</p> <ul style="list-style-type: none"> • William Hohl, “ARM Assembly Language: Fundamentals and Techniques”, CRC Press, 2009. • Steve Furber, “ARM System-on-Chip Architecture”, 2nd edition, Addison-Wesley Professional, 2000. • Andrew Sloss, Dominic Symes and Chris Wright, “ARM System Developer's Guide: Designing and Optimizing System Software”, Morgan Kaufmann, 2004. 										
Website	https://www.scss.tcd.ie/Jeremy.Jones/CS1021/CS1021.htm										

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