

## CS1021 Tutorial 7

### Advanced LDR and STR Instructions

Q1 If R1 = 0x1000 and R4 = 8, what memory location (in hexadecimal) is loaded into R0 and what is the value of R1 (in hexadecimal) after each of the following instructions has been executed.

- (i) LDR R0, [R1, #8] ; R0=MEM[0x1008], R1=0x1000
- (ii) LDR R0, [R1], #-8
- (iii) LDR R0, [R1, #12]!
- (iv) LDR R0, [R1, R4]
- (v) LDR R0, [R1], R4
- (vi) LDR R0, [R1, R4]!
- (vii) LDR R0, [R1, R4, LSL #3]
- (viii) LDR R0, [R1], R4, LSR #1
- (ix) LDR R0, [R1, R4, LSL #2]!

Q2 Given an array **b** stored at memory address 0x40001000 containing 64 32-bit integers b[0] to b[63], write ARM assembly language instructions for the following pseudo code statements. Assume that the 64 32-bit unsigned integers stored in memory locations 0x40000000 and 0x40000004 respectively.

- (i) R0 = b[7]
- (ii) R0 = b[i]
- (iii) i = b[i] + b[j]
- (iv) b[i] = b[10] + b[j]

Q3 In a Scrabble® like game, players form words and each word is awarded a score that is the sum of the points for each letter in the word. English language editions of Scrabble contain 100 letter tiles with the following letter points and letter distribution:

2 blank tiles (scoring 0 points)

- 1 point: E×12, A×9, I×9, O×8, N×6, R×6, T×6, L×4, S×4, U×4
- 2 points: D×4, G×3
- 3 points: B×2, C×2, M×2, P×2
- 4 points: F×2, H×2, V×2, W×2, Y×2
- 5 points: K×1
- 8 points: J×1, X×1
- 10 points: Q×1, Z×1

For example, the word “MAZE” would have a score of 15 (3 + 1 + 10 + 1).

Write an ARM assembly language program that will compute the word score for a NUL terminated string containing UPPER CASE alphabetic characters and spaces (for blanks). The word is stored in memory at the address contained in R1. The score for each letter is stored in flash memory as a sequence (or table or array) of 26 byte values. The first byte is the score for “A”, the second byte is the score for “B”, and so on (use the DCB assembler directive to create this table). Your program should calculate the word score in R0.