

G6059

THE UNIVERSITY OF SUSSEX

BSc and MComp SECOND YEAR EXAMINATION May/June 2017 (A2)

Operating Systems

Assessment Period: May/June 2017 (A2)
ASSIGNMENT Project Exam Help

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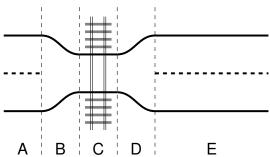
Candidates should answer TWO questions out of THREE. If all three questions are attempted the right two moves will be marked.

The time allowed is TWO hours.

Each question is worth 50 marks.

At the end of the examination the question paper and any answer books/answer sheets, used or unused, will be collected from you before you leave the examination room.

- 1. Process synchronisation.
 - (a) Name three different process synchronisation mechanisms, explain how they work, and discuss their advantages and disadvantages. [15 marks]
 - (b) Implement a simulation of the following road/railway traffic scenario.
 [35 marks]



Explanations:

- - A train is simulated by an instance of a thread Train. You are often the inchoos poid approved () (corrections), void continue() that simulate a train approaching the road, crossing it, and continuing its way after the crossing, respectively.
 - Your implementation must enforce the following rules:
 - There must not be any collisions between cars and trains.
 - Section A can hold one car in each direction.
 - Section E can hold K cars in each direction.
 - Sections B, C and D can only hold a single car (regardless of the direction).
 - Only a single train can cross the road at a time.
 - Trains have priority over cars.
 - A car must not block the railway or deadlock while trying to pass.
 - You should maximise the number of cars driving on the road and the number of trains crossing the road.

Consider the code template on the next page. You are expected to implement four code snippets (10–15 lines each). These code snippets replace the comments // my code n in the template ($n \in \{1,2,3,4\}$). You are not required to copy the template, but clearly label the start of each of your code snippets with the corresponding number n.

```
1 public class TrafficSimulation {
2
     static final int K=50;
3
     // initialize semaphores
4
     // my code 1
5
6
7
     class CarLeftRight implements Runnable {
8
       void run() {
9
         // my code 2
10
11
12
     class CarRightLeft implements Runnable {
13
14
       void run() {
15
         // my code 3
16
     }
17
     signment Project Exam Help Class Train implements Runnable {
18
19
20
       void run() {
         ttps://powcoder.com
21
22
23
24
     public salve edid mait (Strong ager (
25
       // creates and starts threads...
26
       // (no implementation required)
27
28
     }
   }
29
```

The following class might be useful:

Class Semaphore

```
Semaphore(int permits, boolean fair)
Creates a Semaphore with the given number of permits and the given fairness setting (fair=true).

void acquire()
Acquires a permit from this semaphore, blocking until one is available.

void release()
Releases a permit, returning it to the semaphore.
```

- 2. Scheduling, processes and threads.
 - (a) Name <u>five</u> goals pursued by scheduling algorithms, explain them and give application examples for each of them to illustrate their importance. [15 marks]
 - (b) Name three events that can cause a process switch. Give a concrete example for each of these events. [6 marks]
 - (c) Which operations are done by an operating system to perform a process switch? [10 marks]
 - (d) What is a microkernel? Which services does it provide? Discuss why you would (not) use a microkernel. [5 marks]
 - (e) Give an example of an application scenario where the use of threads is preferred to the use of processes. Give an example of an application scenario where the use of processes is preferred to the use of threads. In both cases, justify your choice. [10 marks]

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```
int main(int argc, char** argv) {
   fork();ps://powcoder.com
   return 0;
}
```

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How many child processes are created upon execution of this program?

Justify your answer.

[4 marks]

- 3. Memory management, file systems and I/O.
 - (a) We will compare the number of page faults for the three page replacement strategies OPT, FIFO and LRU. For each of the <u>three</u> strategies fill in a table such as the one given as a template below.

	Α	В	С	D	В	Е	D	F	С	Е	В	F	Α	F	Е	D	F	Α
0																		
1																		
2																		
3																		
PF																		

There are six pages: A, B, C, D, E, F. The header row contains the accesses to pages in the order from left to right. There are four page frames (the four rows 0, 1, 2, 3). The row PF is to mark a page fault. Discuss why the result confirms your expectations or why it does not.

- block allocation in a file system, and explain advantages and drawbacks.

 [16] What grant a file system, and explain advantages and drawbacks.
- (c) What inthe Swin at power and error [4 marks]

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