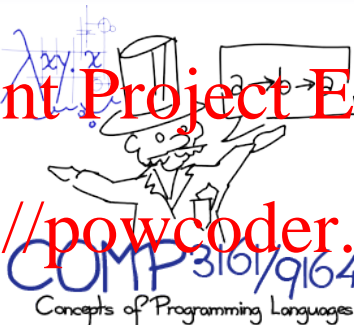


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Abstract Machines

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Big O

We all know that MERGESORT has $O(n \log n)$ time complexity, and that BUBBLESORT has $O(n^2)$ time complexity, but what does that **actually mean**?

Big O Notation

Given functions $f, g: \mathbb{R} \rightarrow \mathbb{R}$, $f \in O(g)$ if and only if there exists a value $x_0 \in \mathbb{R}$ and a coefficient m such that:

$$\forall x > x_0. f(x) \leq m \cdot g(x)$$

What is the **codomain** of f ?

When analysing algorithms, we don't usually time how long they take to run on a real machine.

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Cost Models

A *cost model* is a mathematical model that tries to measure of the cost of executing a program.

There exist *denotational* cost models, that assign a cost directly to syntax:

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However in this course we will focus on *operational cost models*.

Operational Cost Models

First, we define a program-evaluating *abstract machine*. We can determine the time cost by counting the number of steps taken by the abstract machine.

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$\llbracket \cdot \rrbracket : \text{Program} \rightarrow \text{Cost}$

Abstract Machines

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Abstract Machines

An *abstract machine* consists of:

- 1 A set of states Σ
- 2 A set of initial states $I \subseteq \Sigma$,
- 3 A set of final states $F \subseteq \Sigma$, and
- 4 A transition relation $\mapsto \subseteq \Sigma \times \Sigma$.

We've seen this before in structural operational (or small-step) semantics.

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The M Machine

Is just our usual small-step rules:

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$$\frac{e_1 \mapsto_M e'_1}{(\text{Plus } e_1 \ e_2) \mapsto_M (\text{Plus } e'_1 \ e_2)}$$

$$\frac{\frac{e_1 \mapsto_M e'_1}{(\text{If } e_1 \ e_2 \ e_3) \mapsto_M (\text{If } e'_1 \ e_2 \ e_3)}}{(\text{If } (\text{Lit True}) \ e_2 \ e_3) \mapsto_M e_2 \quad (\text{If } (\text{Lit False}) \ e_2 \ e_3) \mapsto_M e_3}$$

$$\frac{\frac{\frac{e_1 \mapsto_M e'_1}{(\text{Apply } e_1 \ e_2) \mapsto_M (\text{Apply } e'_1 \ e_2)}}{e_2 \mapsto_M e'_2}}{(\text{Apply } (\text{Recfun } (f.x. \ e)) \ e_2) \mapsto_M (\text{Apply } (\text{Recfun } (f.x. \ e)) \ e'_2)}$$

$$\frac{v \in F}{(\text{Apply } (\text{Recfun } (f.x. \ e)) \ v) \mapsto_M e[x := v, f := (\text{Recfun } (f.x. \ e))]}$$

The M Machine is **unsuitable** as a basis for a cost model. Why?

Performance

One step in our machine should always only be $\mathcal{O}(1)$ in our language implementation. Otherwise, counting steps will not get an accurate description of the time cost.

This makes for two potential problems:

- 1 **Substitution** occurs in function application, which is potentially $\mathcal{O}(n)$ time.
- 2 **Control Flow** is not explicit – which subexpression to reduce is found by recursively descending the abstract syntax tree each time.

$eval\ (Num\ n) = n$

$eval\ e = eval\ (oneStep\ e)$

$oneStep\ (Plus\ (Num\ n)\ (Num\ m)) = Num\ (n + m)$

$oneStep\ (Plus\ (Num\ n)\ e_2) = Plus\ (Num\ n)\ (oneStep\ e_2)$

$oneStep\ (Plus\ e_1\ e_2) = Plus\ (oneStep\ e_1)\ e_2$

...

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The C Machine

We want to define a machine where **all the rules are axioms**, so there can be no recursive descent into subexpressions. How is recursion typically implemented?

Stacks!

$$\frac{}{f \triangleright s \text{ Stack}} \quad \frac{f \text{ Frame} \quad s \text{ Stack}}{f \triangleright s \text{ Stack}}$$

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Key Idea: States will consist of a **current expression** to evaluate and a stack of **computational contexts** that situate it in the overall computation. An example stack would be:

$$(Plus\ 3\ \square) \triangleright (Times\ \square\ (Num\ 2)) \triangleright \circ$$

This represents the computational context:

$$(Times\ (Plus\ 3\ \square)\ (Num\ 2))$$

The C Machine

Our states will consist of two modes:

- 1 **Evaluate** the current expression within stack s , written $s \succ e$.
- 2 **Return** a value v (either a function, integer, or boolean) back into the context in s , written $s \prec v$.

Initial states are those that start evaluating an expression from an empty stack, i.e.

○ $\succ e$. **Final states** are those that return a value to the empty stack, i.e. ○ $\prec v$.

Stack frames are expressions with holes or values in them:

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e_2 Expr	v_1 Value
<hr/>	<hr/>
(Plus □ e_2) Frame	(Plus v_1 □) Frame

...

Evaluating

There are three axioms about **Plus** now:

When evaluating a **Plus** expression, first evaluate the LHS

$$\frac{}{s \succ (\text{Plus } e_1 \ e_2) \mapsto_C (\text{Plus } \square \ e_2) \triangleright s \succ e_1}$$

Once it is evaluated, switch to the RHS:

$$\frac{}{(\text{Plus } \square \ e_2) \triangleright s \prec v_1 \mapsto_C (\text{Plus } v_1 \ \square) \triangleright s \succ e_2}$$

Once it is evaluated, return the sum:

$$\frac{}{(\text{Plus } v_1 \ \square) \triangleright s \prec v_2 \mapsto_C s \prec v_1 + v_2}$$

We also have a single rule about **Num** that just returns the value:

$$\frac{}{s \succ (\text{Num } n) \mapsto_C s \prec n}$$

Example

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$\vdash_C \circ \succ (\text{Plus } (\text{Plus } (\text{Num } 2) (\text{Num } 3)) (\text{Num } 4))$
 $\vdash_C (\text{Plus } \square (\text{Num } 4)) \triangleright \circ \succ (\text{Plus } (\text{Num } 2) (\text{Num } 3))$
 $\vdash_C (\text{Plus } \square (\text{Num } 3)) \triangleright (\text{Plus } \square (\text{Num } 4)) \triangleright \circ \succ (\text{Num } 2)$
 $\vdash_C (\text{Plus } \square (\text{Num } 3)) \triangleright (\text{Plus } \square (\text{Num } 4)) \triangleright \circ \prec 2$
 $\vdash_C (\text{Plus } 2 \square) \triangleright (\text{Plus } \square (\text{Num } 4)) \triangleright \circ \succ (\text{Num } 3)$
 $\vdash_C (\text{Plus } 2 \square) \triangleright (\text{Plus } \square (\text{Num } 4)) \triangleright \circ \prec 3$
 $\vdash_C (\text{Plus } \square (\text{Num } 4)) \triangleright \circ \prec 4$
 $\vdash_C (\text{Plus } 5 \square) \triangleright \circ \succ (\text{Num } 4)$
 $\vdash_C (\text{Plus } 5 \square) \triangleright \circ \prec 4$
 $\vdash_C \circ \prec 9$

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Other Rules

We have similar rules for the other operators and for booleans. For If

$$\frac{s \succ (\text{If } e_1 \ e_2 \ e_3) \mapsto_C (\text{If } \square \ e_2 \ e_3) \triangleright s \succ e_1}{s \succ (\text{If } e_1 \ e_2 \ e_3) \mapsto_C (\text{If } \square \ e_2 \ e_3) \triangleright s \succ e_1}$$

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$$(\text{If } \square \ e_2 \ e_3) \triangleright s \prec \text{True} \mapsto_C s \succ e_2$$

$$\frac{(\text{If } \square \ e_2 \ e_3) \triangleright s \prec \text{False} \mapsto_C s \succ e_3}{(\text{If } \square \ e_2 \ e_3) \triangleright s \prec \text{False} \mapsto_C s \succ e_3}$$

Functions

Recfun (here abbreviated to **Fun**) evaluates to a *function value*:

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$$\frac{}{s \succ (\text{Fun } (f.x. e)) \mapsto_C s \prec \ll f.x. e \gg}$$

Function application is then handled similarly to Plus.

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$$\frac{}{s \succ (\text{Apply } e_1 \ e_2) \mapsto_C (\text{Apply } \Box \ e_2) \triangleright s \succ e_1}$$

$$\frac{}{(\text{Apply } \Box \ e_2) \triangleright s \prec \ll f.x. e \gg \mapsto_C (\text{Apply } \ll f.x. e \gg \ \Box) \triangleright s \succ e_2}$$

$$\frac{}{(\text{Apply } \ll f.x. e \gg \ \Box) \triangleright s \prec v \mapsto_C s \prec e[x := v, f := (\text{Fun } (f.x.e))]}$$

We are still using *substitution* for now.

What have we done?

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- All the rules are axioms – we can now implement the evaluator with a simple while loop (or a *tail recursive* function).
- We have a lower-level specification – helps with code generation (e.g. in an assembly language)
- Substitution is still a machine operation – we need to find a way to eliminate that.

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Correctness

While the M-Machine is reasonably straightforward definition of the language's semantics, the C-Machine is much more detailed.

We wish to prove a theorem that tells us that the C-Machine behaves analogously to the M-Machine.

Refinement

A low-level (*concrete*) semantics of a program is a *refinement* of a high-level (*abstract*) semantics if *every* possible execution in the low-level semantics has a corresponding execution in the high-level semantics. In our case:

$$\forall e, v. \frac{o \succ e \quad \overset{*}{\mapsto}_C \quad o \prec v}{e \quad \overset{*}{\mapsto}_M \quad v}$$

Functional correctness properties are preserved by refinement, but *security properties* are not (cf. Dining Cryptographers).

How to Prove Refinement

We can't get away with simply proving that each C machine step has a corresponding step in the M-Machine, because the C-Machine makes multiple steps that are no-ops in the M-Machine:

$$\begin{array}{lcl}
 & \circ \succ (+ (+ (N\ 2) (N\ 3)) (N\ 4)) & (+ (+ (N\ 2) (N\ 3)) (N\ 4)) \\
 \mapsto_C & (+ \square (N\ 4)) \triangleright \circ \succ (+ (N\ 2) (N\ 3)) & \\
 \mapsto_C & (+ \square (N\ 3)) \triangleright (+ \square (N\ 4)) \triangleright \circ \succ (N\ 2) & \\
 \mapsto_C & (+ \square (N\ 3)) \triangleright (+ \square (N\ 4)) \triangleright \circ \prec 2 & \\
 \mapsto_C & (+ 2 \square) \triangleright (+ \square (N\ 4)) \triangleright \circ \succ (N\ 3) & \\
 \mapsto_C & (+ 2 \square) \triangleright (+ \square (N\ 4)) \triangleright \circ \prec 3 & \\
 \mapsto_C & (+ \square (N\ 4)) \triangleright \circ \prec 5 & \mapsto_M (+ (N\ 5) (N\ 4)) \\
 \mapsto_C & (+ 5 \square) \triangleright \circ \succ (N\ 4) & \\
 \mapsto_C & (+ 5 \square) \triangleright \circ \prec 4 & \\
 \mapsto_C & \circ \prec 9 & \mapsto_M (N\ 9)
 \end{array}$$

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How to Prove Refinement

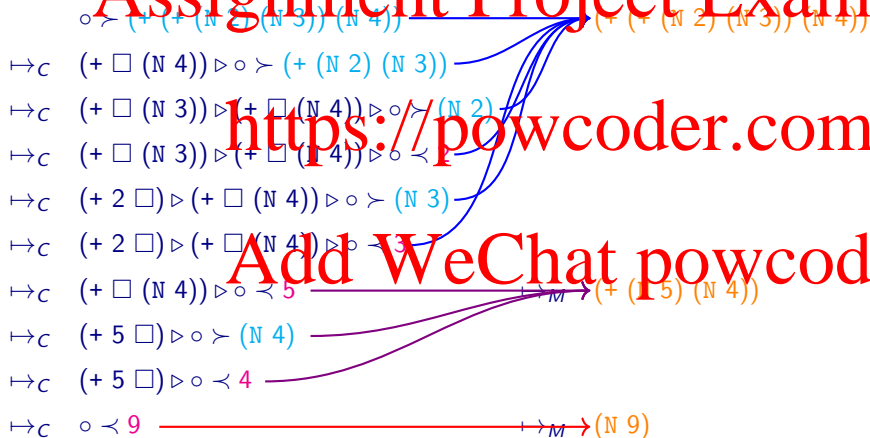
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- 1 Define an *abstraction function* $\mathcal{A} : \Sigma_C \rightarrow \Sigma_M$ that relates C-Machine states to M-Machine states, describing how they “correspond”.
- 2 Prove that for all *initial* states $\sigma \in I_C$, that the corresponding state $\mathcal{A}(\sigma) \in I_M$.
- 3 Prove for each step in the C-Machine $\sigma_1 \mapsto_C \sigma_2$, either:
 - the step is a no-op in the M-Machine and $\mathcal{A}(\sigma_1) = \mathcal{A}(\sigma_2)$, or
 - the step is replicated by the M-Machine $\mathcal{A}(\sigma_1) \mapsto_M \mathcal{A}(\sigma_2)$.
- 4 Prove that for all *final* states $\sigma \in F_C$ that the corresponding state $\mathcal{A}(\sigma) \in F_M$.

In general this abstraction function is called a *simulation relation* and this type of proof is called a *simulation* proof.

The Abstraction Function

Our abstraction function \mathcal{A} will need to relate states such that each transition that corresponds to a no-op in the M-Machine will move between \mathcal{A} -equivalent states:



Abstraction Function

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Given a C-Machine state with a stack and a current expression (or value), we reconstruct the overall expression to get the corresponding M-Machine state.

$$\mathcal{A}(s \succ e) = \mathcal{A}(s \succ \text{Num } v) = \mathcal{A}(s \succ v)$$

$$\mathcal{A}((\text{Plus } \square e_2) \triangleright s \succ e_1) = \mathcal{A}(s \succ (\text{Plus } e_1 e_2))$$

etc.

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By definition, all the initial/final states of the C-Machine are mapped to initial/final states of the M-Machine. So all that is left is the requirement for each transition.

Showing Refinement for Plus

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$$s \succ (\text{Plus } e_1 \ e_2) \mapsto_C (\text{Plus } \square \ e_2) \triangleright s \succ e_1$$

This is a no-op in the M-Machine.

$$\mathcal{A}(RHS) = \mathcal{A}((\text{Plus } \square \ e_2) \triangleright s \succ e_1)$$

$$= \mathcal{A}(s \succ (\text{Plus } e_1 \ e_2))$$

$$= \mathcal{A}(LHS)$$

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Showing Refinement for Plus

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$$\overline{(\text{Plus } \square e_2) \triangleright s \prec v_1 \quad \mapsto_C \quad (\text{Plus } v_1 \square) \triangleright s \succ e_2}$$

Another no-op in the VM-Machine:

$$\begin{aligned} \mathcal{A}(LHS) &= \mathcal{A}((\text{Plus } \square e_2) \triangleright s \prec v_1) \\ &= \mathcal{A}(s \succ (\text{Plus } (\text{Num } v_1) e_2)) \\ &= \mathcal{A}((\text{Plus } v_1 \square) \triangleright s \succ e_2) \\ &= \mathcal{A}(RHS) \end{aligned}$$

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Showing Refinement for Plus

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$$\frac{}{(\text{Plus } v_1 \square) \triangleright s \prec v_2 \quad \mapsto_C \quad s \prec v_1 + v_2}$$

This corresponds to a M-Machine transition:

$$\begin{aligned}
 \mathcal{A}(LHS) &= \mathcal{A}((\text{Plus } v_1 \square) \triangleright s \prec v_2) \\
 &= \mathcal{A}(s \succ (\text{Plus } (\text{Num } v_1) (\text{Num } v_2))) \\
 &\stackrel{\mapsto_M}{=} \mathcal{A}(s \succ (\text{Num } v_1 + v_2)) \\
 &= \mathcal{A}(s \prec v_1 + v_2) \\
 &= \mathcal{A}(RHS)
 \end{aligned}$$

Technically the reduction step (*) requires induction on the stack.