

CS 536 / Fall 2020

Assignment Project Exam Help

<https://powcoder.com>

Introduction to programming languages and compilers

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About me

PhD at University of Toronto

Joined University of Wisconsin in 2015

Part of madPL group

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Program verification

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Program synthesis

<http://pages.cs.wisc.edu/~aws/>

About the course

We will study compilers

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We will understand how they work

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We will build a **full** compiler

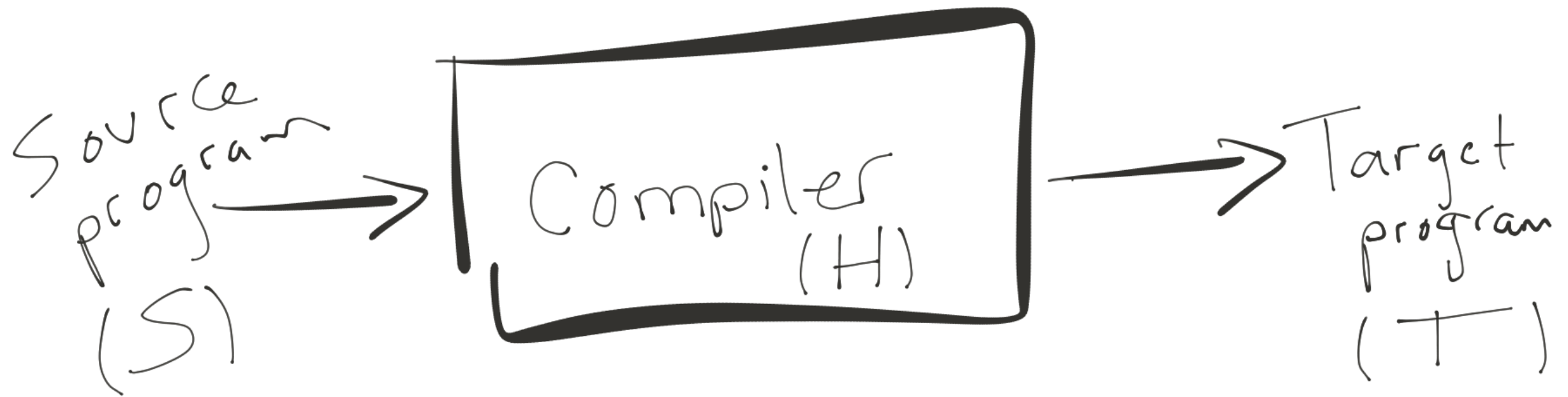
We will have fun

Course Mechanics

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- Home page: <https://powcoder.com> <http://cs.wisc.edu/~aws/courses/cs536-f20/>
- Workload:
 - 6 Programs (40% = 5% + 7% + 7% + 7% + 7% + 7%)
 - 2 exams (midterm: 30% + final: 30%)

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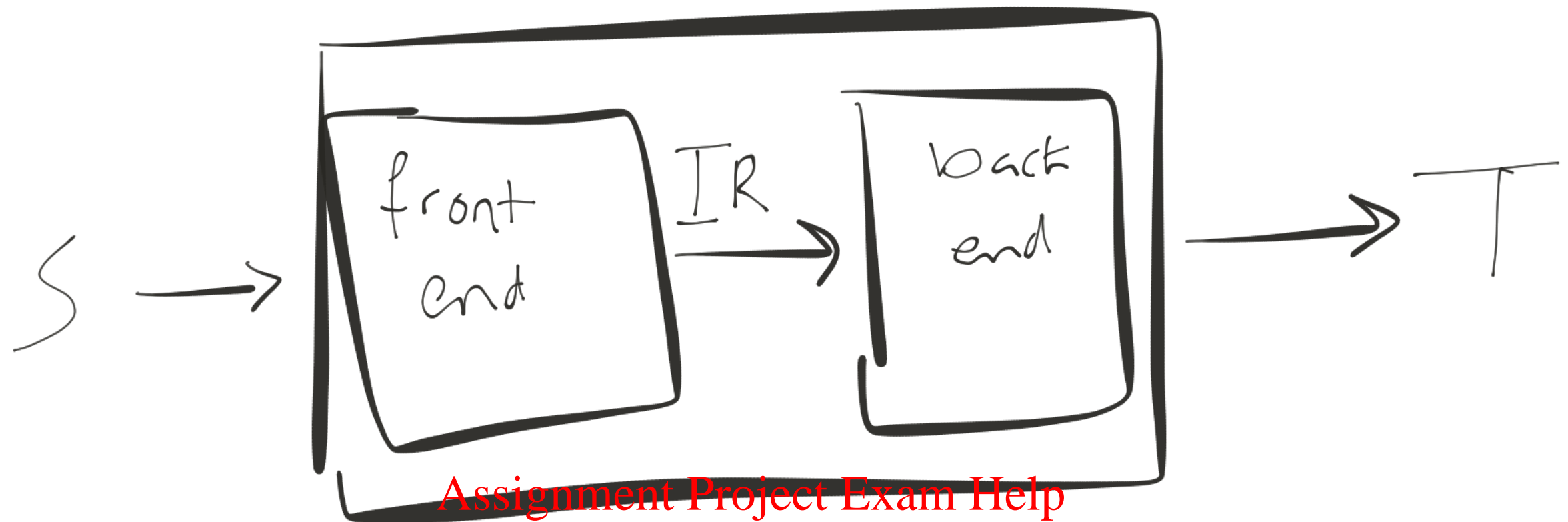


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A compiler is a
recognizer of language S
a translator from S to T
a program in language H



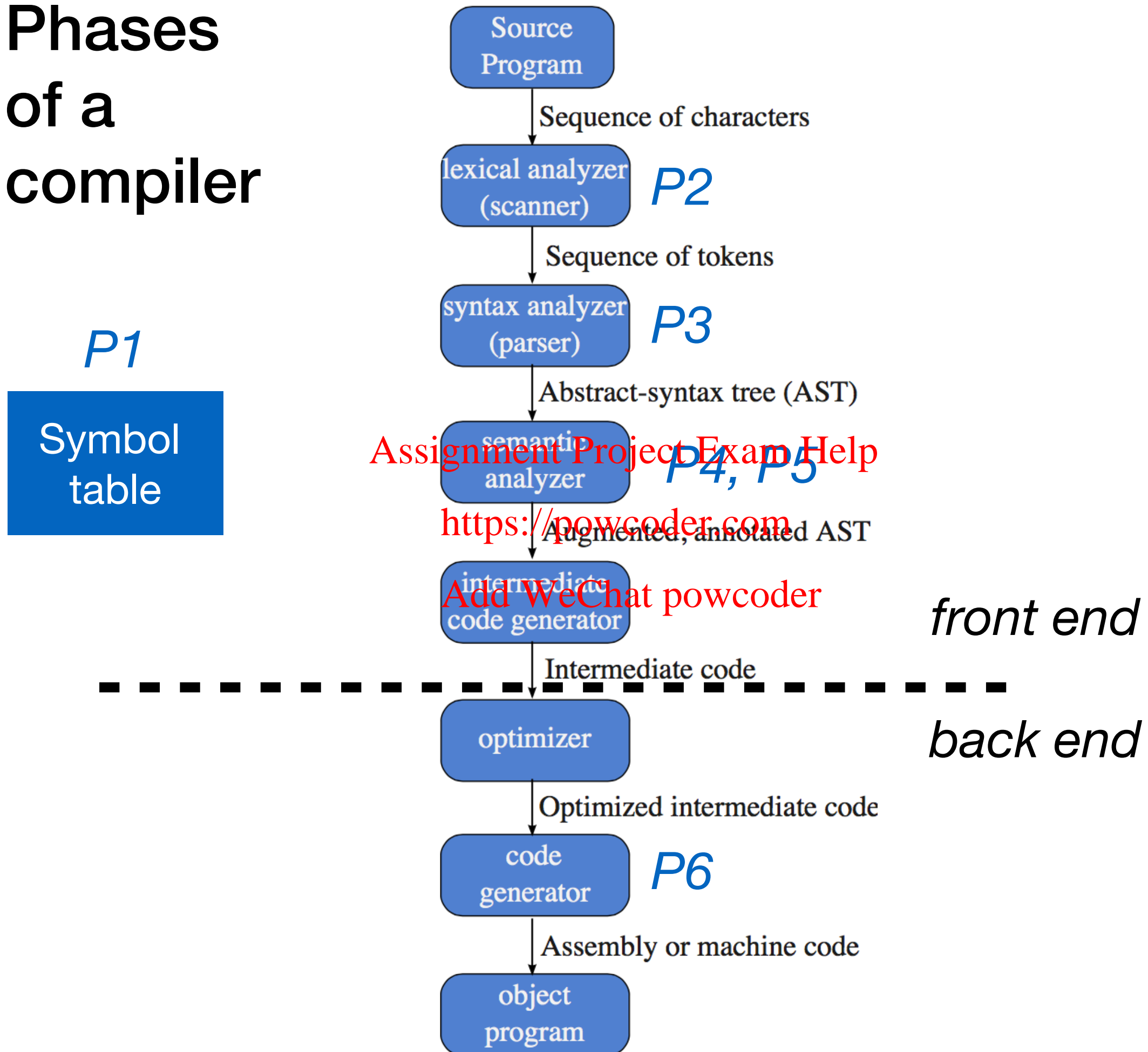
front end = recognize source code S ;
map S to IR

IR = intermediate representation

back end = map IR to T

Executing the T program produces the same result as executing the S program?

Phases of a compiler



Scanner (P2)

Input: characters from source program

Output: sequence of tokens

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Actions:

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group chars into lexemes (tokens)

Identify and ignore whitespace, comments, etc.

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Error checking:

bad characters such as ^

unterminated strings, e.g., "Hello

int literals that are too large

Example

scanner **a = 2 * b + abs(-71)**

ident	asgn	int lit	times	ident	plus	ident	lparens	int lit	rparsens
(a)		(2)		(b)		(abs)	minus	(71)	

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Whitespace (spaces, tabs, and newlines) filtered out
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a	=	2	*b+
		abs	(-
		71)	

The scanner's output is still the sequence

ident	asgn	int lit	times	ident	plus	ident	lparens	int lit	rparsens
(a)		(2)		(b)		(abs)	minus	(71)	

Parser (P3)

Input: sequence of tokens from the scanner

Output: AST (abstract syntax tree)

Actions: <https://powcoder.com>

groups tokens into sentences [Add WeChat powcoder](#)

Error checking:

syntax errors, e.g., $x = y^* = 5$

(possibly) *static semantic* errors, e.g., use of undeclared variables

Semantic analyzer (P4,P5)

Input: AST

Output: annotated AST

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Actions: does more static semantic checks

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Name analysis

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process declarations and uses of variables

enforces scope

Type checking

checks types

augments AST w/ types

Semantic analyzer (P4,P5)

Scope example:

```
...  
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{  
  https://powcoder.com  
  int i = 4;  
  i++;  
}
```

out of scope \longrightarrow `i = 5;`

Intermediate code generation

Input: annotated AST (assumes no errors)

Output: intermediate representation (IR)

e.g., 3-address code

instructions have 3 operands at most

easy to generate from AST

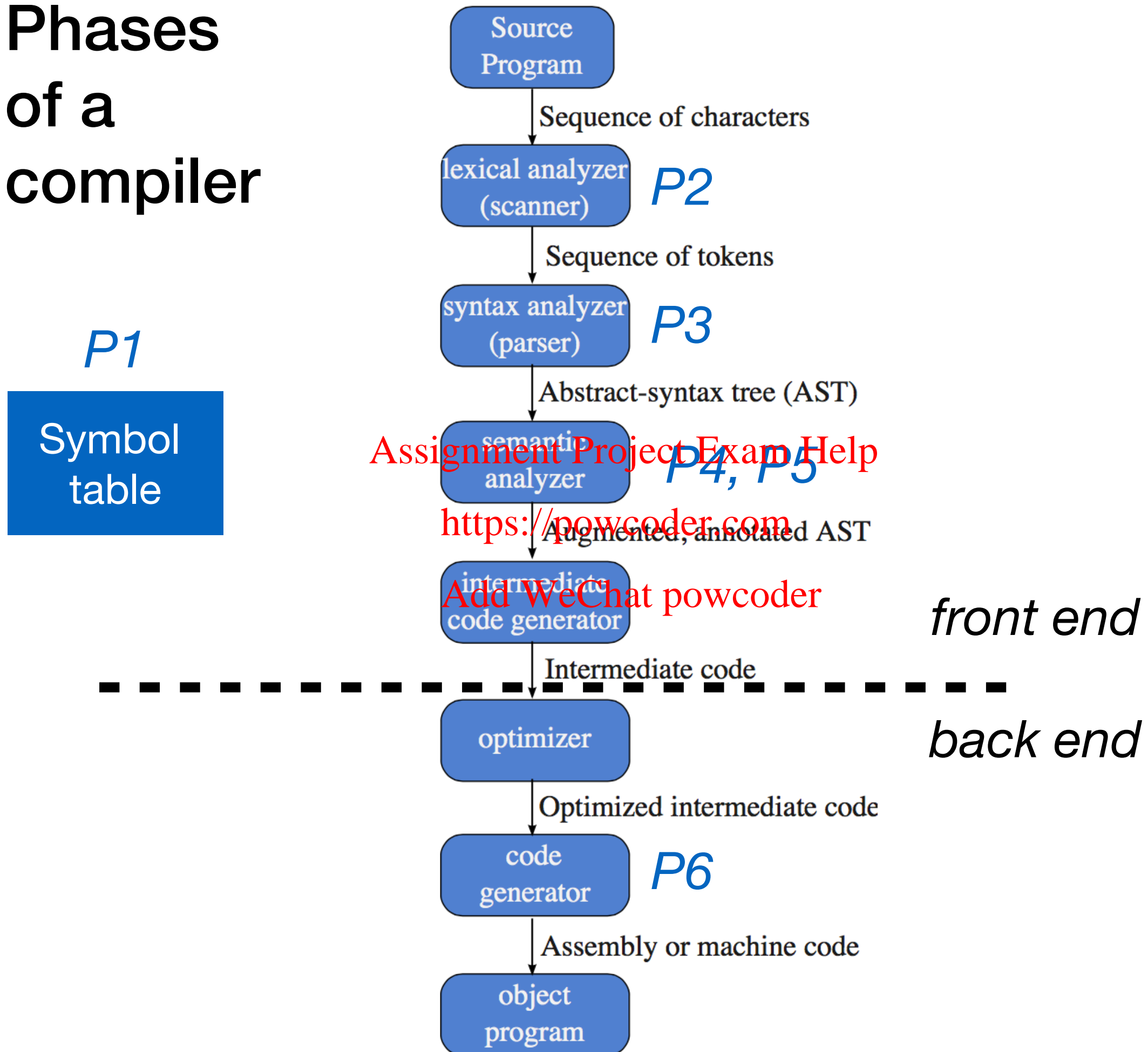
1 instr per AST internal node

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Phases of a compiler

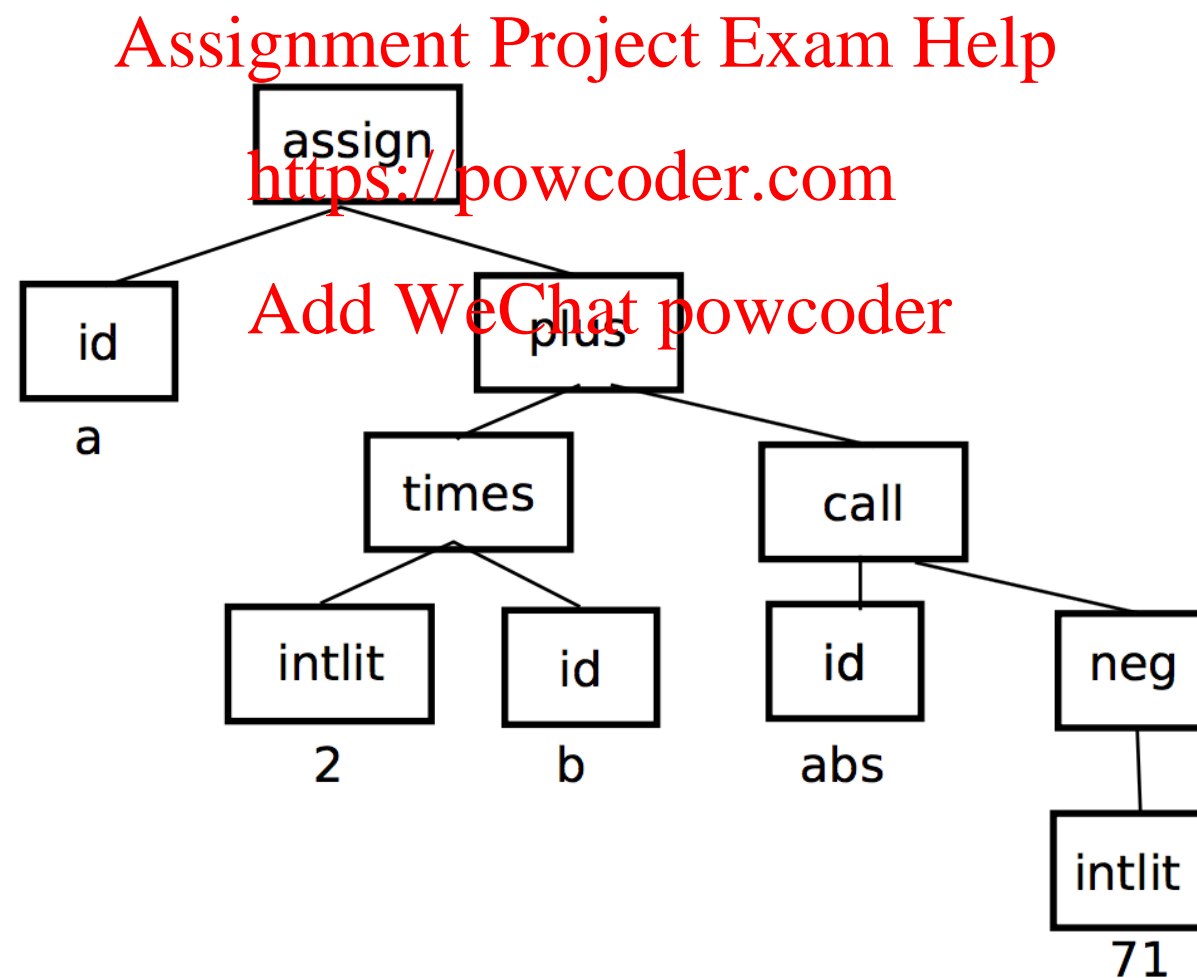


Example

scanner **a = 2 * b + abs(-71)**

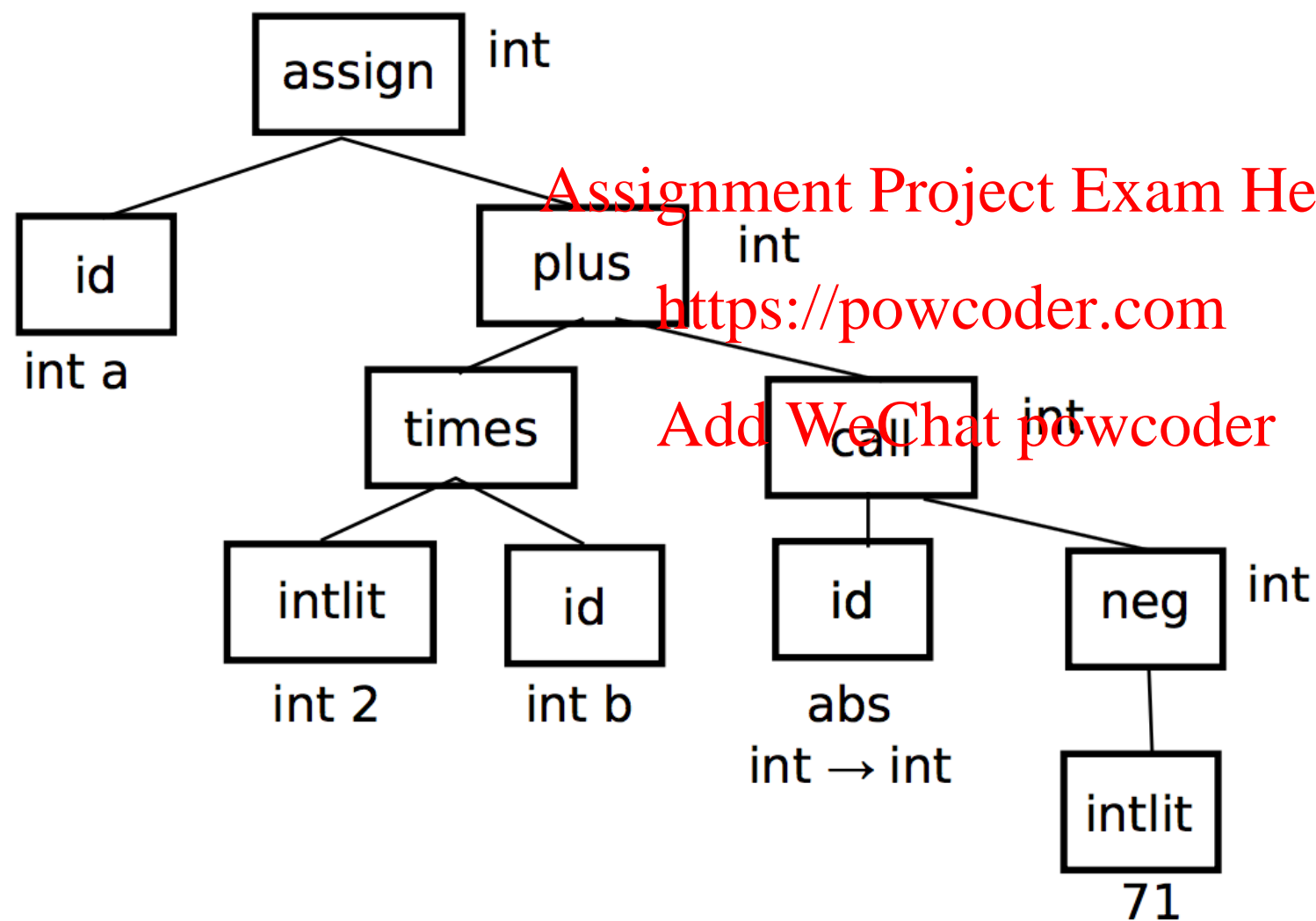
ident	asgn	int lit	times	ident	plus	ident	lparens	int lit	rparsens
(a)		(2)		(b)		(abs)	minus	(71)	

parser



Example (cont'd)

semantic analyzer



Symbol
table

a	var	int
b	var	int
abs	fun	int→int

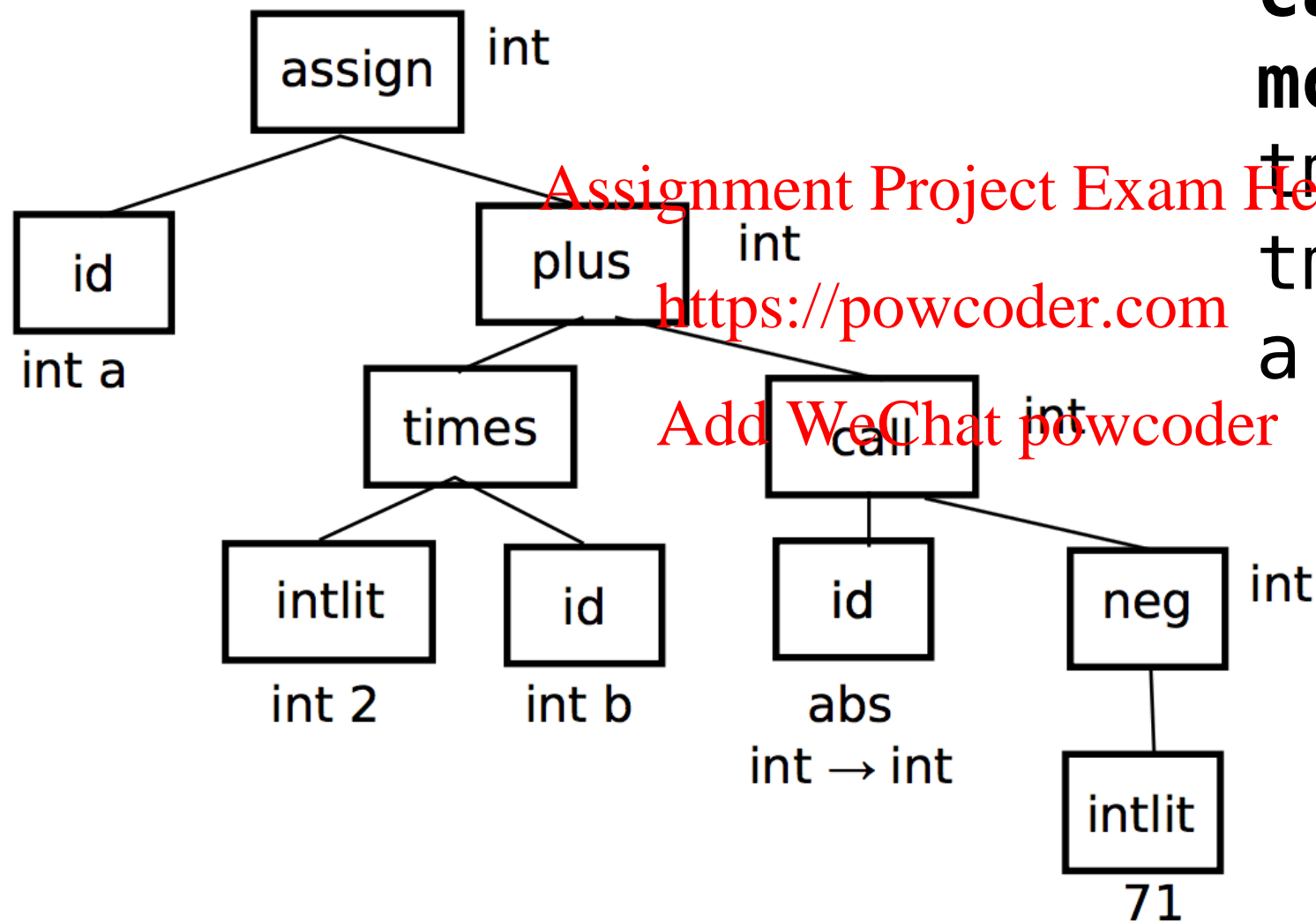
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Example (cont'd)

code generation



tmp1 = 0 - 71
move tmp1 param1
call abs
move ret1 tmp2
tmp3 = 2*b
tmp4 = tmp3 + tmp2
a = tmp4

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Optimizer

Input: IR

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Output: optimized IR

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Actions: *Improve code*

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make it run faster; make it smaller

several passes: local and global optimization

more time spent in compilation; less time in execution

Code generator (~P6)

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Input: IR from optimizer

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Output: target code

Symbol table (P1)

Compiler keeps track of names in

semantic analyzer — both name analysis and type checking
code generation — offsets into stack
optimizer — def-use info

P1: implement symbol table

Symbol table

Block-structured language

Java, C, C++

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Ideas:

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nested visibility of names (no access to a variable out of scope)

easy to tell which def of a name applies (nearest definition)

lifetime of data is bound to scope

Symbol table

```
int x, y;
```

block structure: *need
symbol table with nesting*

```
void A() {
```

implement as list of hashtables

```
    double x, z;
```

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```
    C(x, y, z)
```

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```
}
```

```
void B() {
```

```
    C(x, y, z);
```

```
}
```